

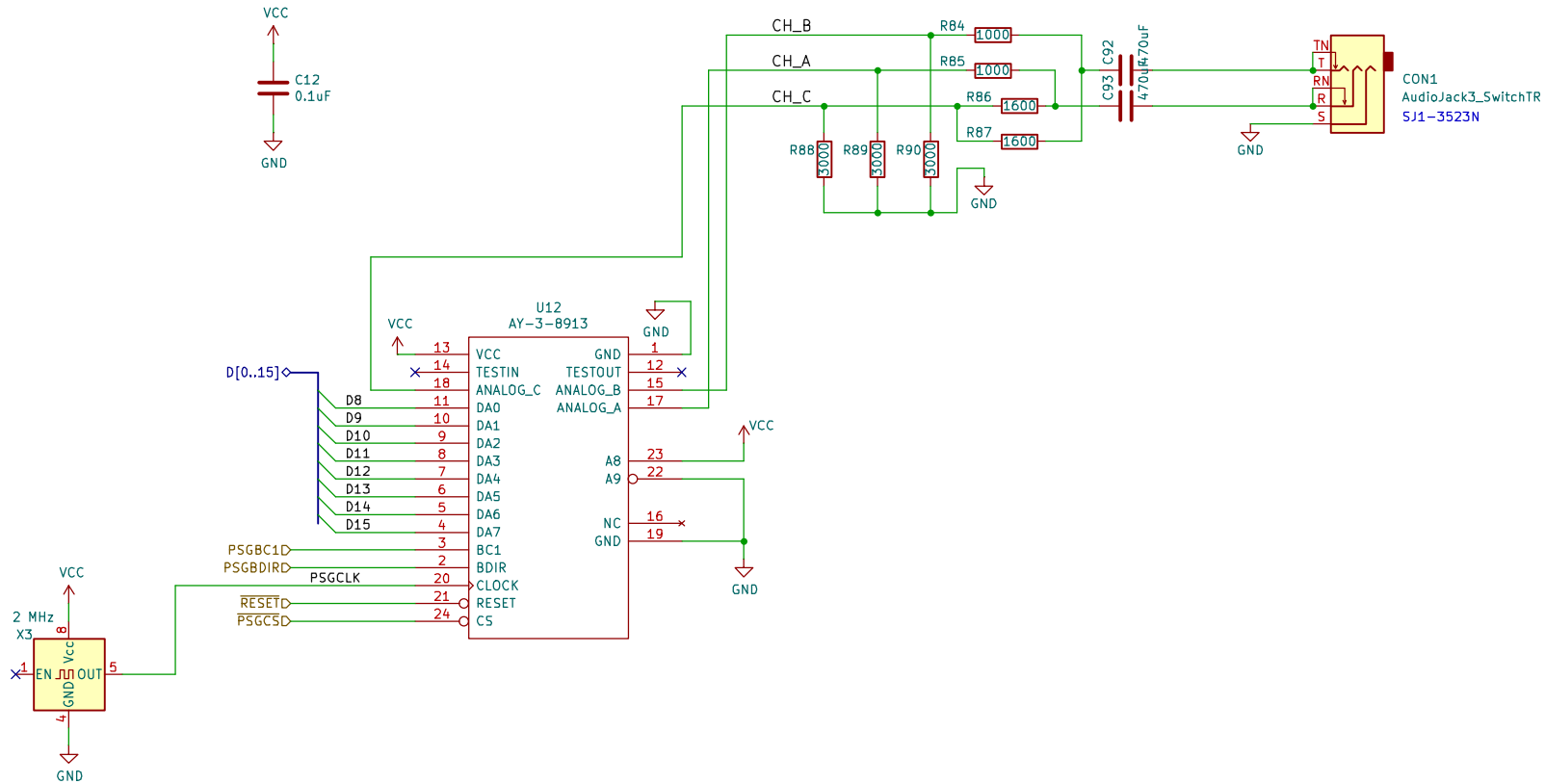
BITSY Mark I is a single board computer based around an MC68000 CPU.
 © 2025 Rob Gowin
https://github.com/codesmythe/BITSY_MK_I
 Licensed under CC-BY-SA-NC 4.0.

Sheet: /MEM/
 File: MEM.kicad_sch

Title: BITSY Mark I – An MC68000 Single Board Computer

Size: A	Date: 2025-06-12	Rev: V1B
KiCad E.D.A. 8.0.9		Id: 2/8

Sound output circuit taken from <https://hw.speccy.cz/melodik2.html>
by way of <https://github.com/electrified/rc2014-ym2149/blob/main/README.md>

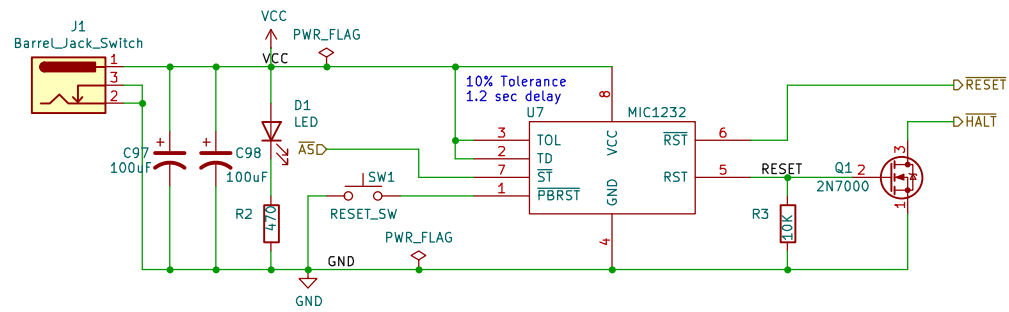


BITSY Mark I is a single board computer based around an MC68000 CPU.
© 2025 Rob Gowin
https://github.com/codesmythe/BITSY_MK_I
Licensed under CC-BY-SA-NC 4.0.

Sheet: /PSG_NOPP/
File: PSG_NOPP.kicad_sch

Title: BITSY Mark I – An MC68000 Single Board Computer

Size: A	Date: 2025-06-12	Rev: V1B
KiCad E.D.A. 8.0.9		Id: 3/8

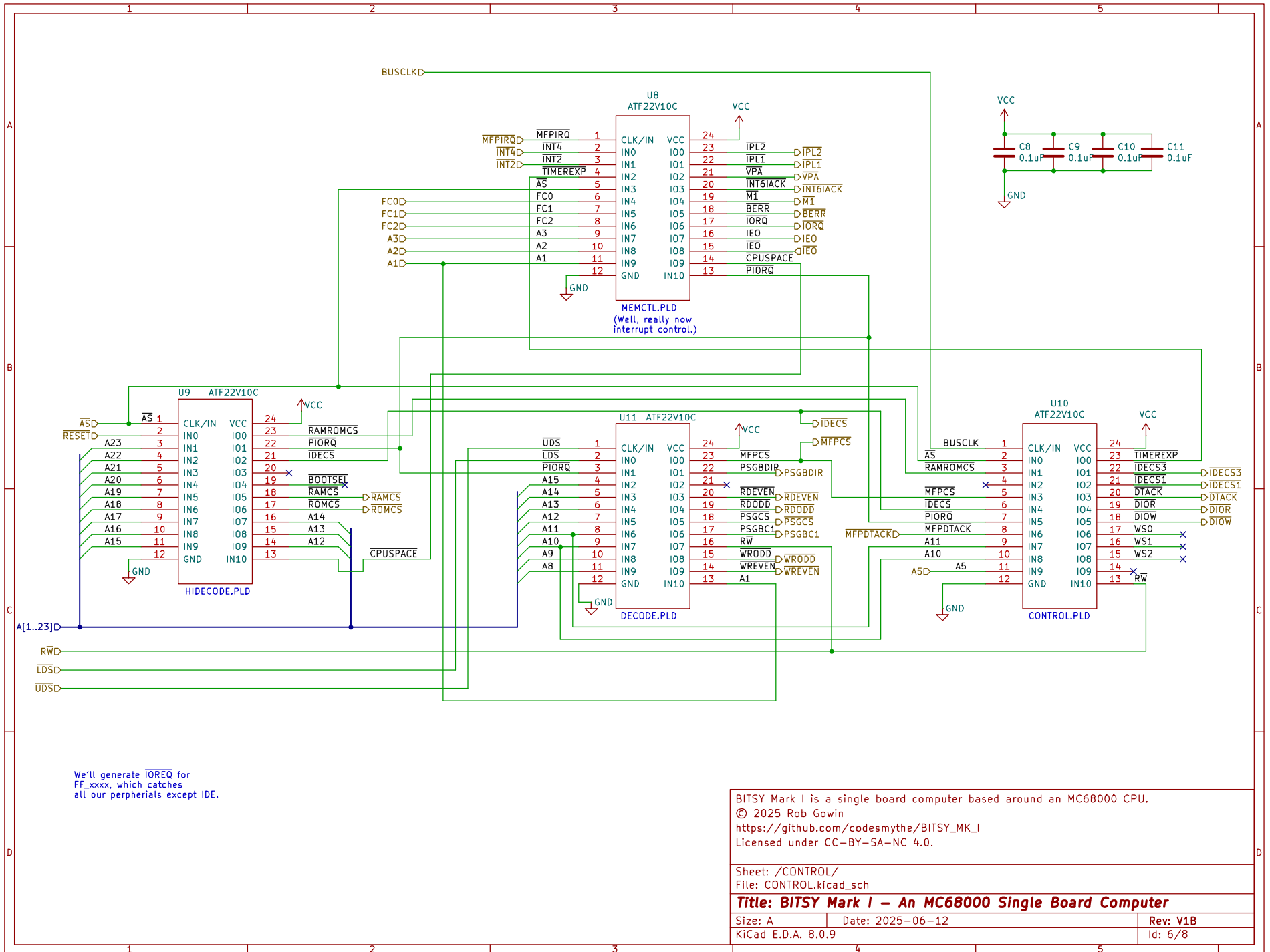


BITSY Mark I is a single board computer based around an MC68000 CPU.
 © 2025 Rob Gowin
https://github.com/codesmythe/BITSY_MK_I
 Licensed under CC-BY-SA-NC 4.0.

Sheet: /PWRRESET/
 File: PWRRESET.kicad_sch

Title: BITSY Mark I – An MC68000 Single Board Computer

Size: A	Date: 2025-06-12	Rev: V1B
KiCad E.D.A. 8.0.9		Id: 5/8

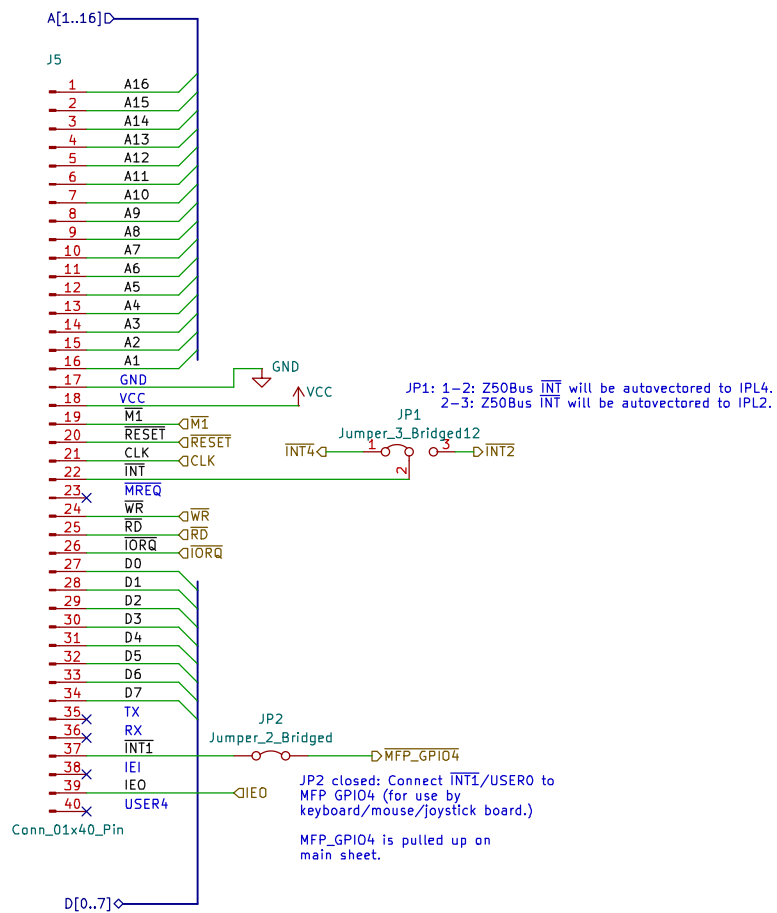


BITSY Mark I is a single board computer based around an MC68000 CPU.
 © 2025 Rob Gowin
https://github.com/codesmythe/BITSY_MK_I
 Licensed under CC-BY-SA-NC 4.0.

Sheet: /CONTROL/
 File: CONTROL.kicad_sch

Title: BITSY Mark I – An MC68000 Single Board Computer

Size: A	Date: 2025-06-12	Rev: V1B
KiCad E.D.A. 8.0.9		Id: 6/8

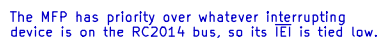


BITSY Mark I is a single board computer based around an MC68000 CPU.
 © 2025 Rob Gowin
https://github.com/codesmythe/BITSY_MK_I
 Licensed under CC-BY-SA-NC 4.0.

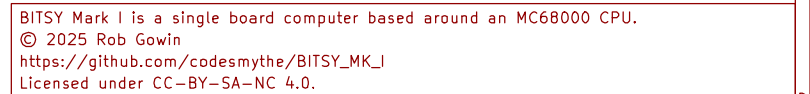
Sheet: /RCBUS/
 File: RCBUS40-Z80.kicad_sch

Title: BITSY Mark I – An MC68000 Single Board Computer

Size: A	Date: 2025-06-12	Rev: V1B
KiCad E.D.A. 8.0.9		Id: 7/8



Rev: V1B
Id: 8/8



Size: A	Date: 2025-06-12	Rev: V1B
KiCad E.D.A. 8.0.9		Id: 9/8