

File: power.kicad_sch



File: buffers.kicad_sch



File: dualPIO.kicad_sch



File: CTC-INT.kicad_sch



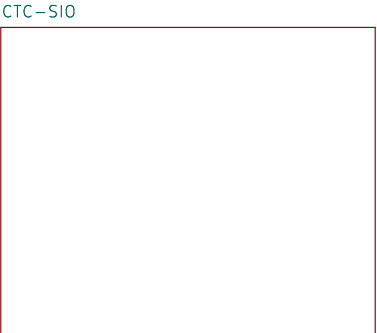
File: bus.kicad_sch



File: IO.kicad_sch

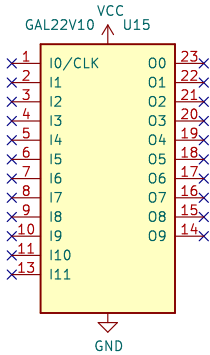


File: GAL.kicad_sch



File: CTC-SIO.kicad_sch

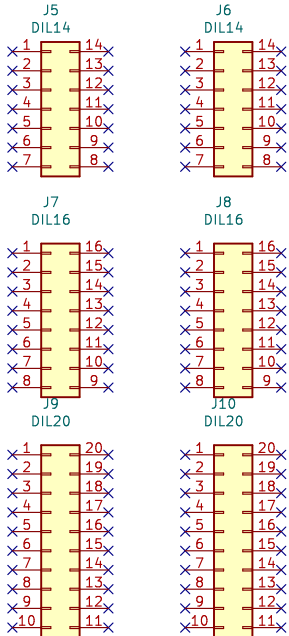
SPARE GAL FOR
DEBUGGING

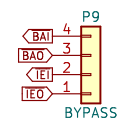
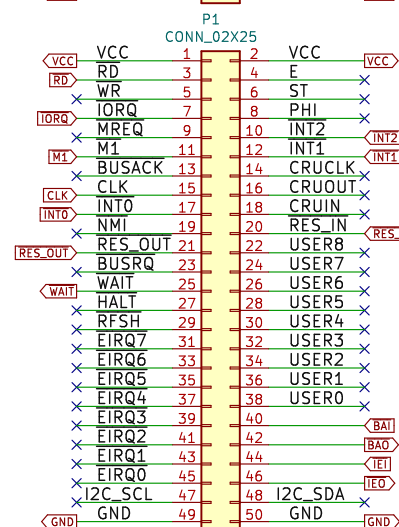
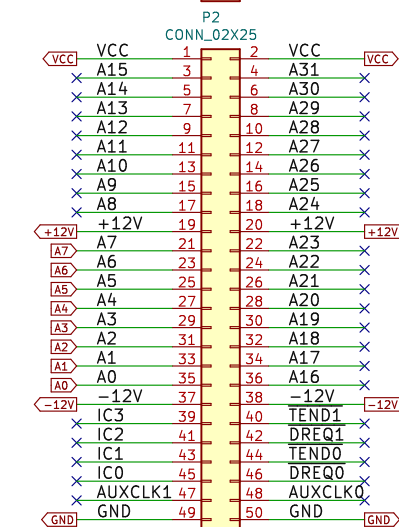
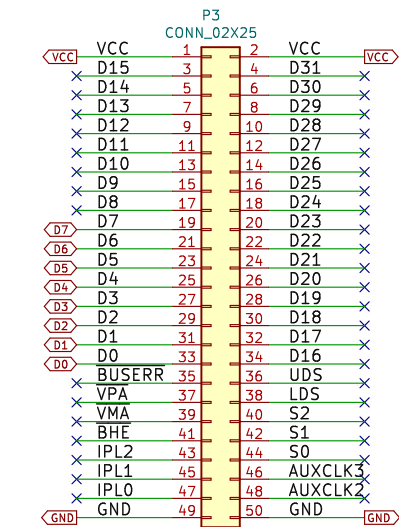


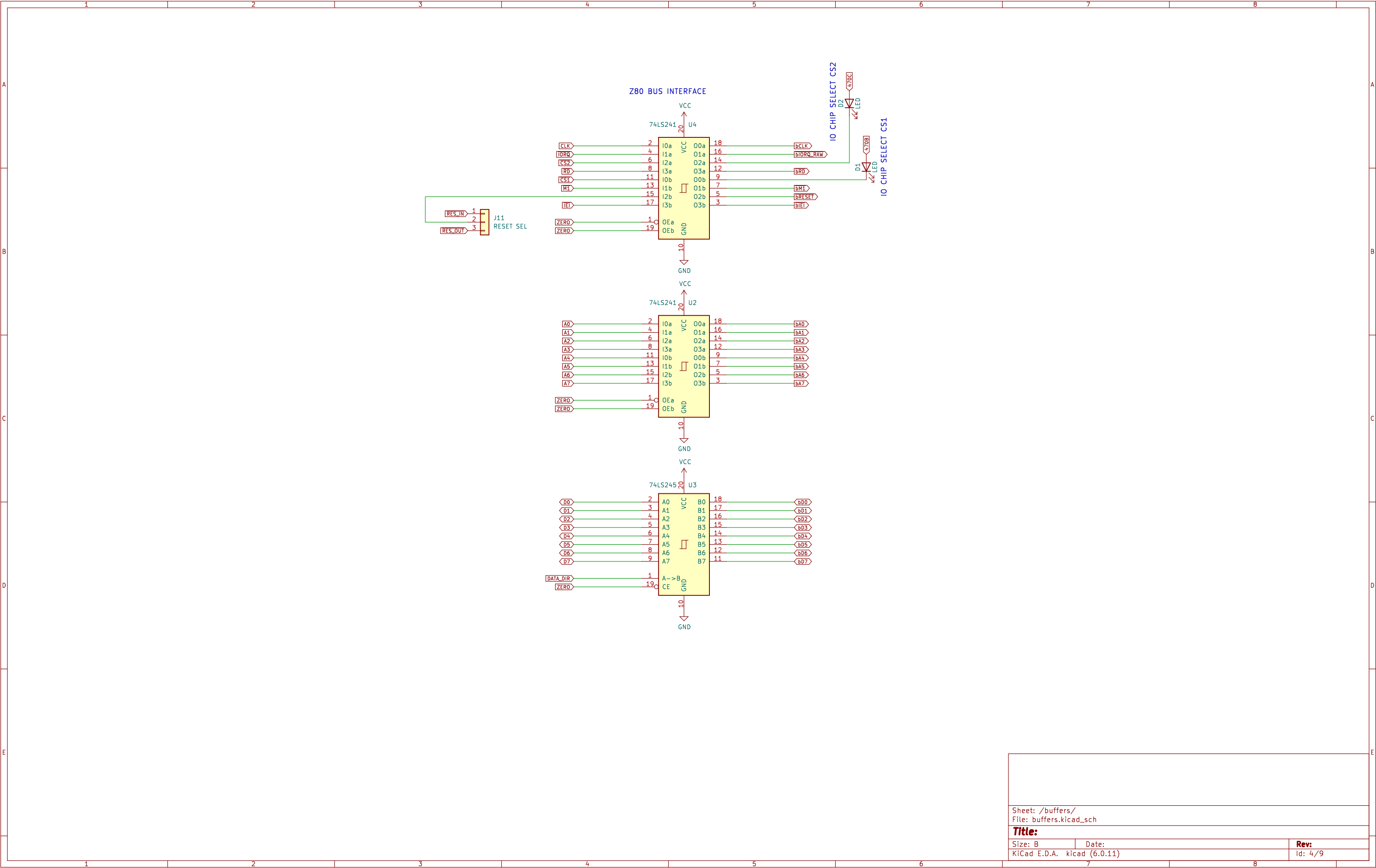
THROUGH HOLES FOR
PATCH WIRE ROUTING
ACROSS SIDES

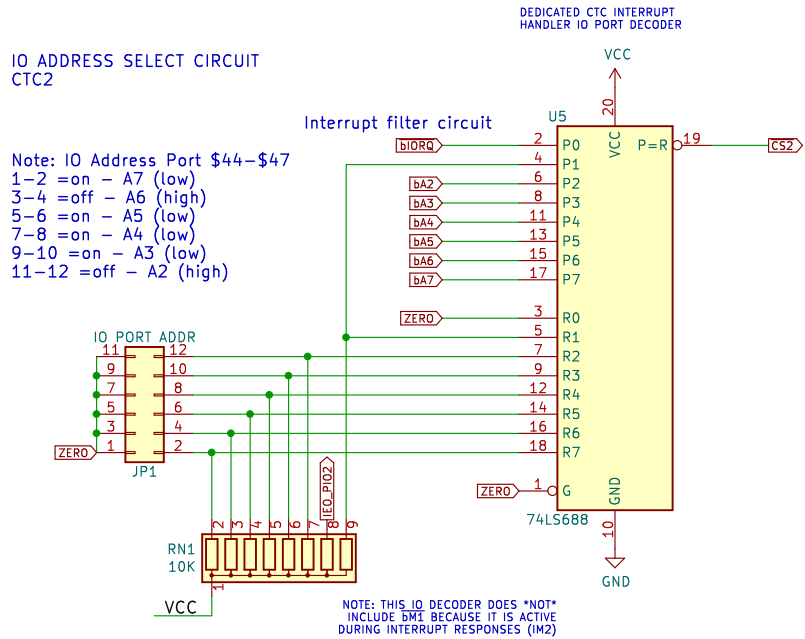
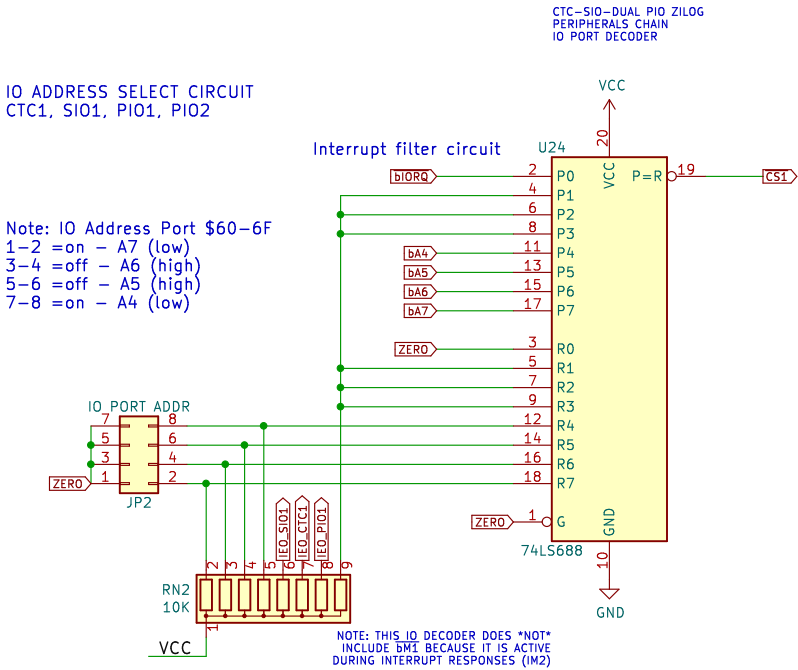
- H3 MountingHole
- H4 MountingHole

SPARE SOCKETS
FOR DEBUGGING









Sheet: /IO/
File: IO.kicad_sch

Title:

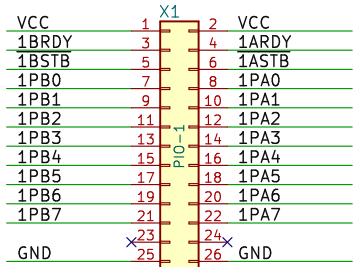
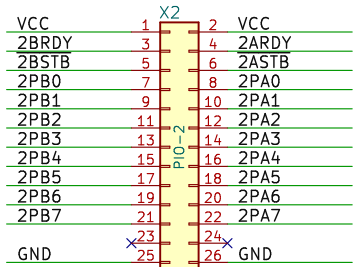
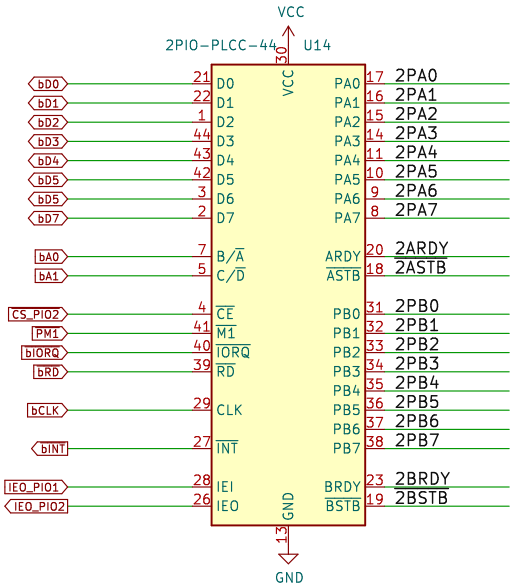
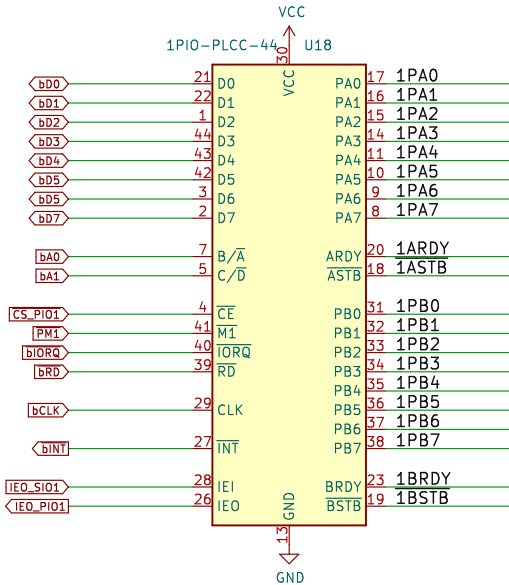
Size: B

Date:

KiCad E.D.A. kicad (6.0.11)

Rev:

Id: 5/9



Sheet: /dualPIO/
File: dualPIO.kicad_sch

Title:

Size: B

Date:

KiCad E.D.A. kicad (6.0.11)

Rev:

Id: 6/9

