

File: power.kicad_sch



File: buffers.kicad_sch



File: dualPIO.kicad_sch



File: CTC-INT.kicad_sch



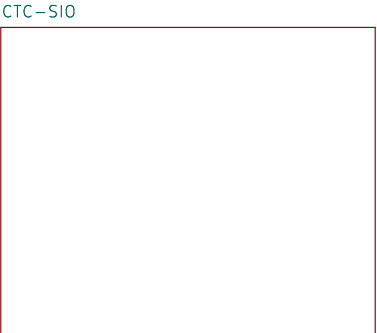
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File: IO.kicad_sch

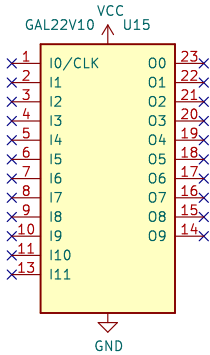


File: GAL.kicad_sch



File: CTC-SIO.kicad_sch

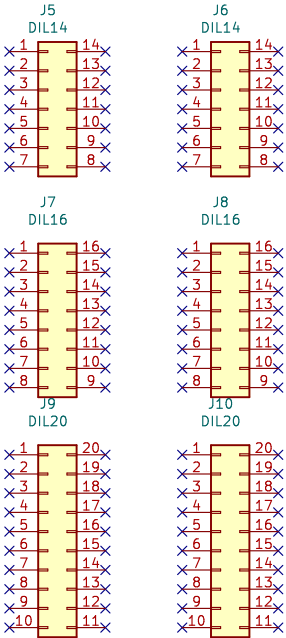
SPARE GAL FOR
DEBUGGING

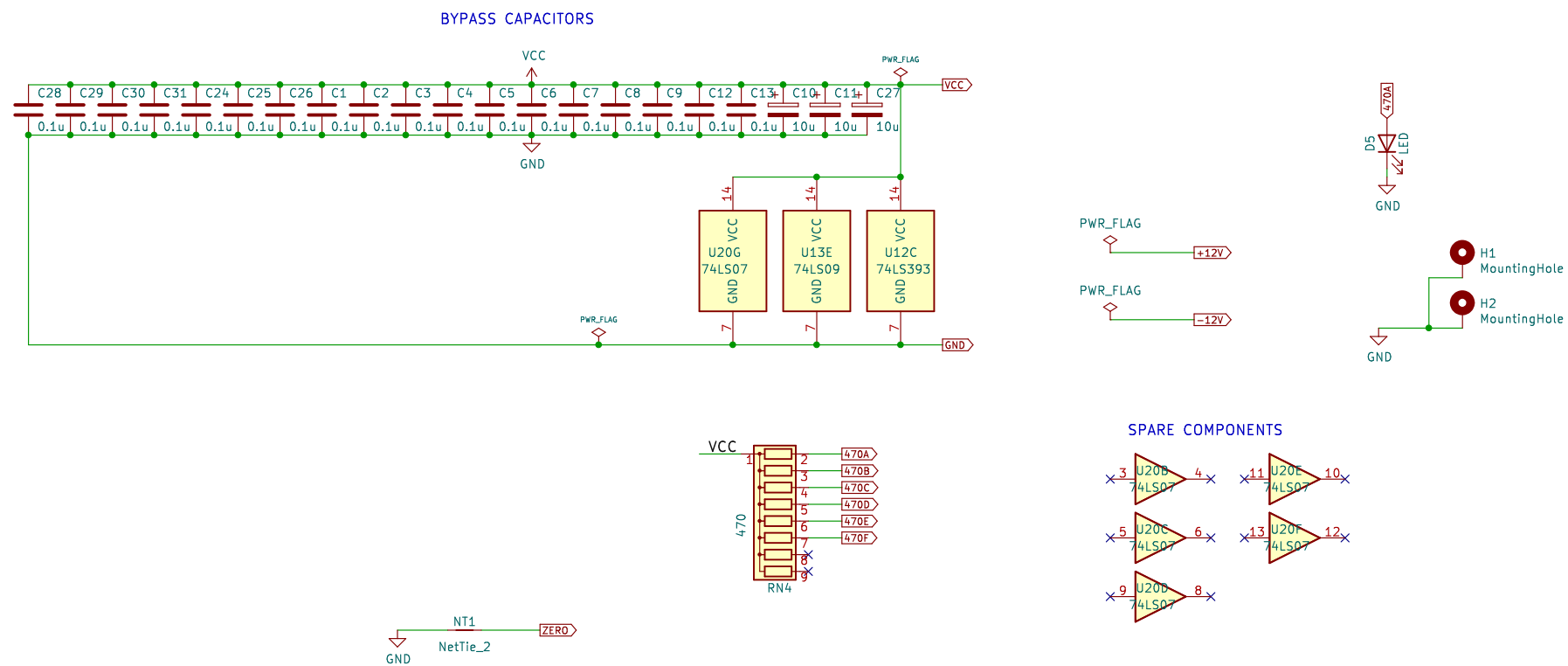


THROUGH HOLES FOR
PATCH WIRE ROUTING
ACROSS SIDES

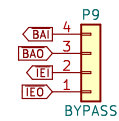
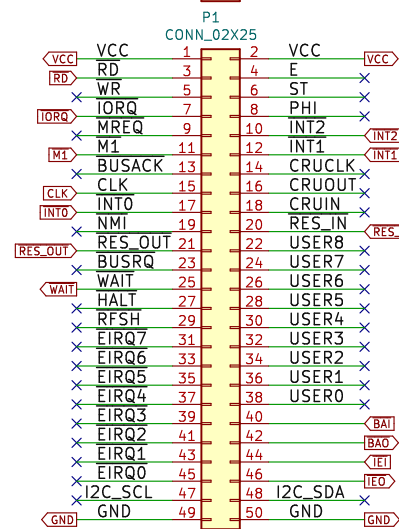
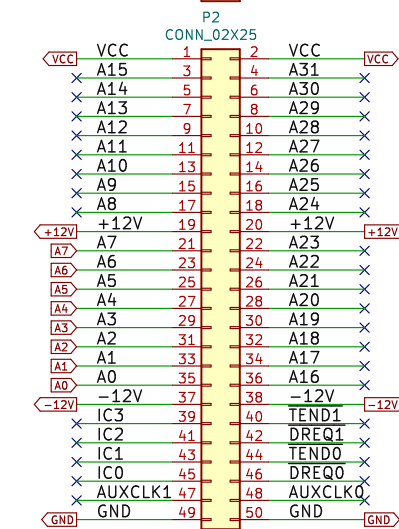
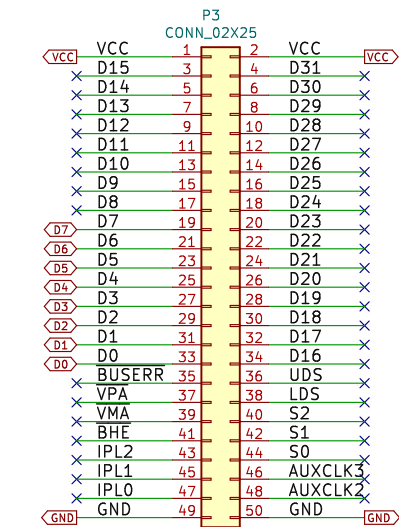
- H3 MountingHole
- H4 MountingHole

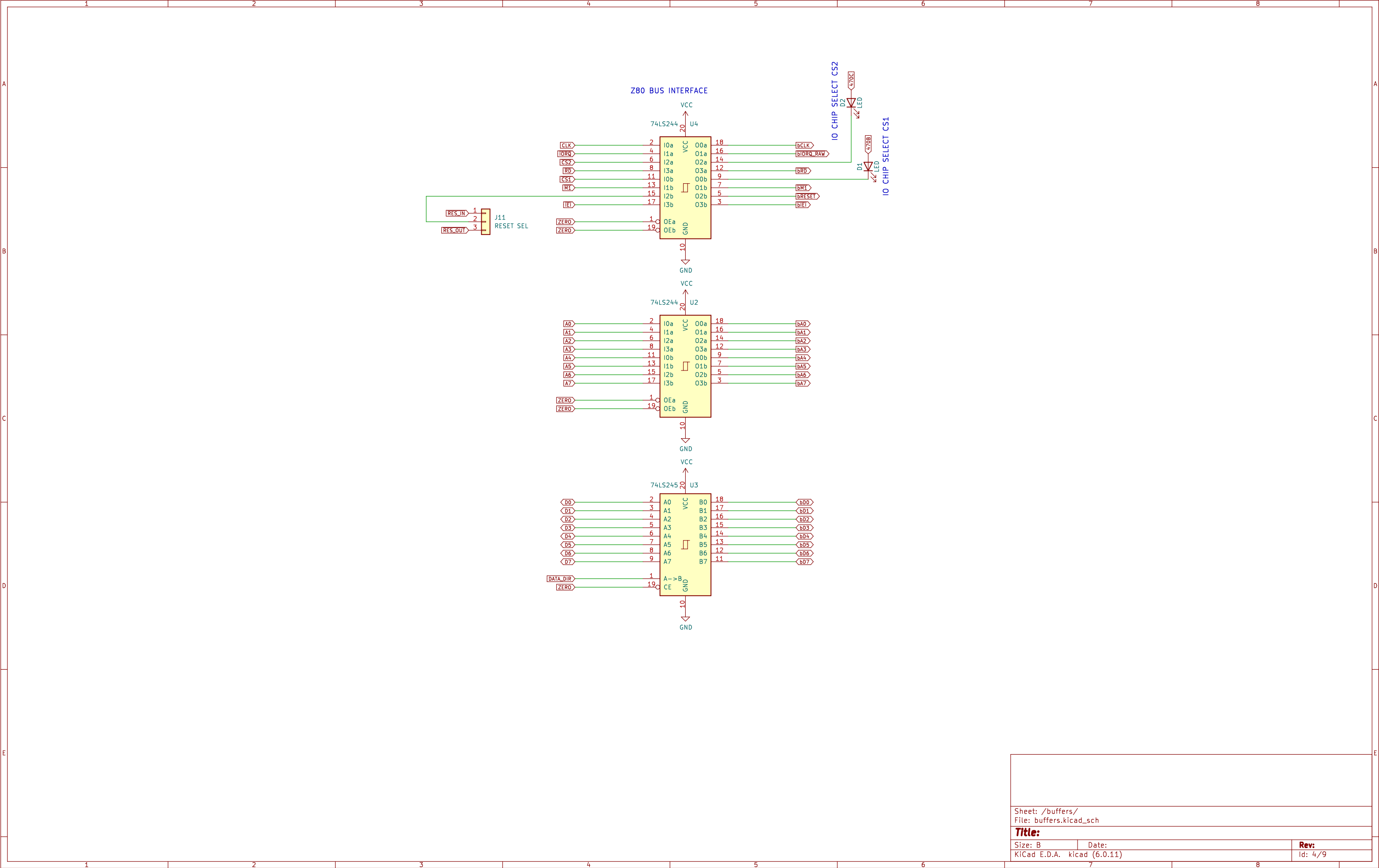
SPARE SOCKETS
FOR DEBUGGING

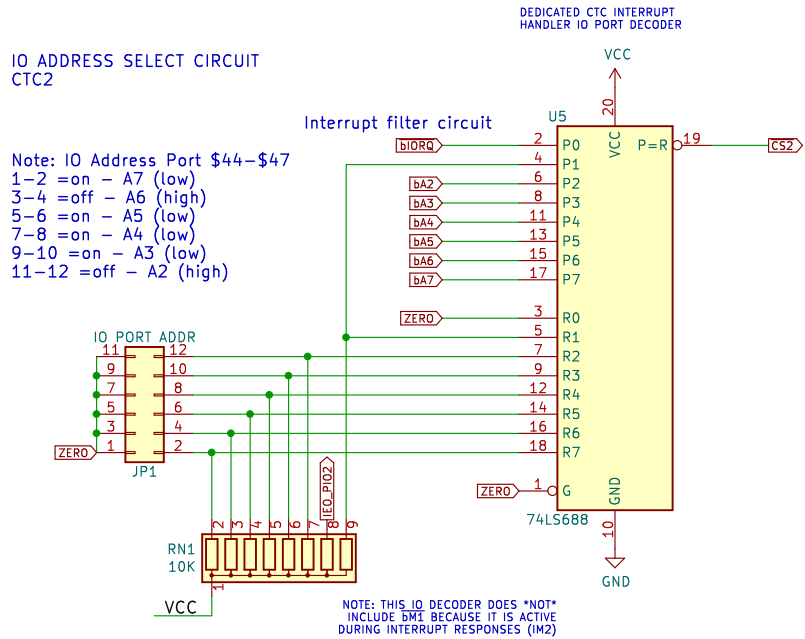
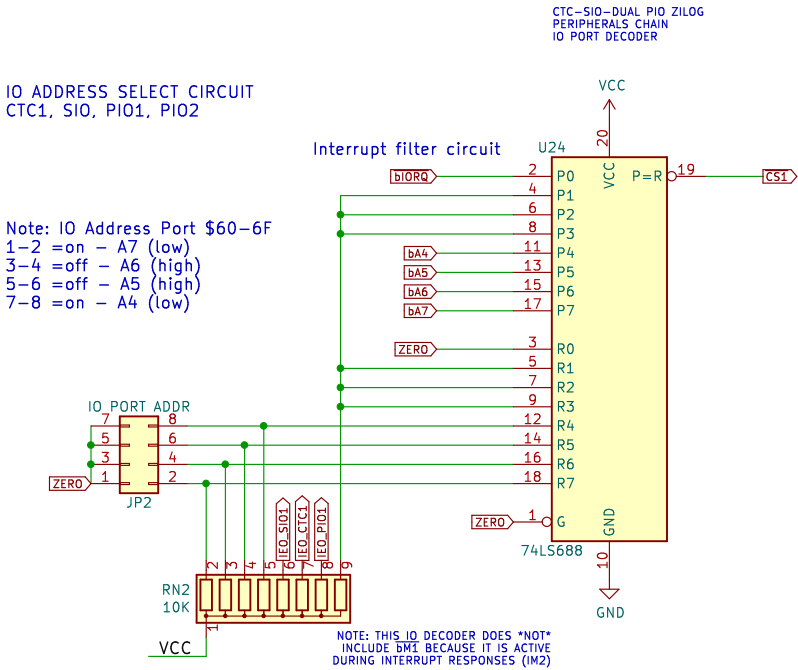




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KiCad E.D.A. kicad (6.0.11)		Id: 2/9







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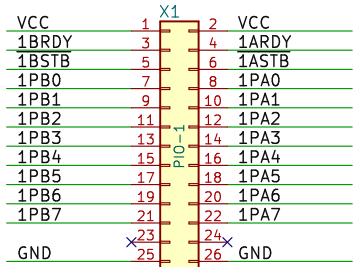
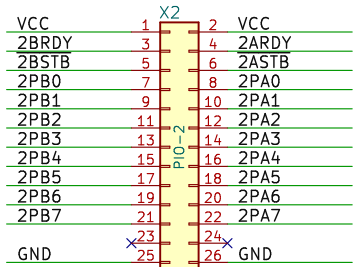
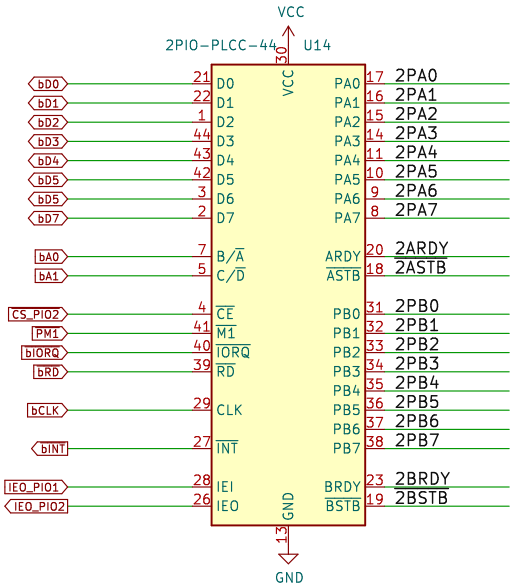
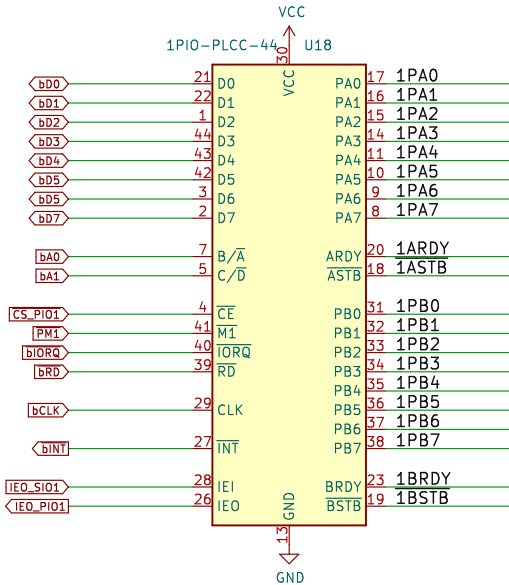
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Id: 5/9



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Rev:

Id: 6/9

