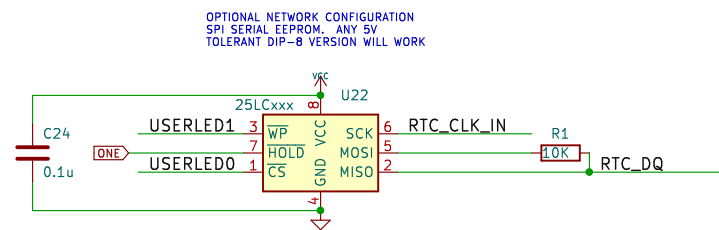
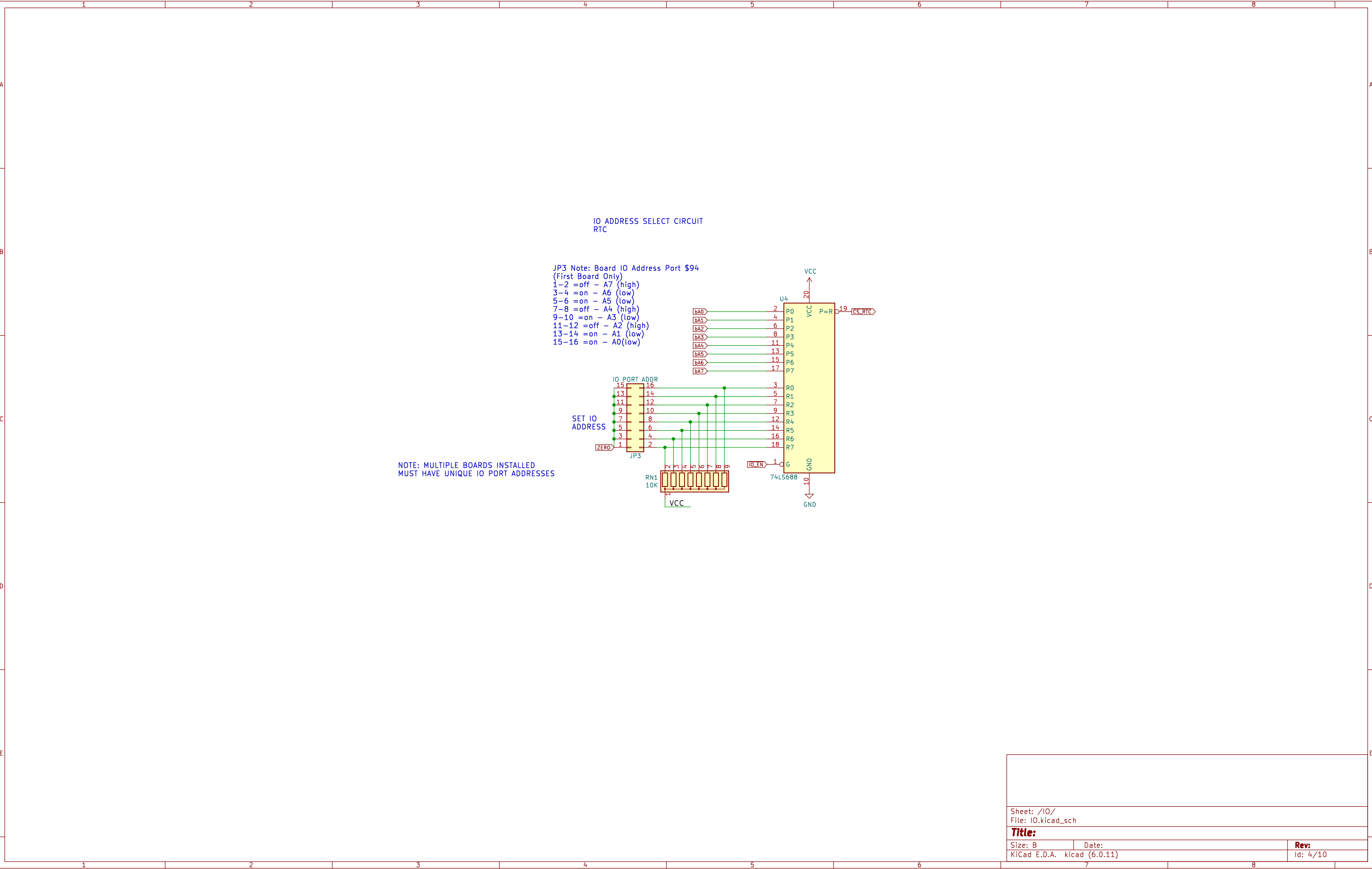
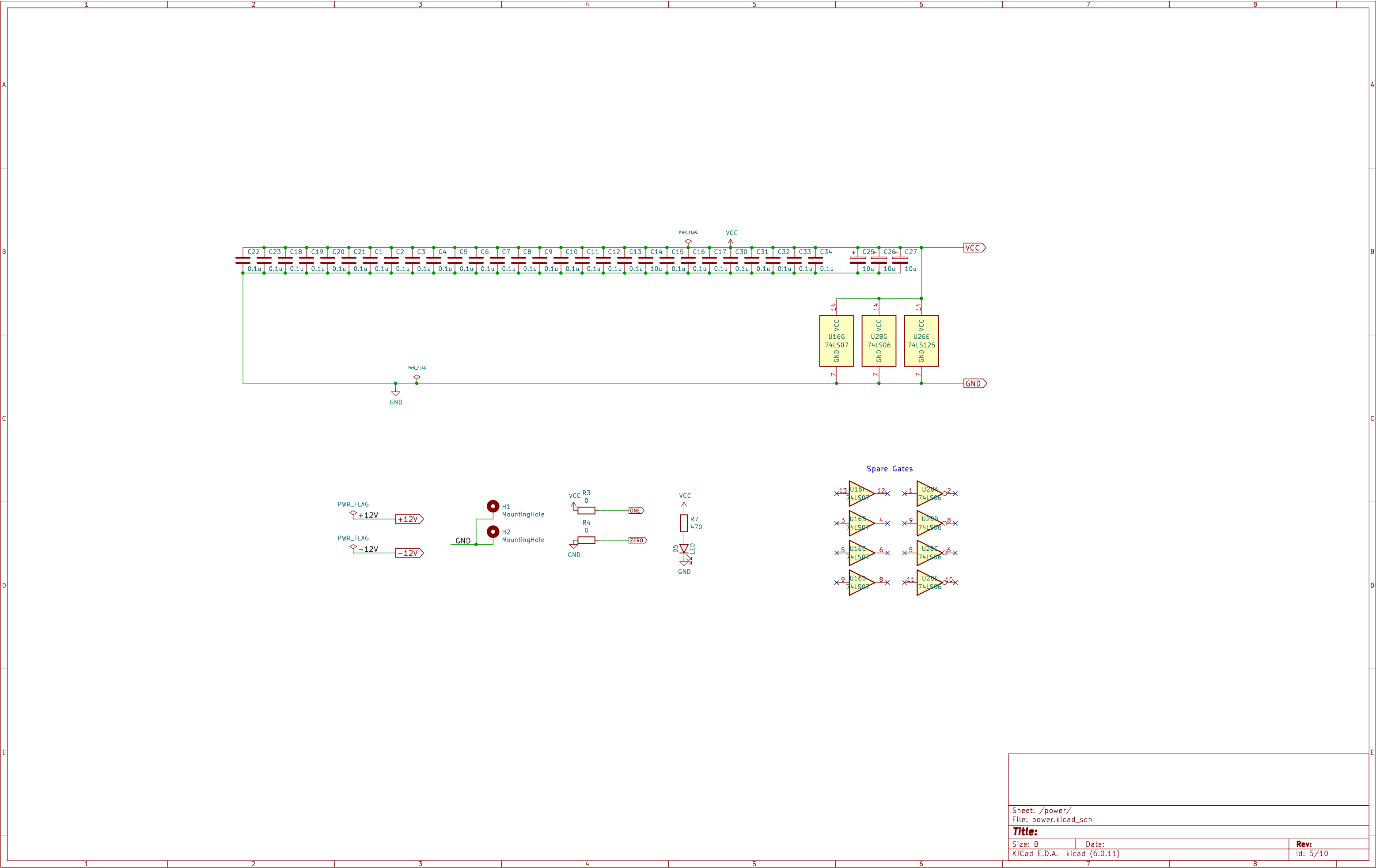


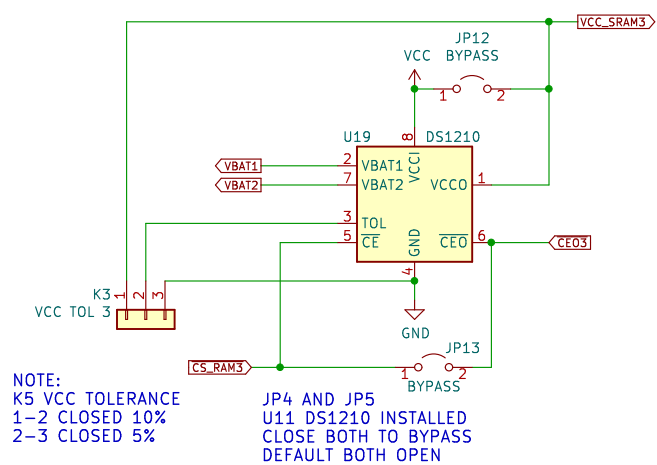
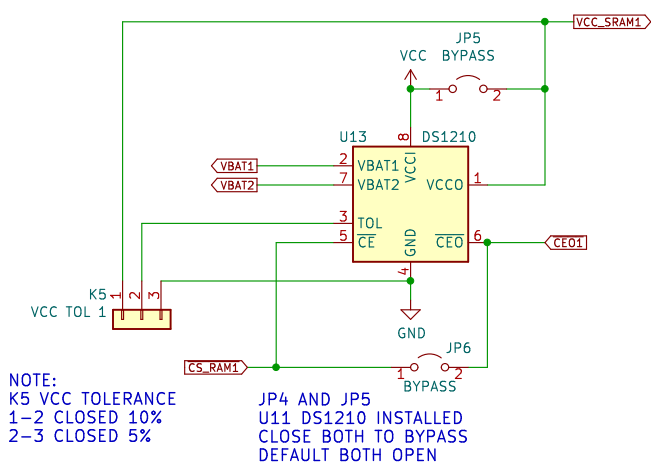
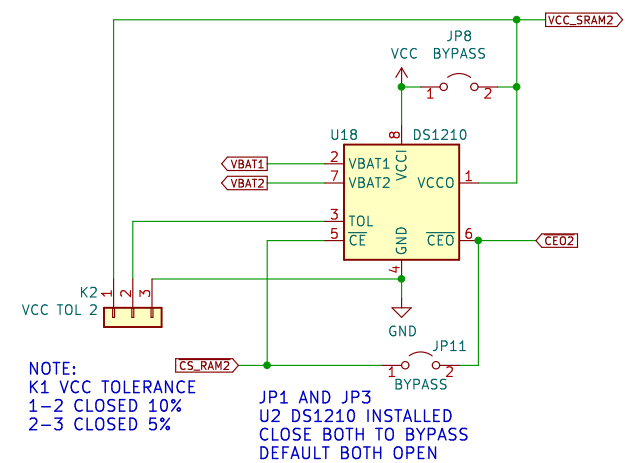
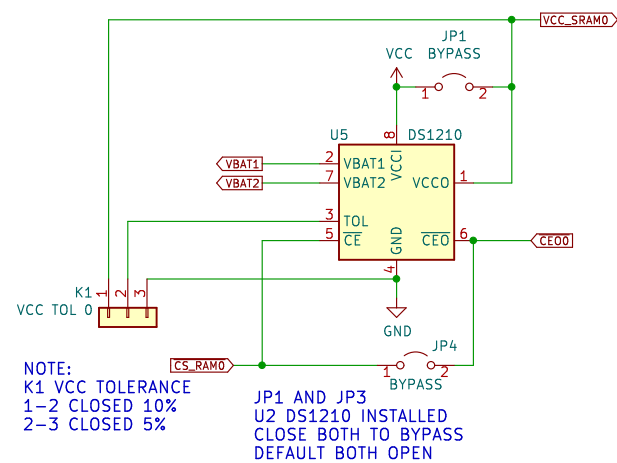
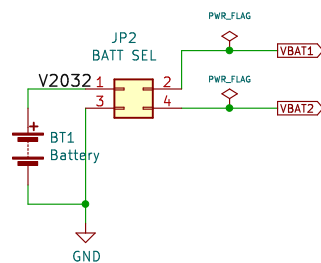
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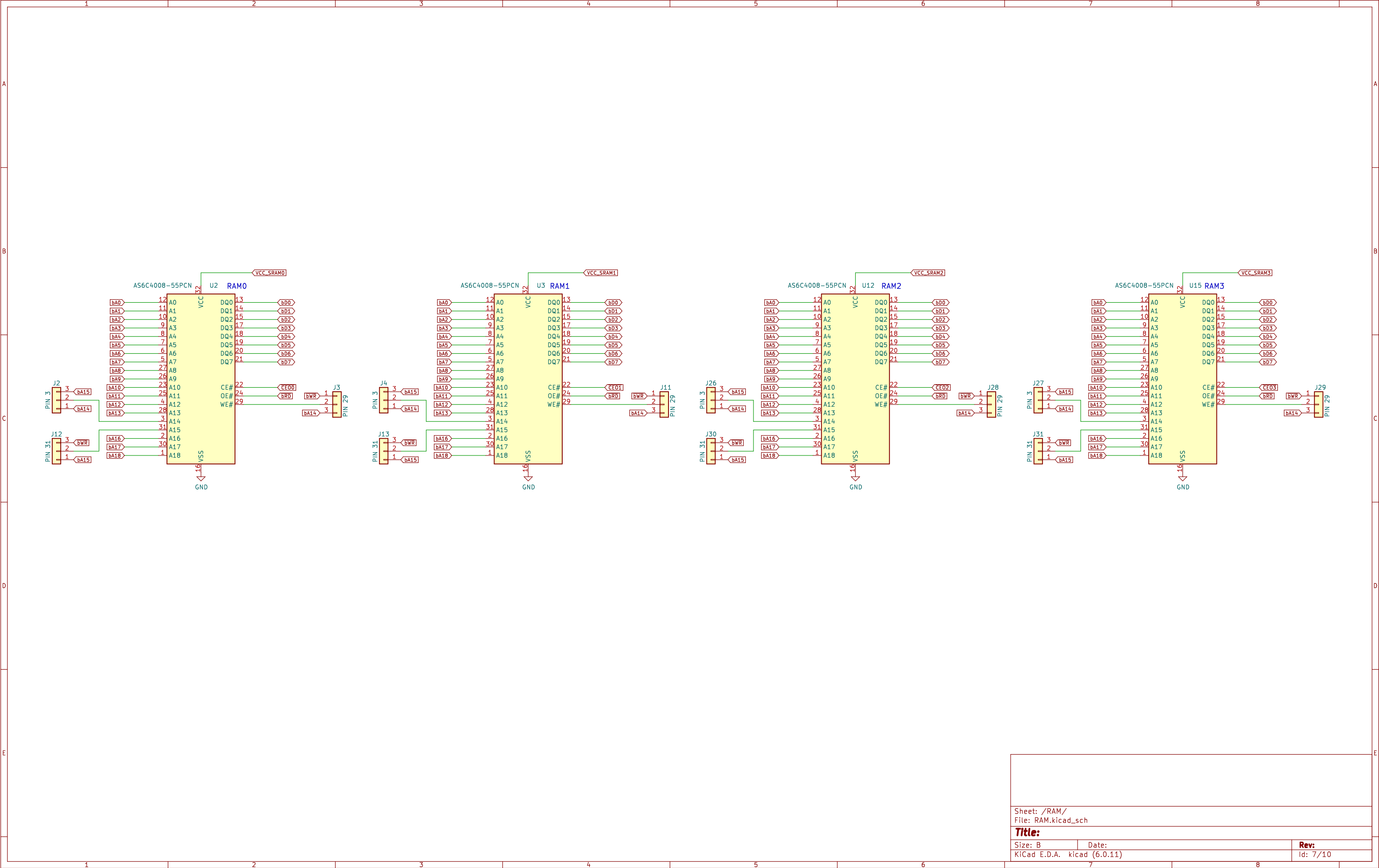


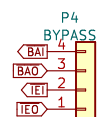
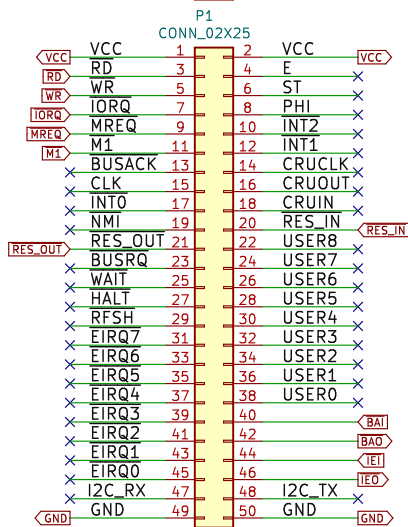
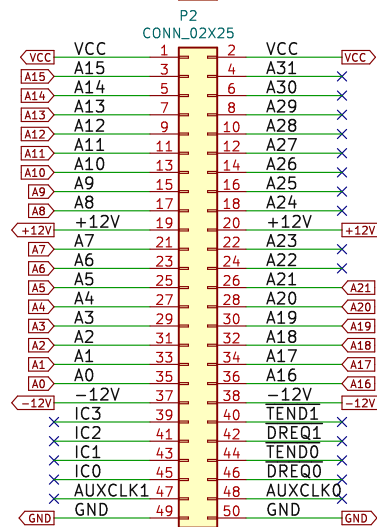
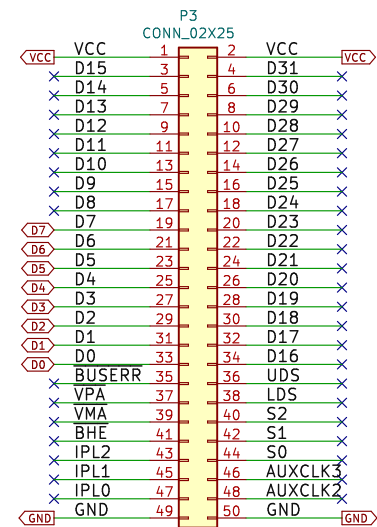
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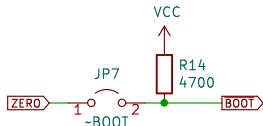
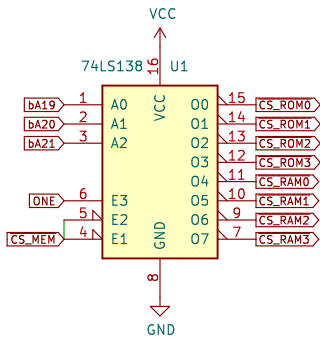
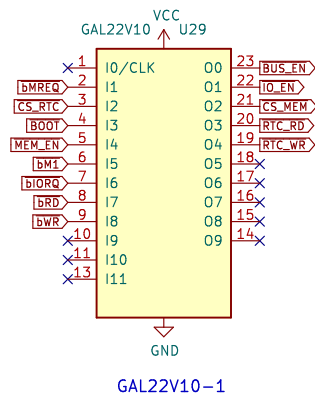
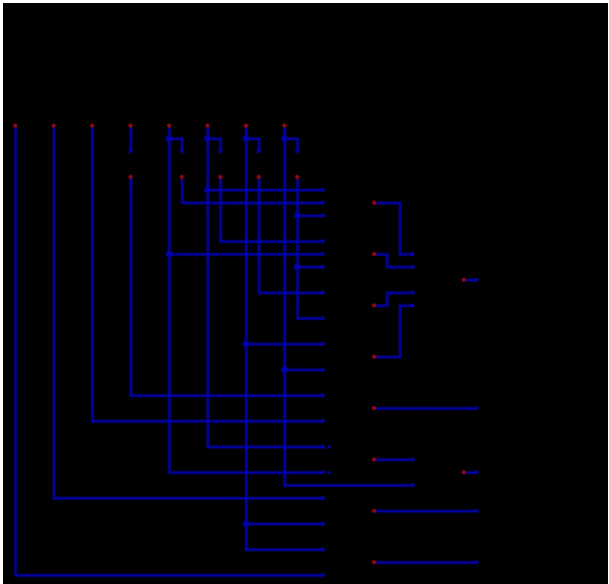








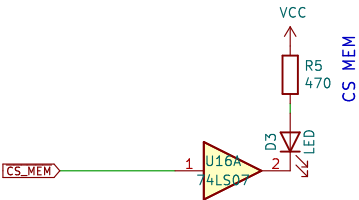




JP7 NOTE: FIRST ROM & RAM BOARD IN SYSTEM MUST HAVE JP7 JUMPER INSTALLED. ALSO MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE. FIRST BOARD ONLY RESPONDS WHEN MEM_EN LOW (BY DEFAULT).

SECOND ROM & RAM BOARD INSTALLED IN SYSTEM MUST HAVE JP7 JUMPER NOT INSTALLED. ALSO SECOND BOARD ONLY RESPONDS WHEN MEM_EN IS HIGH.

ACCESS IS CONTROLLED BY SOFTWARE TO ENSURE THAT ONLY ONE OF BOARDS HAS MEM_EN HIGH AT ANY GIVEN TIME OR BUS CONTENTION.



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File: GALS.kicad_sch

Title:

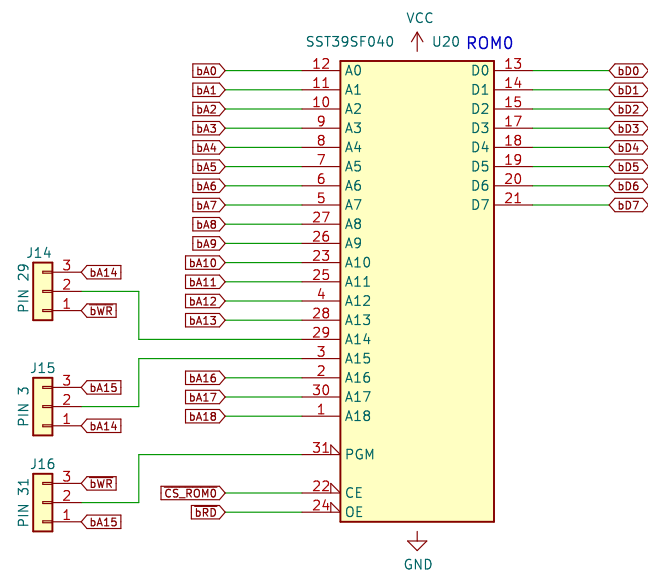
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Id: 9/10



U20 NOTE: FIRST BOARD IN SYSTEM MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE AND JP7 INSTALLED FOR PROPER BOOT SEQUENCE.

