

12345678

A

bus

power

buffers

fpanel

File: bus.kicad_sch
80C188CPU

File: power.kicad_sch
Memory

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: CPU_80C188.kicad_sch

File: Memory.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

D

E

RetroBrew Computer Group
Based on a design by John R Coffman.

Sheet: /
File: processor.80C188.kicad_sch

Title: Duodyne 80C188 CPU board

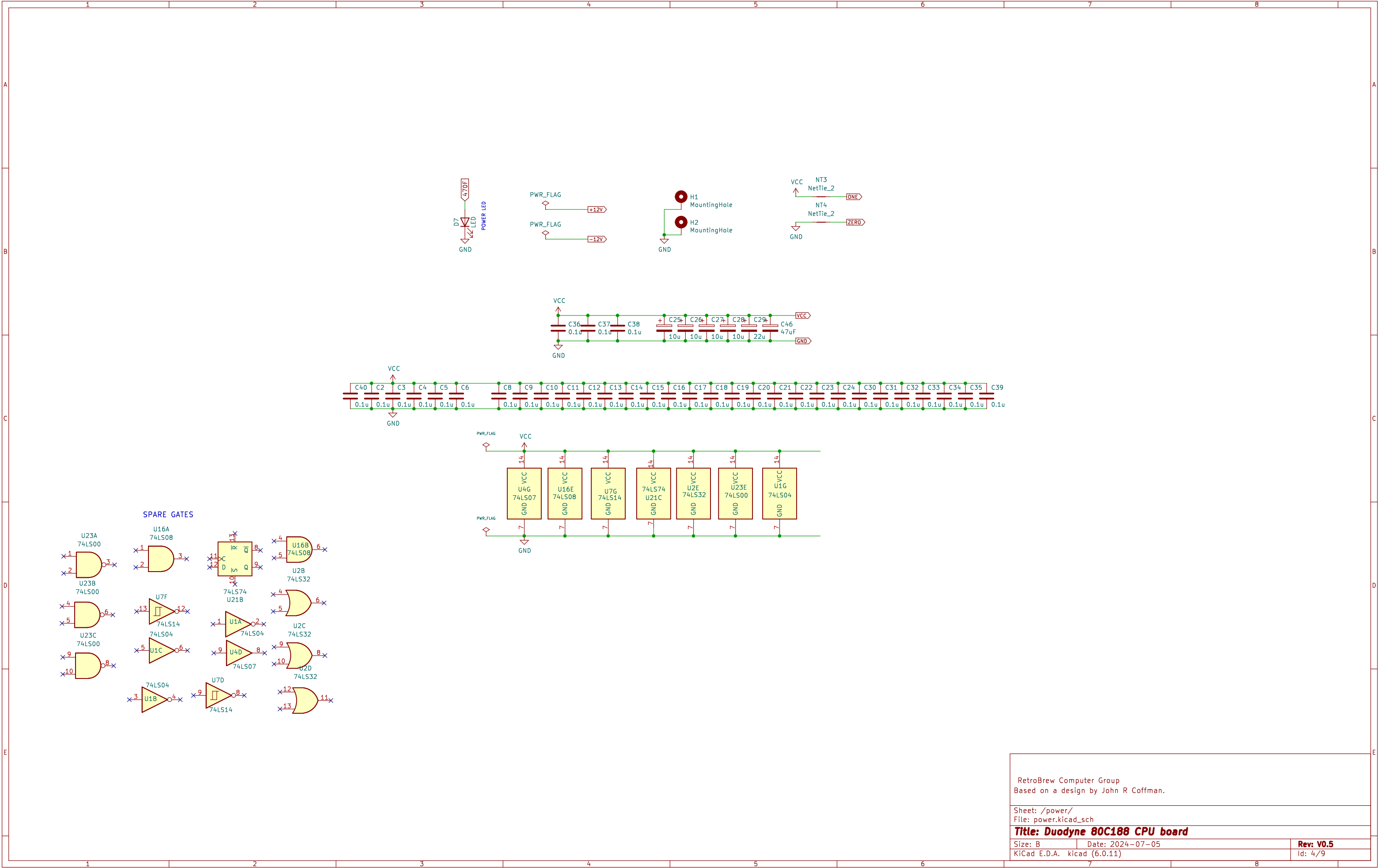
Size: BDate: 2024-07-05

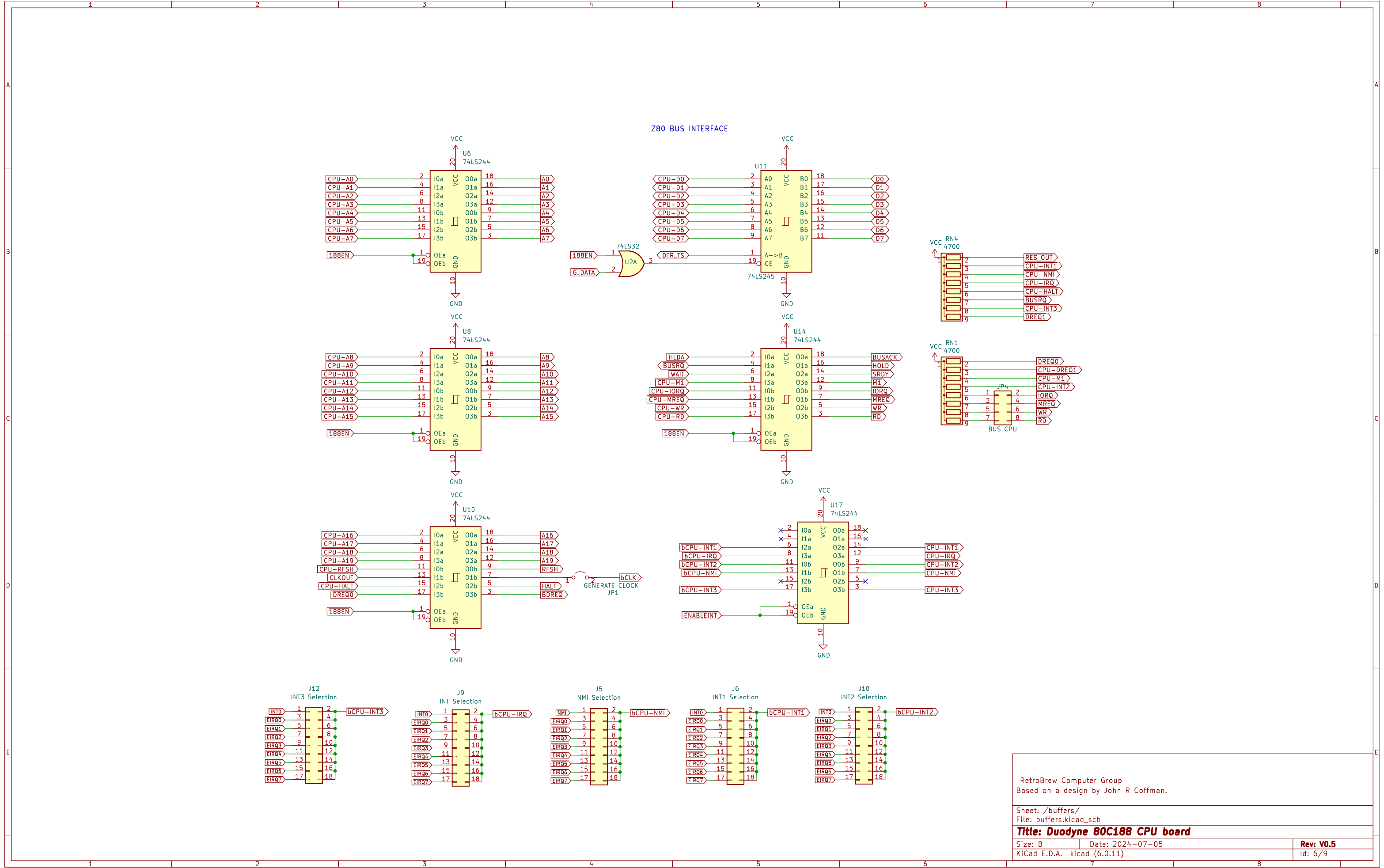
KiCad E.D.A. kicad (6.0.11)

Rev: V0.5

Id: 1/9

12345678



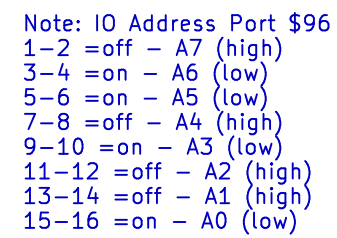


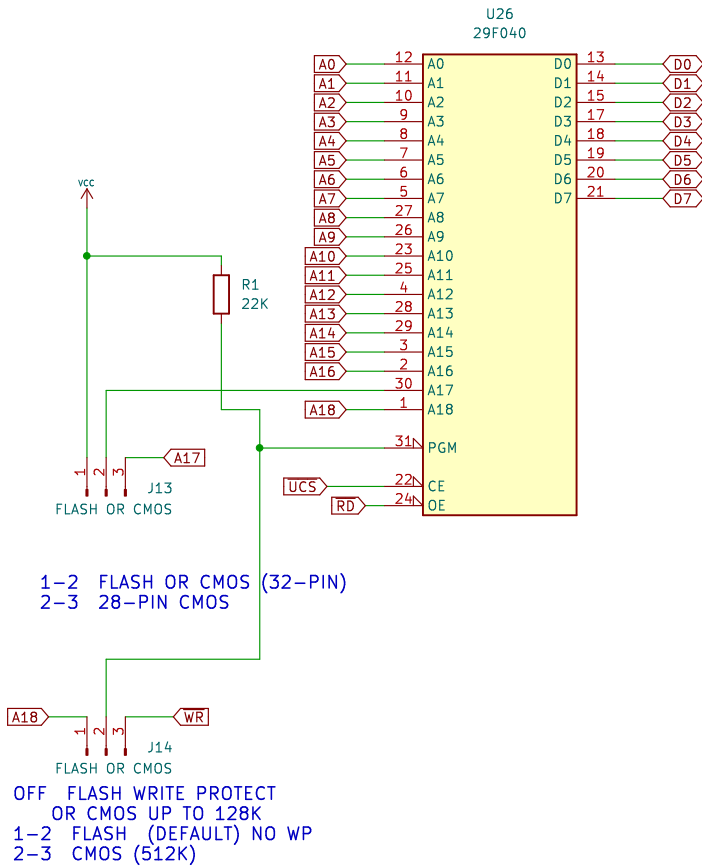
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Sheet: /buffers/
File: buffers.kicad_sch

Title: Duodyne 80C188 CPU board

Size: B	Date: 2024-07-05	Rev: V0.5
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ROM

FLASH 39SF010 (128K)
ALTERNATES: 39SF020, 29F020, 29F010
39SF040, 29F040 (512K) NOT RECOMMENDED

CMOS 27C010 (128K, 32pin)
ALTERNATE 27C020
28pin: 27C512(64K), 27C256(32K)

DEFAULT CHIP: 39SF010 (128K FLASH)
DEFAULT JUMPERS: 1-2, 1-2 (BOTH)

29F040 SHOWS ALL PIN ASSIGNMENTS

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Sheet: /Memory/
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Title: Duodyne 80C188 CPU board

Size: USLedger | Date: 2024-07-05
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Rev: V0.5
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