

12345678

A

bus

power

buffers

fpanel

B

File: bus.kicad_sch
80C188CPU

File: power.kicad_sch
Memory

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

C

D

E

12345678

E

RetroBrew Computer Group
Based on a design by John R Coffman.

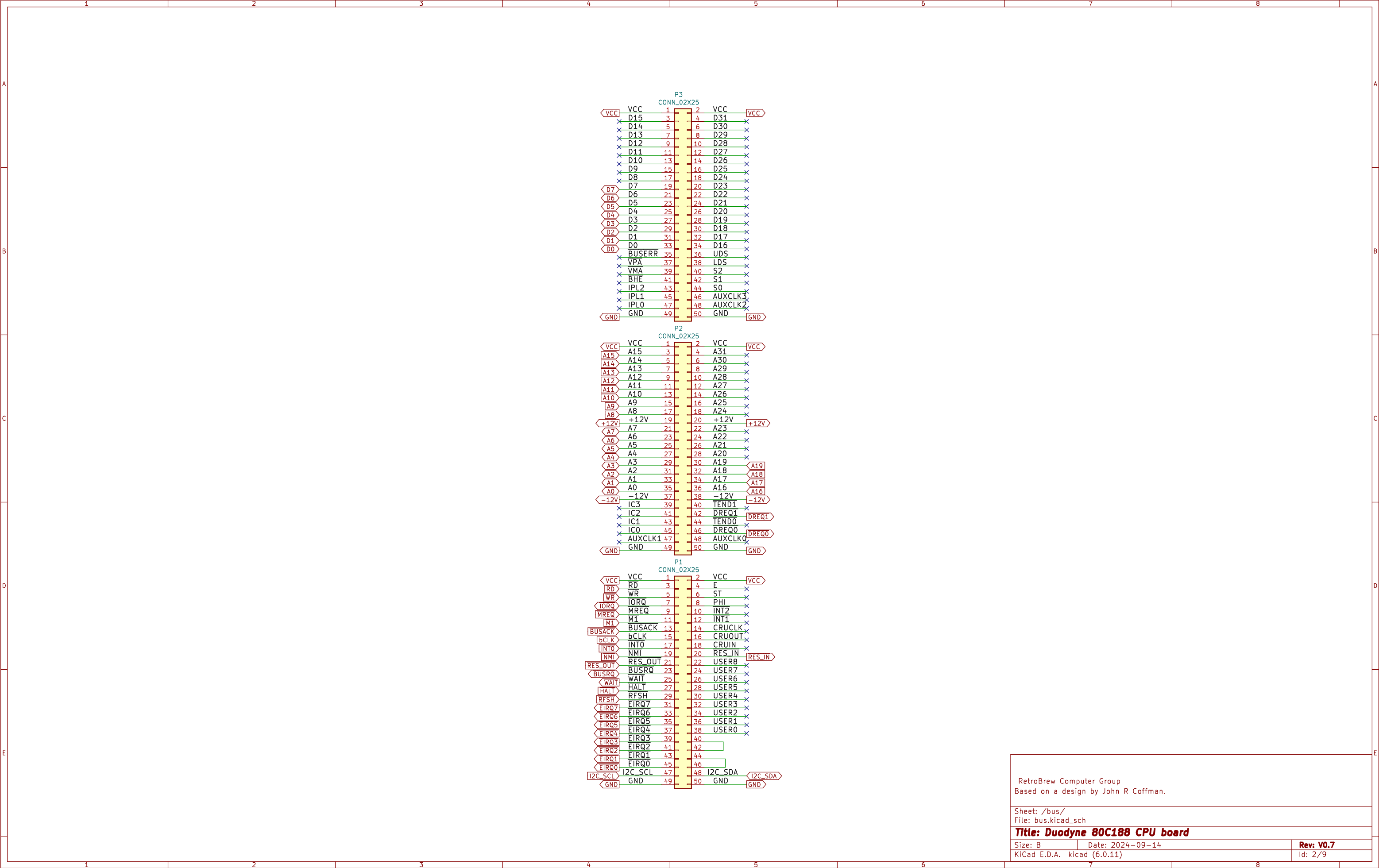
Sheet: /
File: processor.80C188.kicad_sch

Title: Duodyne 80C188 CPU board

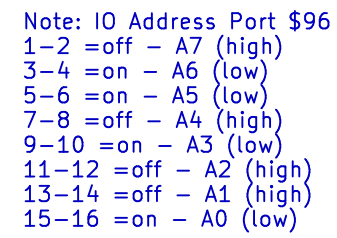
Size: BDate: 2024-09-14

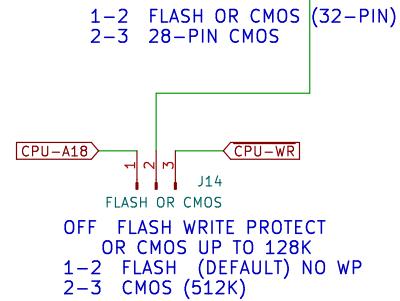
KiCad E.D.A. kicad (6.0.11)Rev: V0.7

Id: 1/9



Rev: V0.7





FLASH 39SF010 (128K)
ALTERNATES: 39SF020, 29F020, 29F010
39SF040, 29F040 (512K) NOT RECOMMENDED

```

DEFAULT CHIP: 39SF010 (128K FLASH)
DEFAULT JUMPERS: 1-2, 1-2 (BOTH)
29F040 SHOWS ALL PIN ASSIGNMENTS

```