

bus

File: bus.kicad_sch

VDP

File: VDP.kicad_sch

GAL

File: GAL.kicad_sch

IO

File: IO.kicad_sch

Amplifier

File: Amplifier.kicad_sch

power

File: power.kicad_sch

PSG

File: PSG.kicad_sch

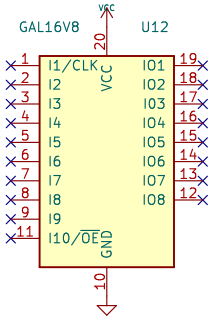
buffers

File: buffers.kicad_sch

ACR

File: ACR.kicad_sch

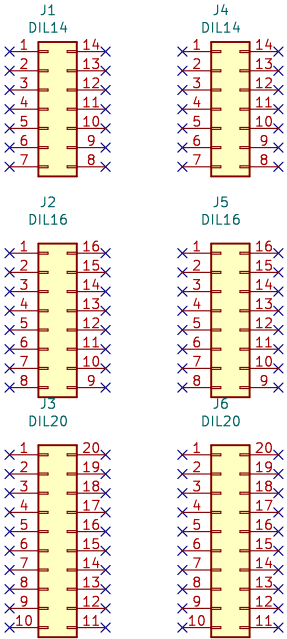
SPARE GAL FOR
DEBUGGING

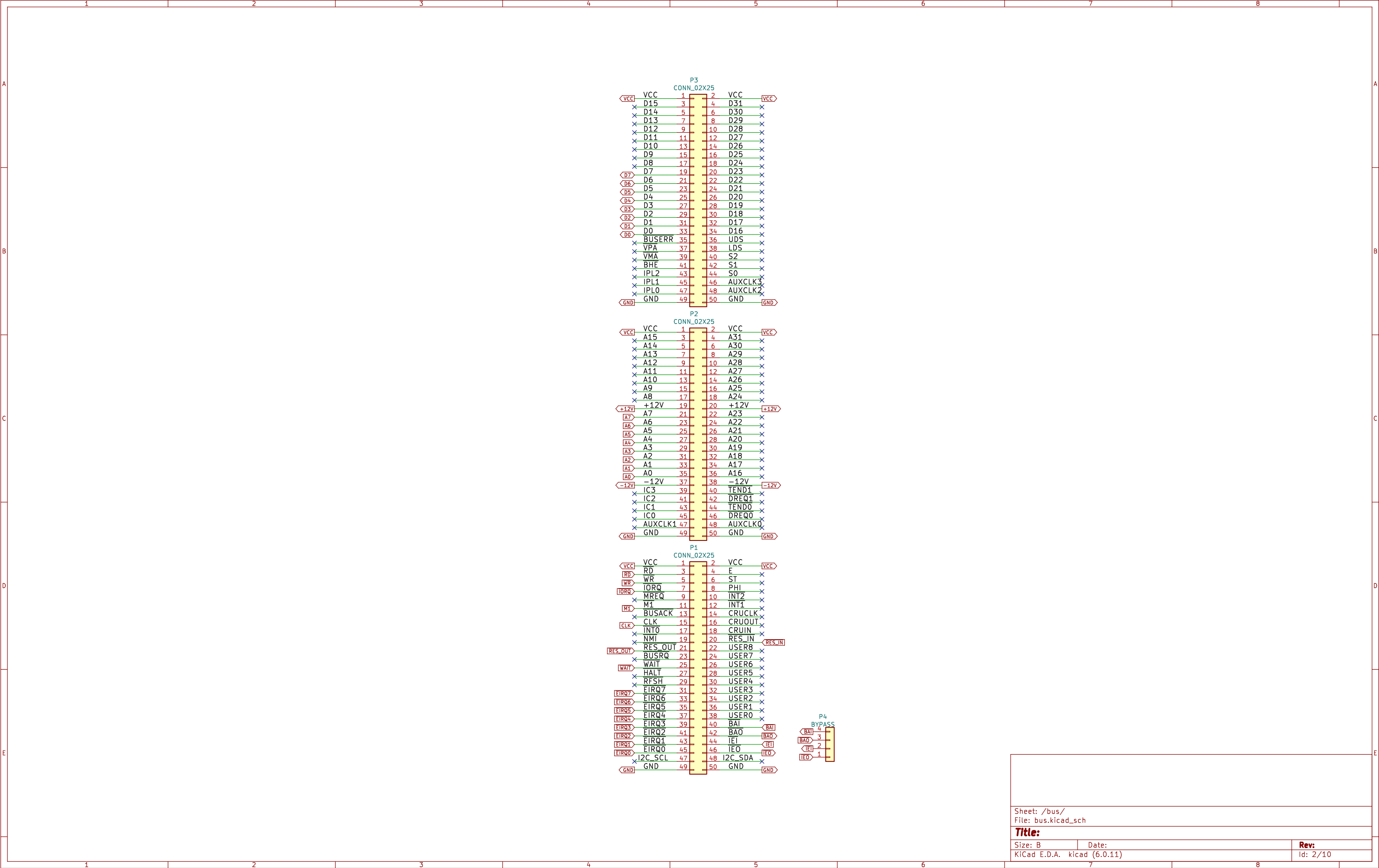


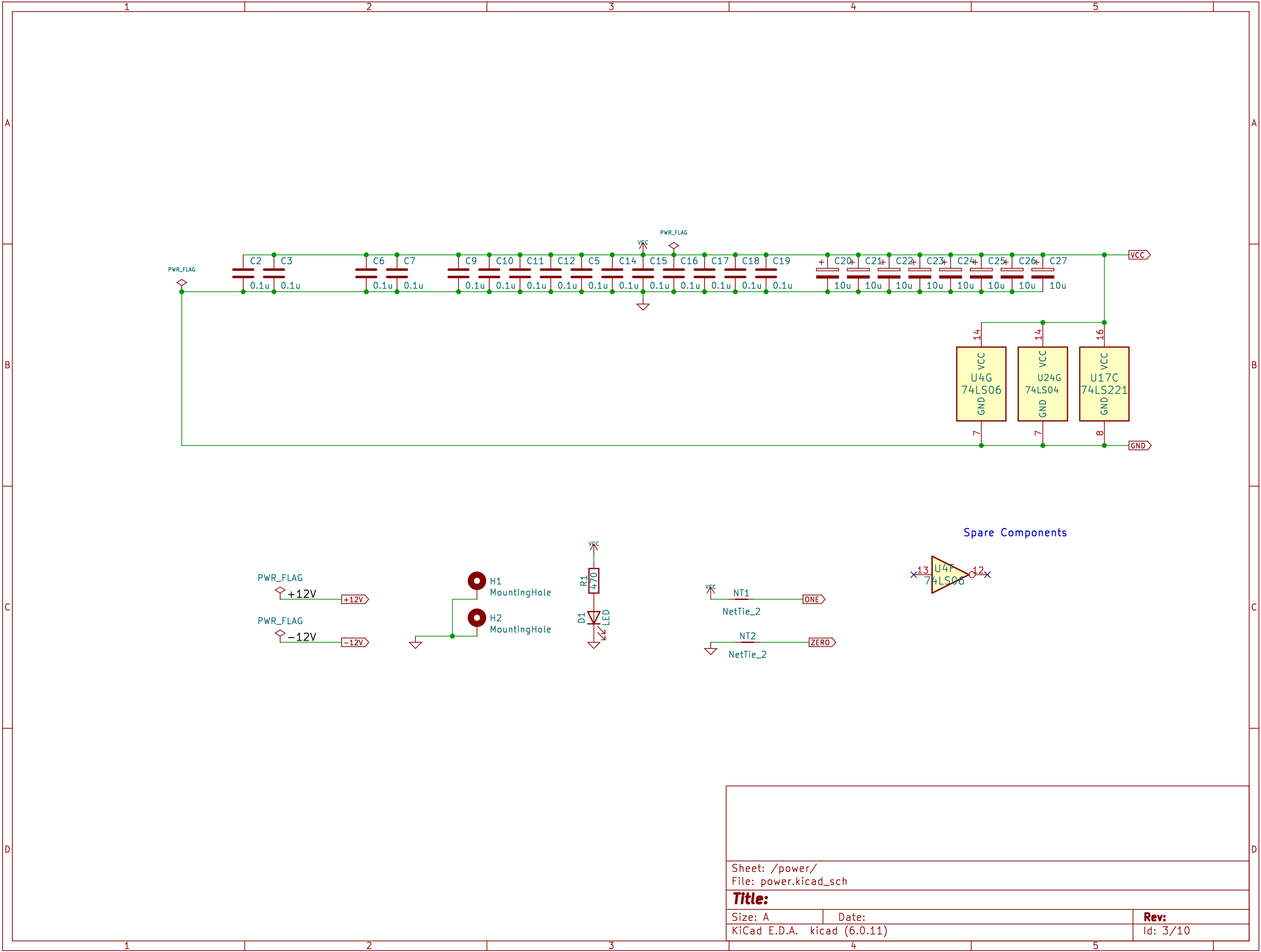
THROUGH HOLES FOR
PATCH WIRE ROUTING
ACROSS SIDES

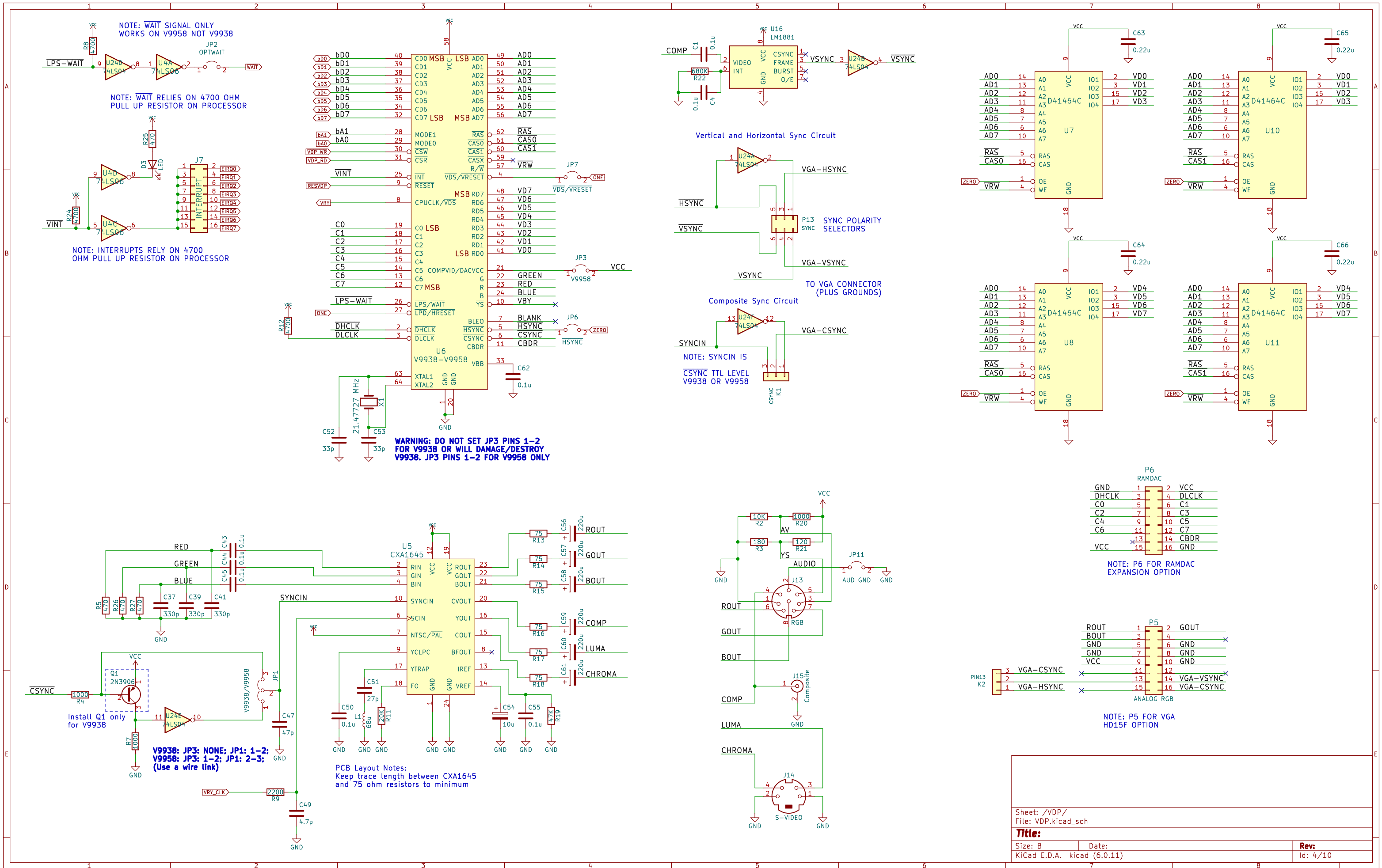
- H3 MountingHole
- H4 MountingHole

SPARE SOCKETS
FOR DEBUGGING









1 2 3 4 5

A

B

C

D

MEDIA CS GAL16V8

U1

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20

I1/CLK I2 I3 I4 I5 I6 I7 I8 I9 I10/OE

VCC VCC VCC VCC VCC VCC VCC VCC VCC VCC

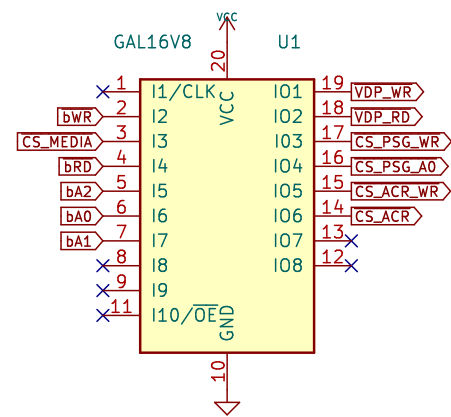
VDP_WR VDP_RD CS_PSG_WR CS_PSG_A0 CS_ACR_WR CS_ACR

Sheet: /GAL/
File: GAL.kicad_sch

Title:

Size: A Date: Rev:

KiCad E.D.A. kicad (6.0.11) Id: 6/10



Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 6/10

