bus

File: bus.kicad_sch

power

File: power.kicad_sch

buffers

File: buffers.kicad_sch

fpanel

File: fpanel.kicad_sch

65816CPU

File: 65816CPU.kicad_sch

GALS

File: GALS.kicad_sch

bus sharing

File: bussharing.kicad_sch

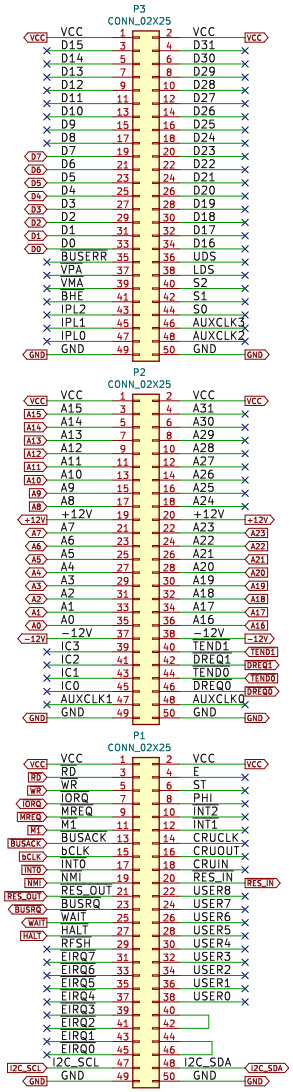
mapper

File: mapper.kicad_sch

Sheet: /
File: processor.65816.kicad_sch

Title: Duodyne 65816 CPU board

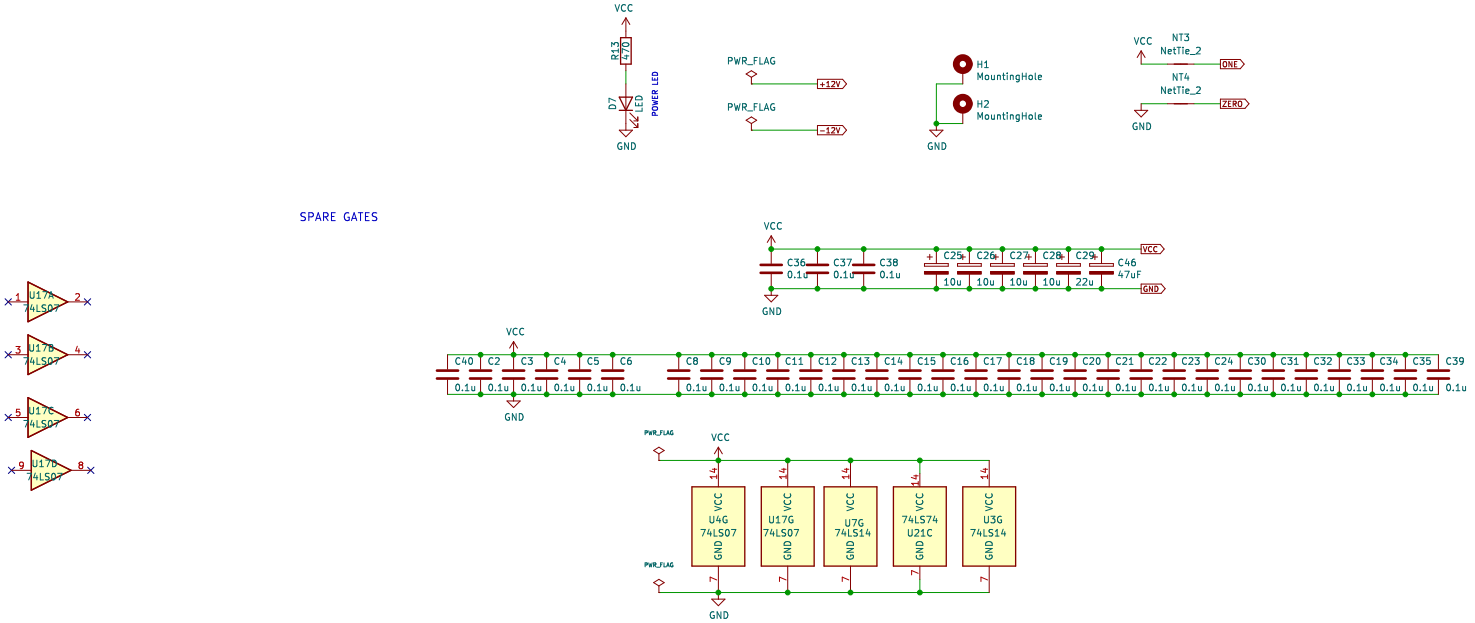
Size: B	Date: 2023-10-01	**Rev: V0.80**
KiCad E.D.A. kicad (6.0.11)		Id: 1/9



Sheet: /bus/
File: bus.kicad_sch

Title: Duodyne 65816 CPU board

Size: B	Date: 2023-10-01	**Rev: V0.80**
KiCad E.D.A. kicad (6.0.11)		Id: 2/9



Sheet: /power/	
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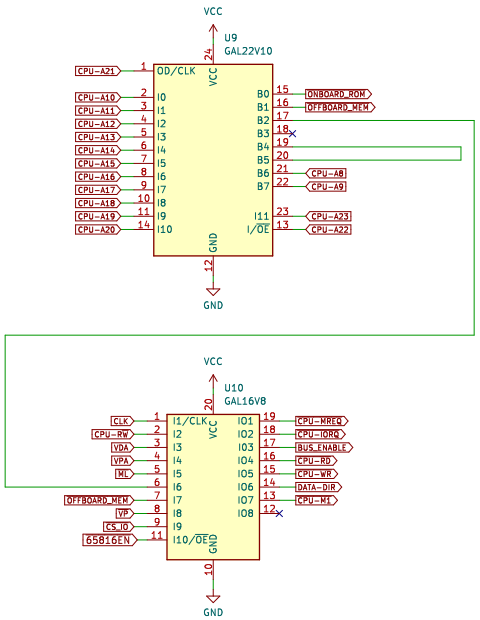
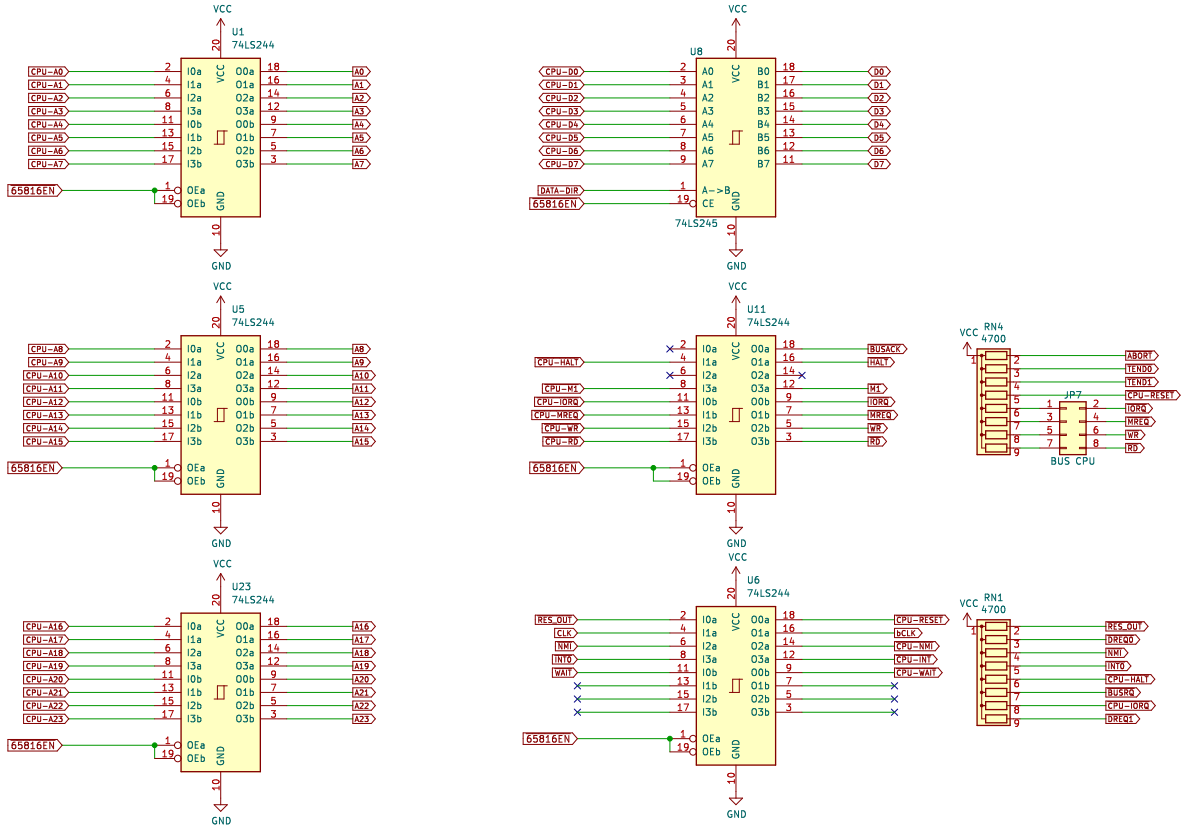
Title: Duodyne 65816 CPU board	
Size: B	Date: 2023-10-01
KiCad E.D.A. kicad (6.0.11)	Id: 4/9

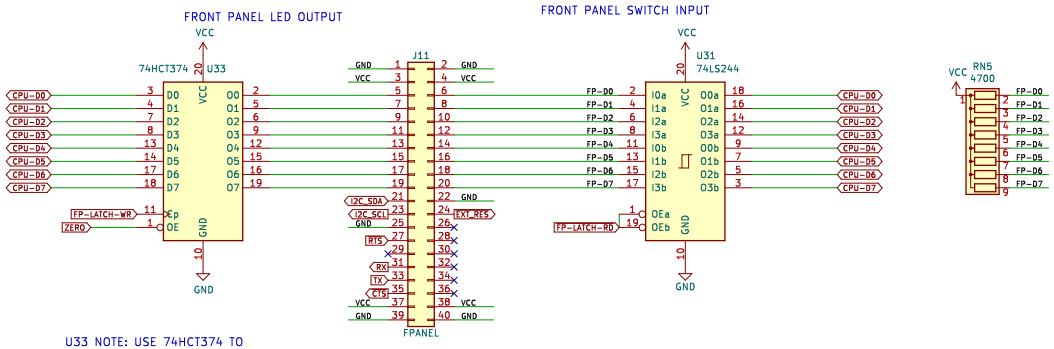
Rev: V0.80



Sheet: /GALS/		
File: GALS.kicad_sch		
Title: Duodyne 65816 CPU board		
Size: B	Date: 2023-10-01	**Rev: V0.80**
KiCad E.D.A. kicad (6.0.11)		Id: 5/9



Sheet: /buffers/		
File: buffers.kicad_sch		
Title: Duodyne 65816 CPU board		
Size: B	Date: 2023-10-01	**Rev: V0.80**
KiCad E.D.A. kicad (6.0.11)		Id: 6/9



U33 NOTE: USE 74HCT374 TO SOURCE OR SINK ADEQUATE CURRENT TO DRIVE LEDS ON FRONT PANEL

J11 NOTE:

PINS 5, 7, 9, 11, 13, 15, 17, 19 ARE FOR LED STATUS FOR ROMWBW FRONT PANEL INDICATOR

PINS 6, 8, 10, 12, 14, 16, 18, 20 ARE FOR SWITCH INPUTS FOR ROMWBW FRONT PANEL

PINS 21, 23 ARE FOR I2C CONNECTION (SDA & SCL)

PINS 25, 27, 29, 31, 33, AND 35 ARE FOR TTL SERIAL CONNECTOR TO UART

PINS 29, 37 ARE FOR SERIAL VCC POWER ENABLE

PINS 22, 24 ARE FOR EXTERNAL RESET SWITCH

PINS 26, 28 ARE FOR IM2 CIRCUIT ENABLE

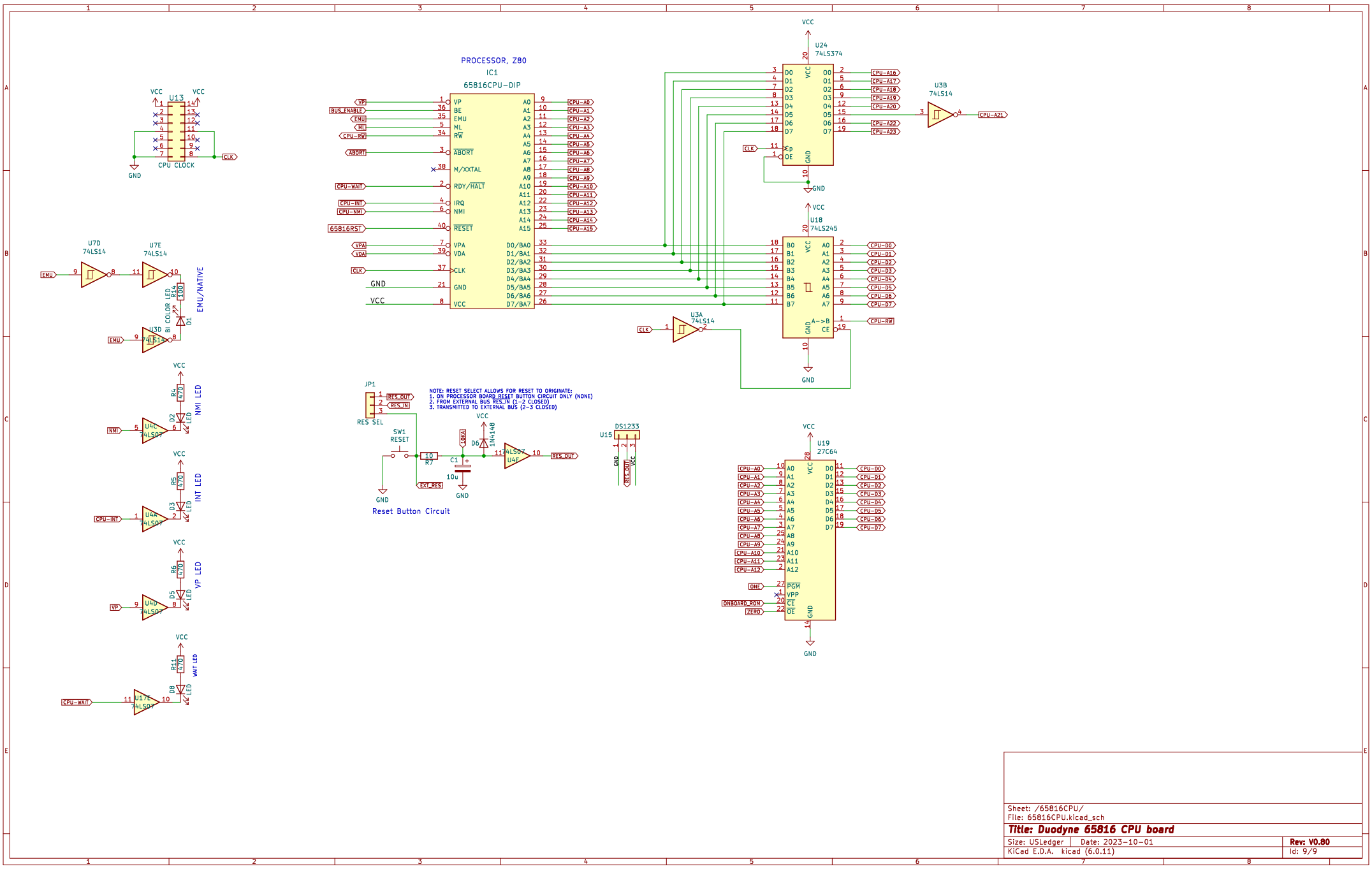
PINS 30, 32 ARE FOR INTERRUPT 0 ENABLE

PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE

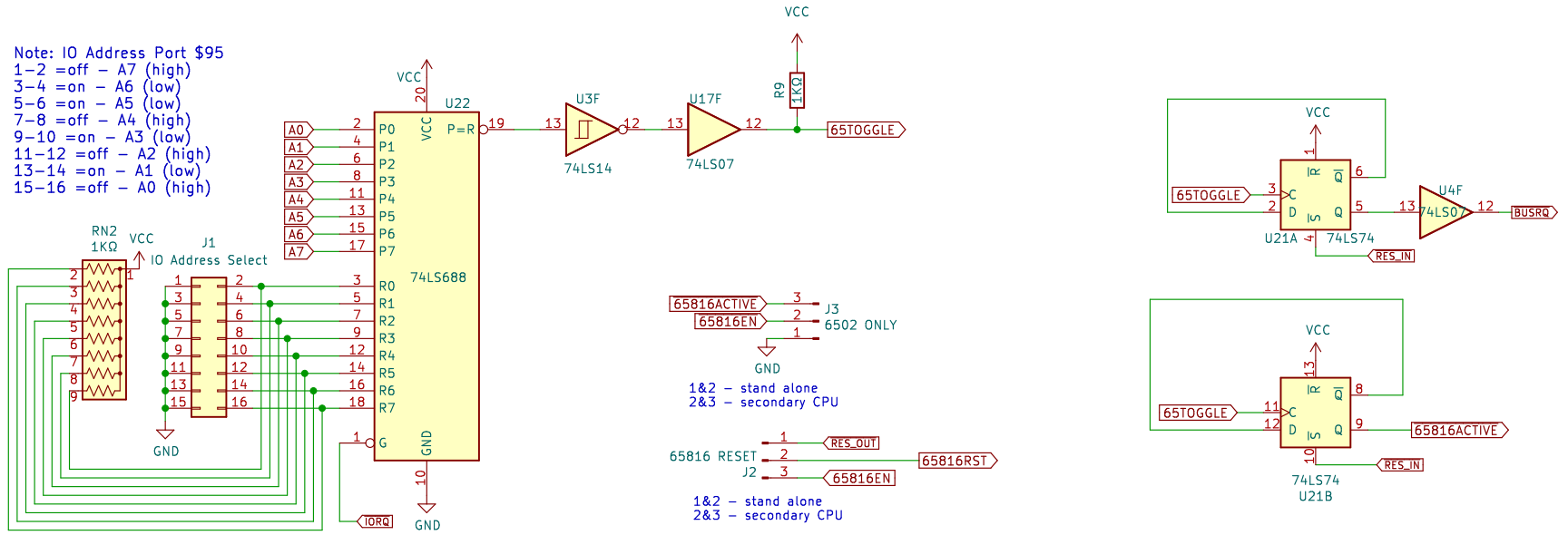
Sheet: /fpanel/
File: fpanel.kicad_sch

Title: Duodyne 65816 CPU board

Size: B	Date: 2023-10-01	**Rev: V0.80**
KiCad E.D.A. kicad (6.0.11)		Id: 8/9

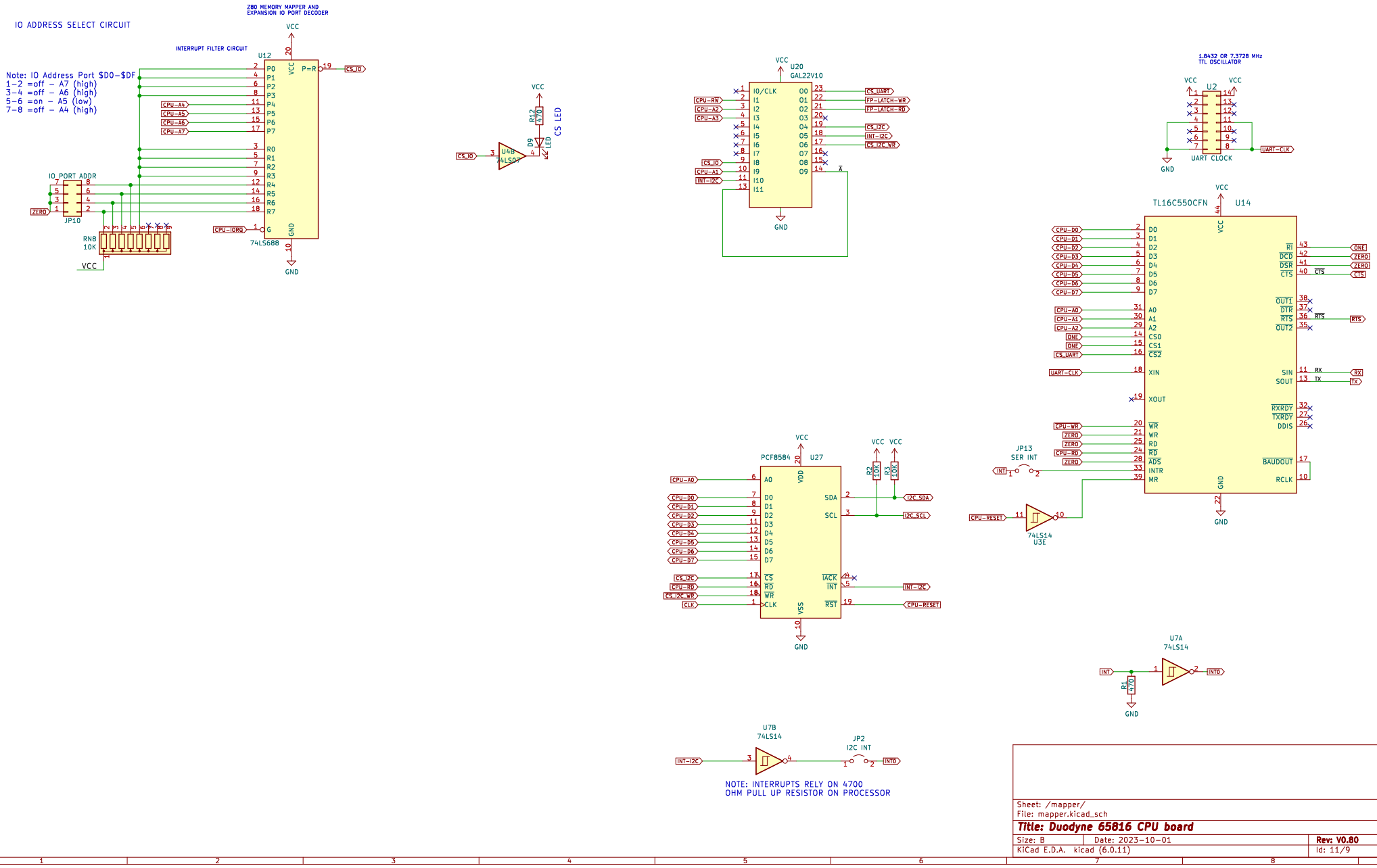


Sheet: /65816CPU/	
File: 65816CPU.kicad_sch	
Title: Duodyne 65816 CPU board	
Size: USLedger | Date: 2023-10-01	**Rev: V0.80**
KiCad E.D.A. kicad (6.0.11)	Id: 9/9



Sheet: /bus sharing/	
File: bussharing.kicad_sch	
Title: Duodyne 65816 CPU board	
Size: A4	Date: 2023-10-01
KiCad E.D.A. kicad (6.0.11)	Id: 9/9

Rev: V0.80


Z80 MEMORY MAPPER AND
EXPANSION IO PORT DECODER
IO ADDRESS SELECT CIRCUIT
INTERRUPT FILTER CIRCUIT
Note: IO Address Port $D0-$DF
1-2 =off - A7 (high)
3-4 =off - A6 (high)
5-6 =on - A5 (low)
7-8 =off - A4 (high)
U12
74LS688
IO_PORT ADDR
JP10
RN8
10K
VCC
GND
CS_IO
CPU-IORQ
ZERO
CPU-A4
CPU-A5
CPU-A6
CPU-A7
U4B
74LS03
R12
470
D9
LED
CS LED
U20
GAL22V10
CPU-RW
CPU-A2
CPU-A3
CPU-A1
INT-I2C
CS_UART
FP-LATCH-WR
FP-LATCH-RD
CS_I2C
CS_I2C_WR
1.8432 OR 7.3728 MHz
TTL OSCILLATOR
U2
UART CLOCK
UART-CLK
TL16C550CFN
U14
CPU-D0
CPU-D1
CPU-D2
CPU-D3
CPU-D4
CPU-D5
CPU-D6
CPU-D7
ONE
RI
DCD
DSR
CTS
OUT1
DTR
RTS
OUT2
XIN
XOUT
SIN
SOUT
RX
TX
RXRDY
TXRDY
DDIS
BAUDOUT
RCLK
CPU-WR
CPU-RD
JP13
SER INT
INT
CPU-RESET
U3E
74LS14
PCF8584
U27
SDA
SCL
I2C_SDA
I2C_SCL
R2
R3
10K
U7A
R1
U7B
JP2
I2C INT
INTO
NOTE: INTERRUPTS RELY ON 4700
OHM PULL UP RESISTOR ON PROCESSOR
Sheet: /mapper/
File: mapper.kicad_sch
Title: Duodyne 65816 CPU board
Size: B
Date: 2023-10-01
Rev: V0.80
KiCad E.D.A. kicad (6.0.11)
Id: 11/9