

12345678

bus

power

buffers

fpanel

File: bus.kicad_sch
65816CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 65816CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: processor.65816.kicad_sch

Title: Duodyne 65816 CPU board

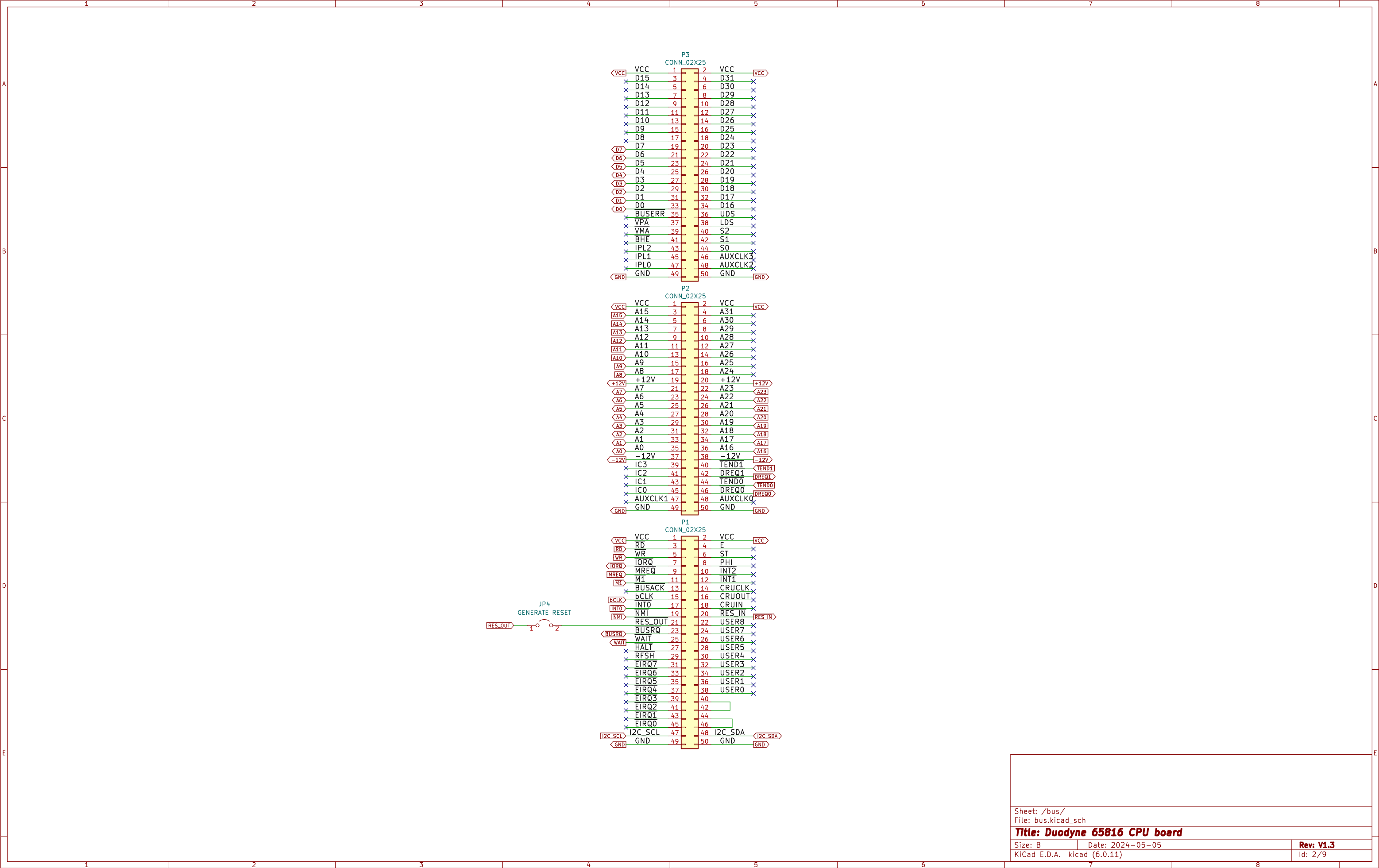
Size: BDate: 2024-05-05

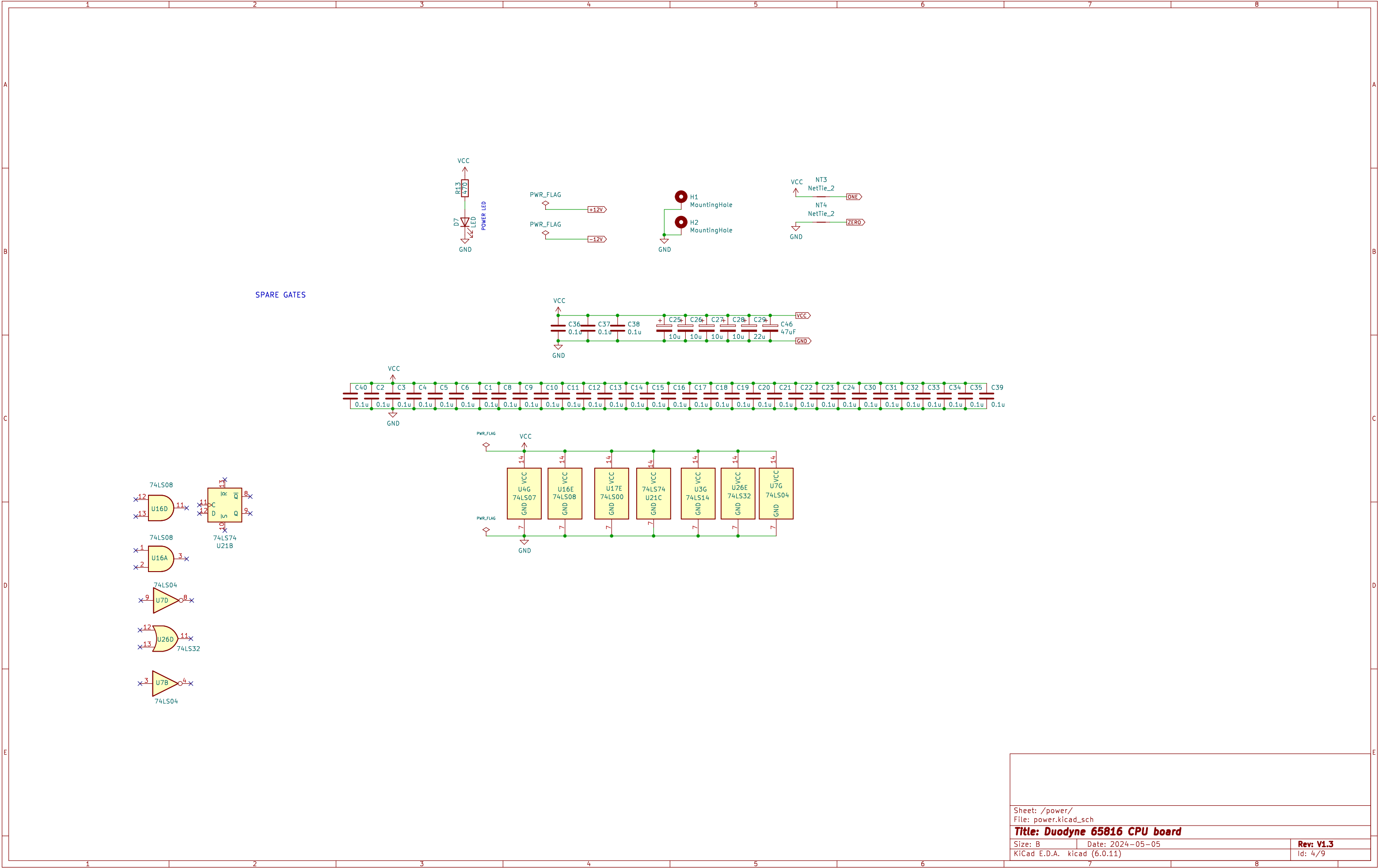
KiCad E.D.A. kicad (6.0.11)

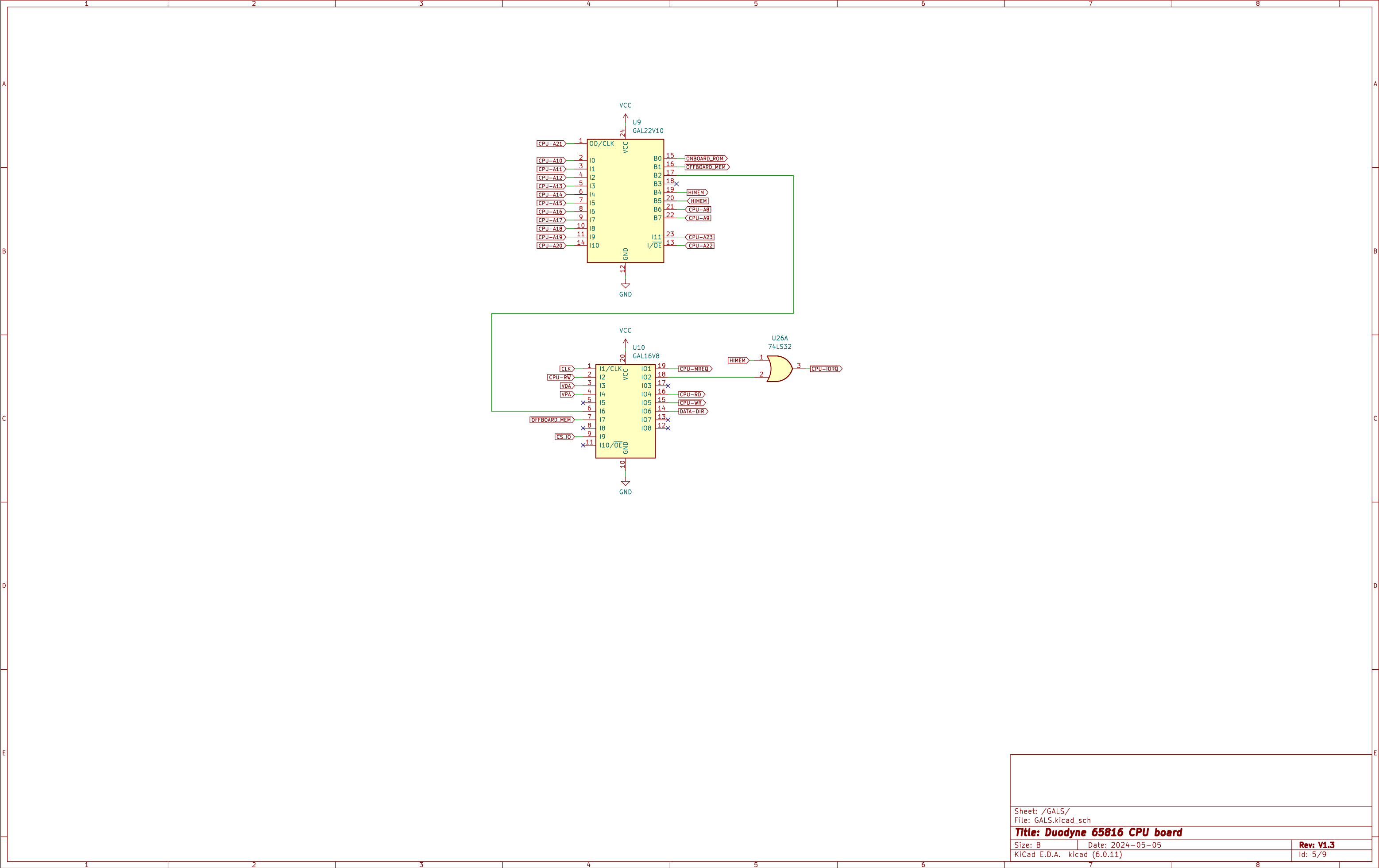
Rev: V1.3

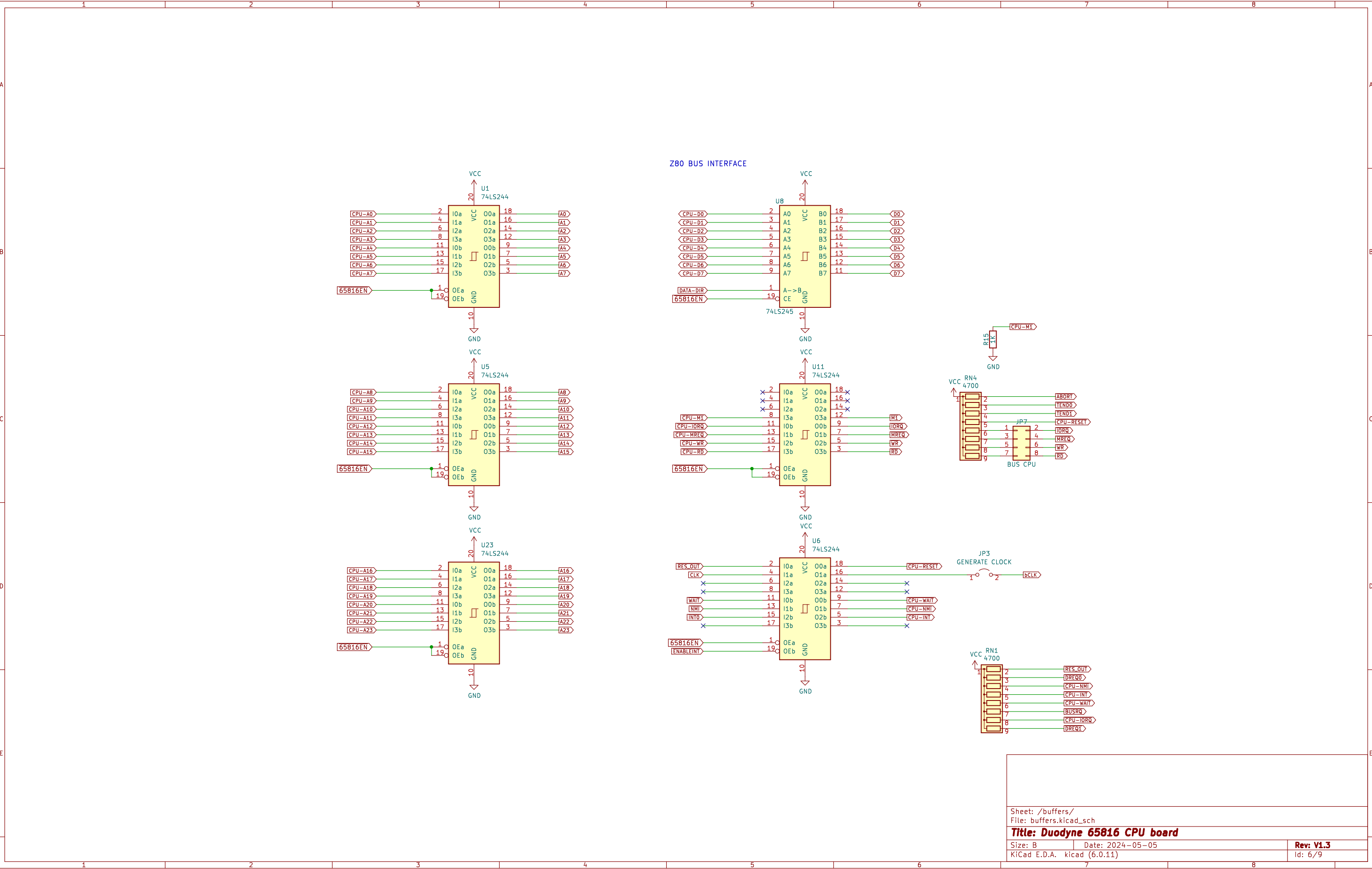
Id: 1/9

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J11 NOTE:

PINS 5, 7, 9, 11, 13, 15, 17, 19 ARE FOR LED STATUS FOR ROWMWB FRONT PANEL INDICATOR

PINS 6, 8, 10, 12, 14, 16, 18, 20 ARE FOR SWITCH INPUTS FOR ROWMWB FRONT PANEL

PINS 21, 23 ARE FOR I2C CONNECTION (SDA & SCL)

PINS 25, 27, 29, 31, 33, AND 35 ARE FOR TTL SERIAL CONNECTOR TO UART

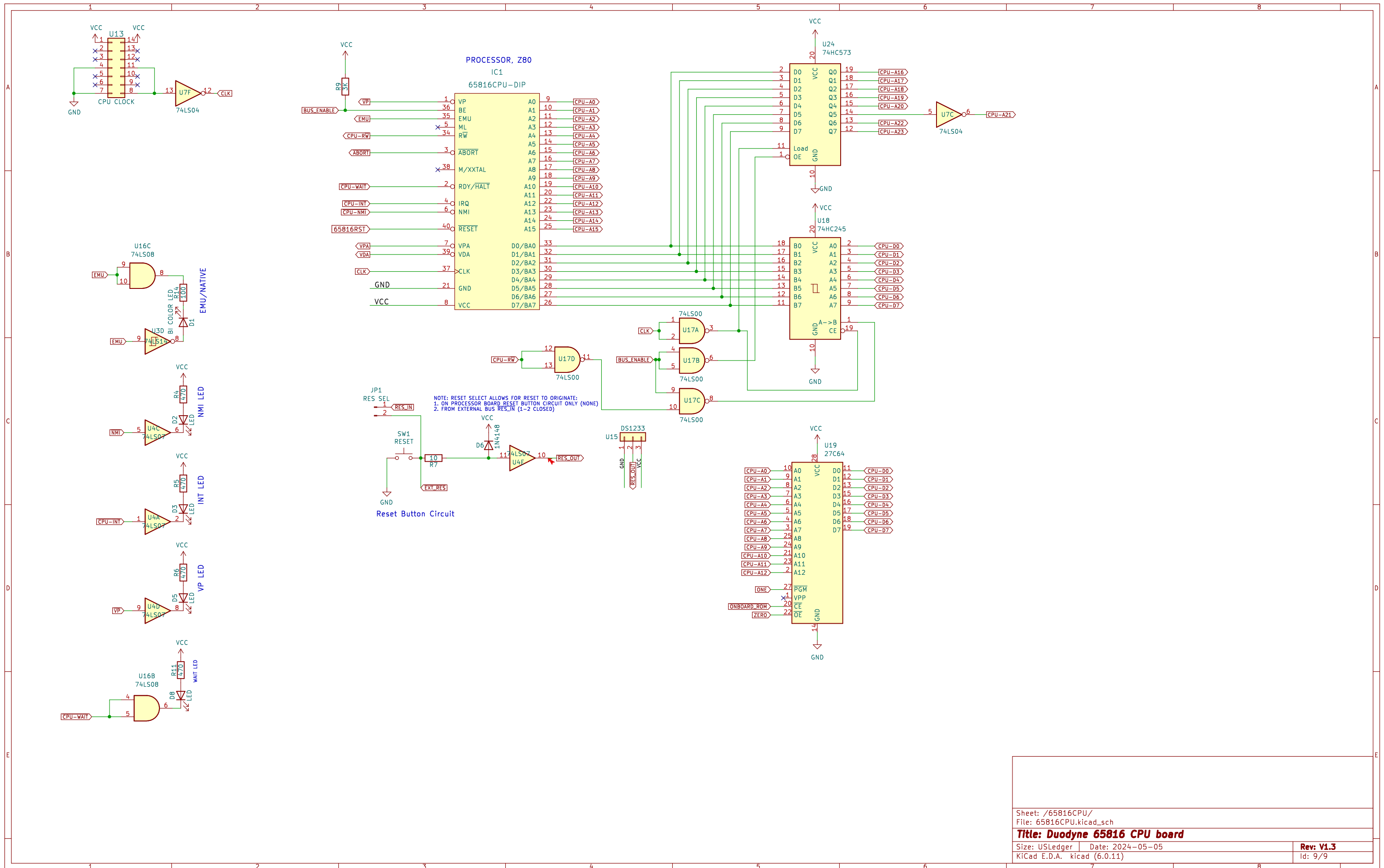
PINS 29, 37 ARE FOR SERIAL VCC POWER ENABLE

PINS 22, 24 ARE FOR EXTERNAL RESET SWITCH

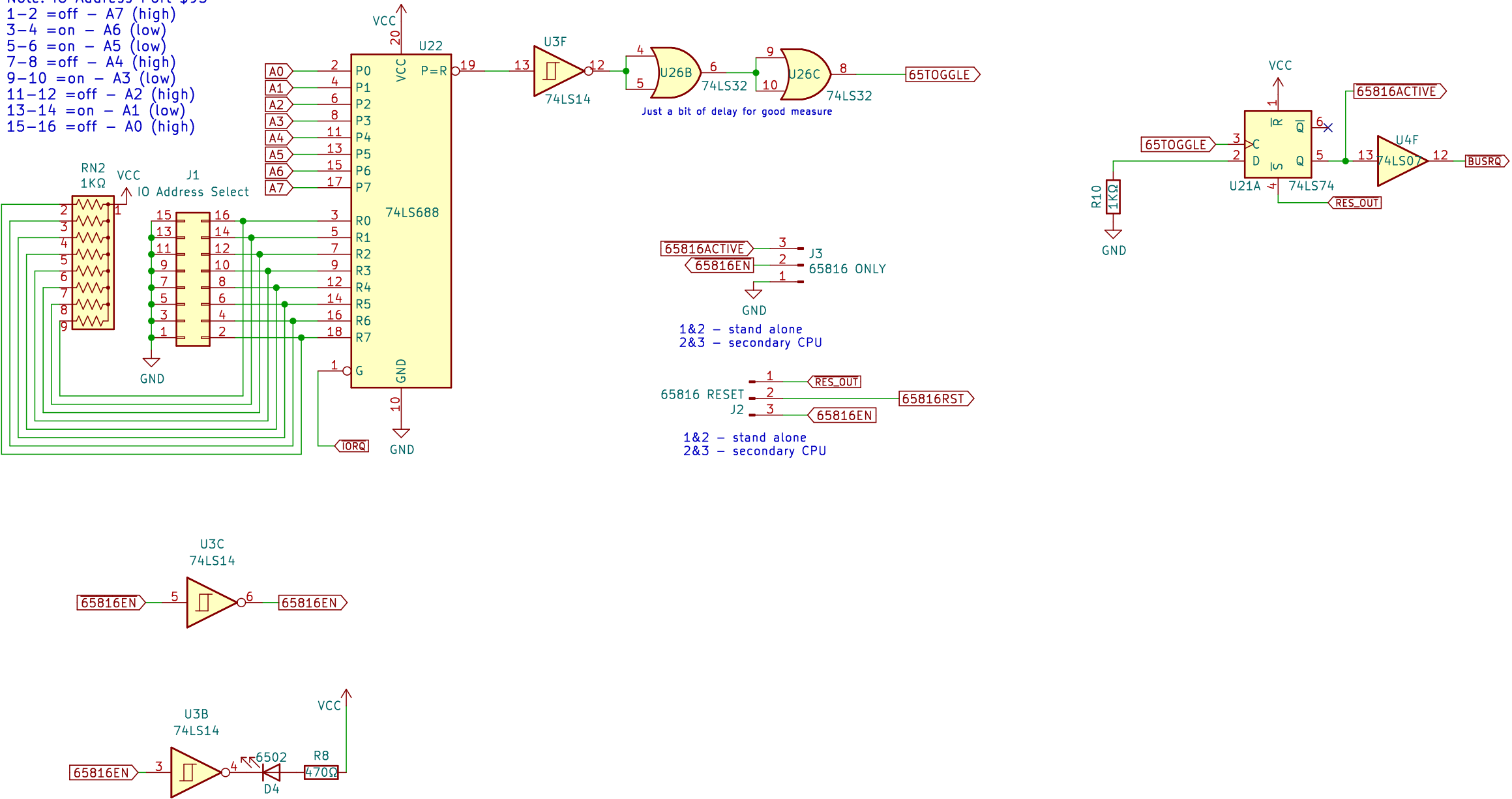
PINS 26, 28 ARE FOR IM2 CIRCUIT ENABLE

PINS 30, 32 ARE FOR INTERRUPT 0 ENABLE

PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE



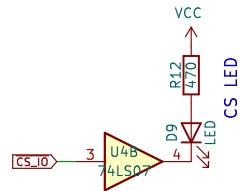
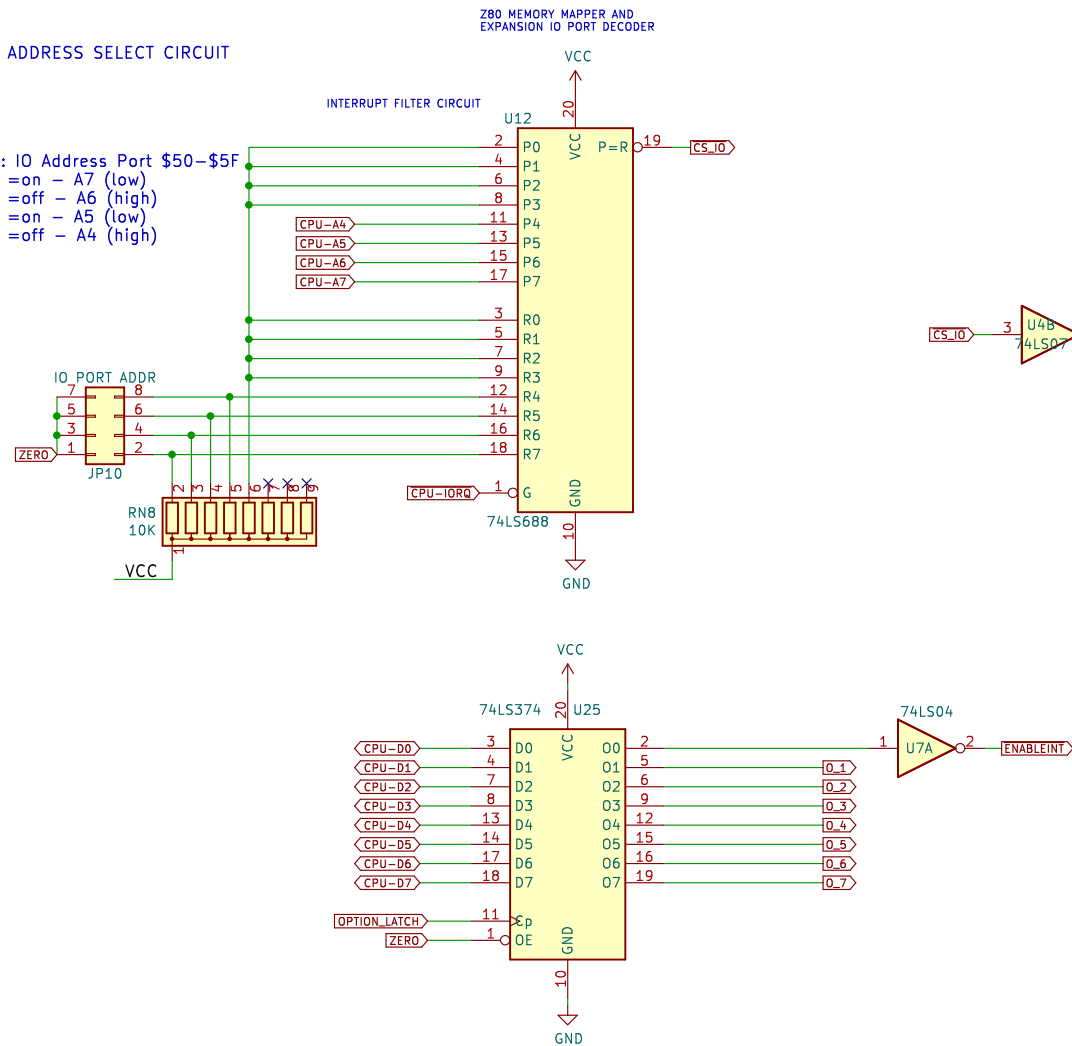
Note: IO Address Port \$95
1-2 =off - A7 (high)
3-4 =on - A6 (low)
5-6 =on - A5 (low)
7-8 =off - A4 (high)
9-10 =on - A3 (low)
11-12 =off - A2 (high)
13-14 =on - A1 (low)
15-16 =off - A0 (high)





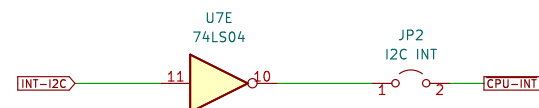
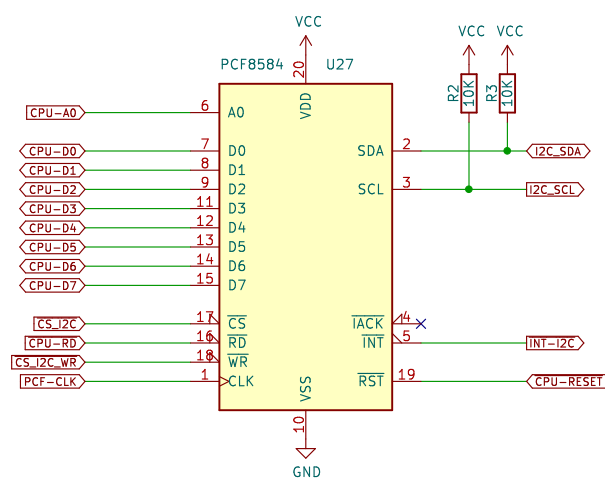
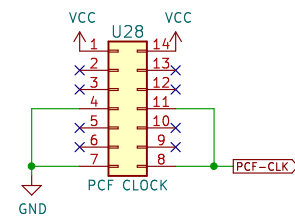
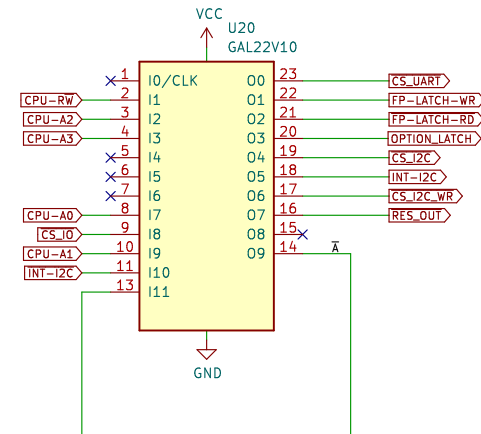
Note: IO Address Port \$50-\$5F

1-2 = on - A7 (low)
3-4 = off - A6 (high)
5-6 = on - A5 (low)
7-8 = off - A4 (high)



IO Address Port \$50–\$5F

| | |
|-----------|--------------|
| \$50 | FRONT PANEL |
| \$51 | OPTION LATCH |
| \$52 | |
| \$53 | RESET |
| \$54 | |
| \$55 | RESET SYSTEM |
| \$56-\$57 | I2C(Rw) |
| \$58-\$5F | UART (Rw) |



NOTE: INTERRUPTS RELY ON 4700
OHM PULL UP RESISTOR ON PROCESSOR

