

12345678

A

bus

power

buffers

fpanel

File: bus.kicad\_sch  
6809CPU

File: power.kicad\_sch  
GALS

File: buffers.kicad\_sch  
bus sharing

File: fpanel.kicad\_sch  
mapper

File: 6809CPU.kicad\_sch

File: GALS.kicad\_sch

File: bussharing.kicad\_sch

File: mapper.kicad\_sch

12345678

E

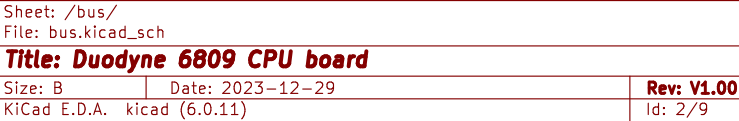
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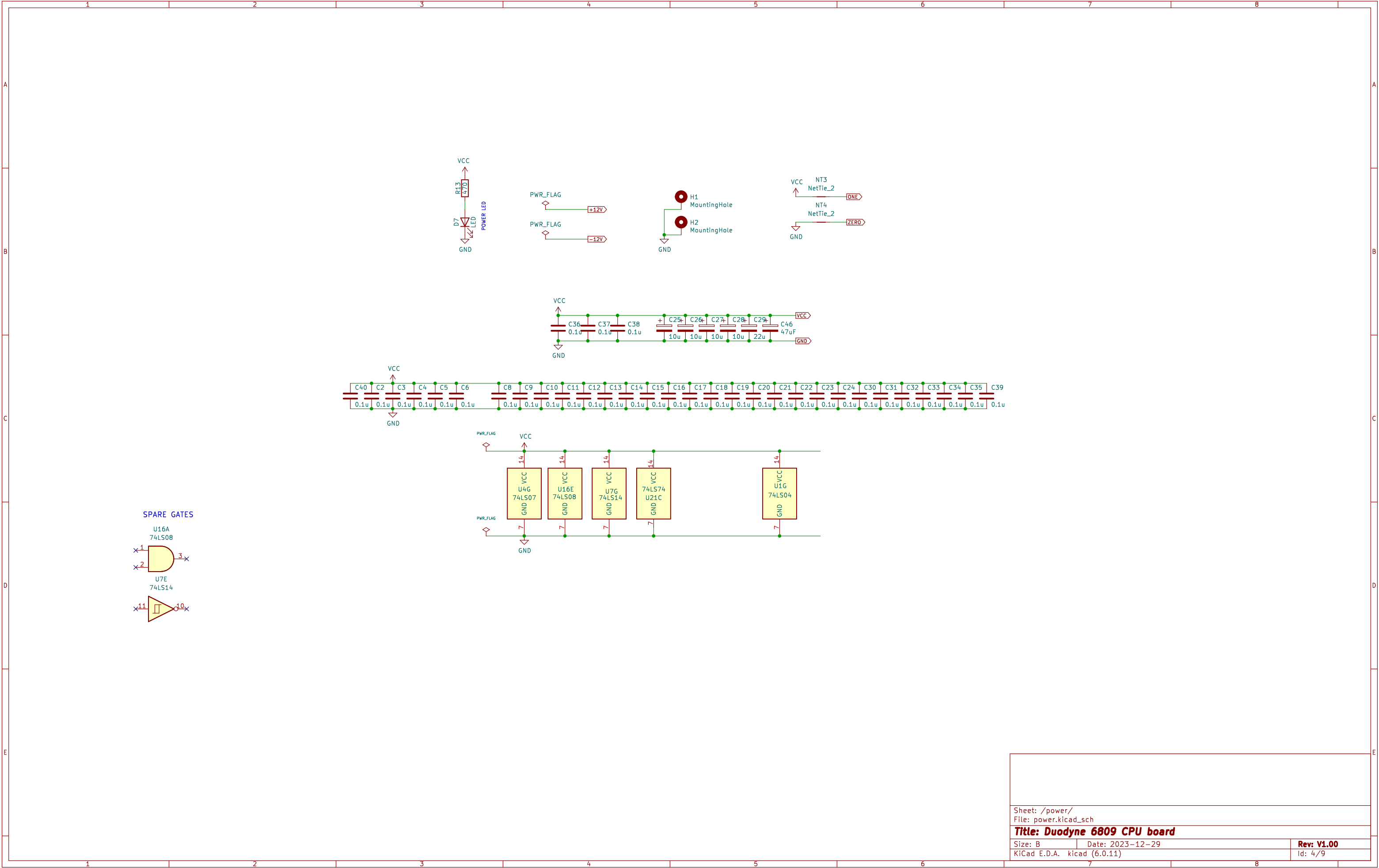
**Title: Duodyne 6809 CPU board**

Size: BDate: 2023-12-29

KiCad E.D.A. kicad (6.0.11)Rev: V1.00

Id: 1/9

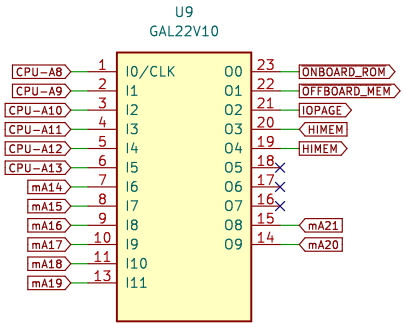




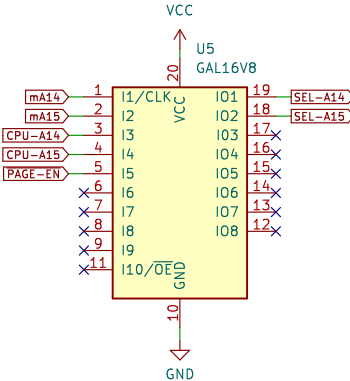
IO Address Port \$50-\$5F

\$50 Front Panel (RW)  
\$51 OPTION REG (WR)  
\$52 PAGER (WR)  
\$53 PAGE EN (WR)  
\$54  
\$55  
\$56-\$57 I2C(RW)  
\$58-\$5F UART (RW)

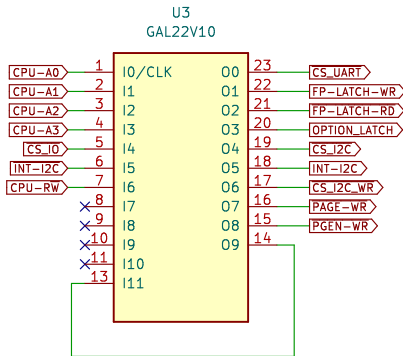
ADDRESS DECODER



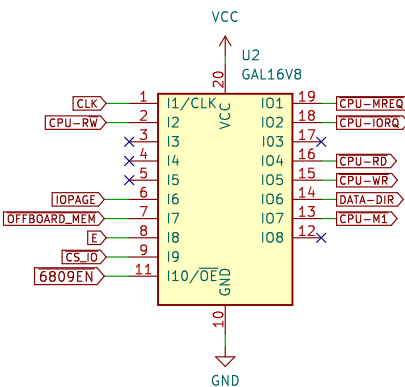
CPU MAPPER V3 GAL22V10



IO DECODER



BUS SIGNALS



Sheet: /GALS/  
File: GALS.kicad\_sch

**Title: Duodyne 6809 CPU board**

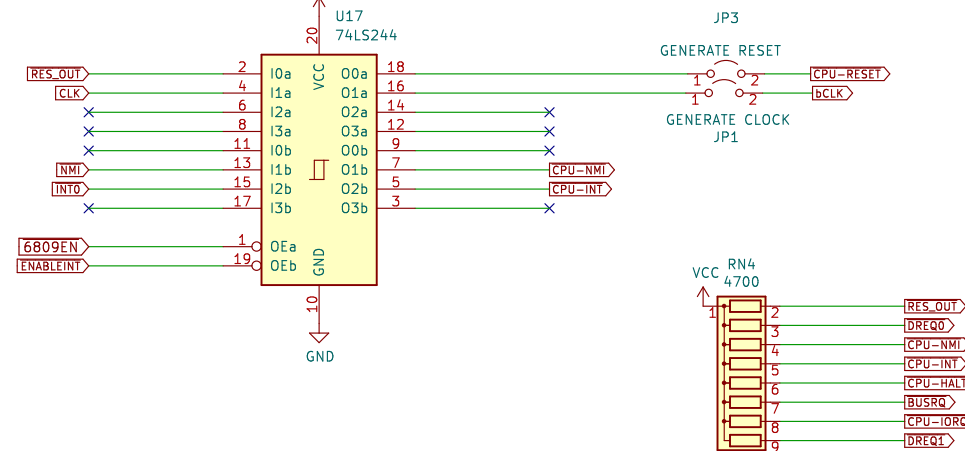
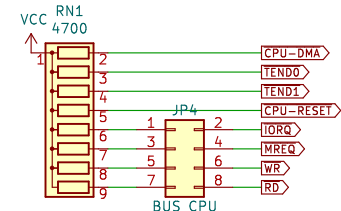
Size: B

Date: 2023-12-29

Rev: V1.00

KiCad E.D.A. kicad (6.0.11)

Id: 5/9





**J11 NOTE:**

PINS 5, 7, 9, 11, 13, 15, 17, 19 ARE FOR LED STATUS FOR ROWMBW FRONT PANEL INDICATOR

PINS 6, 8, 10, 12, 14, 16, 18, 20 ARE FOR SWITCH INPUTS FOR ROWMBW FRONT PANEL

PINS 21, 23 ARE FOR I2C CONNECTION (SDA & SCL)

PINS 25, 27, 29, 31, 33, AND 35 ARE FOR TTL SERIAL CONNECTOR TO UART

PINS 29, 37 ARE FOR SERIAL VCC POWER ENABLE

PINS 22, 24 ARE FOR EXTERNAL RESET SWITCH

PINS 26, 28 ARE FOR IM2 CIRCUIT ENABLE

PINS 30, 32 ARE FOR INTERRUPT 0 ENABLE

PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE

