

12345678

bus

power

buffers

fpanel

File: bus.kicad_sch
65816CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 65816CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: processor.65816.kicad_sch

Title: **Duodyne 65816 CPU board**

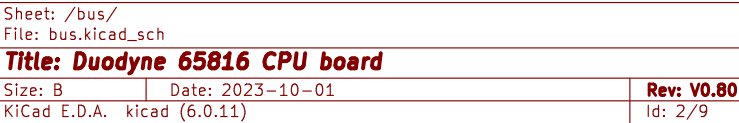
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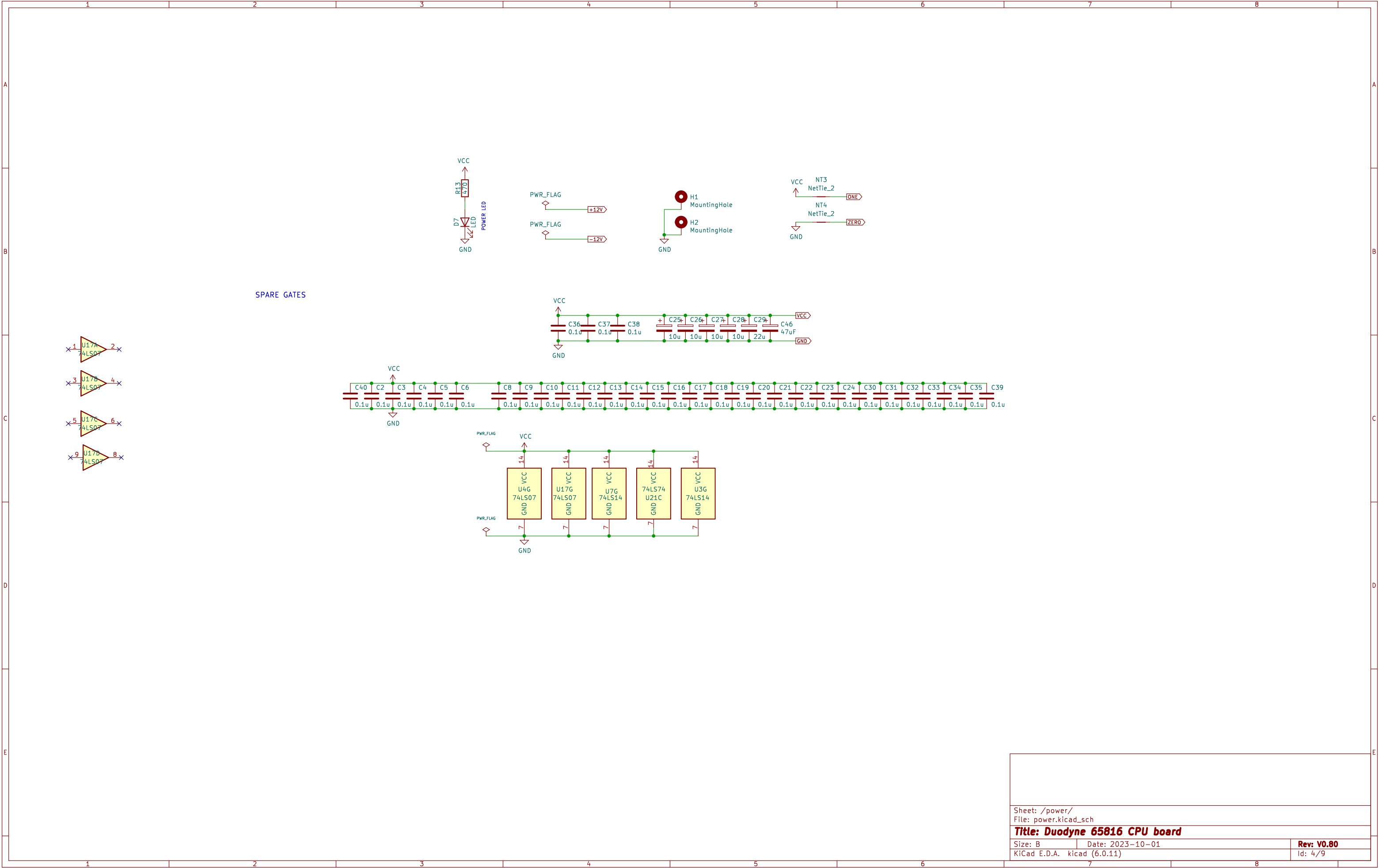
Date: 2023-10-01

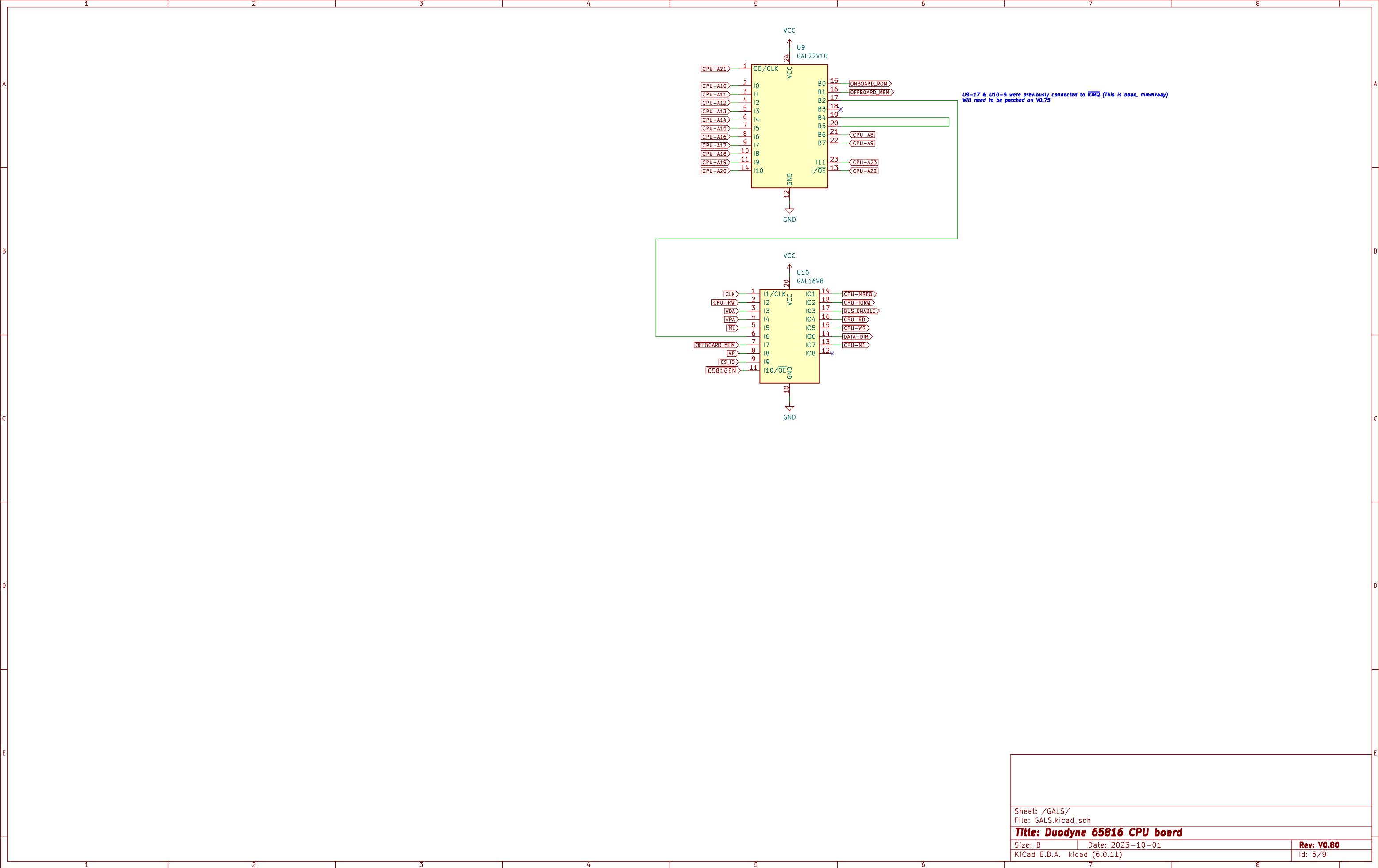
Rev: **V0.80**

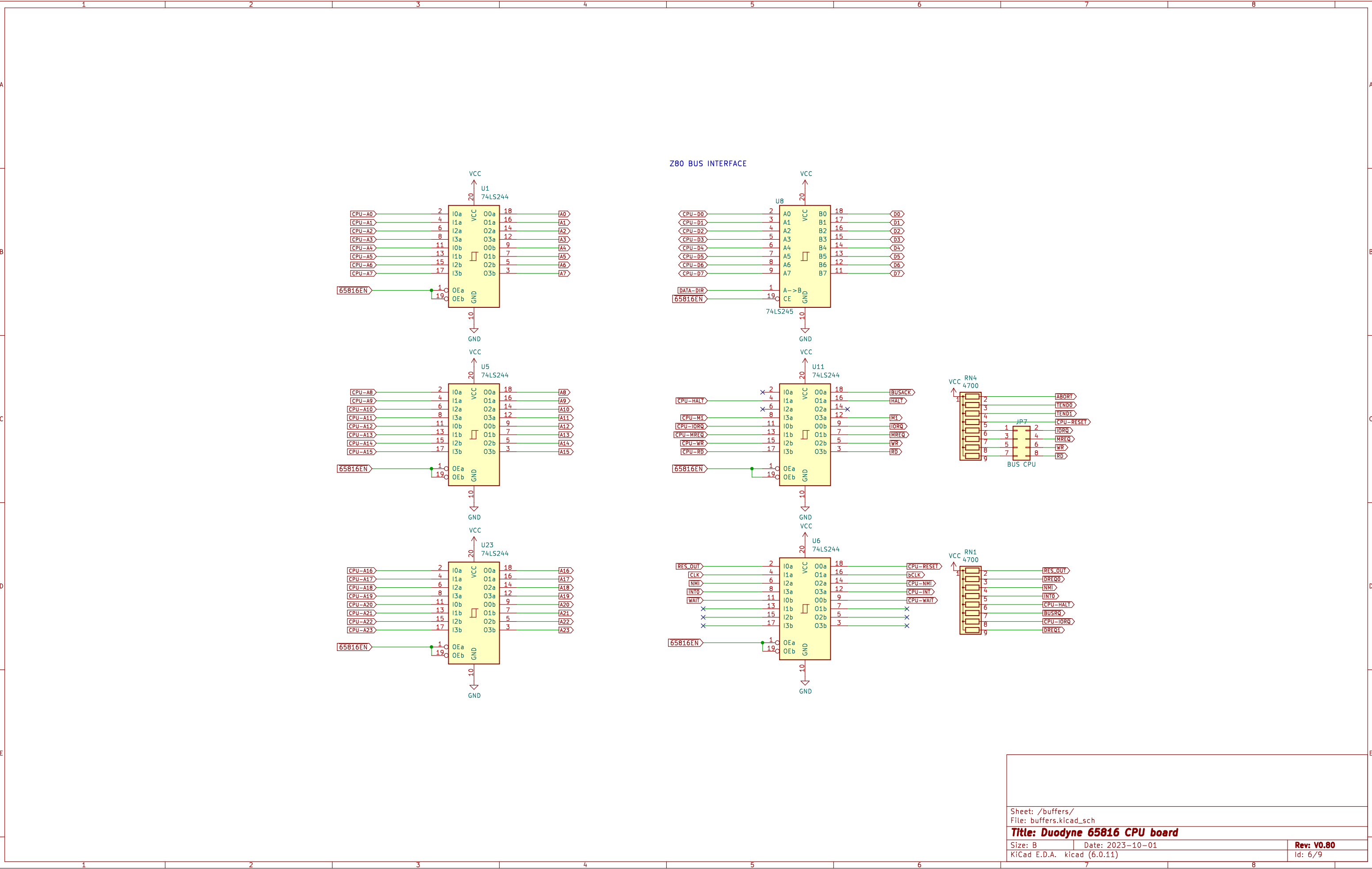
KiCad E.D.A. kicad (6.0.11)

Id: 1/9











PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE

