H1
MountingHole

LED LED LED 6502 BI COLOR LED LED LED LED
D7   D5   D3   D4   D1 EMU   D2   D9   D8
POWER VP INT 65816 EN EMU NMI CS WAIT

470 470 470 470Ω 100 470 470 470
R13 R6   R5   R8   R14 R4   R12 R11

J3   J2   JP4
65816 ONLY   65816 RESET   GENERATE RESET

J11
FRONT PANEL

GND RTS VCC RX TX CTS
FPANEL

H2
MountingHole

SW1
RESET
10
R7

C35
0.1u
U14
TL16C550CFN

0.1u U33 74LS374 U31 74LS244
C31   RN5 4700
C36

0.1u U21 74LS374 1KΩ R10
C34

0.1u U26 74LS32   C46 47uF
C33

U2   UART CLOCK SER INT JP13
C39

0.1u U25 74LS374
C14

0.1u U16 74LS08
C11

R1 470 U10 GAL16V8   0.1u U9 GAL22V10
C15   C24

0.1u U13 CPU CLOCK
C3

0.1u U19 27C64
C17

C1 U17 74LS00

0.1u U7 74LS04
C18

3K
R9
1N4148
D6
C26 IC1 C12
10u

0.1u U4 74LS07
C6

65816CPU-DIP C23 0.1u

U20 GAL22V10 U18 74HC245
C4   C13

0.1u U5 74LS14
C10

RN2 1KΩ

0.1u C21

GENERATE CLOCK JP3

JP2 JP1
RES SEL

U15 DS1233
I2C INT
C19 0.1u

RN4 4700

BUS CPU
JP7

C25 P1
10u
CONN_02X25

U23 74LS244
C38 0.1u

U6 74LS244 C5
RN1 4700 1K R15

U1 74LS244
C20

U11 74LS244
C16 0.1u

C27 P2
10u
CONN_02X25

PROCESSOR.65816, VERSION 1.30, AS OF 6 MAY 2Q24

U22 74LS688
C22 0.1u

IO Address Select
J1

U8 74LS245
C2 0.1u

JP10
A7 A6 A5 A4   RN8
IO PORT ADDR $50–$5F
10K

R2 10K 0.1u C30
R3 10K

U12 74LS688 U28 PCF CLOCK
C9 0.1u   C32

U24 74HC573 U27 PCF8584
C37 0.1u   C40

C28 P3
10u
CONN_02X25

PATCH LEVEL 0_ 1_ 2_ 3_ 4_ 5_ 6_ 7_ 8_ 9_
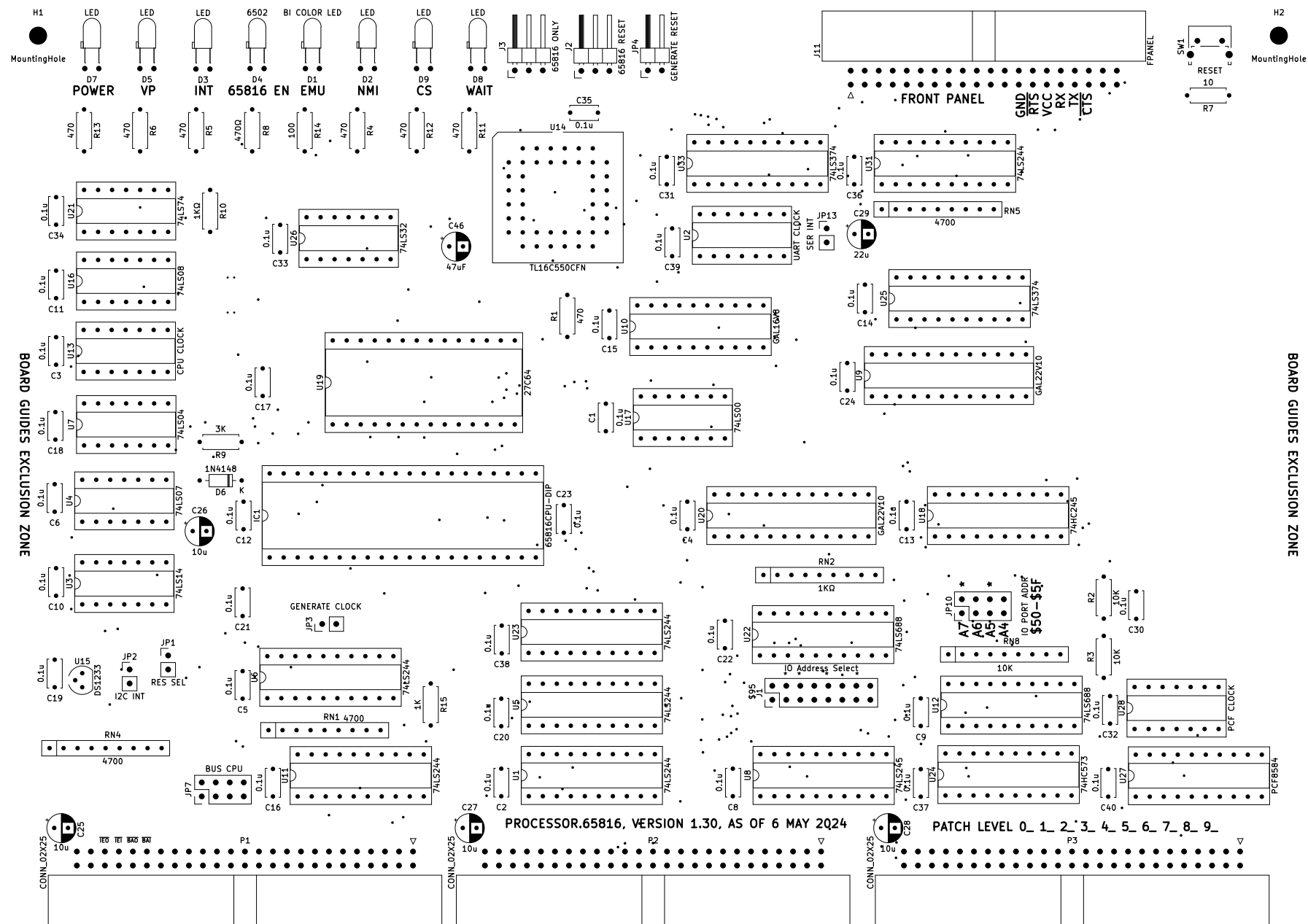
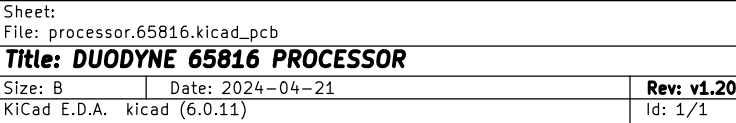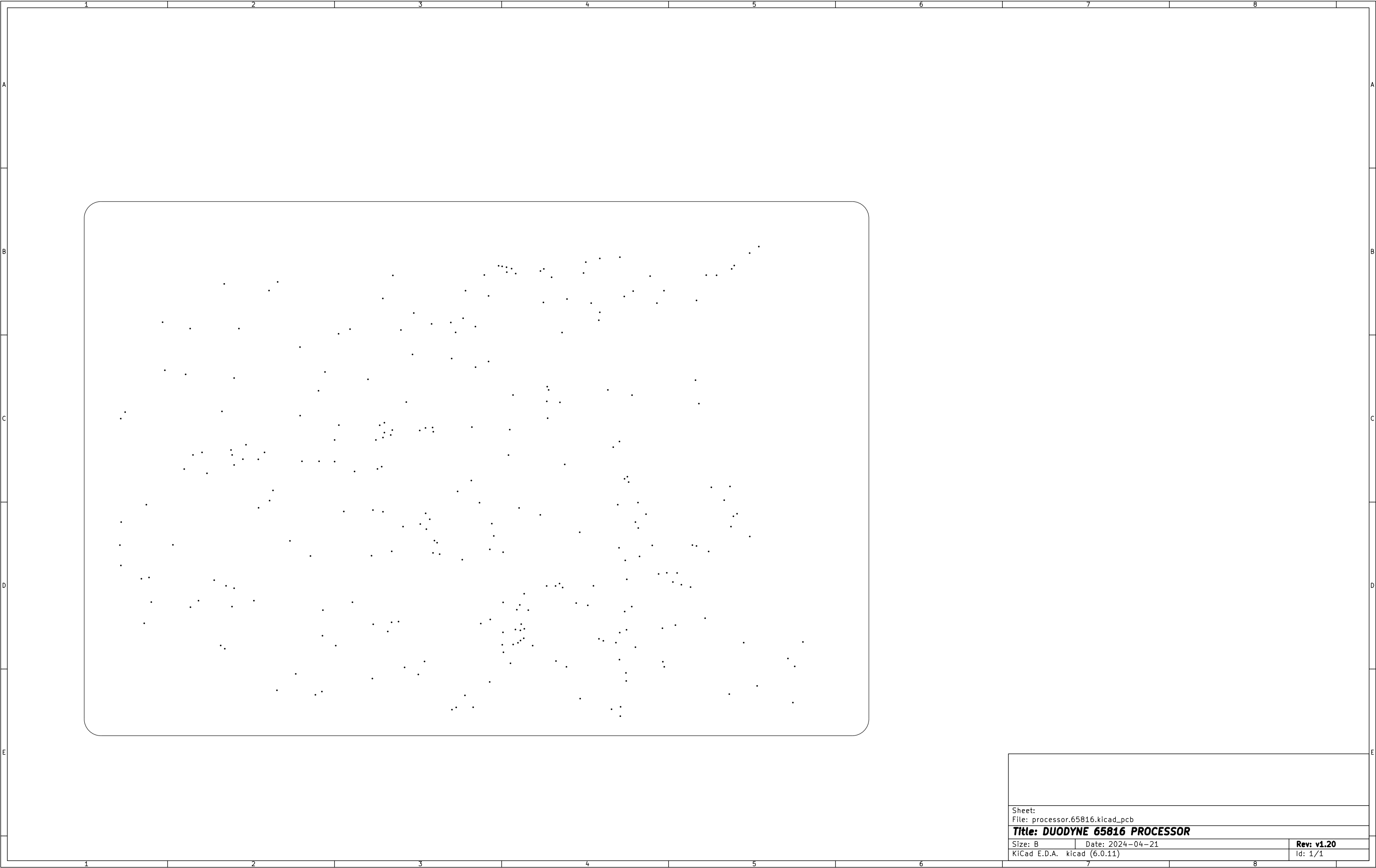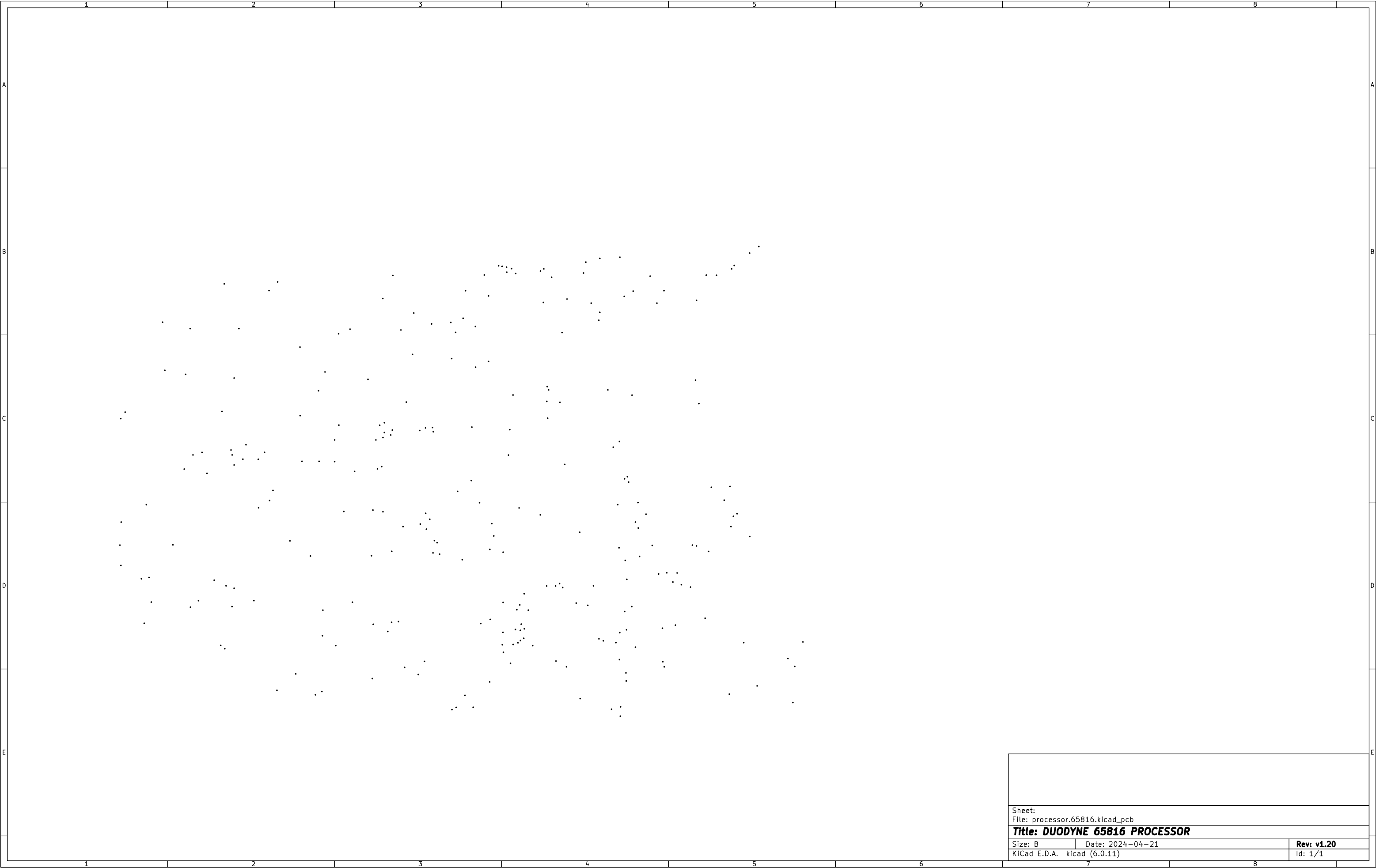BOARD GUIDES EXCLUSION ZONE

Sheet:
File: processor.65816.kicad_pcb
Title: DUODYNE 65816 PROCESSOR
Size: B   Date: 2024-04-21   Rev: v1.20
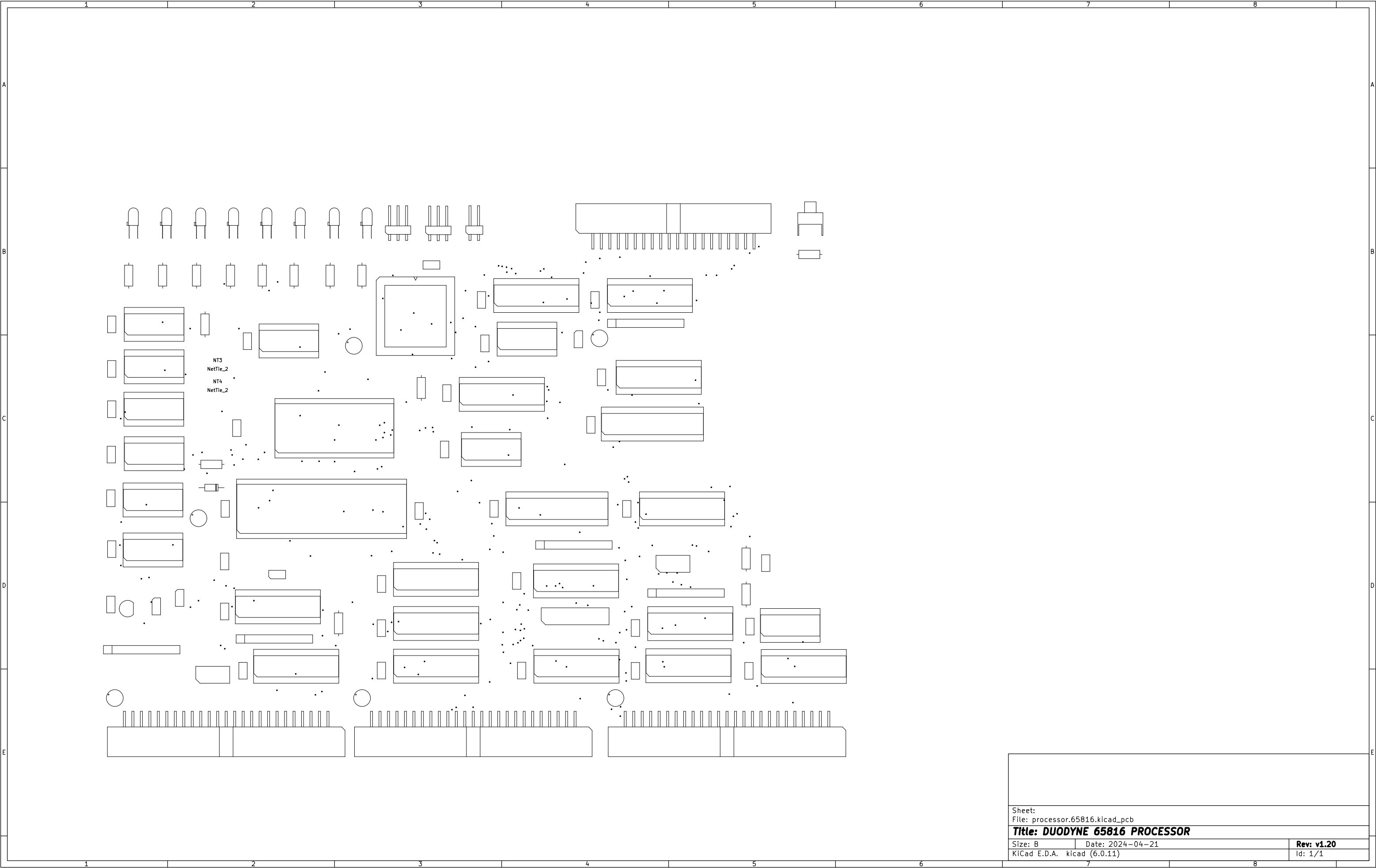KiCad E.D.A. kicad (6.0.11)   Id: 1/1

NT3
NetTie_2

NT4
NetTie_2