bus

File: bus.kicad_sch

power

File: power.kicad_sch

buffers

File: buffers.kicad_sch

fpanel

File: fpanel.kicad_sch

65816CPU

File: 65816CPU.kicad_sch

GALS

File: GALS.kicad_sch

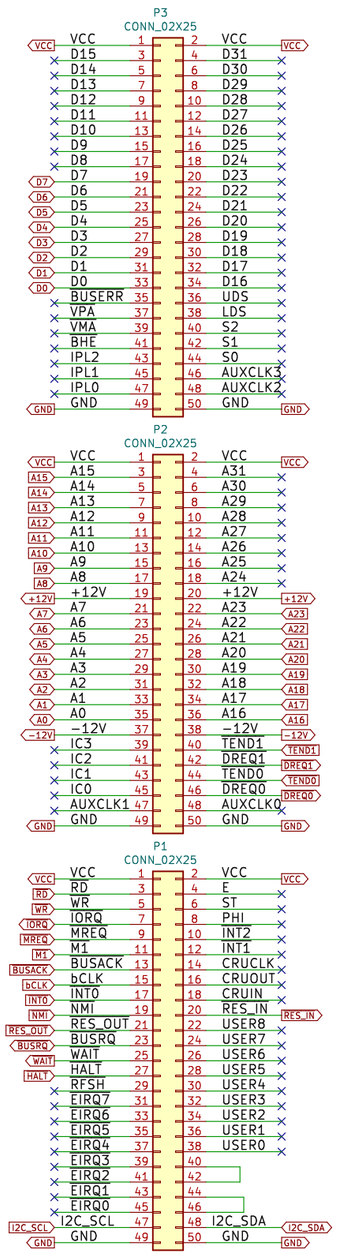
mapper

File: mapper.kicad_sch

Sheet: /
File: processor.65816.kicad_sch

Title: DuoDyne 65816 CPU board

Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 1/8



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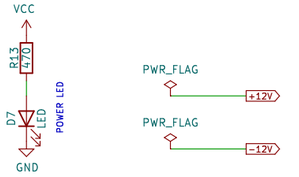
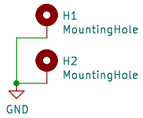
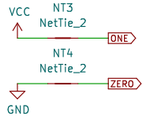
Title: DuoDyne 65816 CPU board

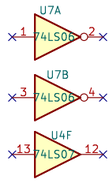
Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 2/8

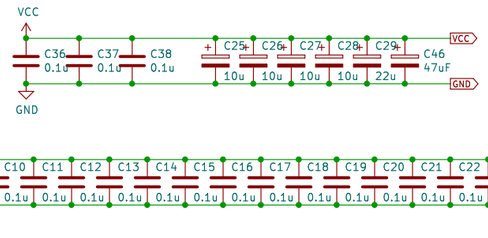


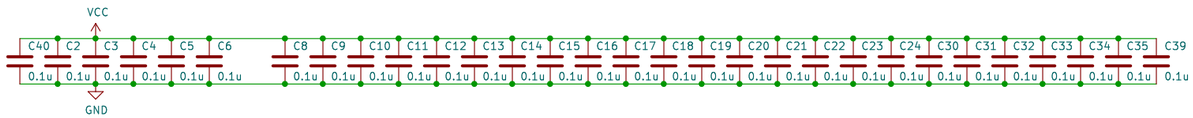


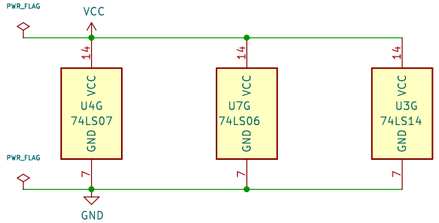


SPARE GATES









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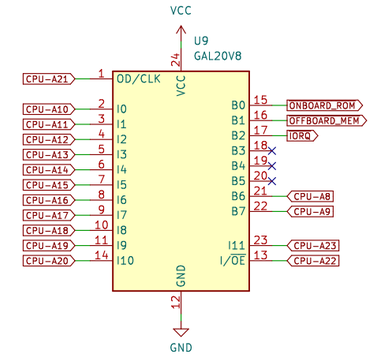
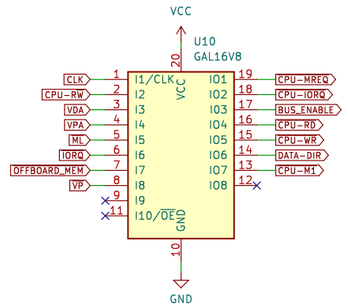
File: power.kicad_sch

Title: DuoDyne 65816 CPU board

Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 4/8



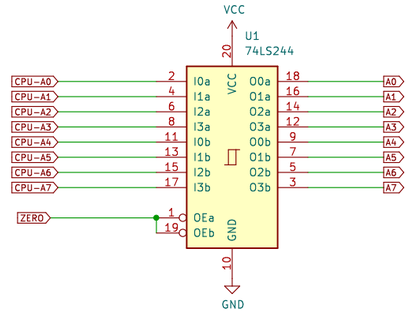


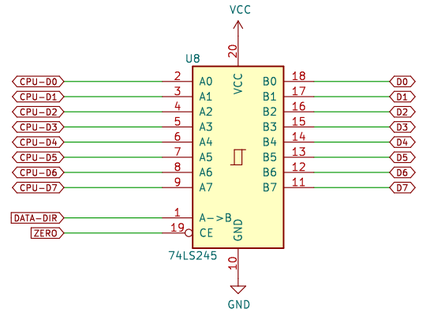
Sheet: /GALS/
File: GALS.kicad_sch

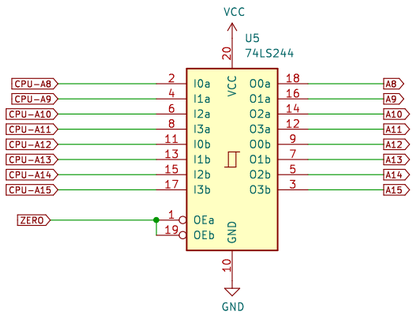
Title: DuoDyne 65816 CPU board

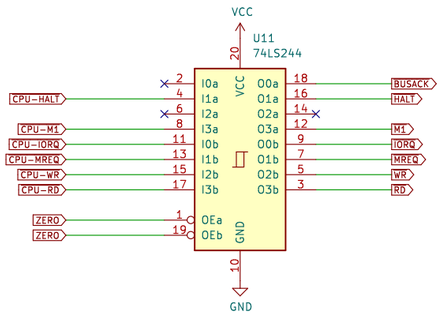
Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 5/8

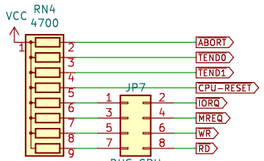
Z80 BUS INTERFACE

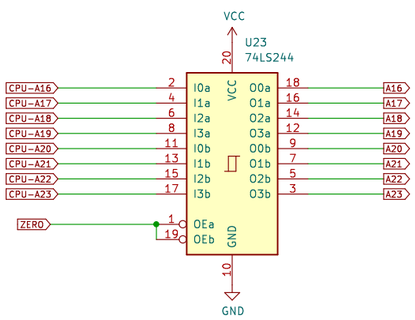


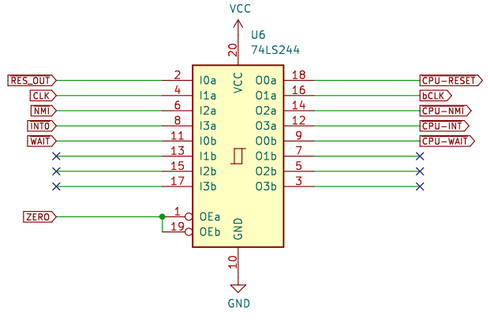


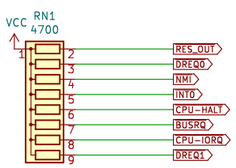








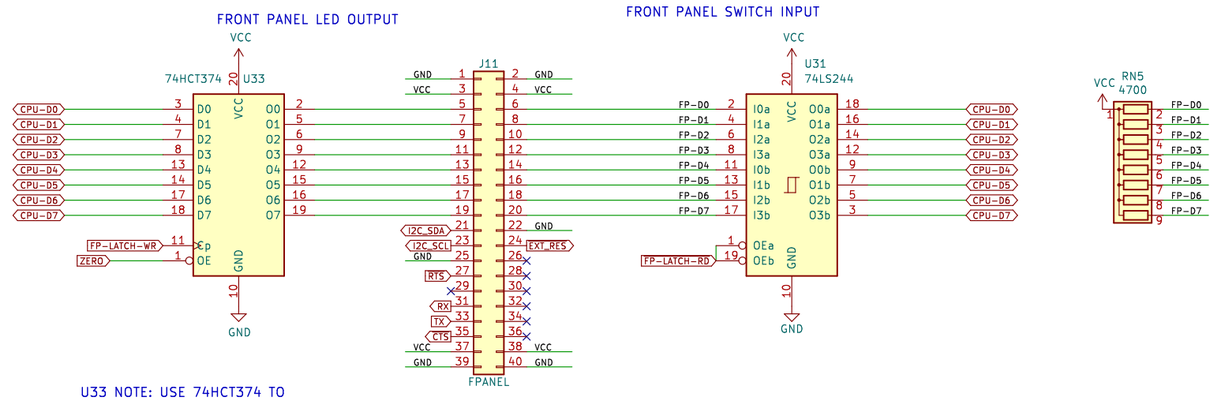




Sheet: /buffers/
File: buffers.kicad_sch

Title: DuoDyne 65816 CPU board

Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 6/8



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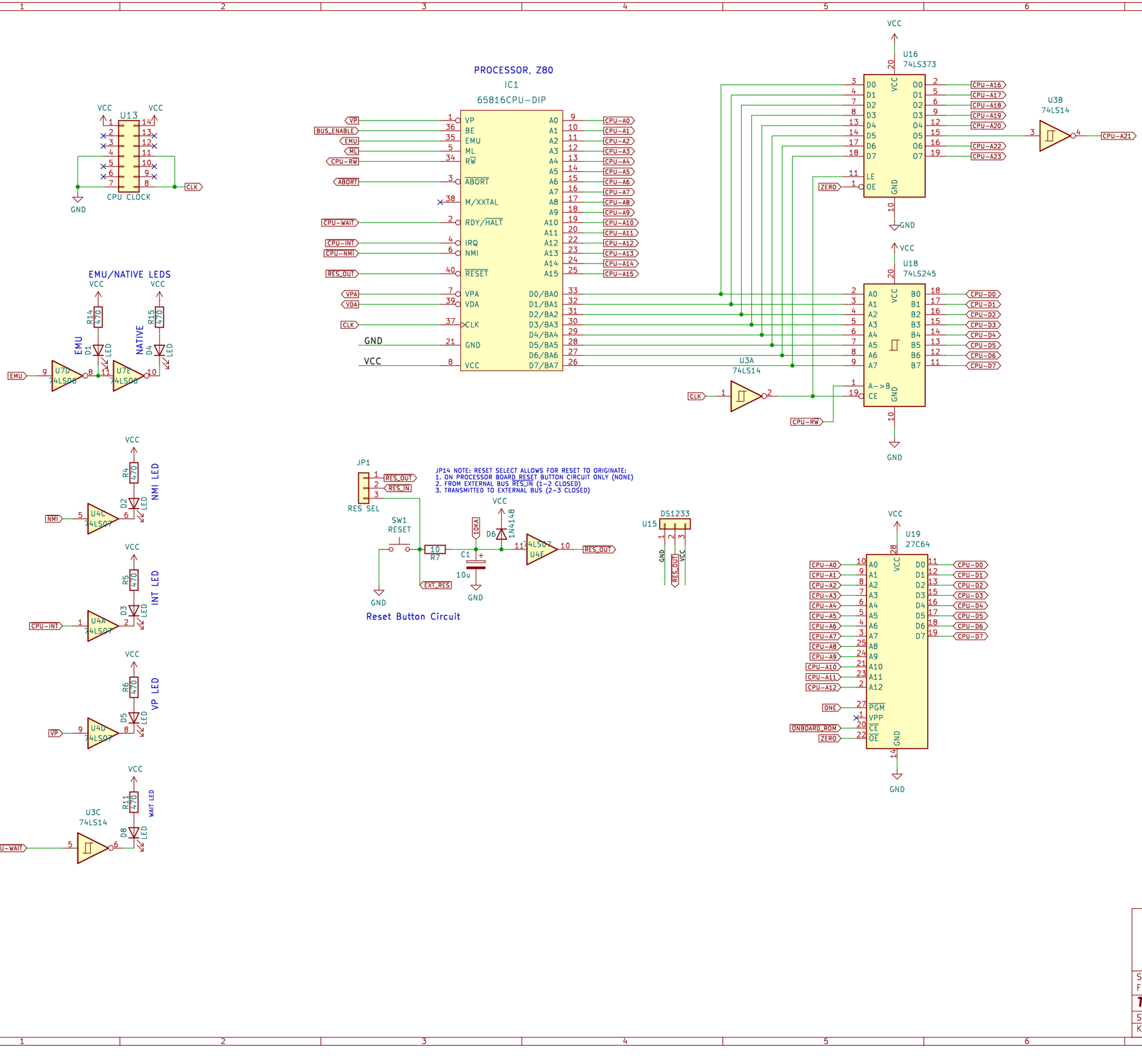
File: fpanel.kicad_sch		
Title: DuoDyne 65816 CPU board		
Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 8/8



Sheet: /65816CPU/
File: 65816CPU.kicad_sch

Title: DuoDyne 65816 CPU board

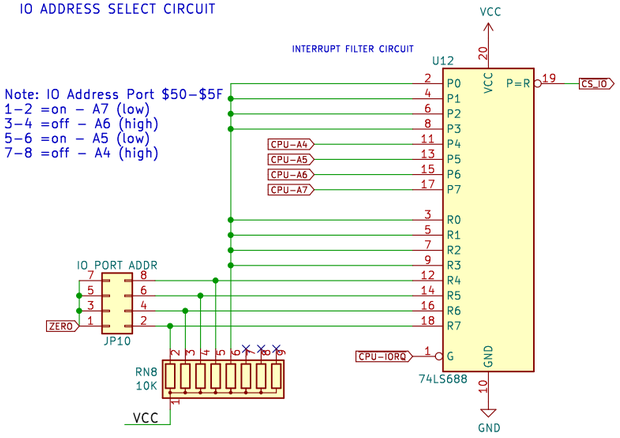
Size: USLedger | Date: 2023-09-01 | **Rev: V0.5**
KiCad E.D.A. kicad (6.0.11) | Id: 9/8

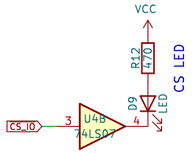
Z80 MEMORY MAPPER AND
EXPANSION IO PORT DECODER

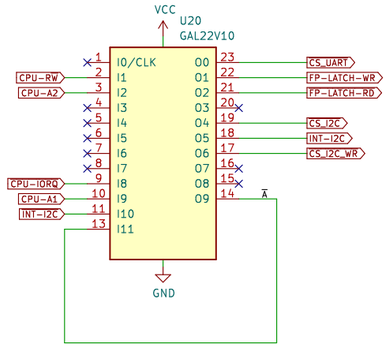
IO ADDRESS SELECT CIRCUIT

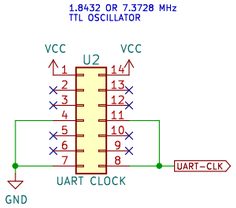
INTERRUPT FILTER CIRCUIT

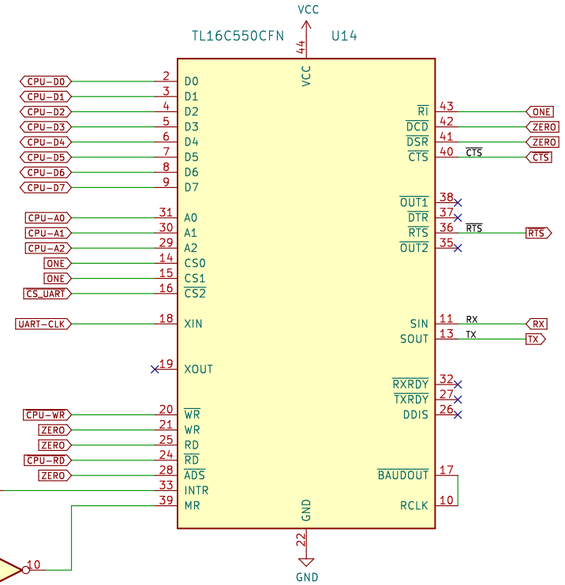
Note: IO Address Port $50-$5F
1-2 =on - A7 (low)
3-4 =off - A6 (high)
5-6 =on - A5 (low)
7-8 =off - A4 (high)

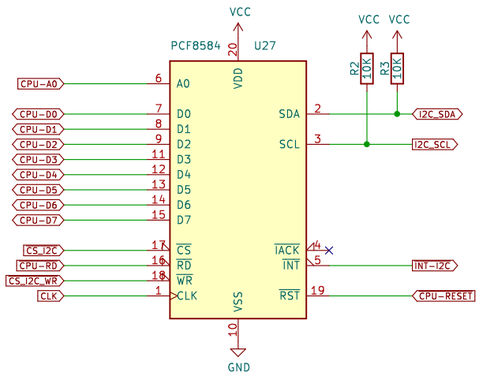


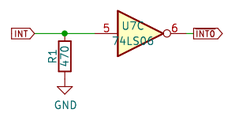


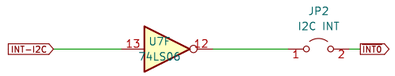












NOTE: INTERRUPTS RELY ON 4700
OHM PULL UP RESISTOR ON PROCESSOR

Sheet: /mapper/
File: mapper.kicad_sch

Title: DuoDyne 65816 CPU board

Size: B	Date: 2023-09-01	**Rev: V0.5**
KiCad E.D.A. kicad (6.0.11)		Id: 11/8