

12345678

A

bus

File: bus.kicad\_sch  
DMA

power

File: power.kicad\_sch  
CPLDs

buffers

File: buffers.kicad\_sch  
IM2

fpanel

File: fpanel.kicad\_sch  
waitstate

Z80CPU

File: Z80CPU.kicad\_sch  
mapper

B

C

D

E

12345678

Sheet: /  
File: Slick.kicad\_sch

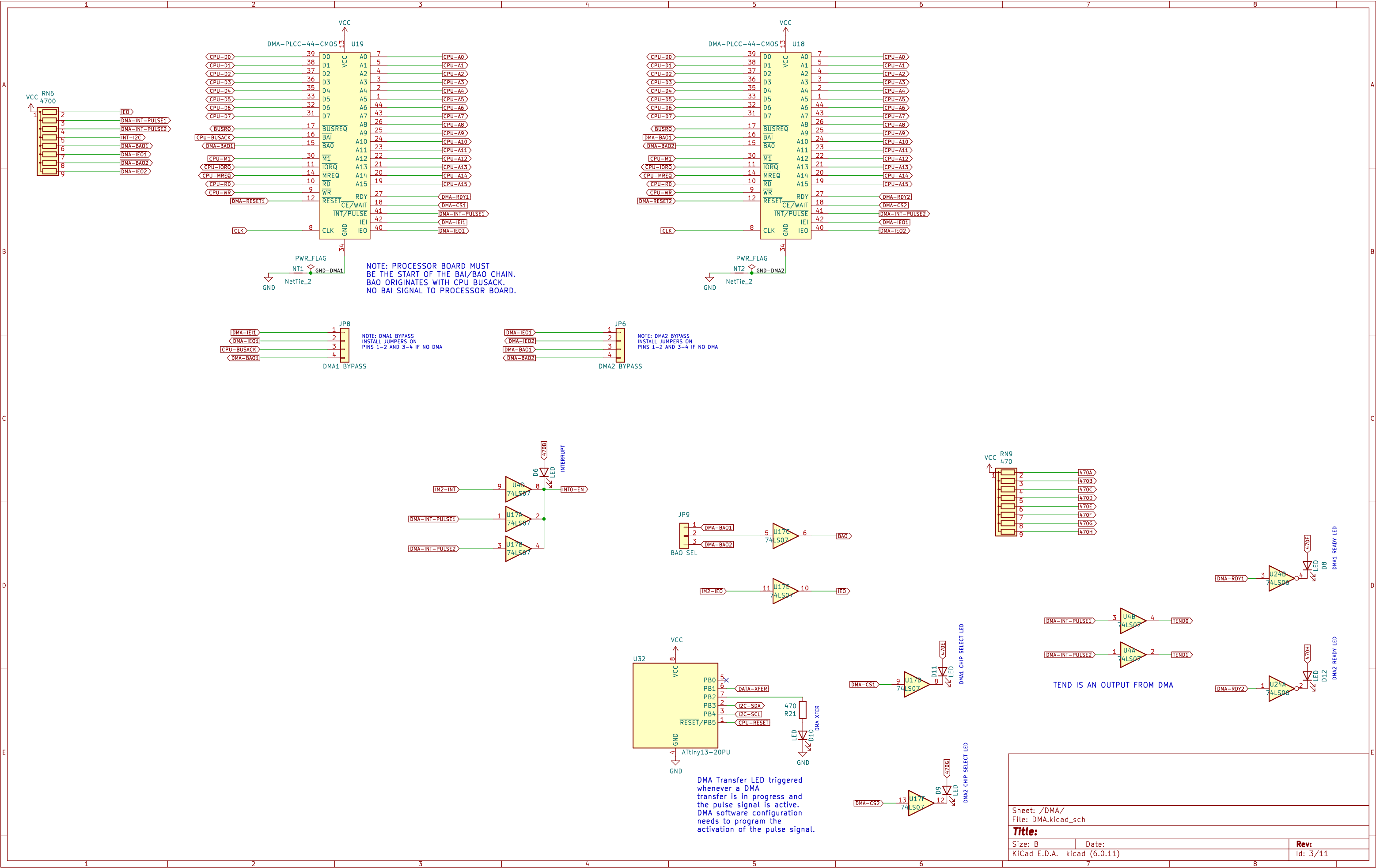
Title:

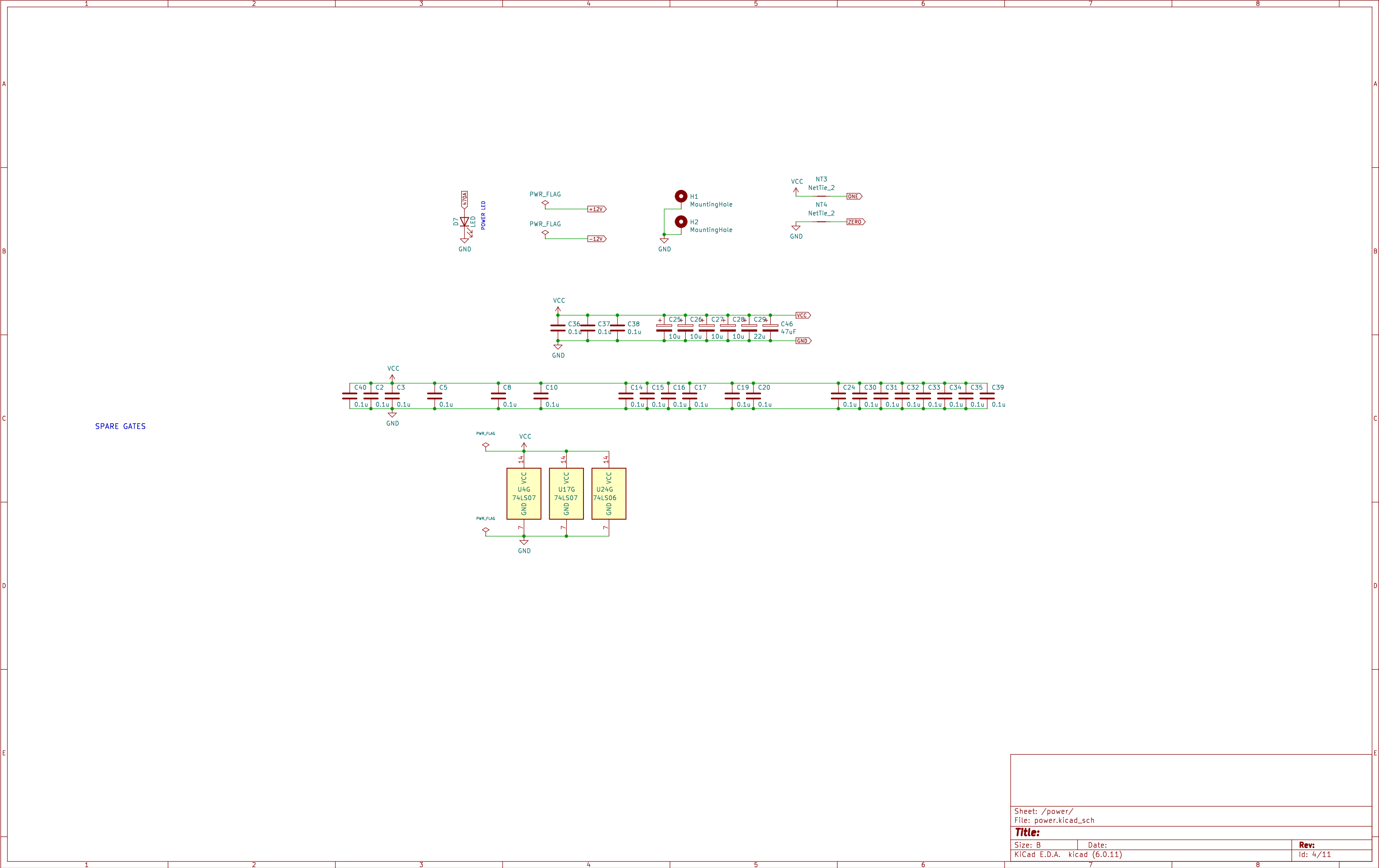
Size: BDate:KiCad E.D.A. kicad (6.0.11)

Rev:Id: 1/11

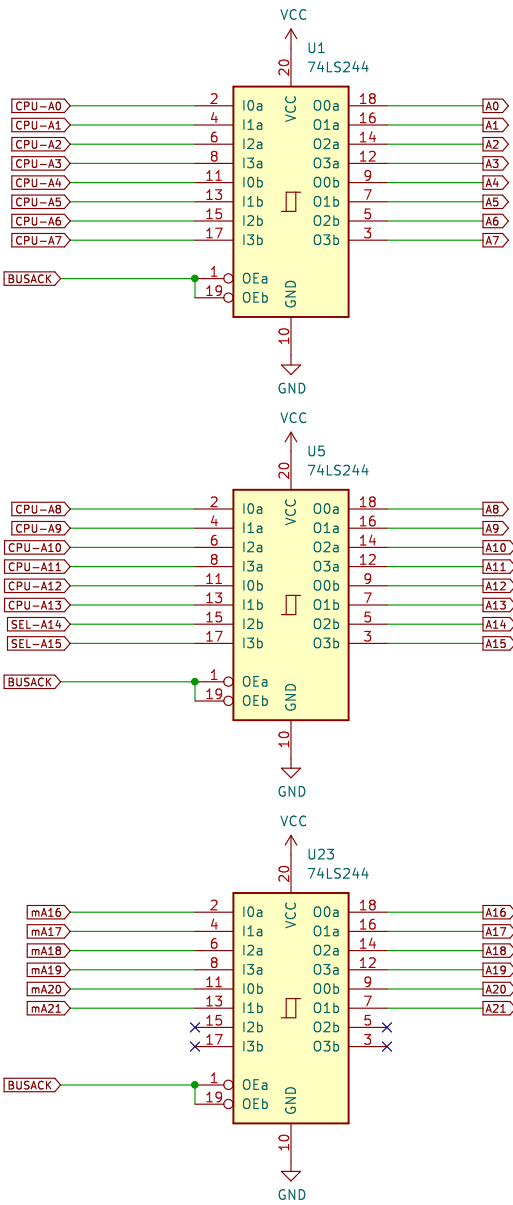


Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 2/11

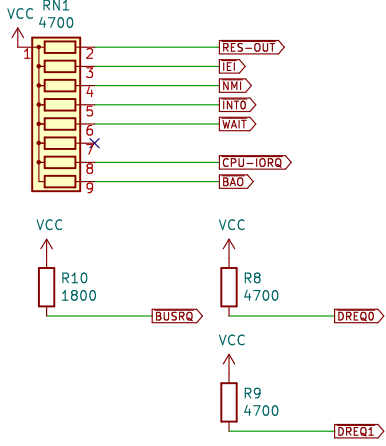
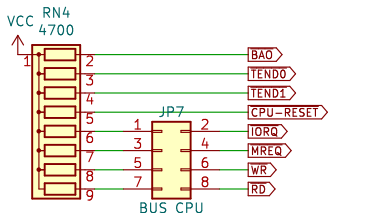
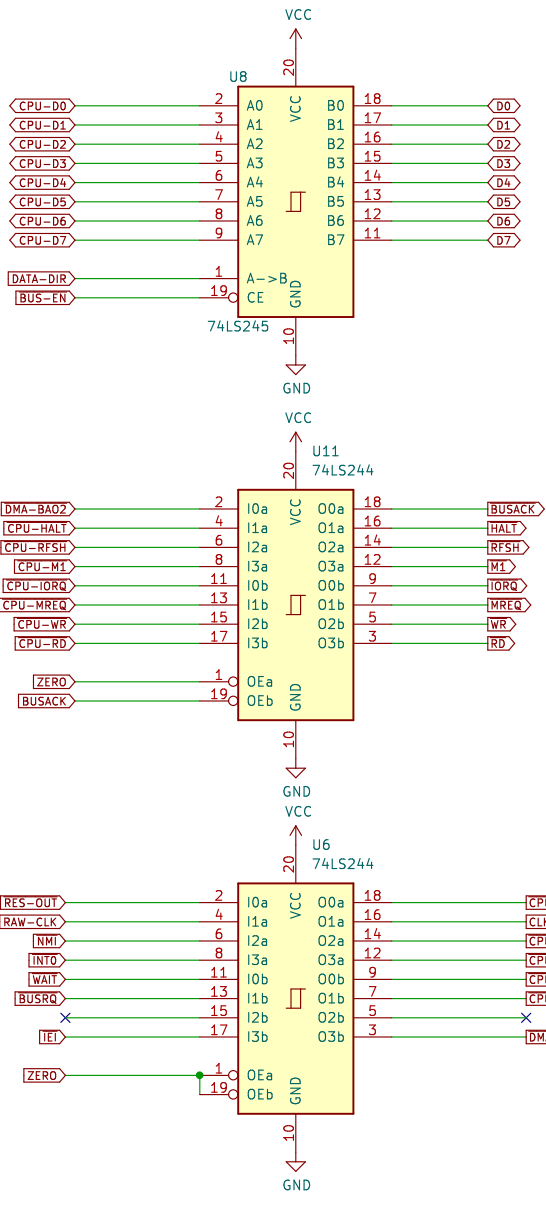


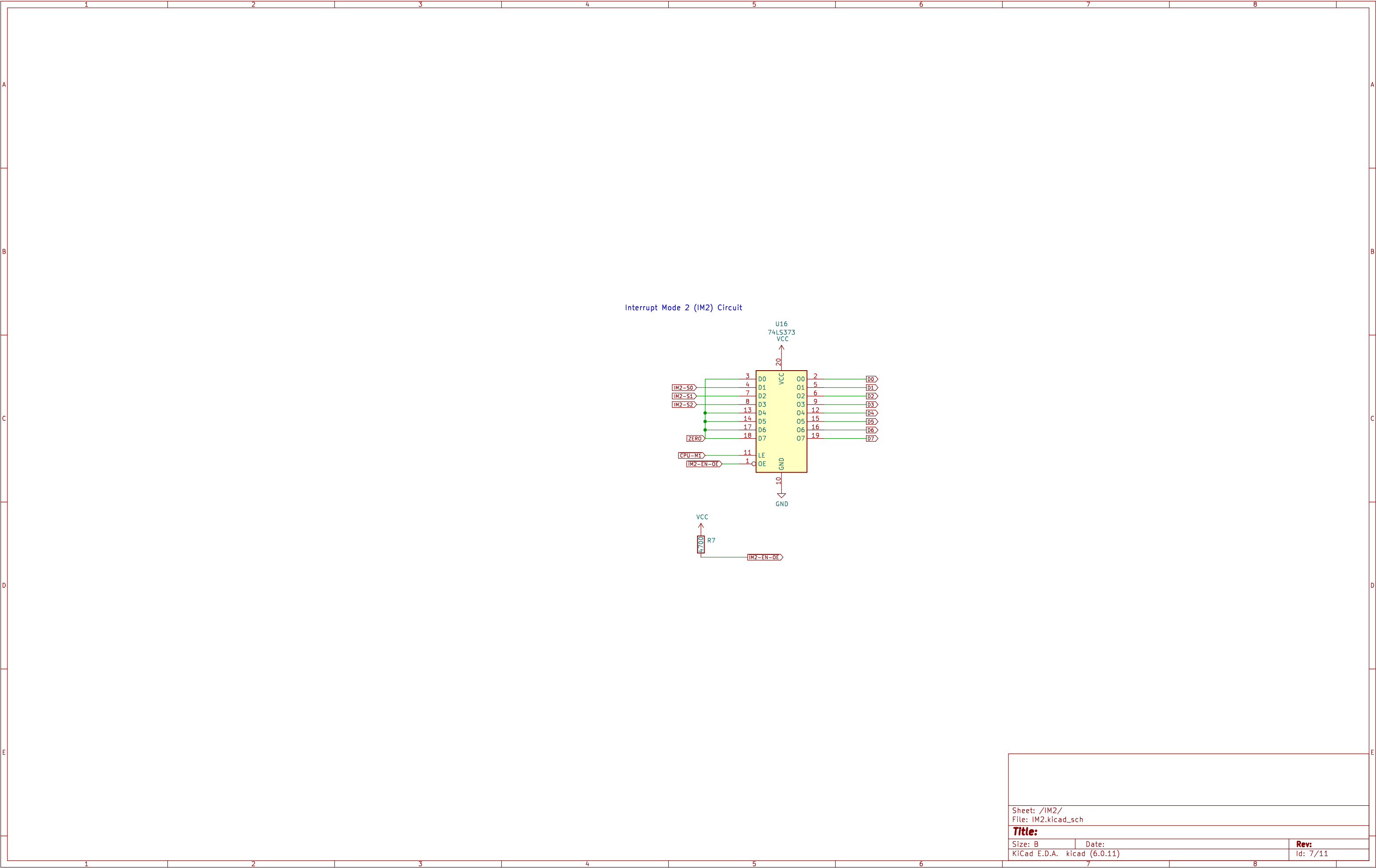






Z80 BUS INTERFACE

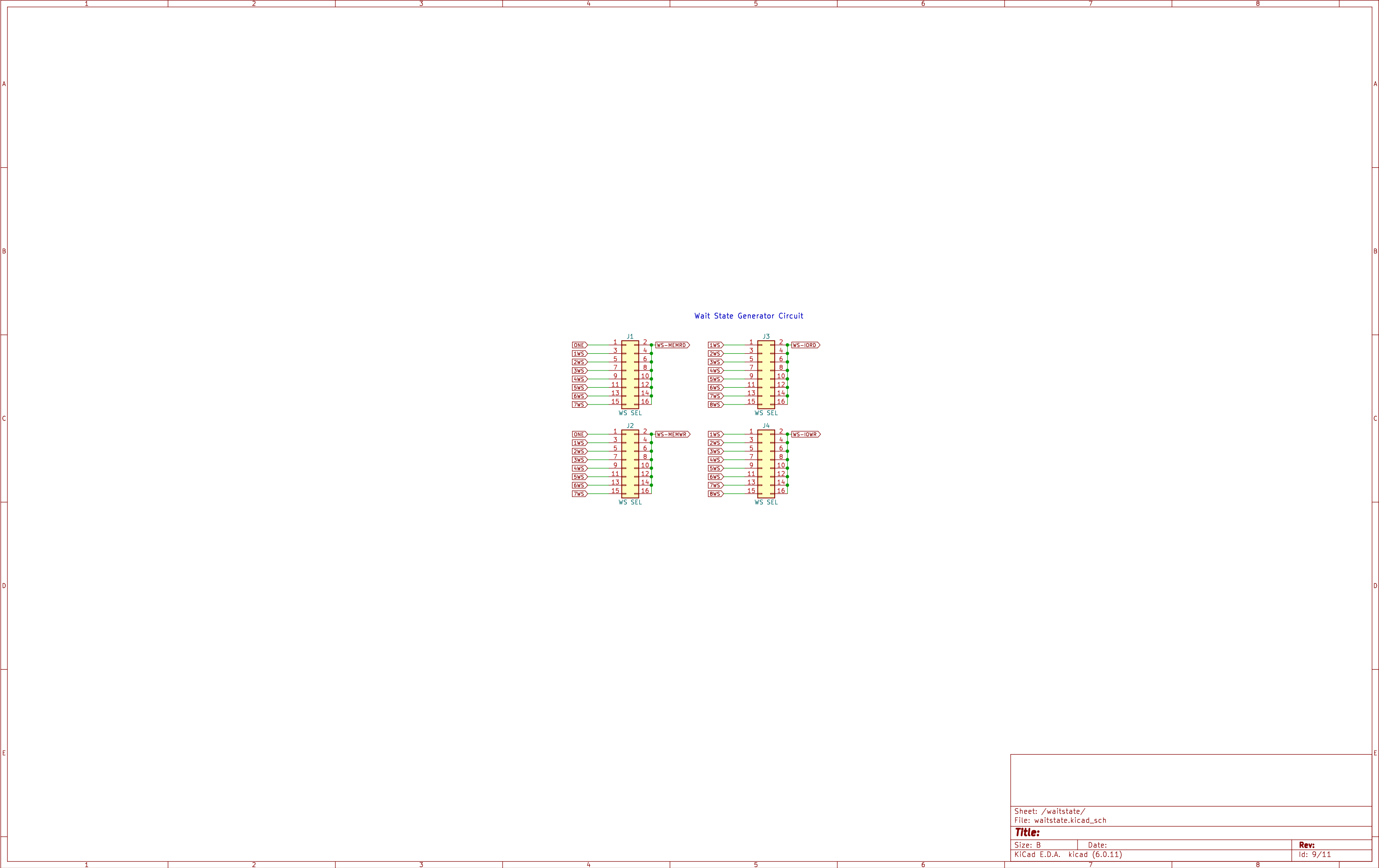


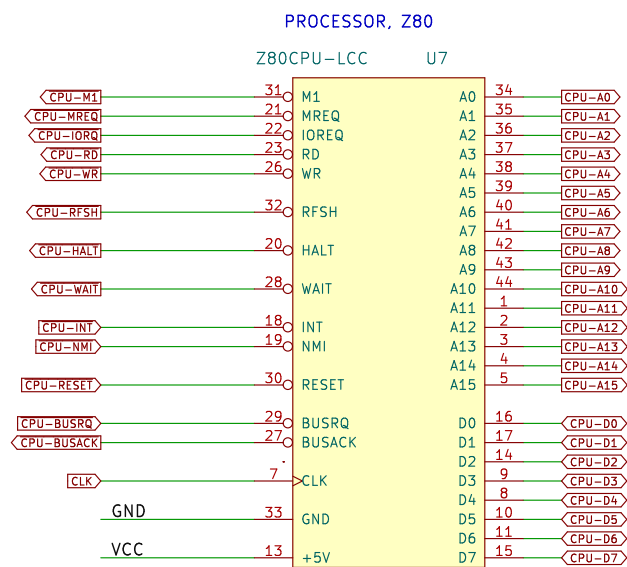
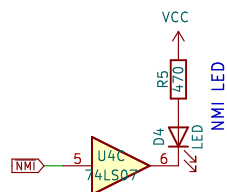
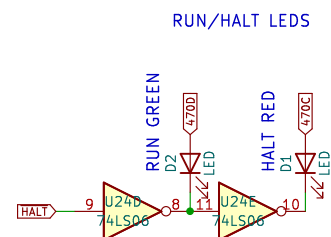
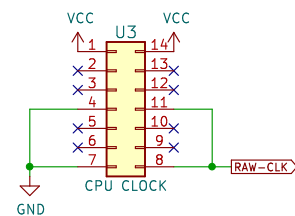




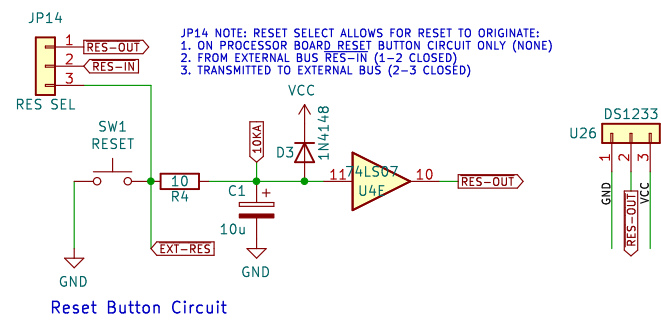
PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE

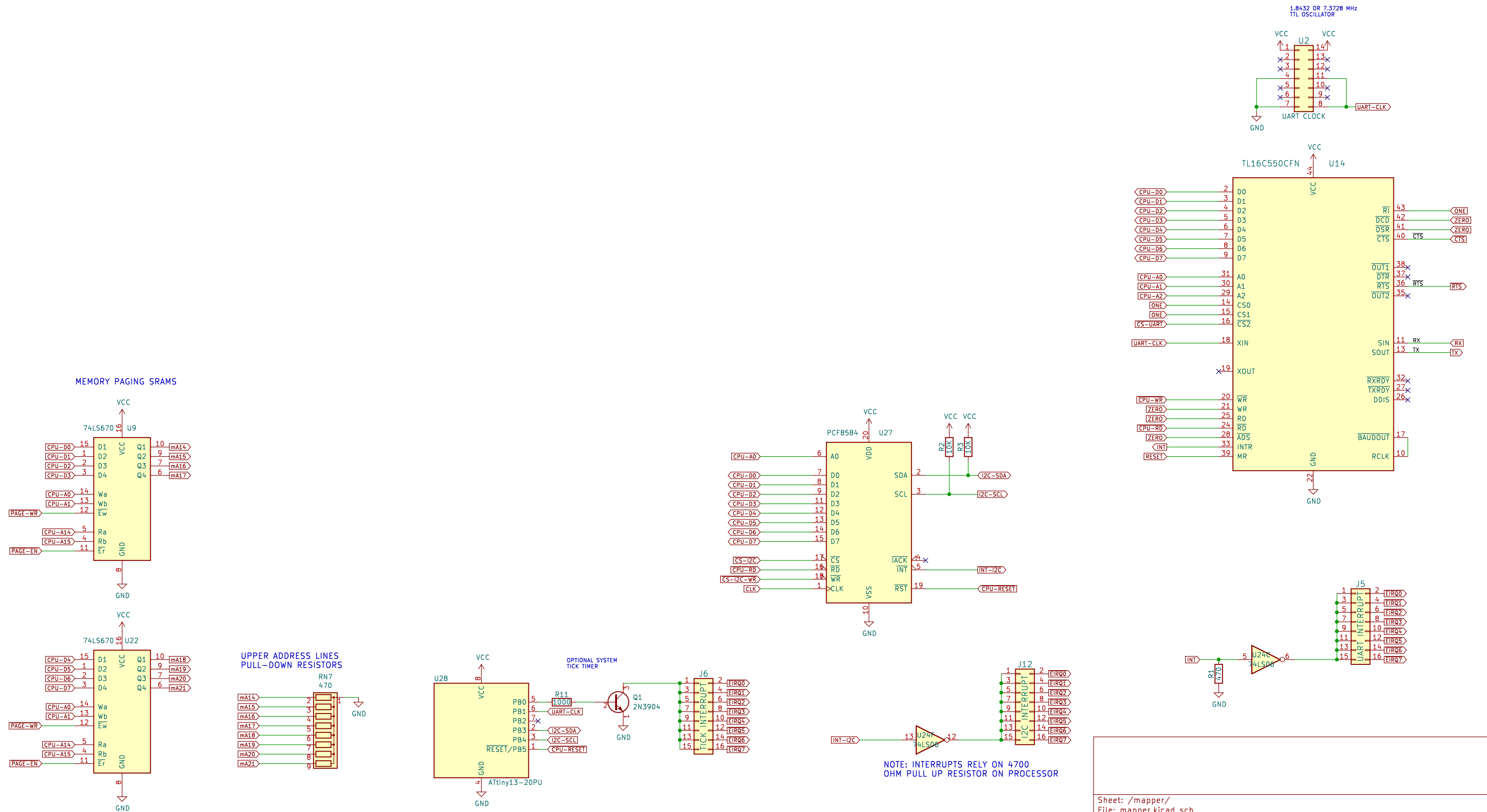






Tri-state address, data, & control bus during DMA





Sheet: /mapper/

File: mapper.kicad\_sch

**Title:**

Size: B

Date:

Size: 9	Date:
KiCad E.D.A.	kicad (6.0.11)

Rev:

id: 11/11