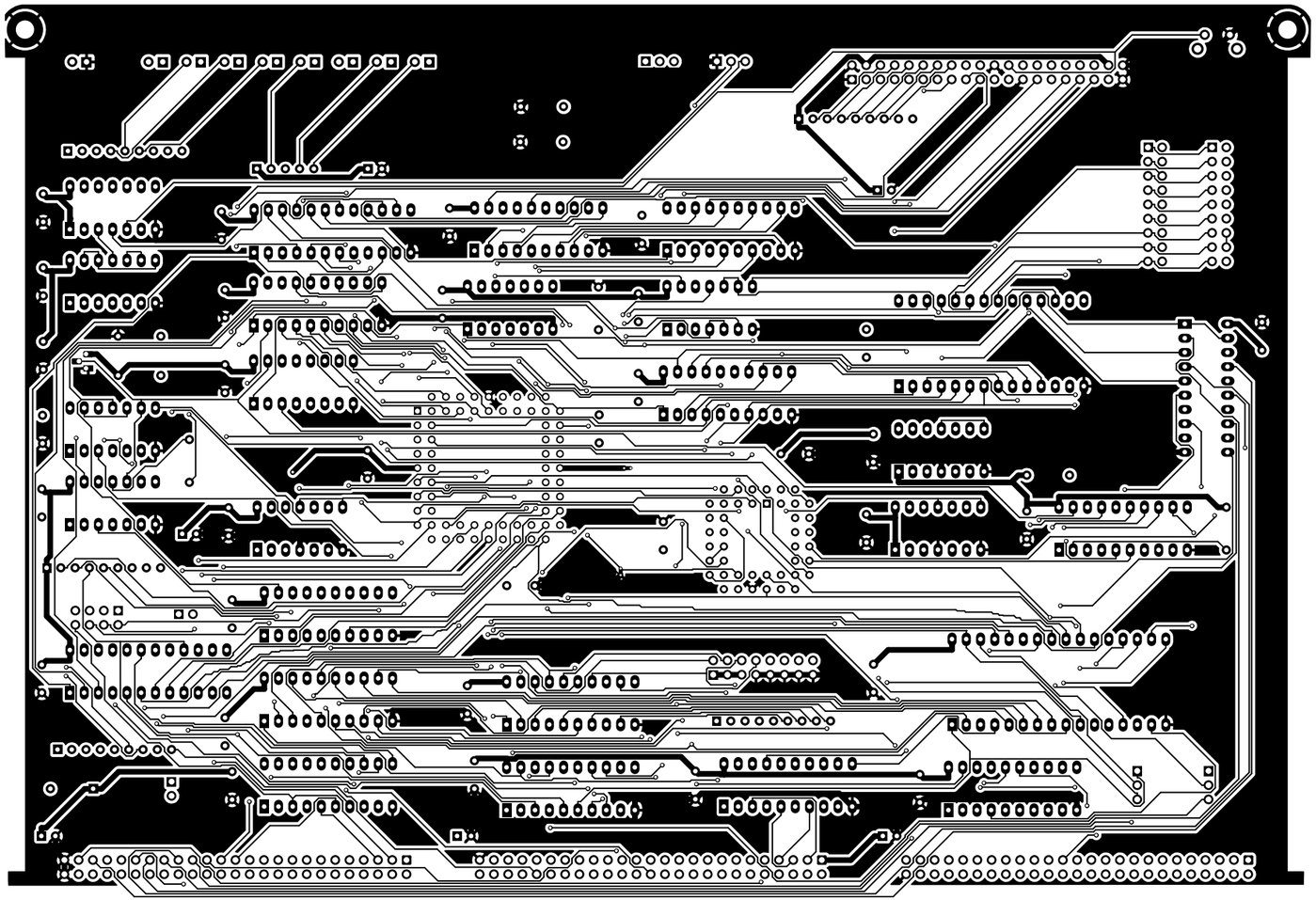
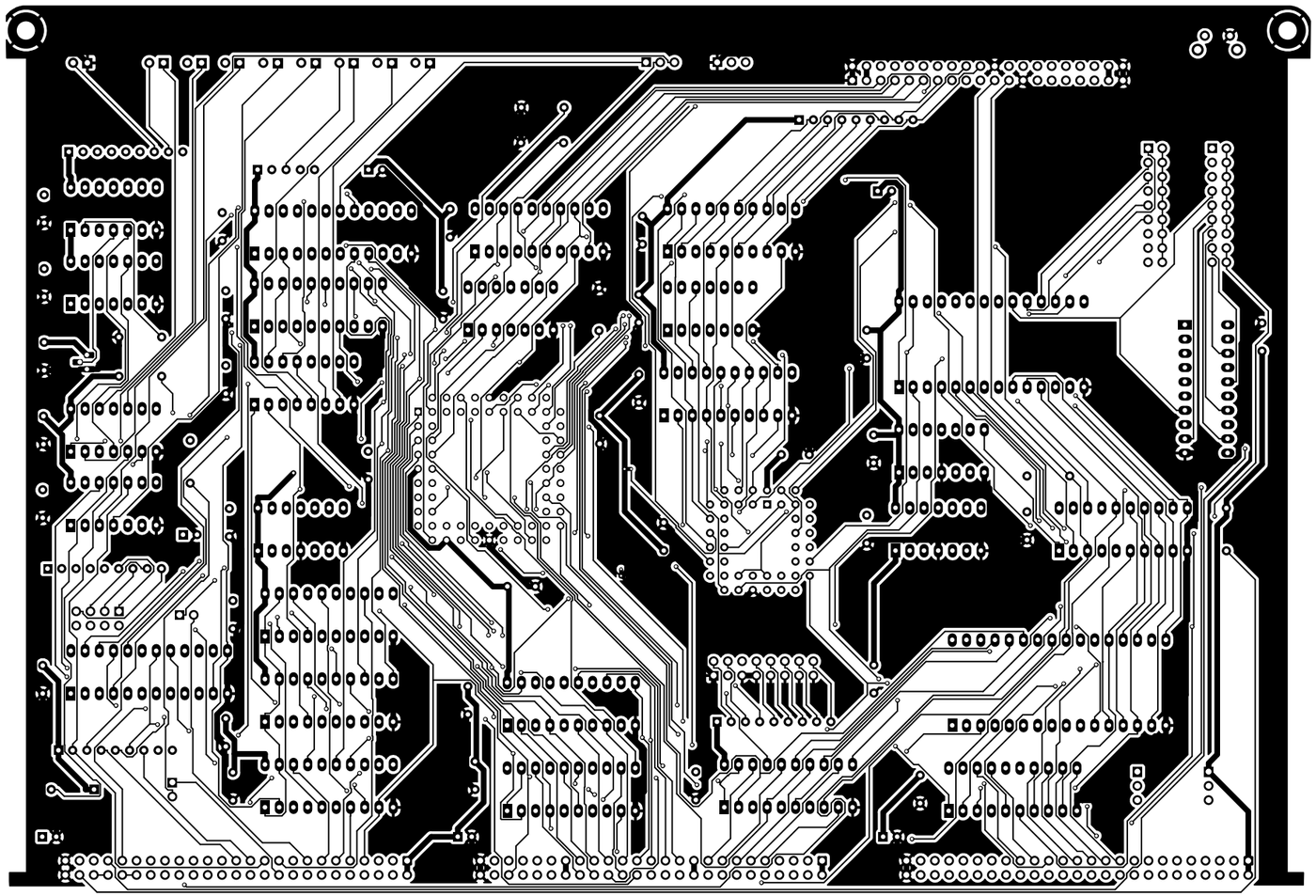


Sheet:
File: processor.80C188.kicad_pcb

Title: DUODYNE 80c188 PROCESSOR

Size: B	Date: 2024-09-14	**Rev: v0.7**
KiCad E.D.A. kicad (6.0.11)		Id: 1/1



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H1 MountingHole

LED D7 POWER

80C188 EN

LED D4 HALT

LED D2 RUN

LED D5 UART

D9 I/01

D8 I/02

D10 I/03

D11 I/04

D12

J2 80C188 RESET

J3 80C188 ONLY

J11 FPANEL

FRONT PANEL

GND RTS VCC RX TX CTS

SW1 RESET

H2 MountingHole

RN5 4700

R4 1K

R6 1K

RN3 470

RN6 470

PERIF188

C46 47uF

C29 22u

UART IRQ Selection

J7 J8

INT0 EIRQ0 EIRQ1 EIRQ2 EIRQ3 EIRQ4 EIRQ5 EIRQ6 EIRQ7

I2C IRQ Selection

U21 74LS74 C34

U25 GAL22V10 C33

U33 74LS374 C31

C36 U31 74LS244

U16 74LS08 C11

U3 74LS373 C39

U2 74LS32 C38

R7 1K C14

U23 74LS00

U24 74LS259 C22

U19 74LS373 C15

U5 8259 C17

U28 PCF CLOCK C24

U18 UART CLOCK C13

U27 PCF8584 C40

R3 10K

R2 10K

U7 74LS240 C35

BOARD GUIDES EXCLUSION ZONE

1KΩ R9

R1 10K

U13 DS1235 C3

U1 74LS04 C18

R5 10

U4 74LS07 C6

C32

C26 10u

U9 CPU CLOCK C12

RN1 4700

80C188 U12

NT3 NetTie_2

C2

C4

U20 0.1u

C23

NT4 NetTie_2

TL16C550CFN

C9

GENERATE CLOCK

BUS CPU JP4

JP1

U17 74LS244 C21

U15 GAL22V10 C19

STAT188

U6 74LS244

RN4 4700

C5

1N4148 D6

J4 RES SEL

U14 74LS244 C10

C25 10u

CONN_02X25

P1

IO Address Select

$96 J1

RN2 10KΩ

U22 74LS688 C37

C27 10u

U10 74LS244 C20

U8 74LS244 C8

P2

PROCESSOR.80C188, VERSION 0.8, AS OF 20 OCTOBER 2024

U26 29F040 C30

U11 74LS245 C16

C28 10u

P3

PATCH LEVEL 0_ 1_ 2_ 3_ 4_ 5_ 6_ 7_ 8_ 9_

J14 FLASH OR CMOS

J13 FLASH OR CMOS

BOARD GUIDES EXCLUSION ZONE

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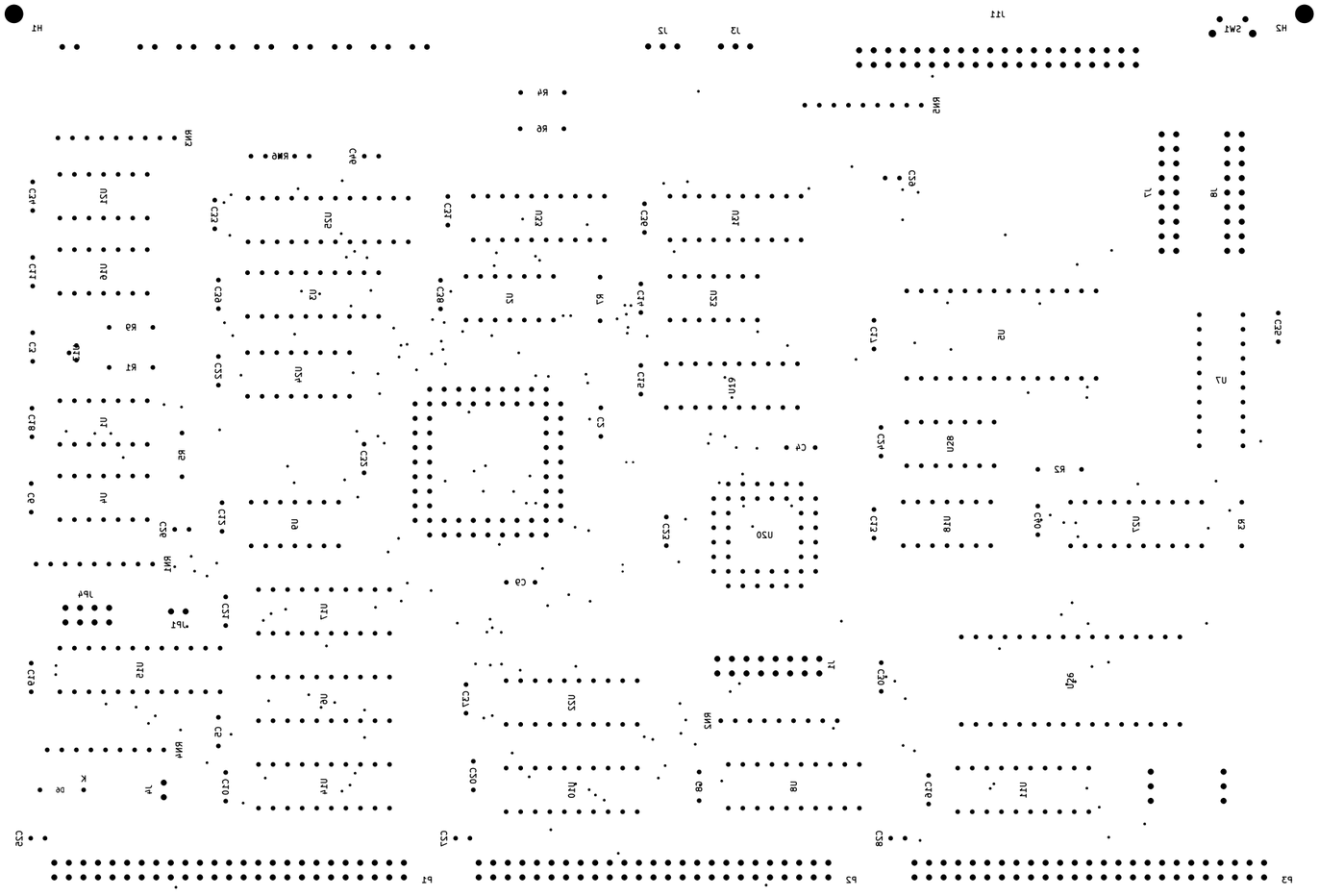

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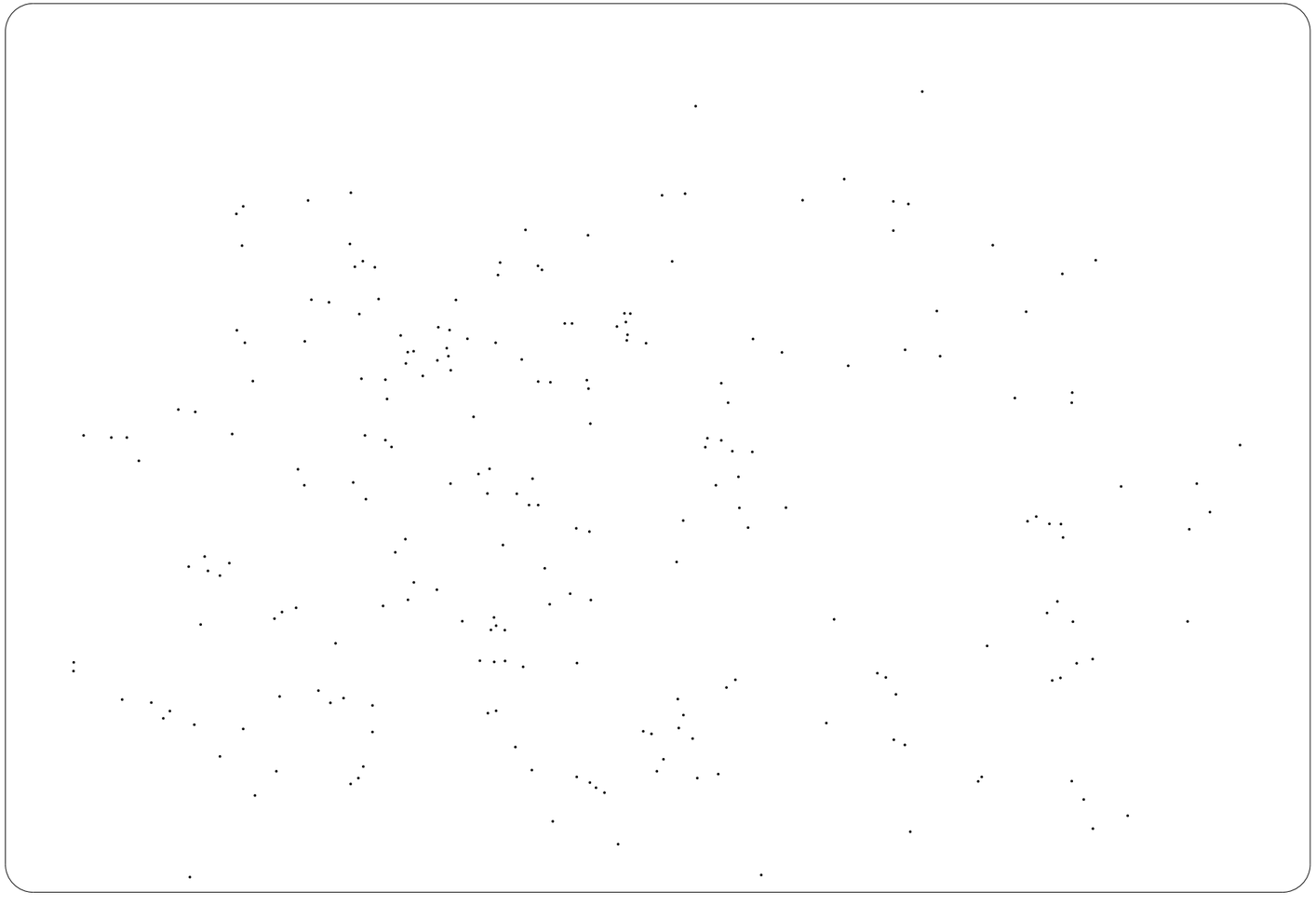

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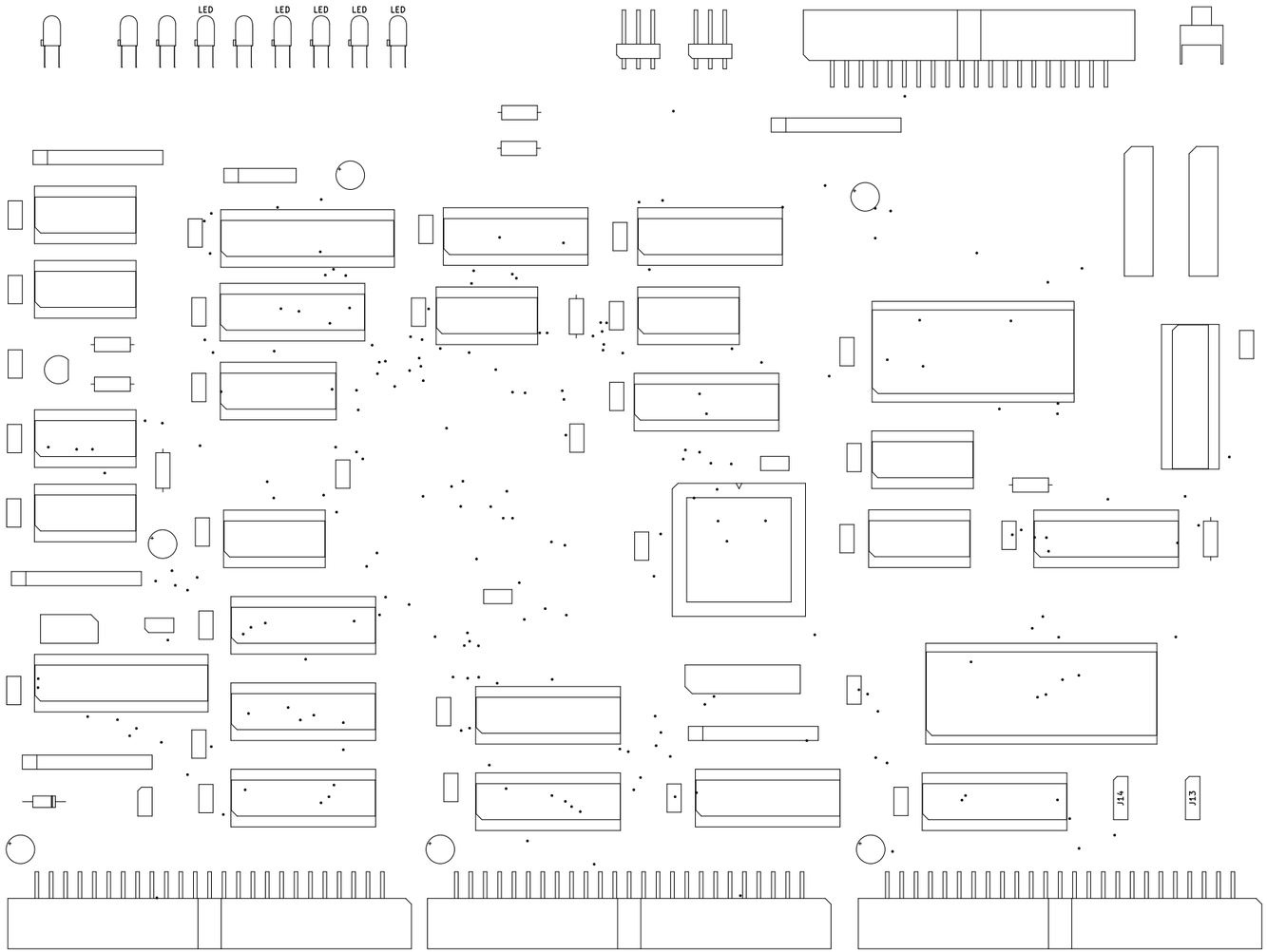

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