

12345678

bus

power

buffers

fpanel

File: bus.kicad_sch
6809CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 6809CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: processor.6809.kicad_sch

Title: Duodyne 6809 CPU board

Size: B

Date: 2024-04-20

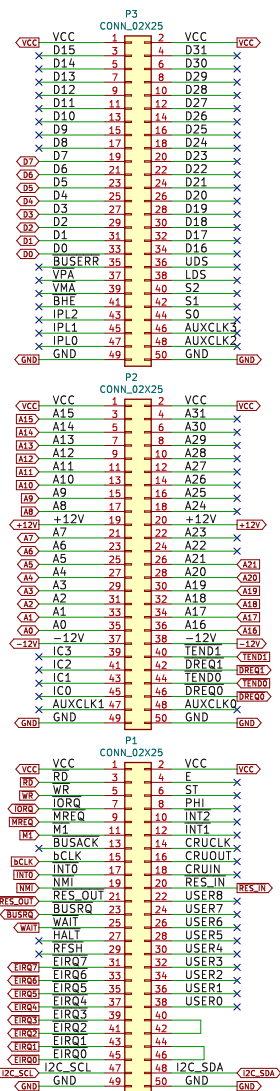
Rev: V1.1

KiCad E.D.A.

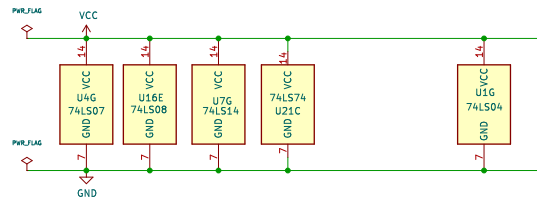
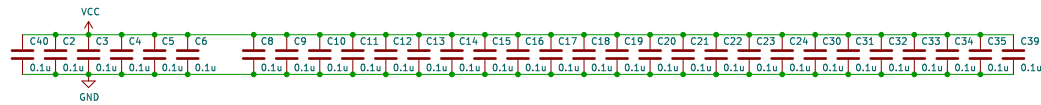
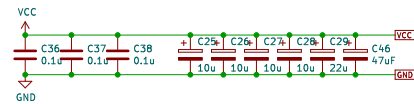
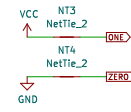
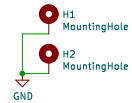
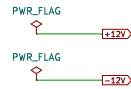
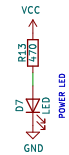
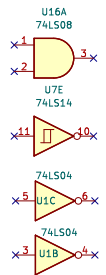
kicad (6.0.11)

Id: 1/9

12345678



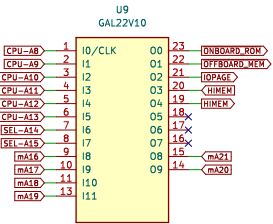
SPARE GATES



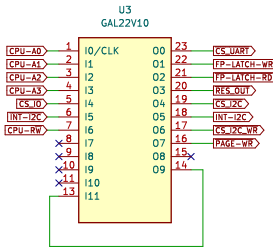
IO Address Port \$50-\$5F

\$50 PAGER BANK 0 (WR)
\$51 PAGER BANK 1 (WR)
\$52 PAGER BANK 2 (WR)
\$53 PAGER BANK 3 (WR)
\$54 Front Panel (RW)
\$55 RESET SYSTEM
\$56-\$57 I2C (RW)
\$58-\$5F UART (RW)

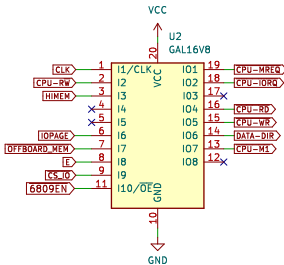
ADDRESS DECODER

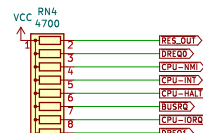
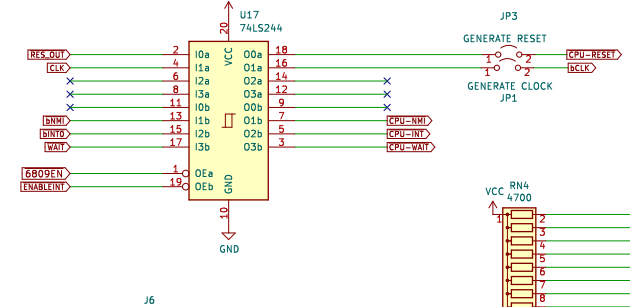
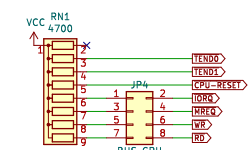
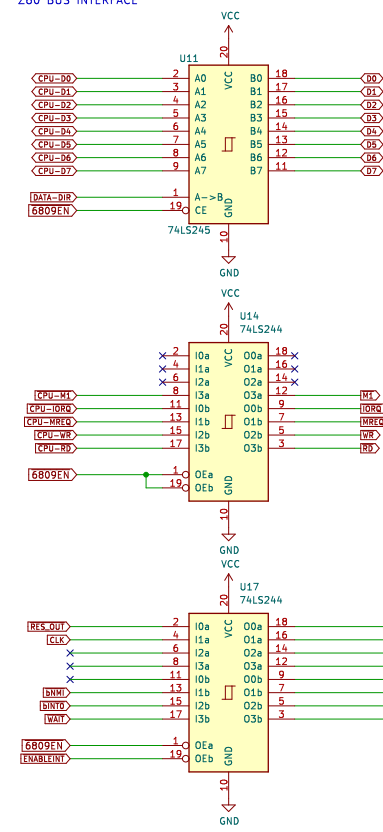
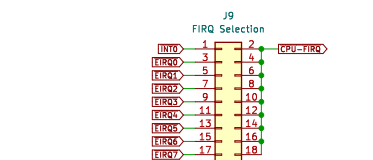


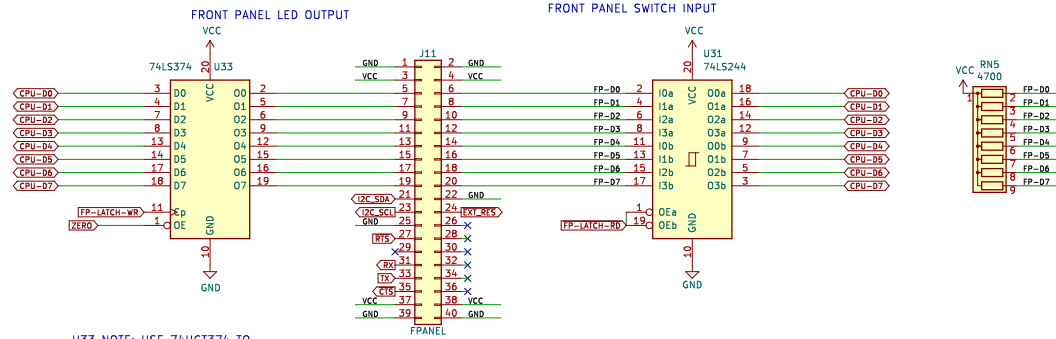
IO DECODER

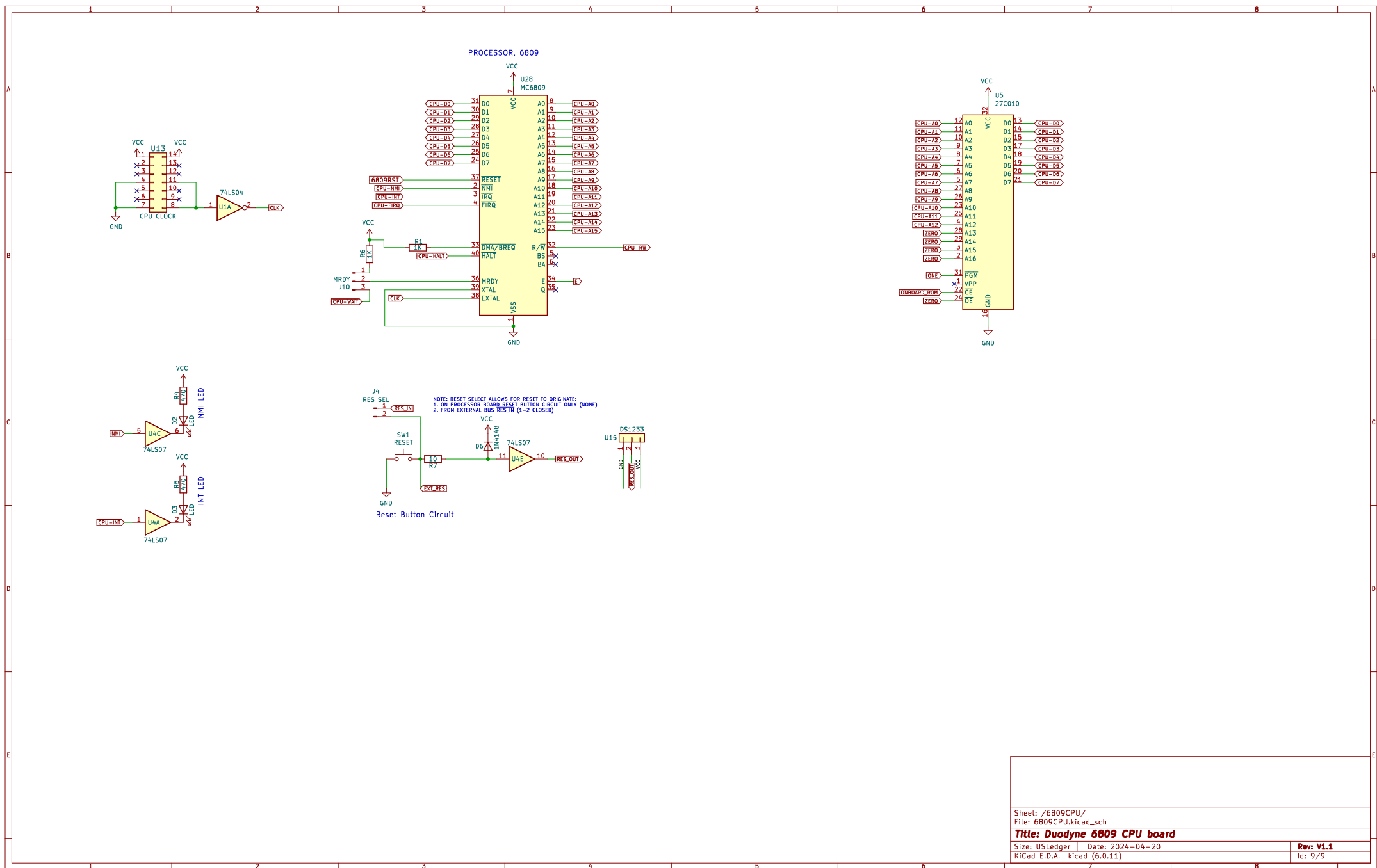


BUS SIGNALS

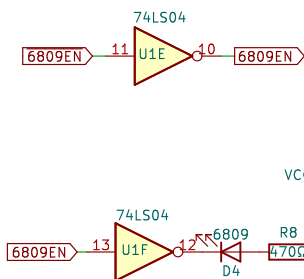
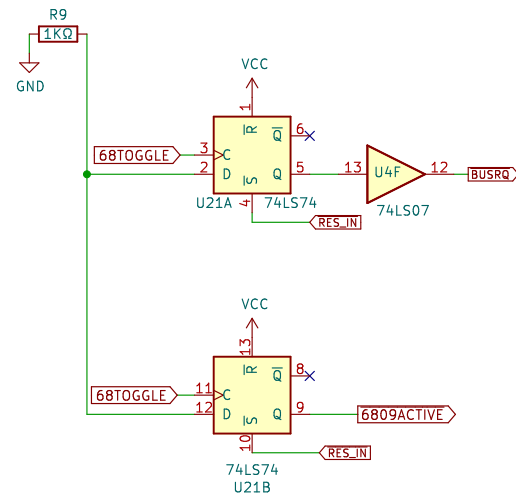
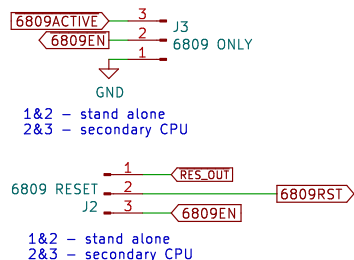
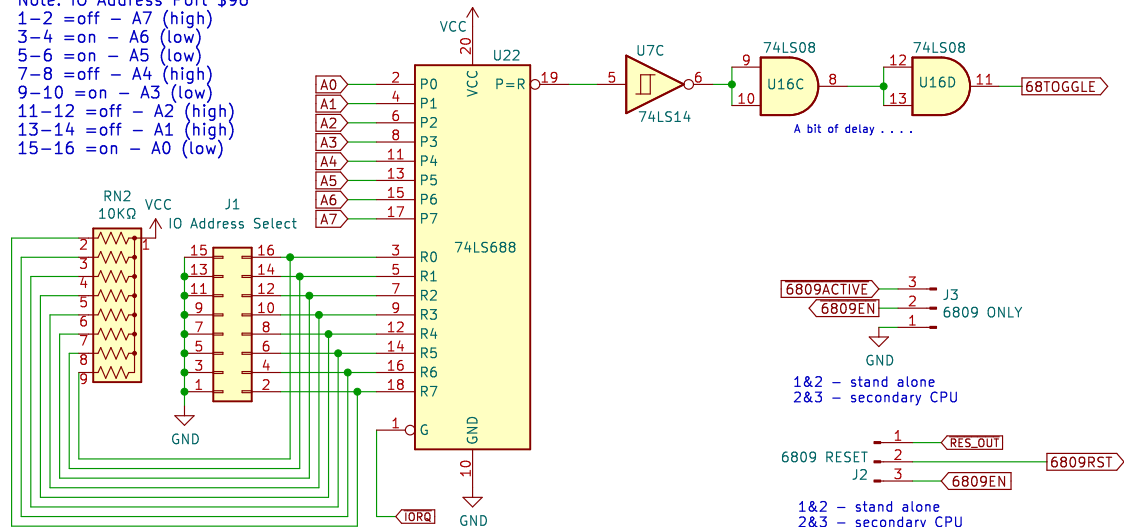








Note: IO Address Port \$96
 1-2 = off - A7 (high)
 3-4 = on - A6 (low)
 5-6 = on - A5 (low)
 7-8 = off - A4 (high)
 9-10 = on - A3 (low)
 11-12 = off - A2 (high)
 13-14 = off - A1 (high)
 15-16 = on - A0 (low)



Sheet: /bus sharing/
 File: bussharing.kicad_sch

Title: Duodyne 6809 CPU board

Size: A4 Date: 2024-04-20

KiCad E.D.A. kicad (6.0.11)

Rev: V1.1

Id: 9/9

