

1

2

3

4

5

6

7

8

bus

PPIDE

GAL

IO

power

FDC

buffers

WIZNET

File: bus.kicad\_sch

File: PPIDE.kicad\_sch

File: GAL.kicad\_sch

File: IO.kicad\_sch

File: power.kicad\_sch

File: FDC.kicad\_sch

File: buffers.kicad\_sch

File: WIZNET.kicad\_sch

SPARE SOCKETS  
FOR DEBUGGING

J1  
DIL14

1

2

3

4

5

6

7

14

13

12

11

10

9

8

J2  
DIL16

1

2

3

4

5

6

7

8

16

15

14

13

12

11

10

9

J3  
DIL20

1

2

3

4

5

6

7

8

9

10

20

19

18

17

16

15

14

13

12

11

J4  
DIL14

1

2

3

4

5

6

7

14

13

12

11

10

9

8

J5  
DIL16

1

2

3

4

5

6

7

8

16

15

14

13

12

11

10

9

J6  
DIL20

1

2

3

4

5

6

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8

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10

20

19

18

17

16

15

14

13

12

11

Sheet: /  
File: input-output.Disk.kicad\_sch

Title:

Size: B

Date:

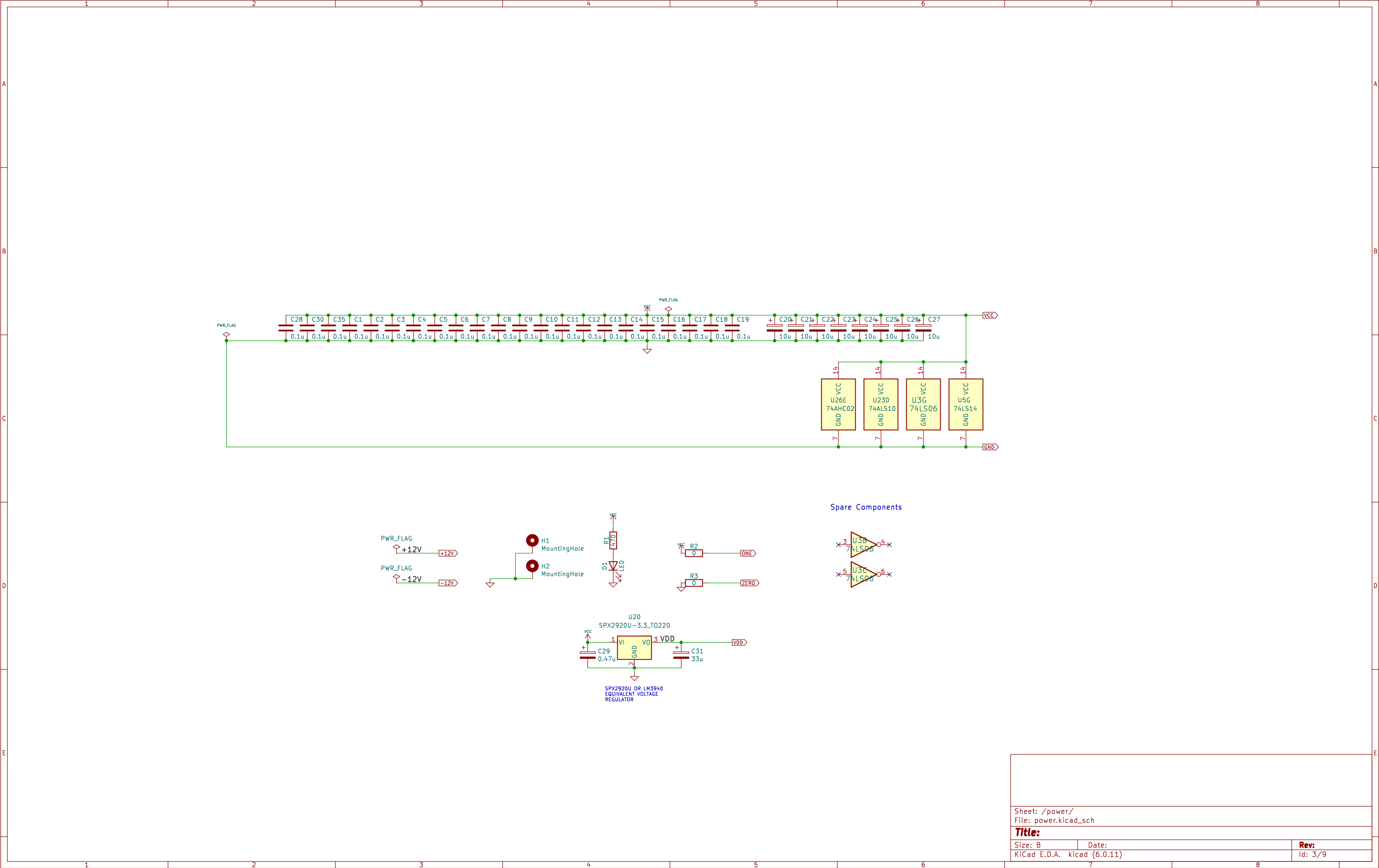
KiCad E.D.A.   kicad (6.0.11)

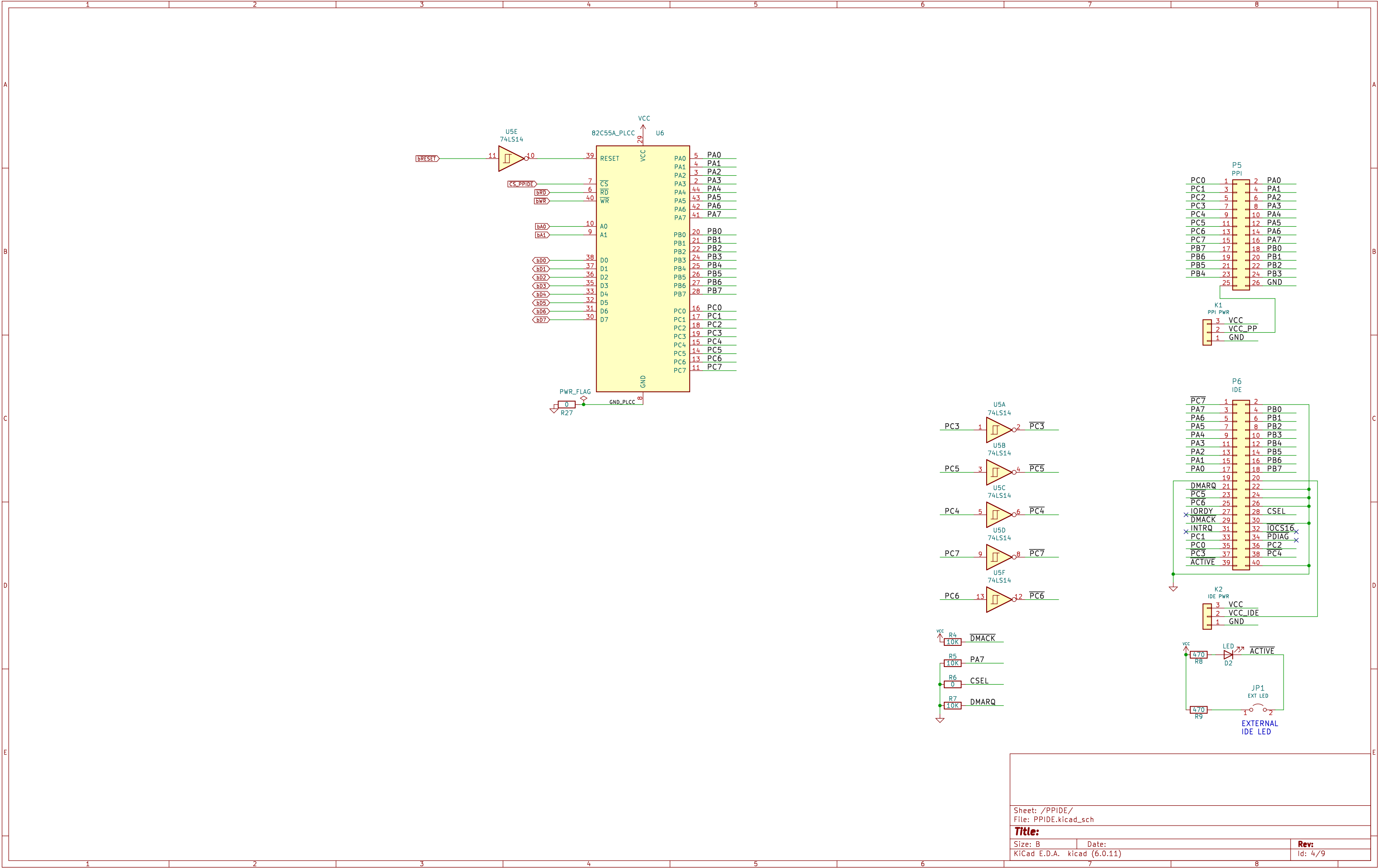
Rev:

Id: 1/9



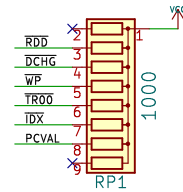
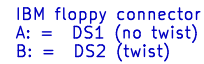
|                             |       |         |
|-----------------------------|-------|---------|
| Size: B                     | Date: | Rev:    |
| KiCad E.D.A. kicad (6.0.11) |       | Id: 2/9 |



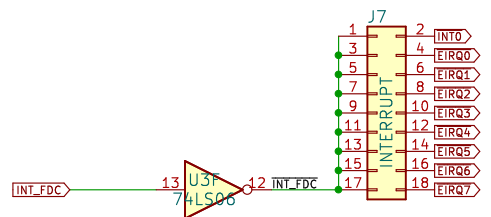


IO Ports

- x0 read Main Status Register
- x1 read/write Data In/Out
- x2 read/write Terminal Count
- x3 read/write User Reset FDC
- x4 <none>
- x5 write Load Control Register
- x6 read DMA Acknowledge/write Load Operations Register
- x7 read DMA Acknowledge and Terminal Count

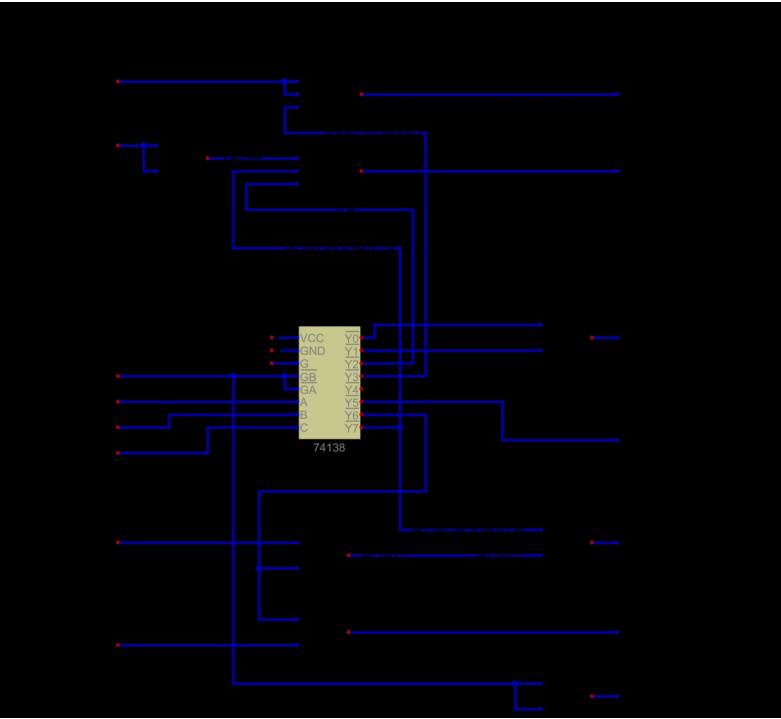
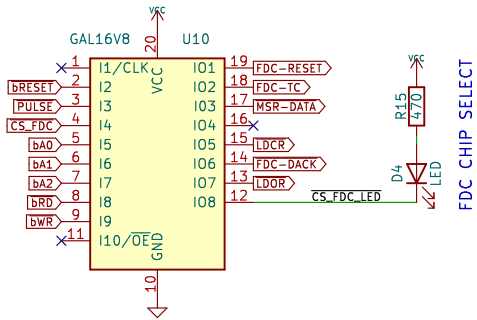


NOTE: REPLACE RP1 WITH 150 OHM  
9 PIN SIP RESISTOR PACK FOR  
LEGACY FLOPPY DRIVES; 1000 OHM  
FOR 3.5" IBM PC 1.44MB COMPATIBLE

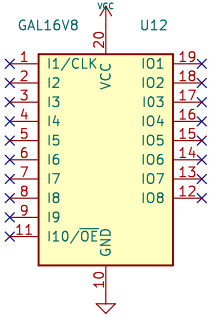


NOTE: RELIES ON 4700 OHM PULL UP  
RESISTOR ON Z80 PROCESSOR BOARD

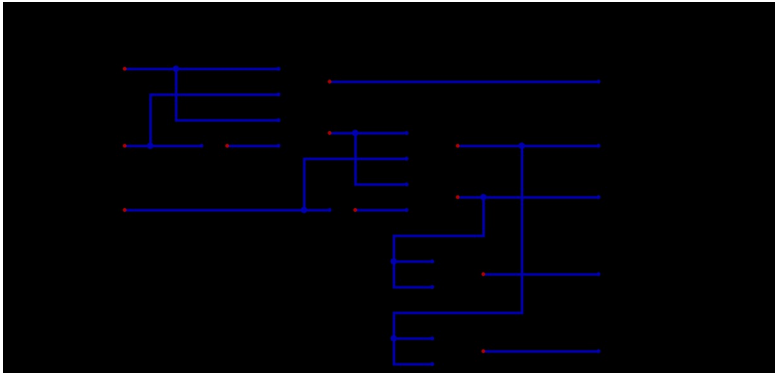
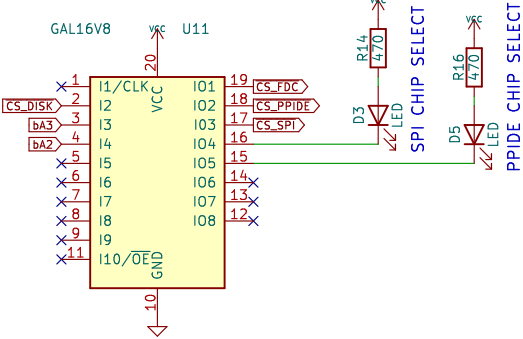
FDC DMA GAL16V8

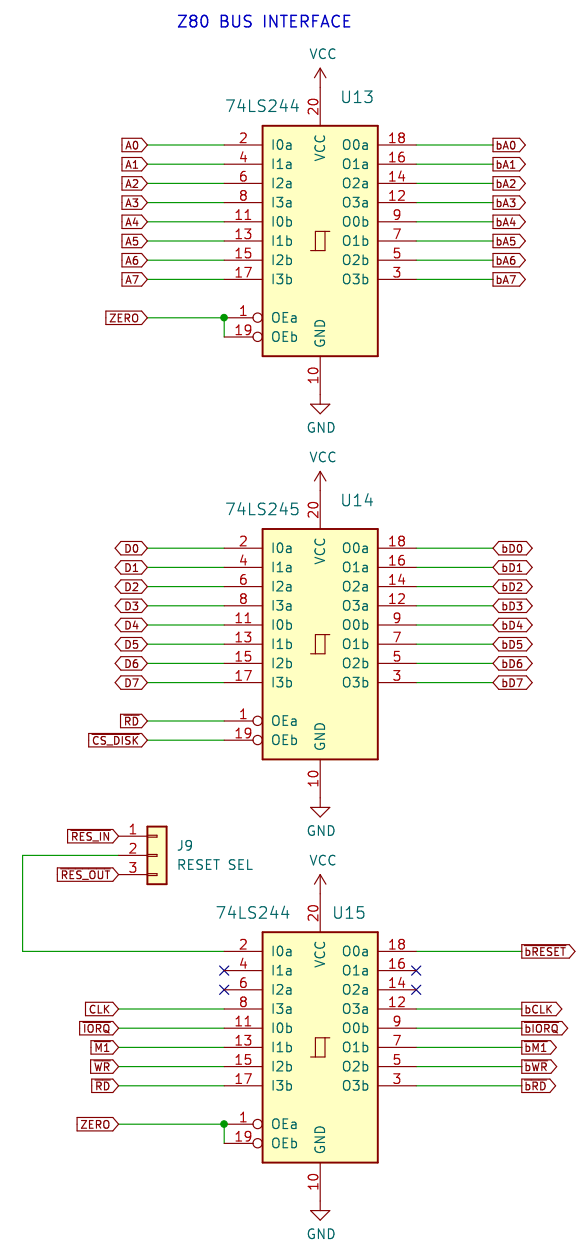


SPARE GAL FOR DEBUGGING

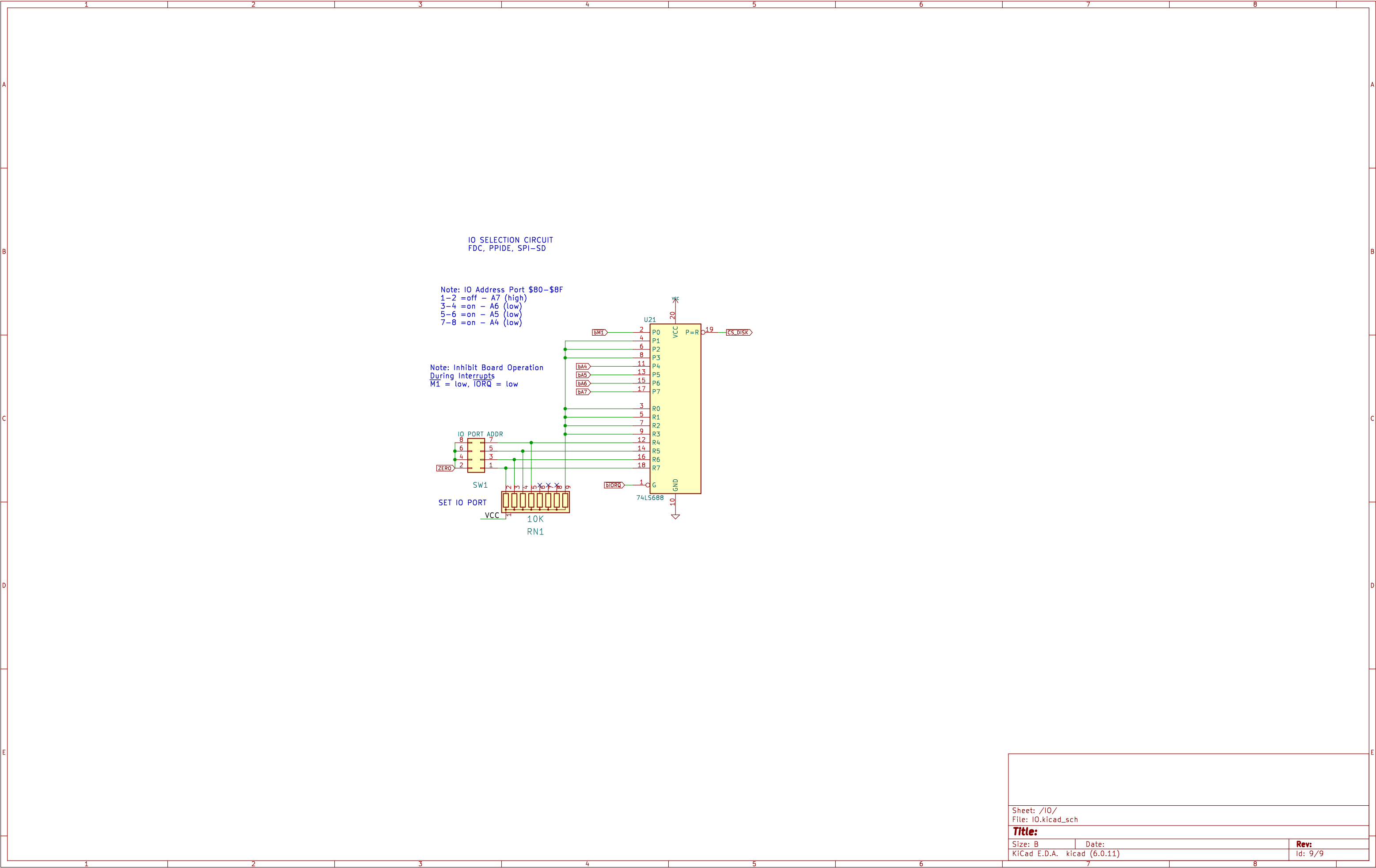


DISK CS GAL16V8

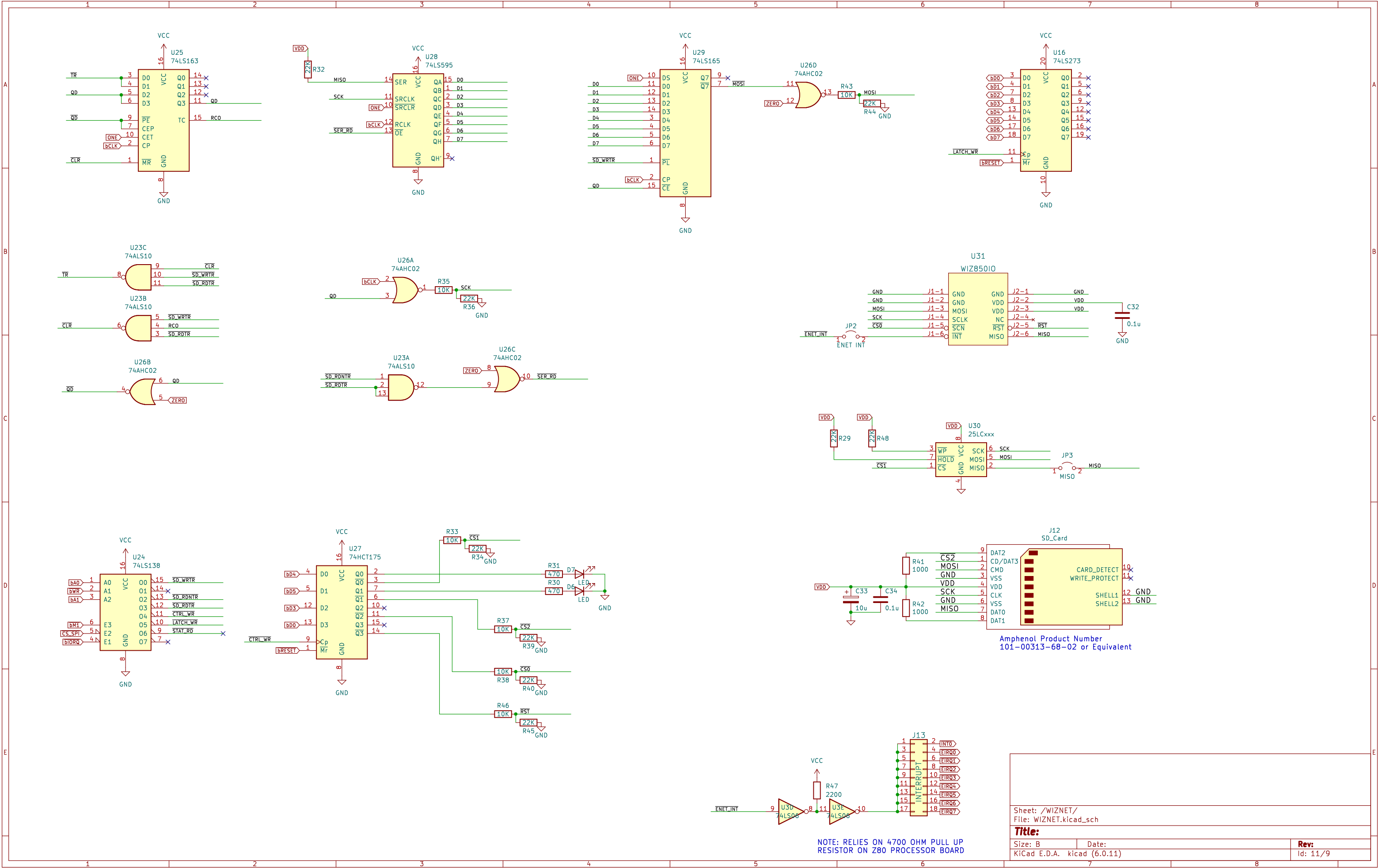




Note: Buffers and Transceivers respond to IO and MEM cycles







NOTE: RELIES ON 4700 OHM PULL UP RESISTOR ON Z80 PROCESSOR BOARD