

12345678

A

Sheet: bus

Sheet: power

Sheet: buffers

Sheet: fpanel

Sheet: Z80CPU

B

File: bus.sch

File: power.sch

File: buffers.sch

File: fpanel.sch

File: Z80CPU.sch

Sheet: DMA

Sheet: GALS

Sheet: IM2

Sheet: waitstate

Sheet: mapper

C

File: DMA.sch

File: GALS.sch

File: IM2.sch

File: waitstate.sch

File: mapper.sch

D

E

12345678

SPARE SOCKETS
FOR DEBUGGING

J5
DIL14

J6
DIL14

J7
DIL16

J8
DIL16

J9
DIL20

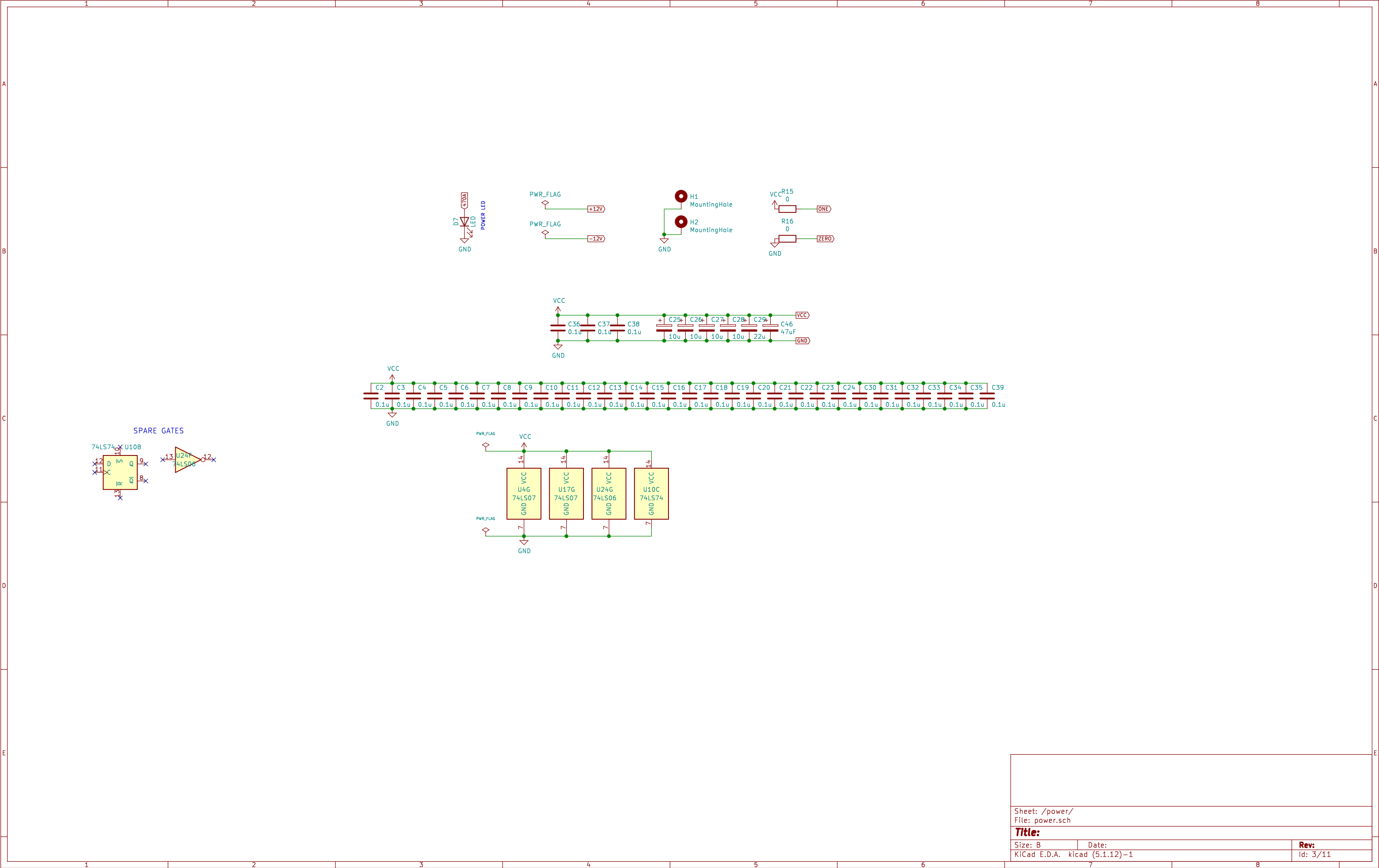
J10
DIL20

Sheet: /
File: processor.Z80.sch

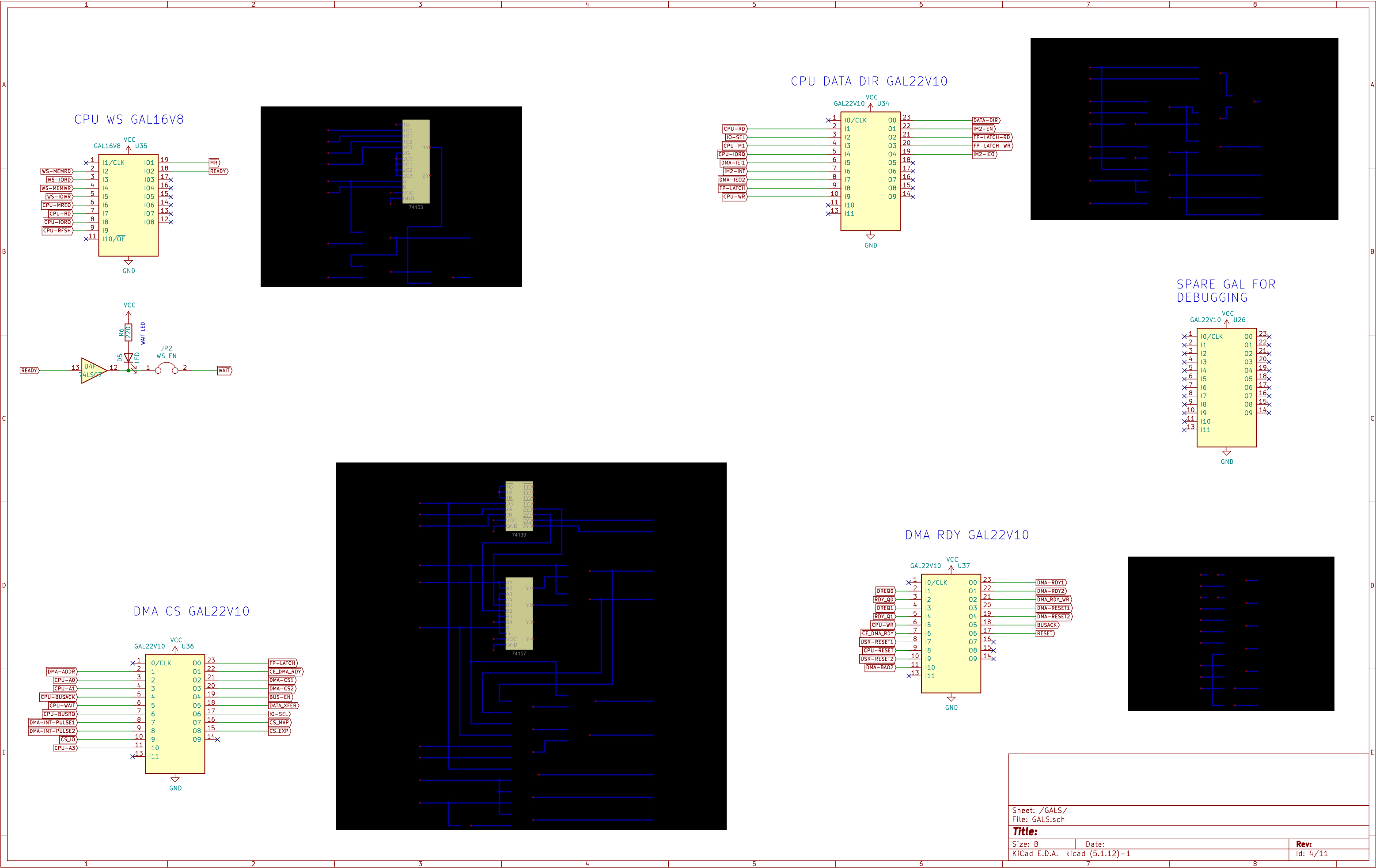
Title:

Size: BDate:KiCad E.D.A. kicad (5.1.12)–1

Rev:Id: 1/11

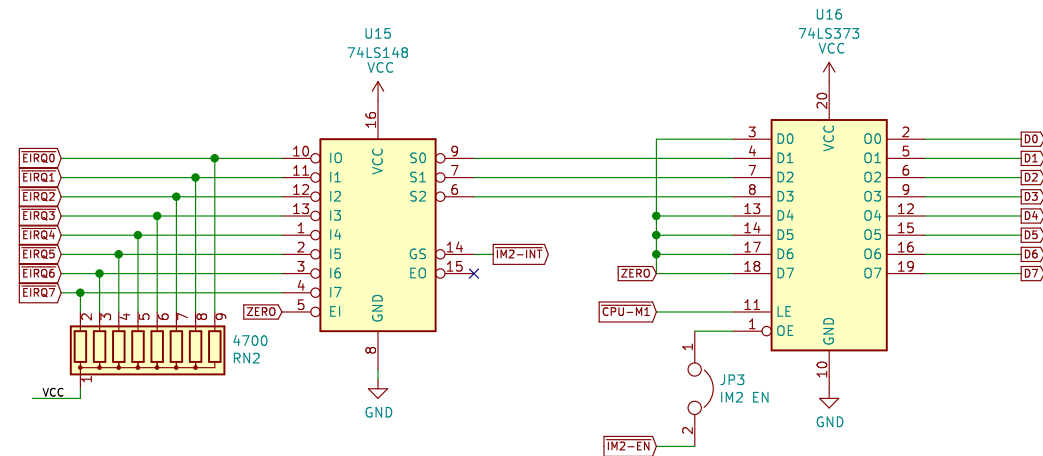


Sheet: /power/ File: power.sch		
Title:		
Size: B	Date:	Rev:
KiCad E.D.A. kicad (5.1.12)-1		Id: 3/11



d: 5/11

Interrupt Mode 2 (IM2) Circuit



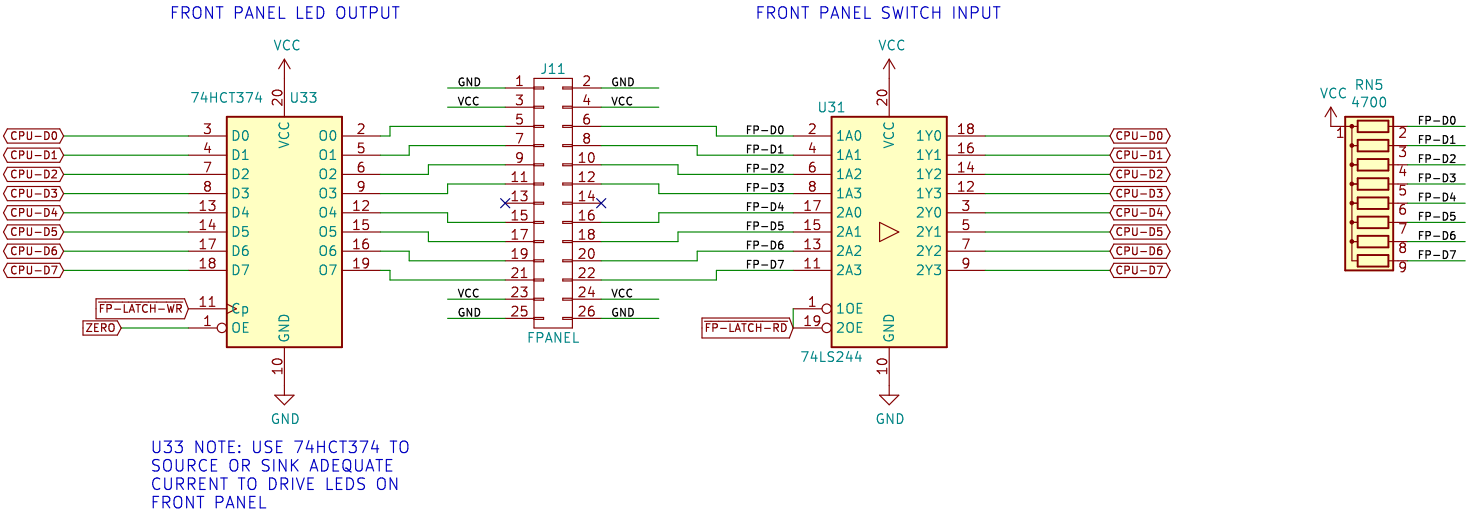
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File: IM2.sch

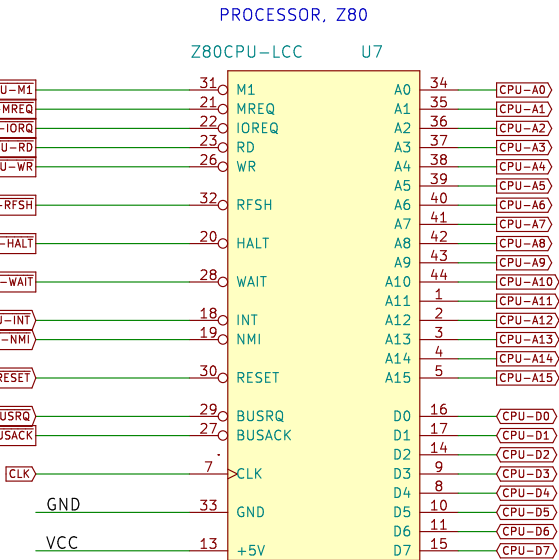
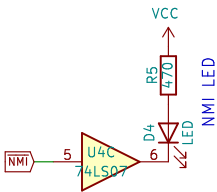
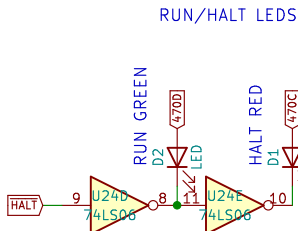
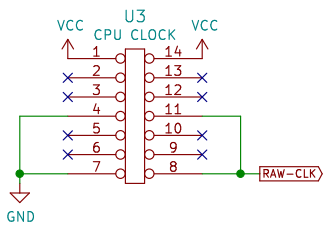
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Size: B

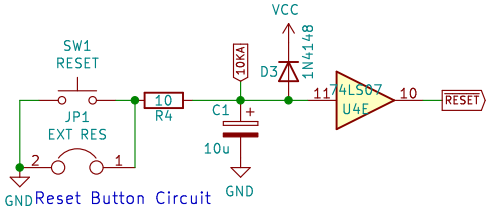
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KiCad E.D.A. kicad (5.1.12)-1	

Rev: 6/11





Tri-state address, data, & control bus during DMA



Sheet: /Z80CPU/
File: Z80CPU.sch

Title:

Size: B

Date:

KiCad E.D.A. kicad (5.1.12)-1

Rev:

Id: 9/11

