

12345678

A

B

C

D

E

bus

power

buffers

fpanel

File: bus.kicad_sch
65816CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 65816CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

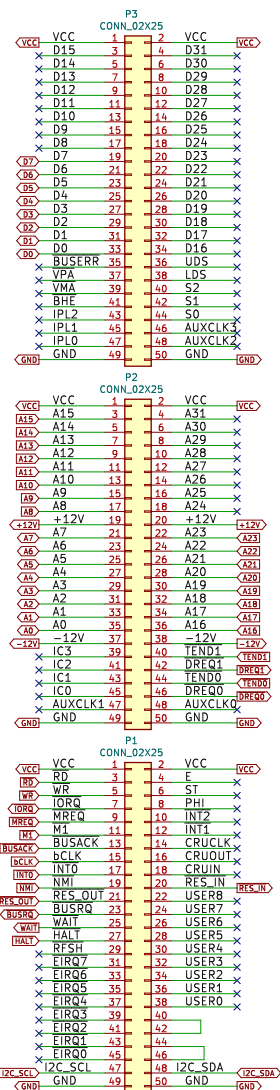
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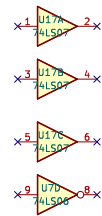
Sheet: /
File: processor.65816.kicad_sch

Title: DuoDyne 65816 CPU board

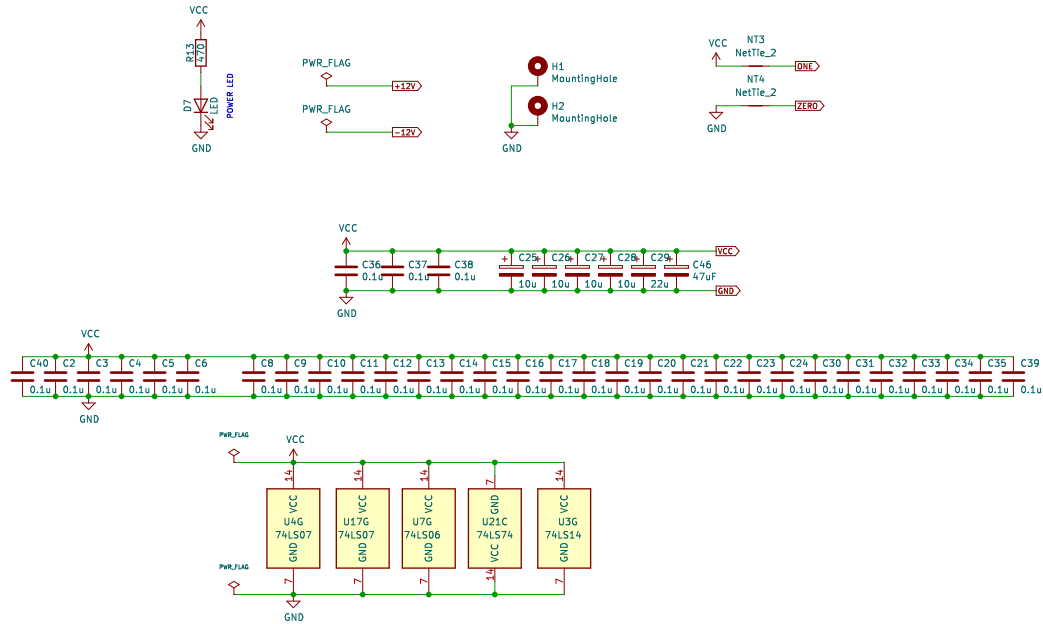
Size: B | Date: 2023-09-21 | Rev: V0.75

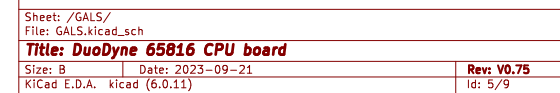
KiCad E.D.A. | kicad (6.0.11) | Id: 1/9

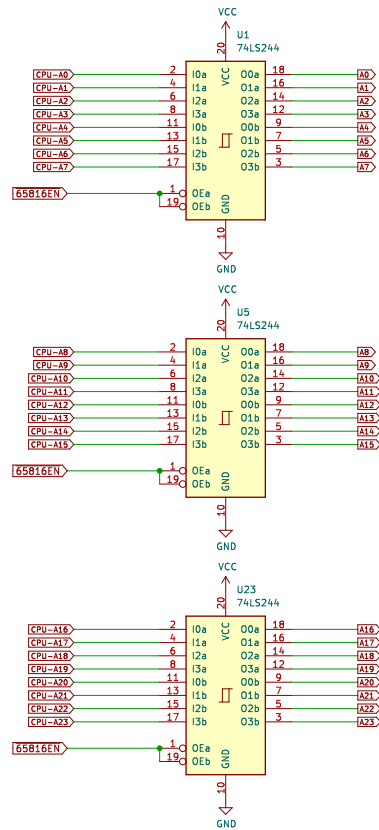




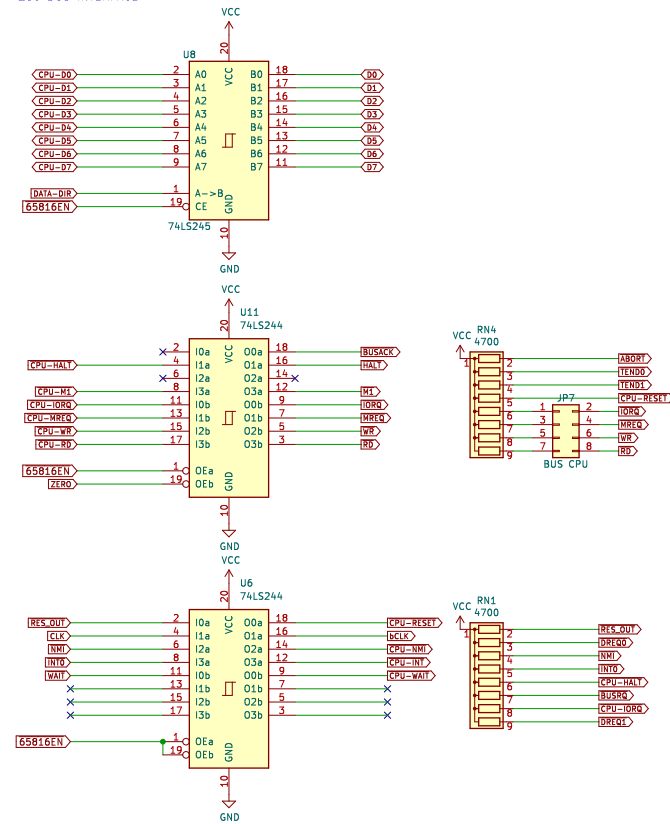
SPARE GATES





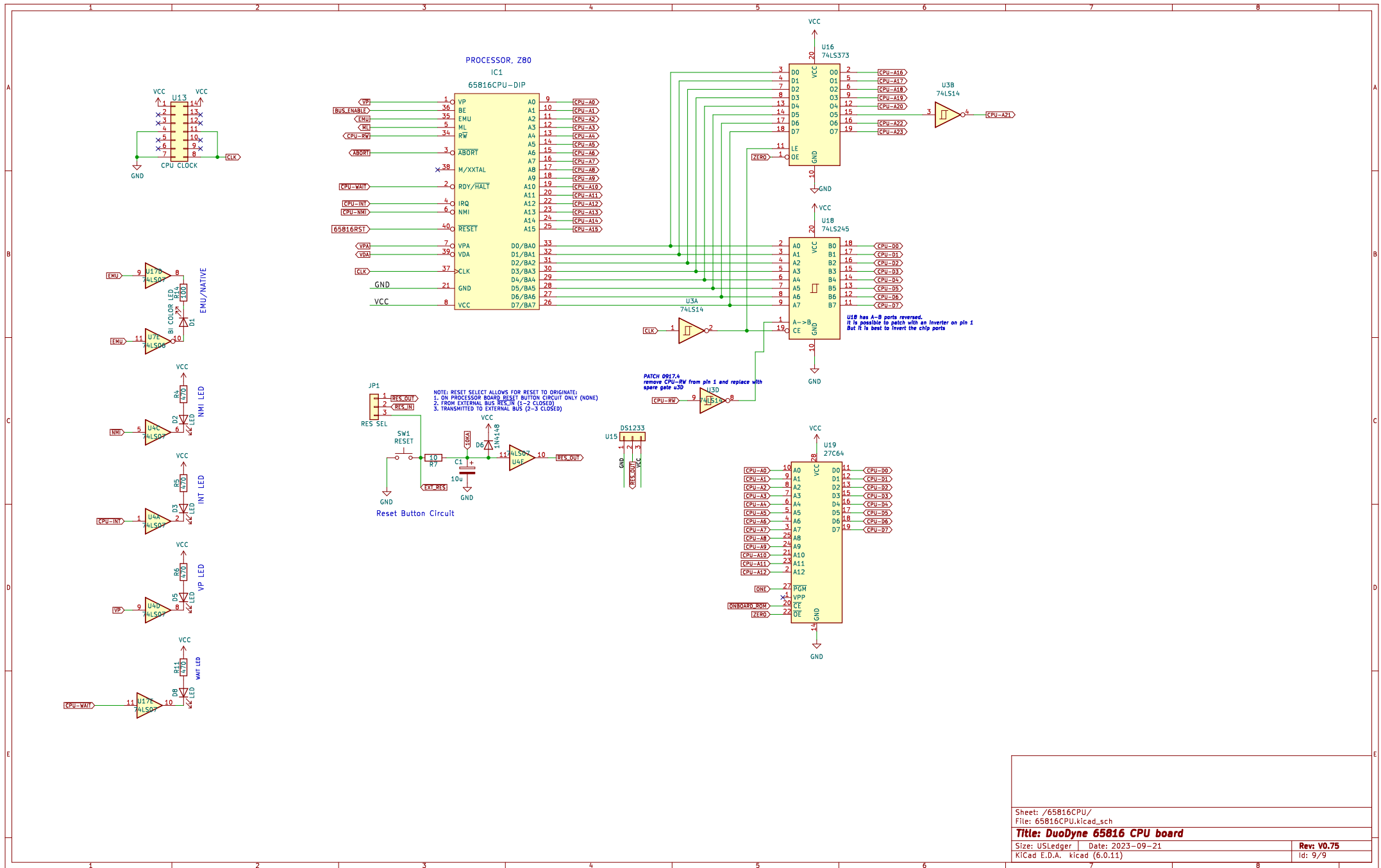


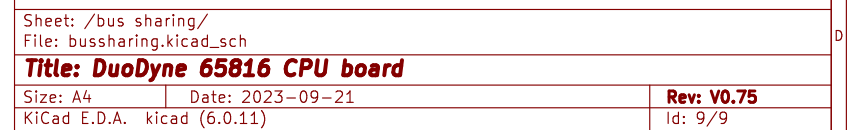
Z80 BUS INTERFACE





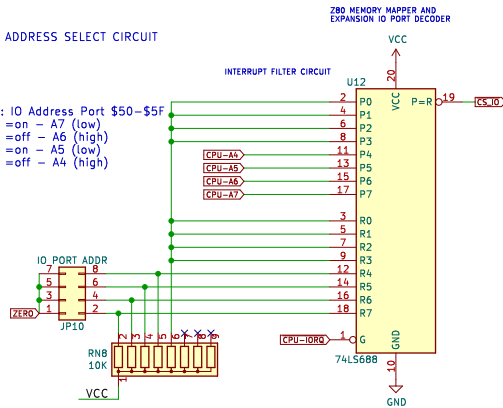
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File: fpanel.kicad_sch		
Title: DuoDyne 65816 CPU board		
Size: B	Date: 2023-09-21	Rev: V0.75
KiCad E.D.A.	kicad (6.0.11)	Id: 8/9



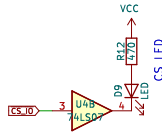


IO ADDRESS SELECT CIRCUIT

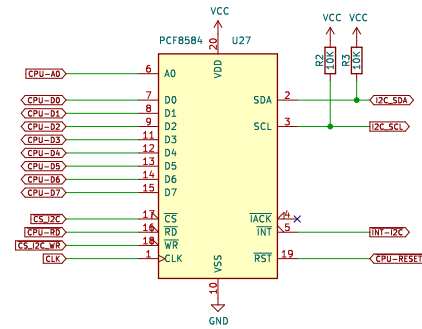
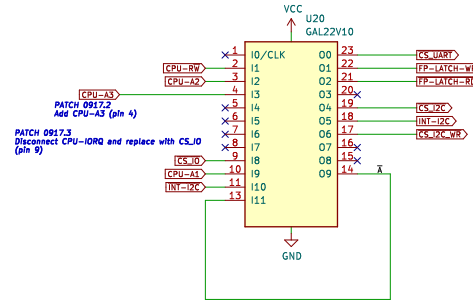
Note: IO Address Port \$50-\$5F
1-2 = on - A7 (low)
3-4 = off - A6 (high)
5-6 = on - A5 (low)
7-8 = off - A4 (high)



INTERRUPT FILTER CIRCUIT

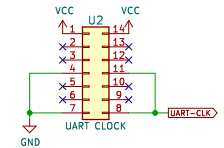


PATCH 0917.2
Add CPU-AS (pin 4)
PATCH 0917.3
Disconnect CPU-IO and replace with CS-IO
(pin 9)



NOTE: INTERRUPTS RELY ON 4700 OHM PULL UP RESISTOR ON PROCESSOR

1.8432 OR 7.3728 MHz
TTL OSCILLATOR



TL16C550CFN U14

