

File: power.kicad_sch



File: buffers.kicad_sch



File: dualPIO.kicad_sch



File: CTC-INT.kicad_sch



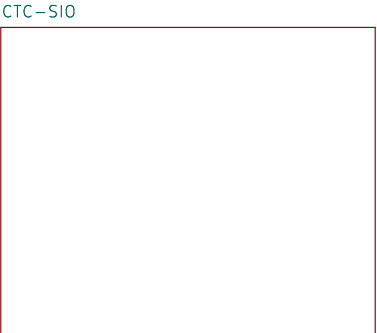
File: bus.kicad_sch



File: IO.kicad_sch

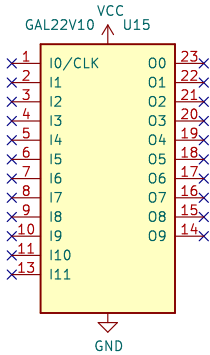


File: GAL.kicad_sch



File: CTC-SIO.kicad_sch

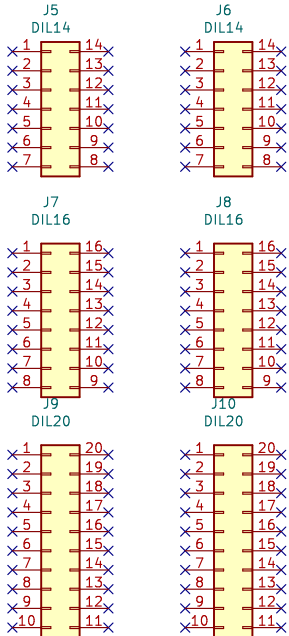
SPARE GAL FOR
DEBUGGING

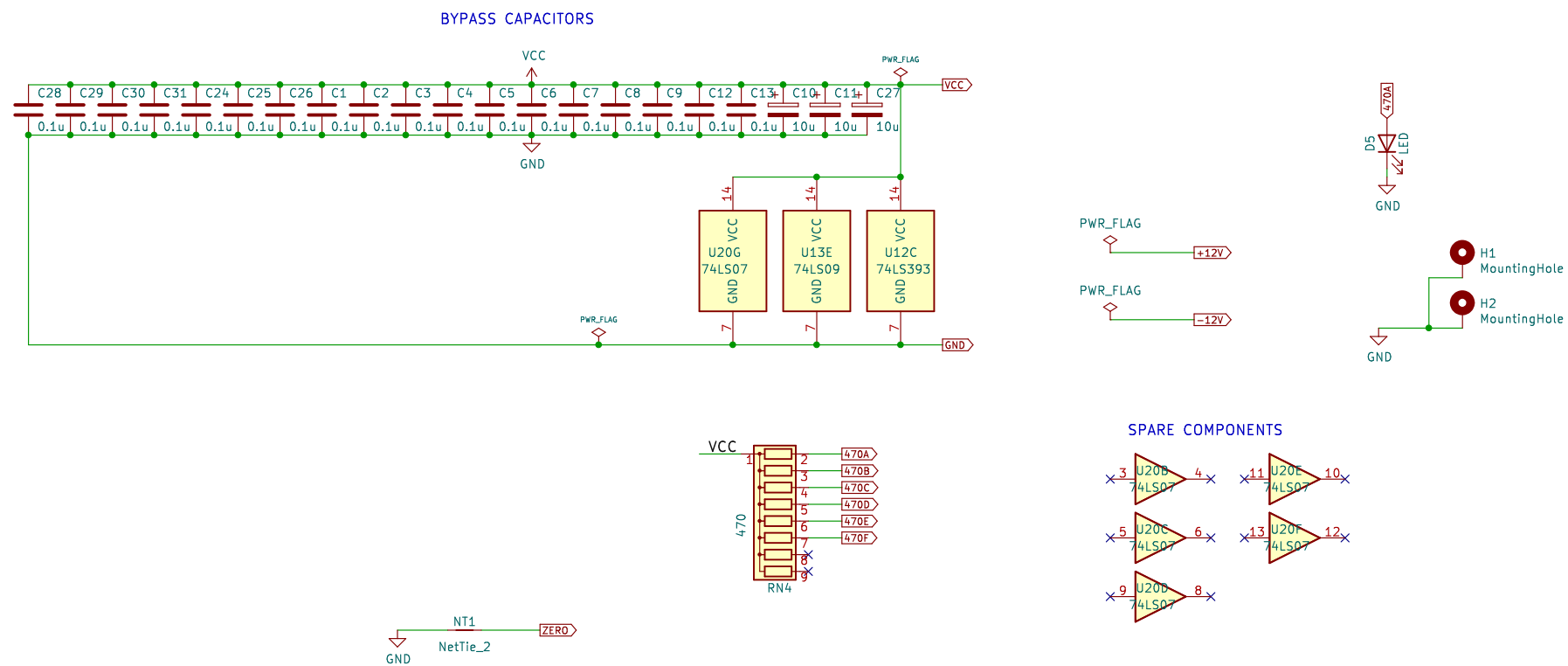


THROUGH HOLES FOR
PATCH WIRE ROUTING
ACROSS SIDES

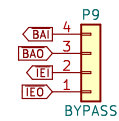
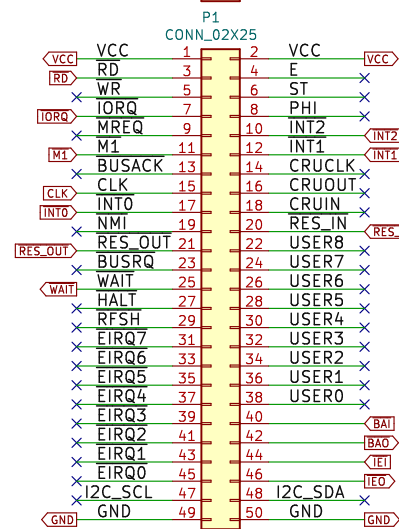
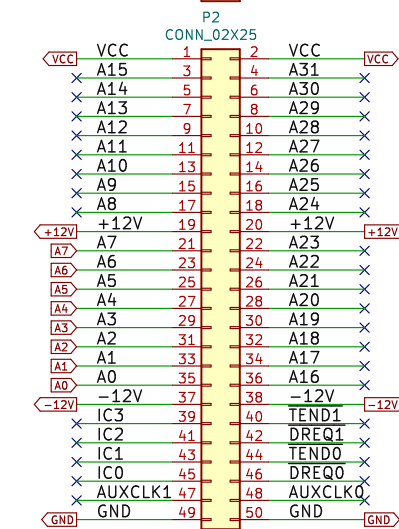
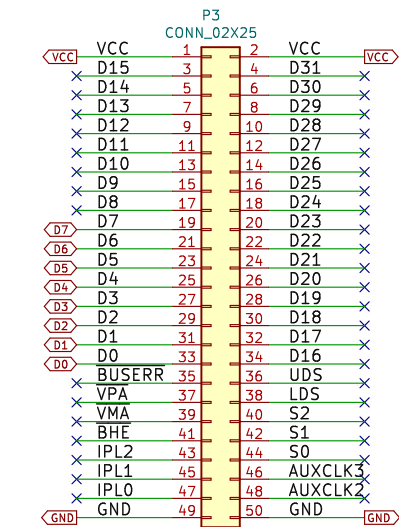
- H3 MountingHole
- H4 MountingHole

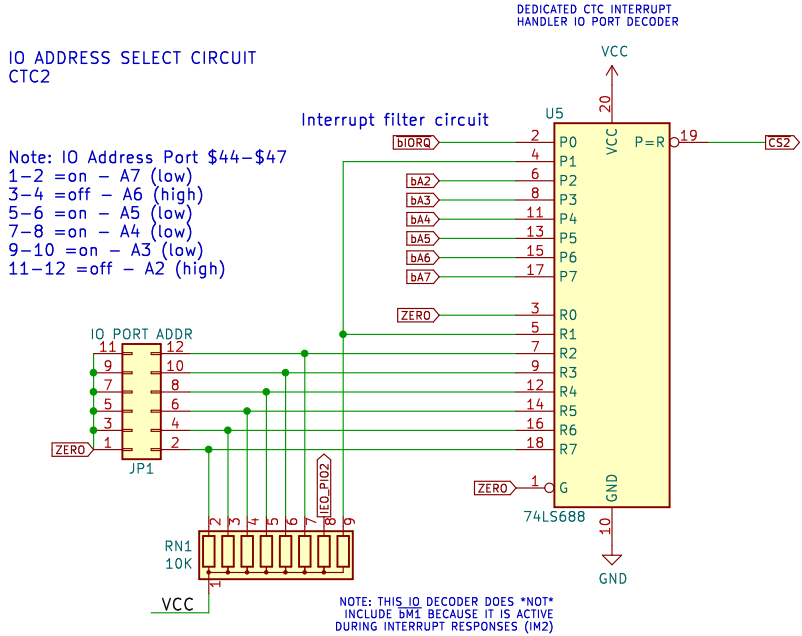
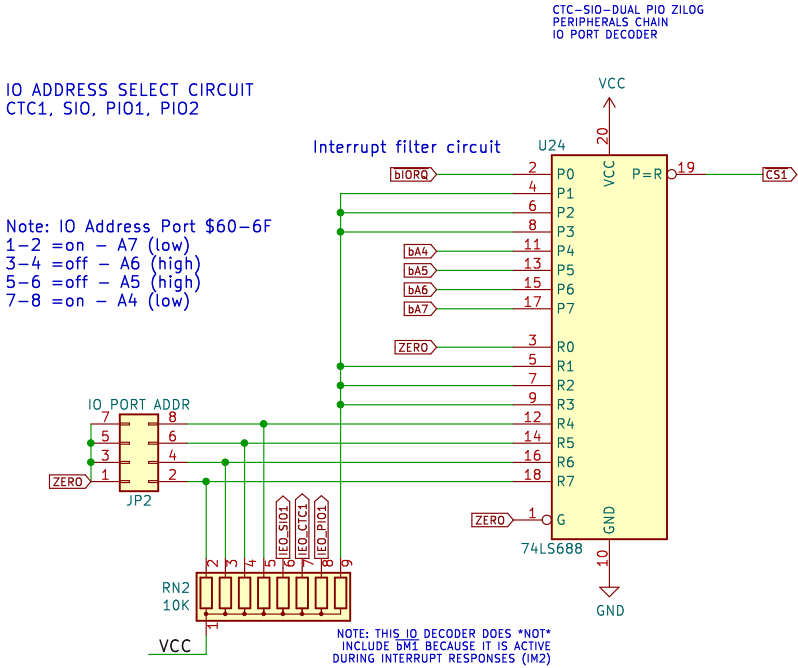
SPARE SOCKETS
FOR DEBUGGING





Sheet: /power/		
File: power.kicad_sch		
Title:		
Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 2/9





Sheet: /IO/
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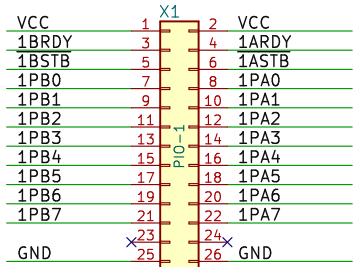
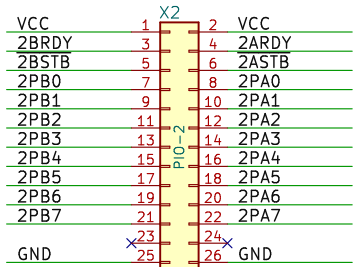
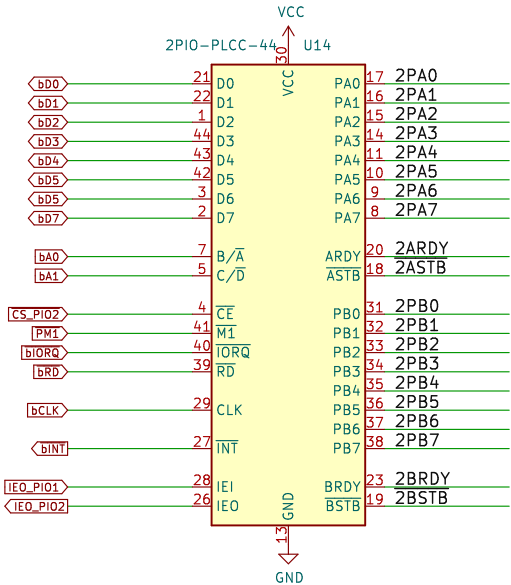
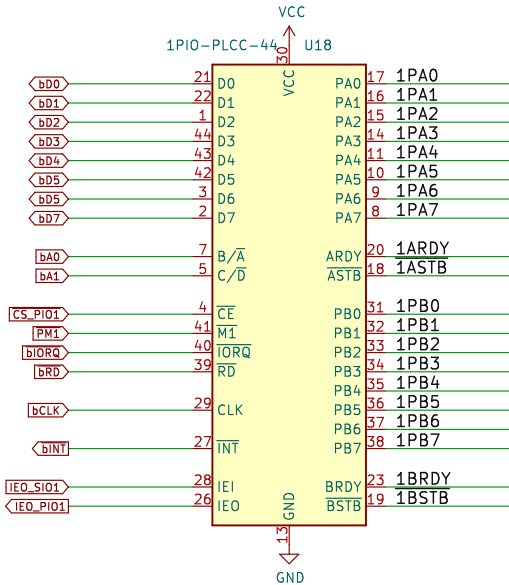
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Date:

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Rev:

Id: 5/9



Sheet: /dualPIO/
File: dualPIO.kicad_sch

Title:

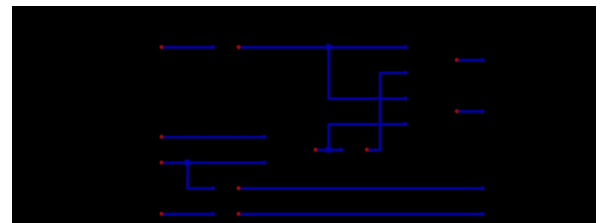
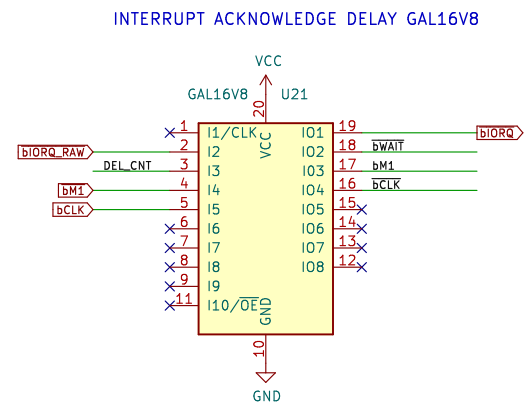
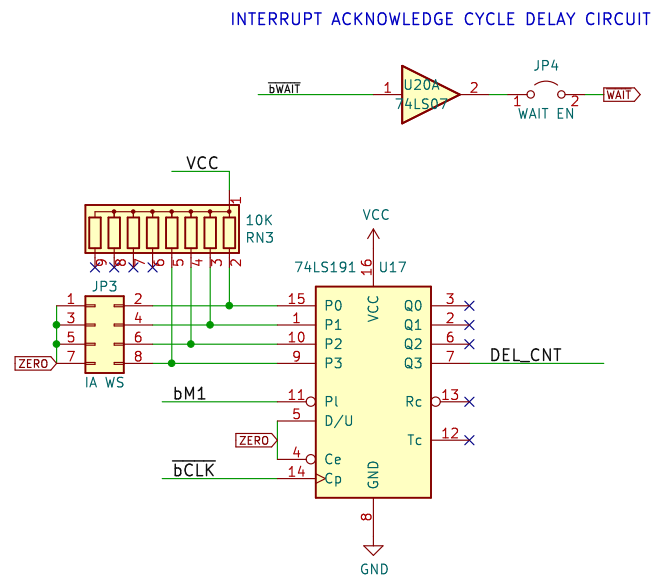
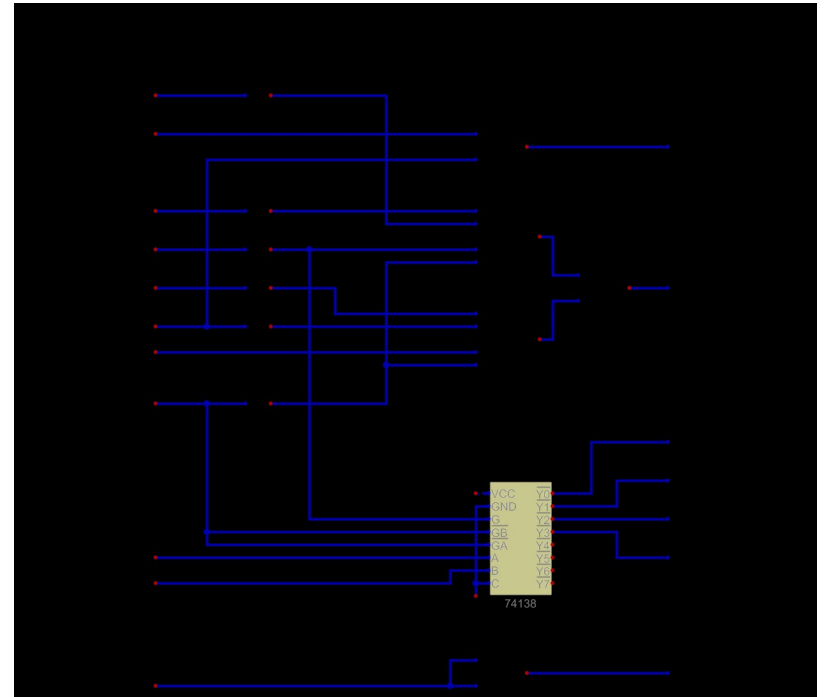
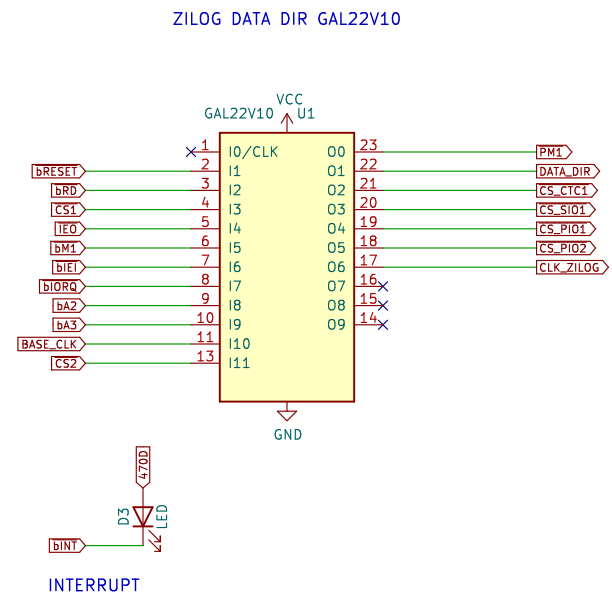
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Date:

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Rev:

Id: 6/9



NOTE: WAIT RELIES ON 4700 OHM
PULL UP RESISTOR ON PROCESSOR
BOARD

