

By Dan Werner

Sheet: /bus/
File: bus.kicad_sch

Title: Dual ESP32 IO

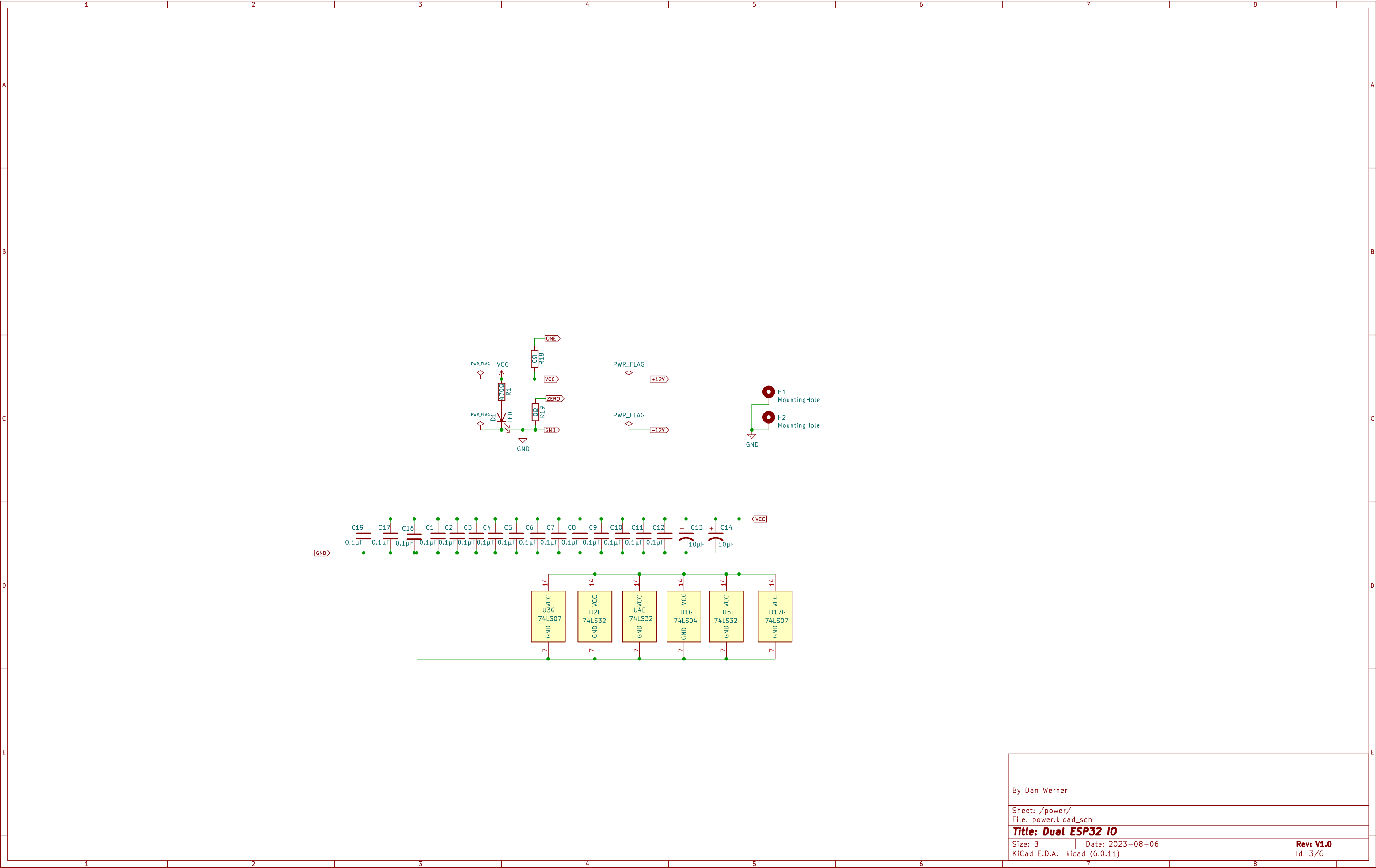
Size: B

Date: 2023-08-06

Rev: V1.0

Size: 5	Date: 202
KiCad E.D.A.	kicad (6.0.11)

Id: 2/6



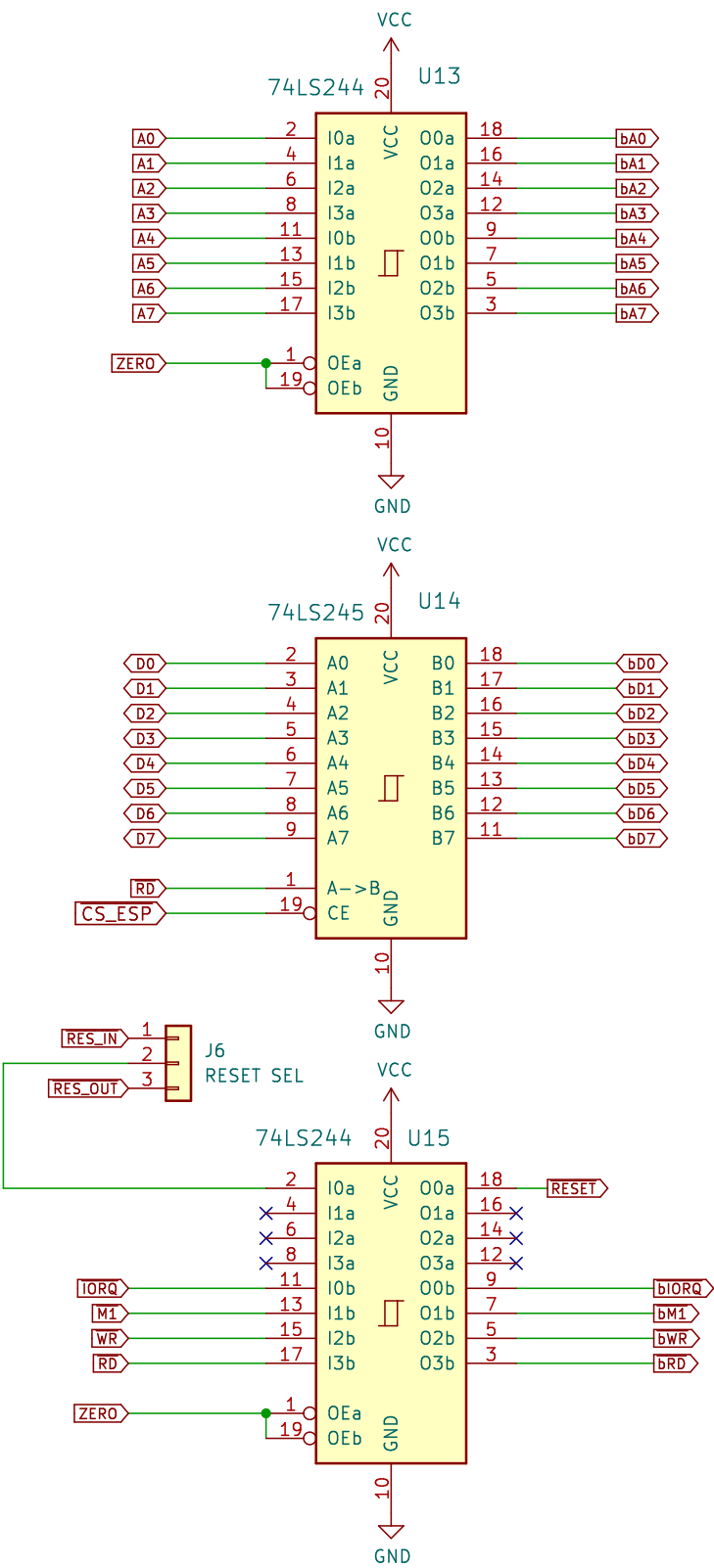
By Dan Werner

Sheet: /power/
File: power.kicad_sch

Title: Dual ESP32 IO

Size: B	Date: 2023-08-06	Rev: V1.0
KiCad E.D.A. kicad (6.0.11)		Id: 3/6

Z80 BUS INTERFACE



Note: Buffers and Transceivers respond to IO and MEM cycles

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Sheet: /buffers/
File: buffers.kicad_sch

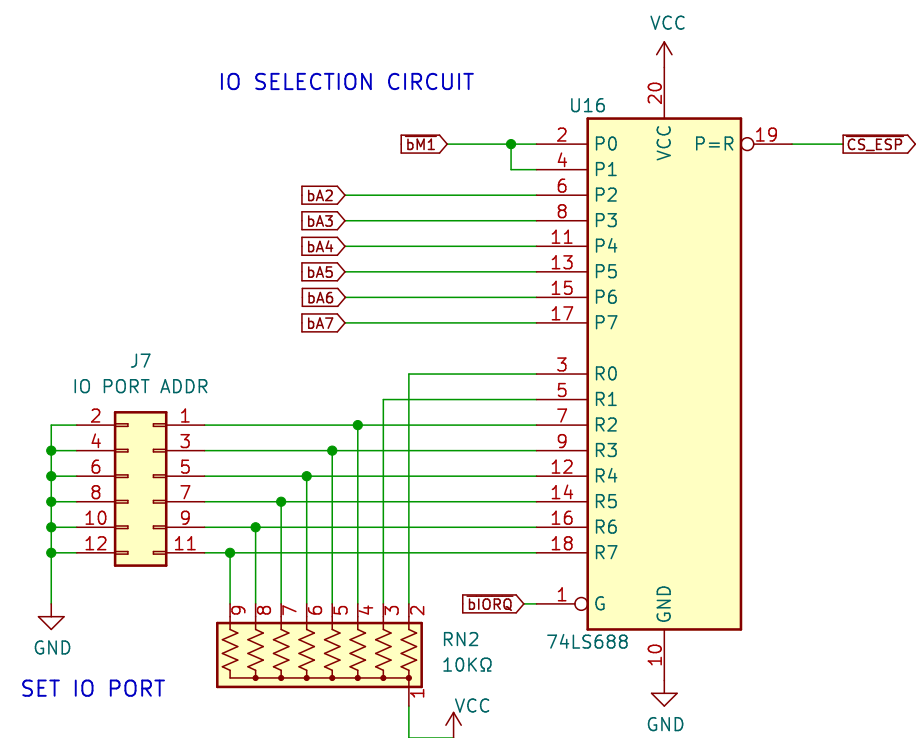
Title: Dual ESP32 IO

Size: A4 Date: 2023-08-06

KiCad E.D.A. kicad (6.0.11)

Rev: V1.0

Id: 5/6



Note: Inhibit Board Operation
During Interrupts
 $\overline{M1} = \text{low}, \overline{IORQ} = \text{low}$

Note: IO Address Port \$9C-\$9F

1=off	- A2	{ high }
3=off	- A3	{ high }
5=off	- A4	{ high }
7=on	- A5	{ low }
9=on	- A6	{ low }
11=off	- A7	{ high }

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Sheet: /IOSelection/
File: IOSelection.kicad_sch

Title: Dual ESP32 IO

Size: A4	Date: 2023-08-06
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Size: A1	Date: 202
KiCad E.D.A.	kicad (6.0.11)

Rev: V1.0

Id: 6/6