

12345678

bus

power

buffers

fpanel

File: bus.kicad_sch
65816CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 65816CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: processor.65816.kicad_sch

Title: **Duodyne 65816 CPU board**

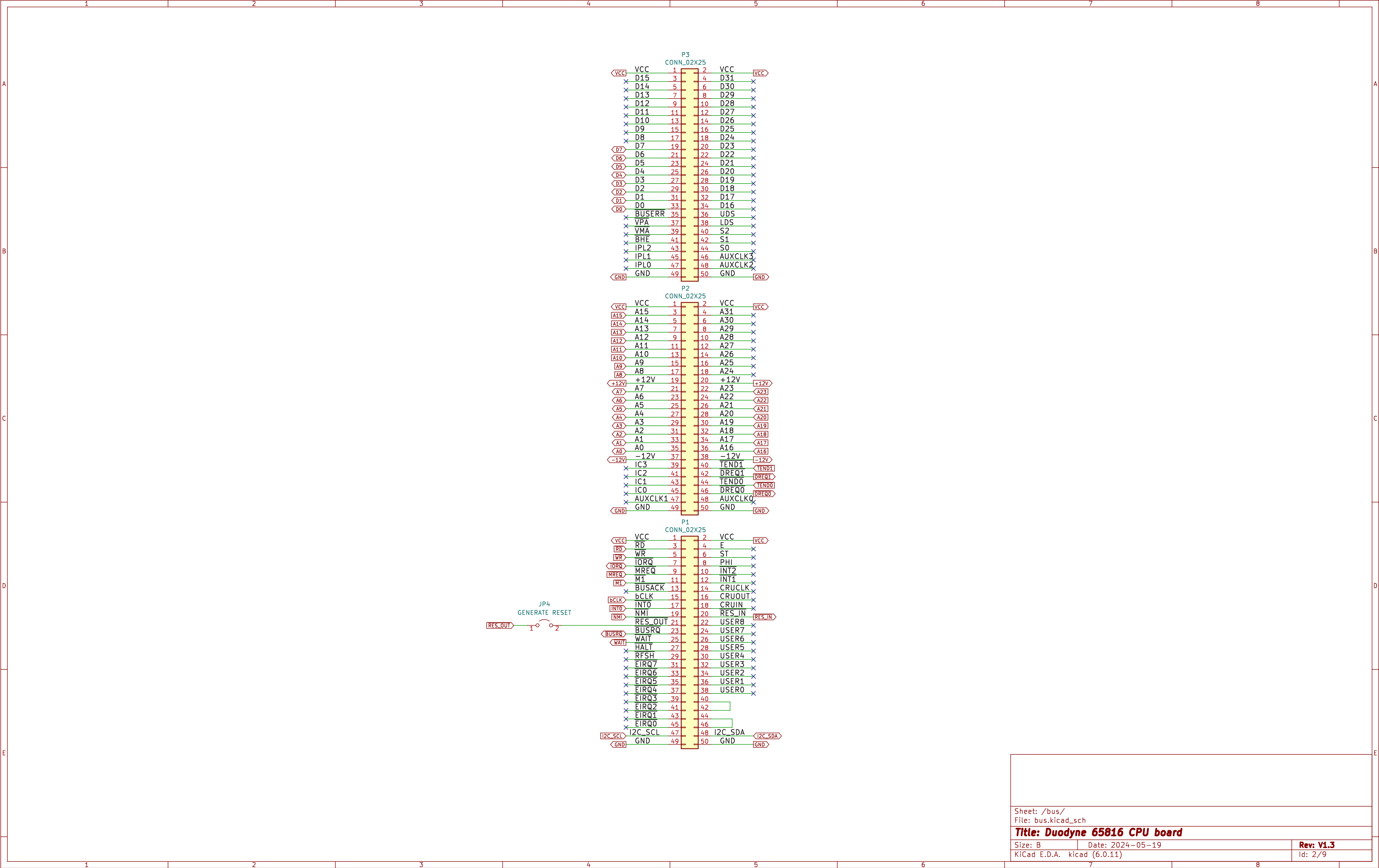
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Date: 2024-05-19

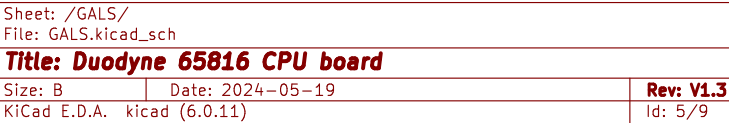
Rev: **V1.3**

KiCad E.D.A. kicad (6.0.11)

Id: 1/9

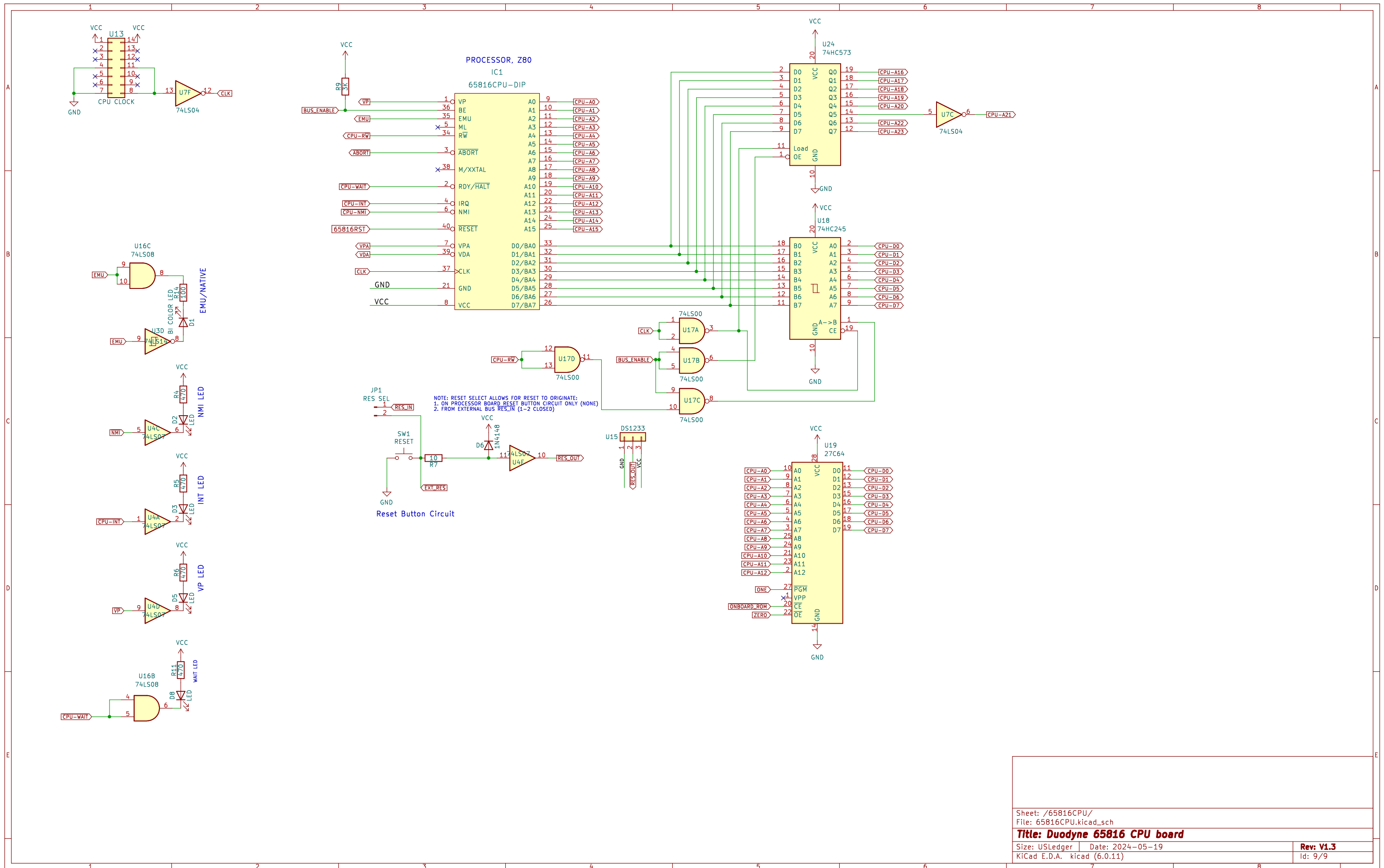






Size: D	Date: 202
KiCad E.D.A.	kicad (6.0.11)



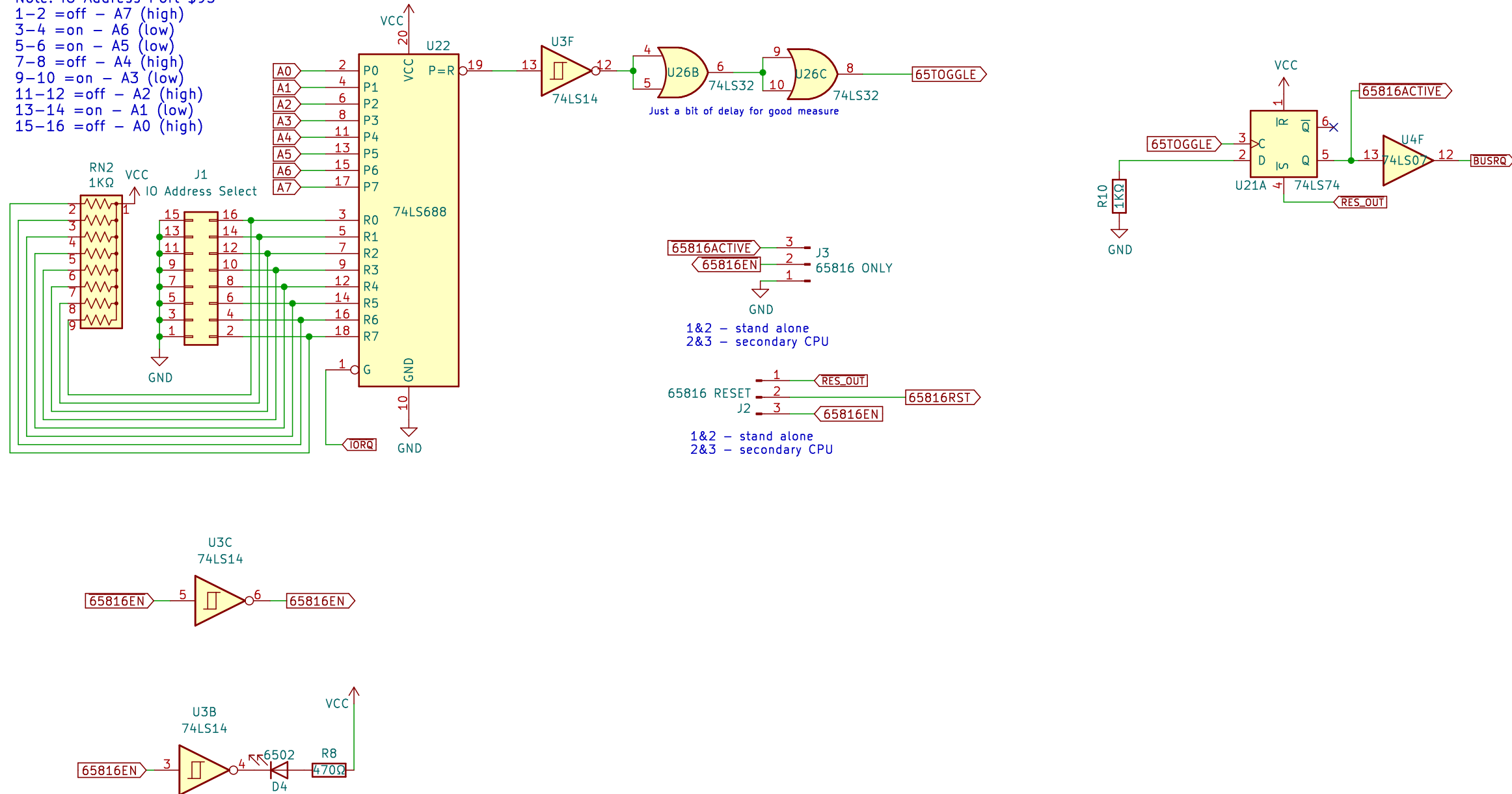




```

1-2 = off - A7 (high)
3-4 = on  - A6 (low)
5-6 = on  - A5 (low)
7-8 = off - A4 (high)
9-10 = on - A3 (low)
11-12 = off - A2 (high)
13-14 = on - A1 (low)
15-16 = off - A0 (high)

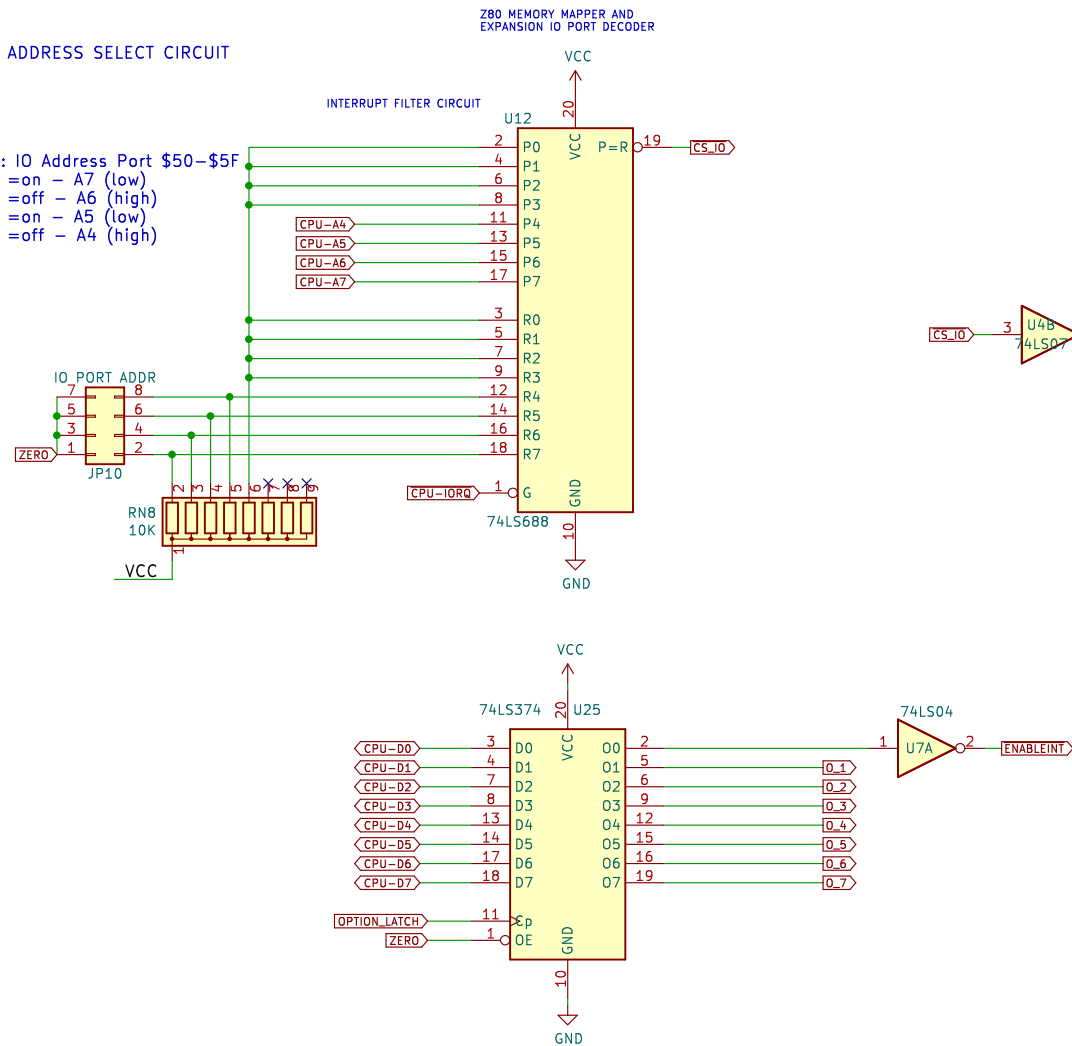
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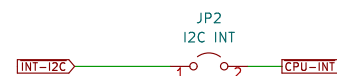
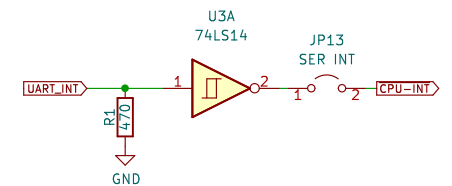
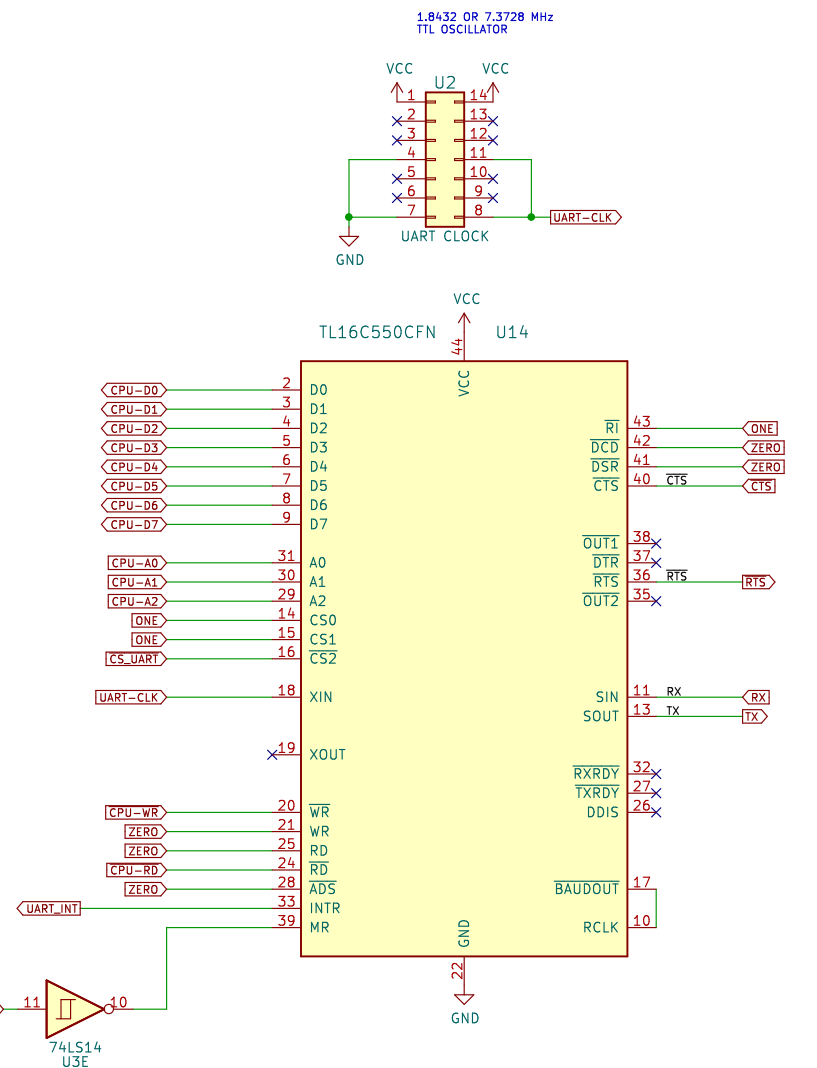
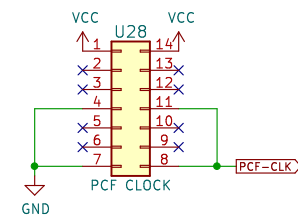
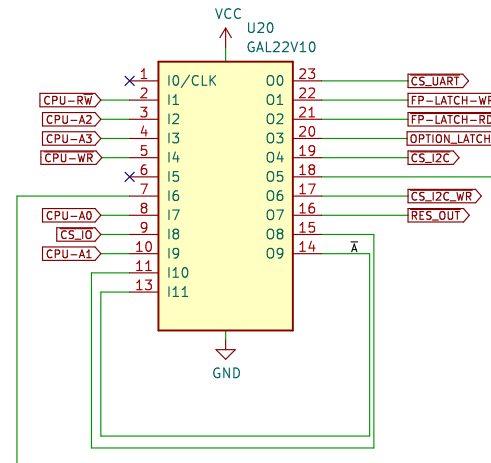


Note: IO Address Port \$50-\$5F

1-2 = on - A7 (low)
3-4 = off - A6 (high)
5-6 = on - A5 (low)
7-8 = off - A4 (high)



\$50	FRONT PANEL
\$51	OPTION LATCH
\$52	
\$53	RESET
\$54	
\$55	
\$56-\$57	I2C(Rw)
\$58-\$5F	UART (Rw)



NOTE: INTERRUPTS RELY ON 4700
OHM PULL UP RESISTOR ON PROCESSOR