

12345678

bus

power

buffers

fpanel

File: bus.kicad_sch
65816CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 65816CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: processor.65816.kicad_sch

Title: **Duodyne 65816 CPU board**

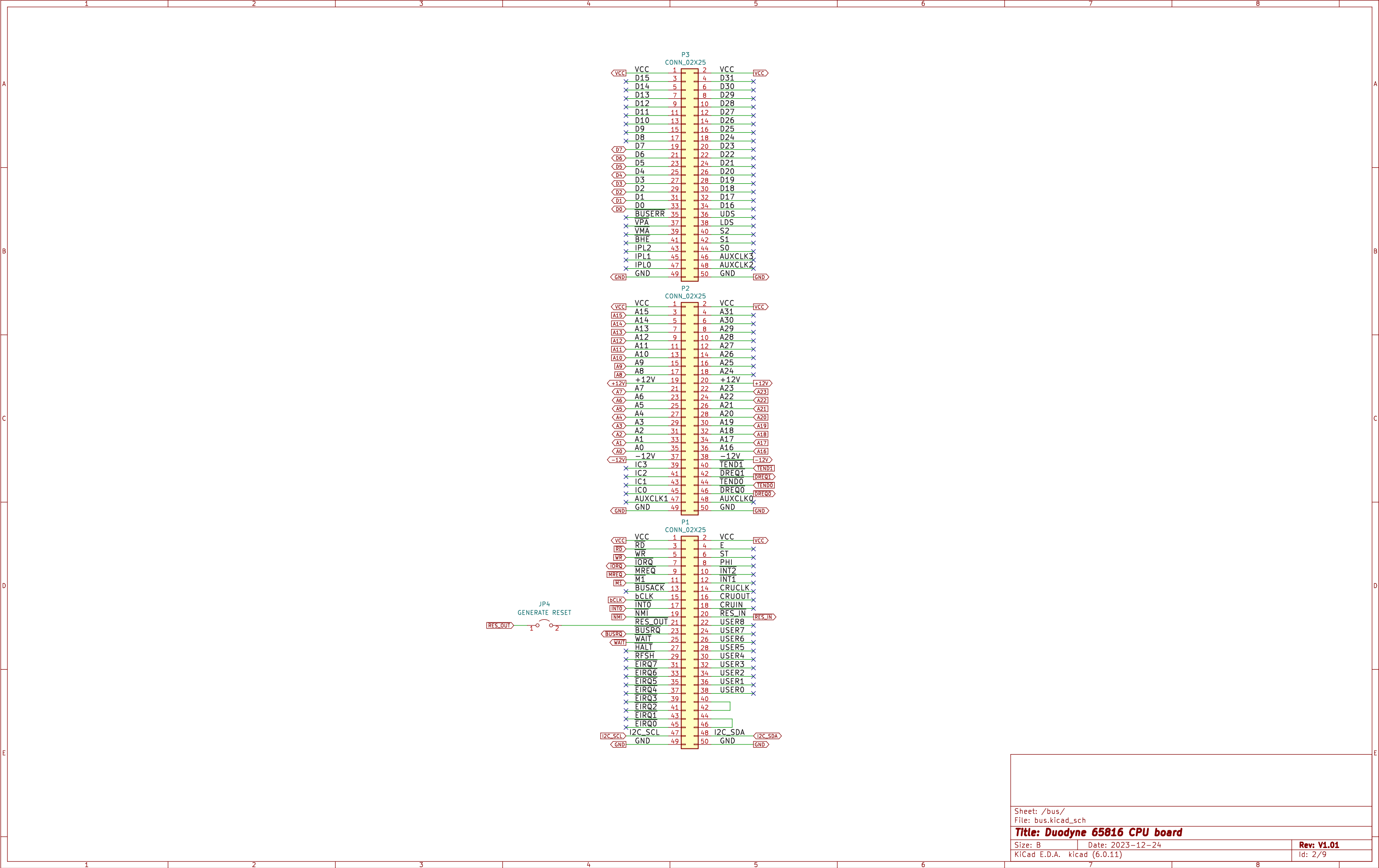
Size: B

Date: 2023-12-24

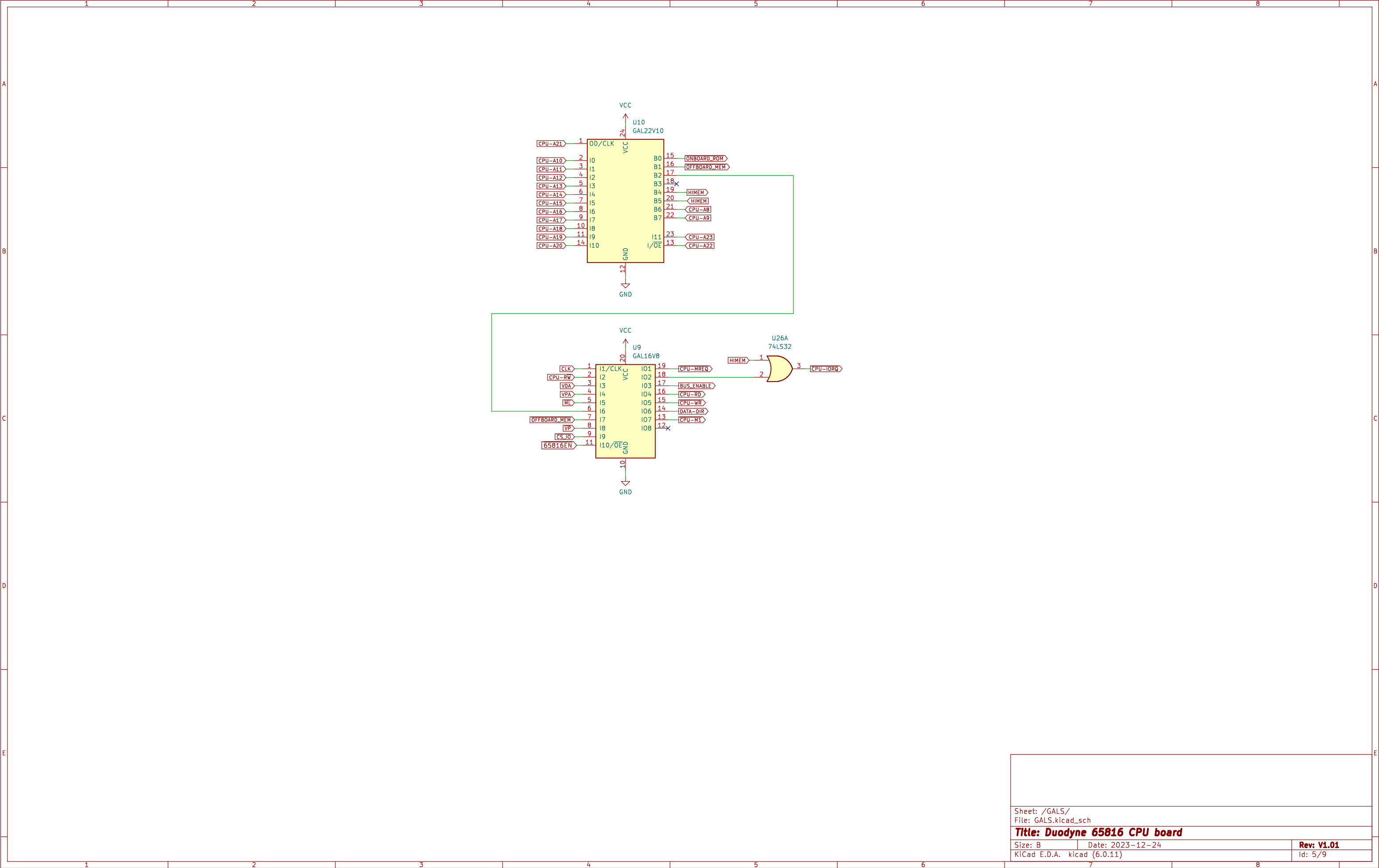
Rev: **V1.01**

KiCad E.D.A. kicad (6.0.11)

Id: 1/9

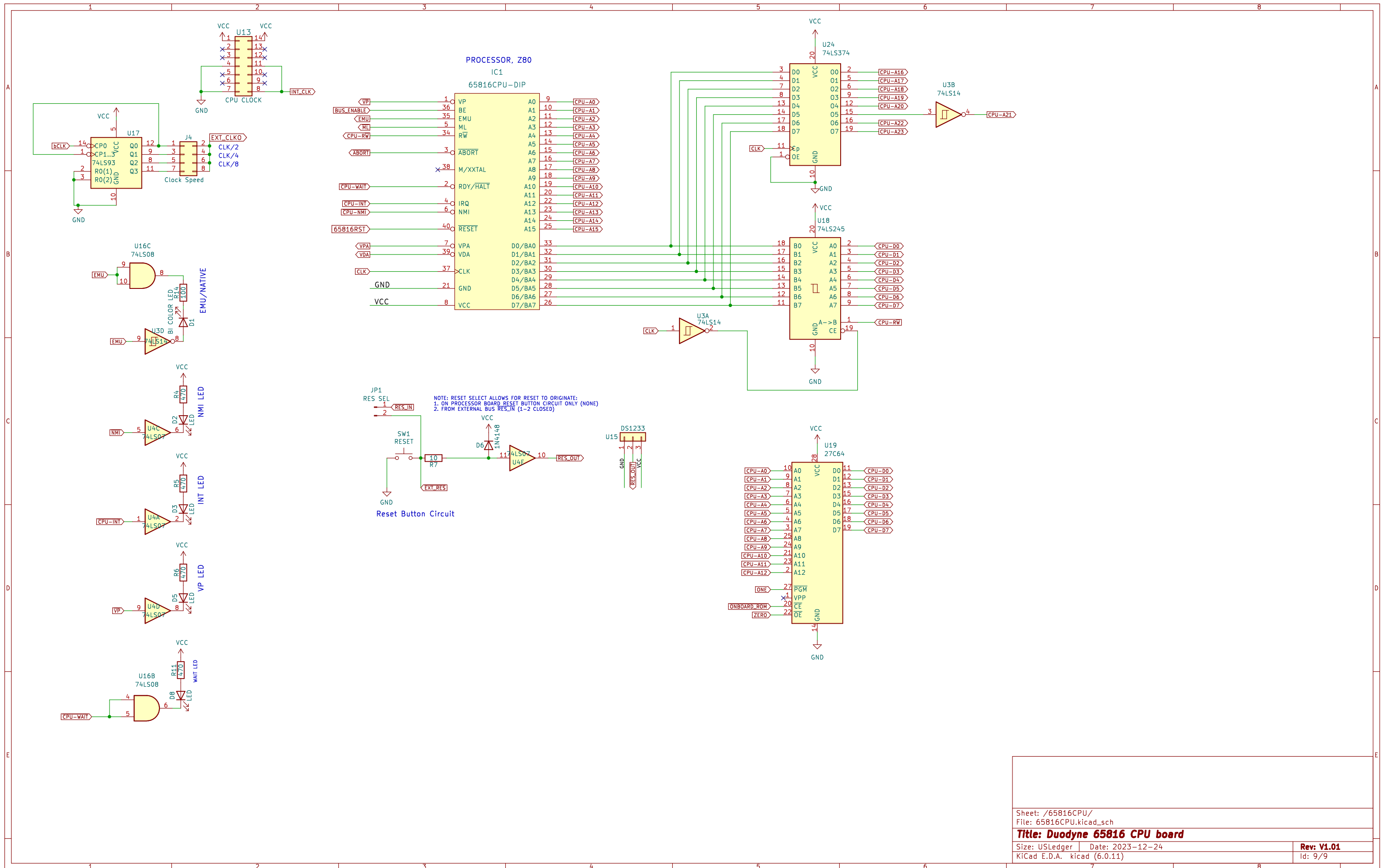








Sheet: /fpanel/
 File: fpanel.kicad_sch
Title: Duodyne 65816 CPU board
 Size: B Date: 2023-12-24 Rev: V1.01
 KiCad E.D.A. kicad (6.0.11) Id: 8/9



Note: IO Address Port \$95
1-2 =off - A7 (high)
3-4 =on - A6 (low)
5-6 =on - A5 (low)
7-8 =off - A4 (high)
9-10 =on - A3 (low)
11-12 =off - A2 (high)
13-14 =on - A1 (low)
15-16 =off - A0 (high)

