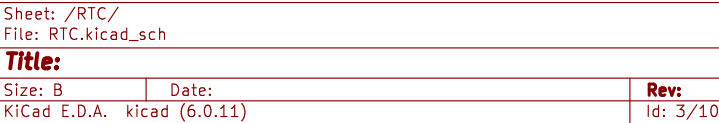
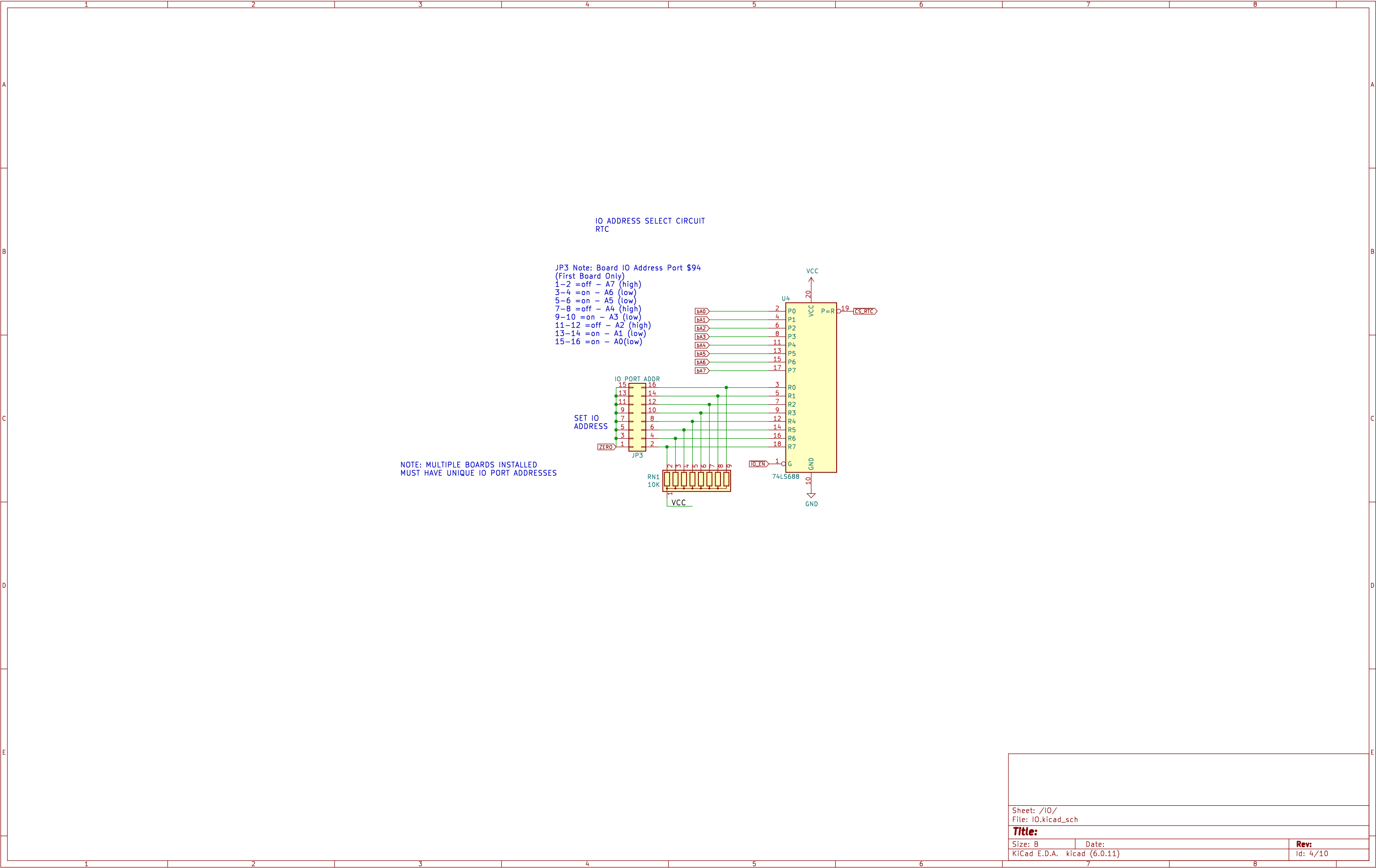


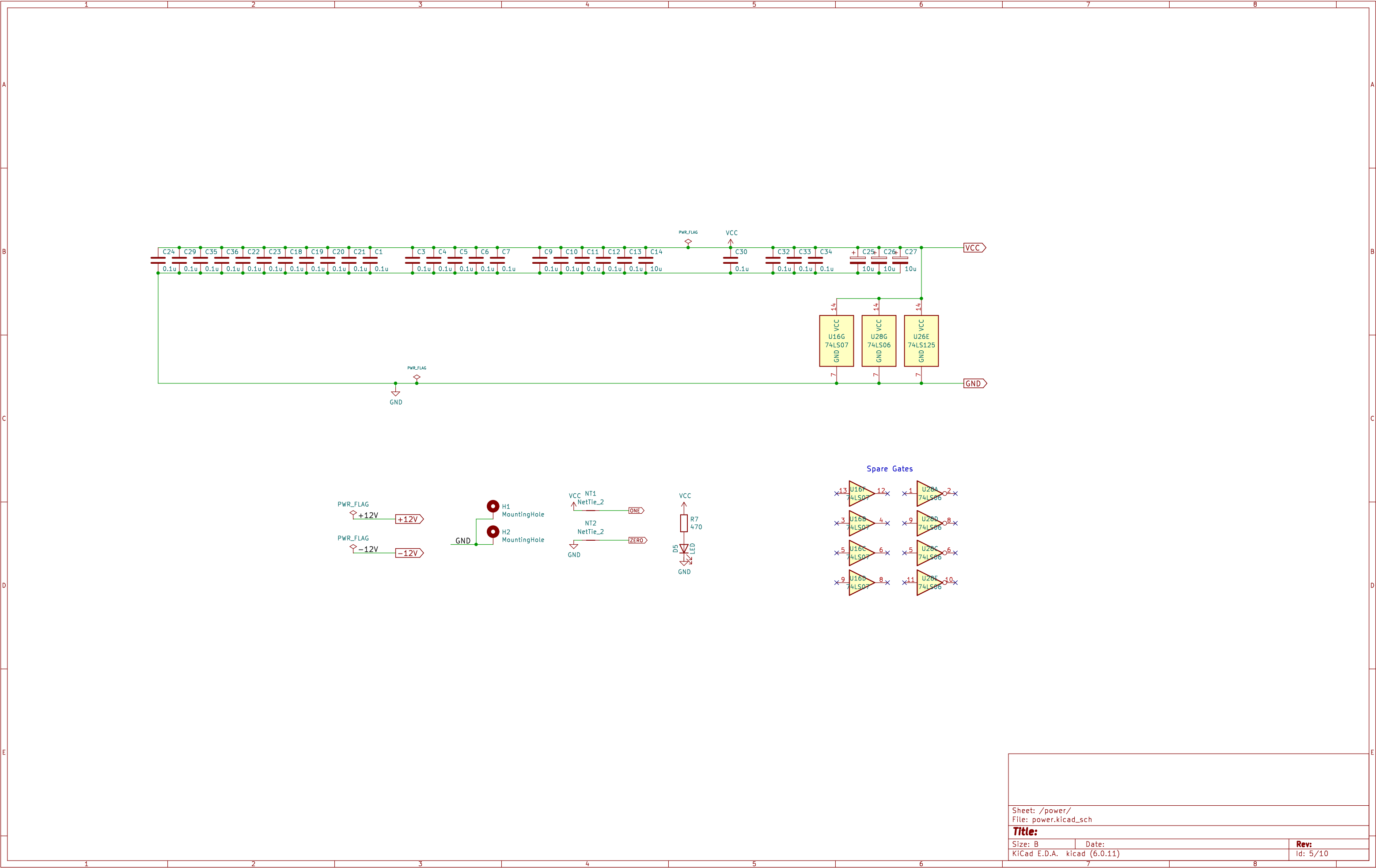
1	2	3	4	5	6	7	8
A							A
B	buffers	power	bus				
	File: buffers.kicad_sch RTC	File: power.kicad_sch battery	File: bus.kicad_sch GALS				
C							
	File: RTC.kicad_sch IO	File: battery.kicad_sch RAM	File: GALS.kicad_sch ROM				
D							
E							
1	2	3	4	5	6	7	8

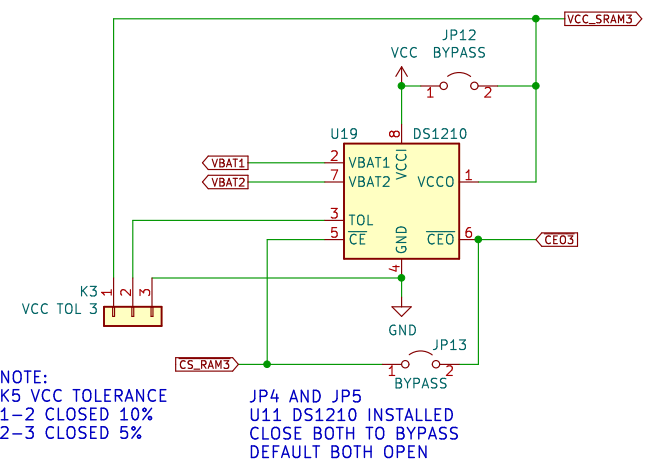
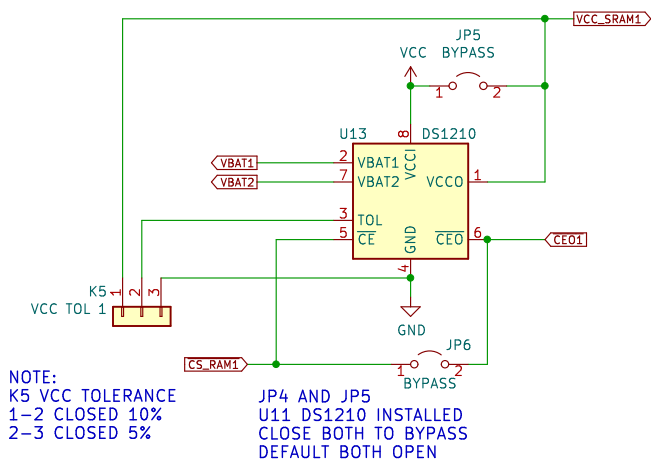
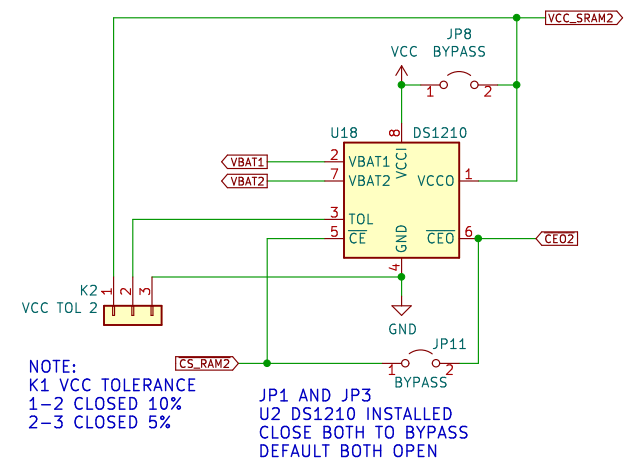
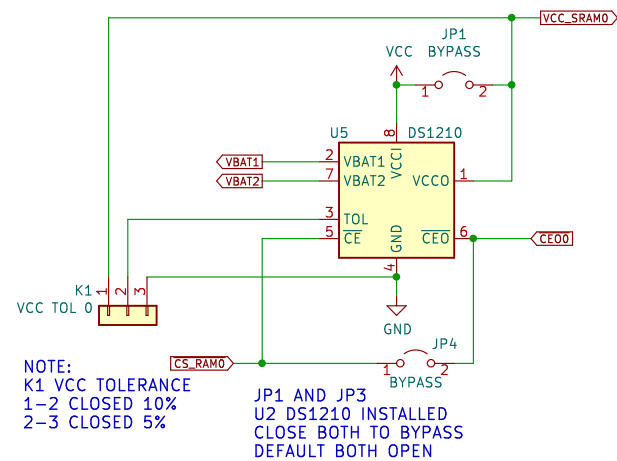
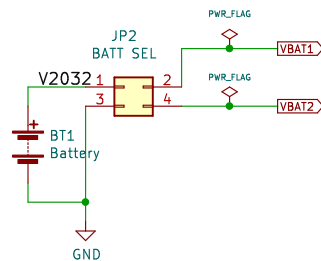
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Title:		
Size: B	Date:	Rev:
KiCad E.D.A.    kicad (6.0.11)	Id: 1/10	

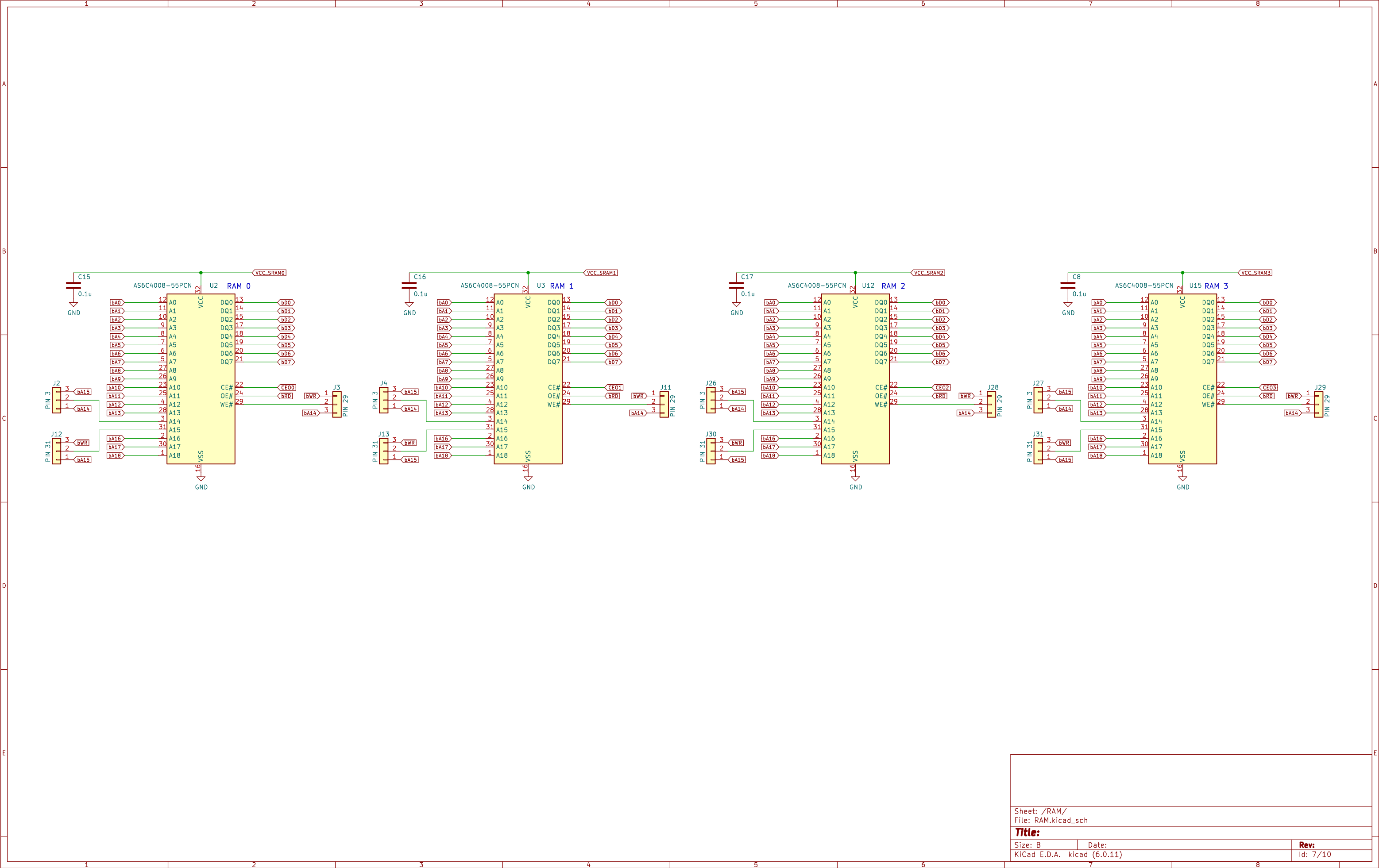
1	2	3	4	5	6	7	8	
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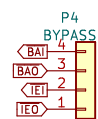
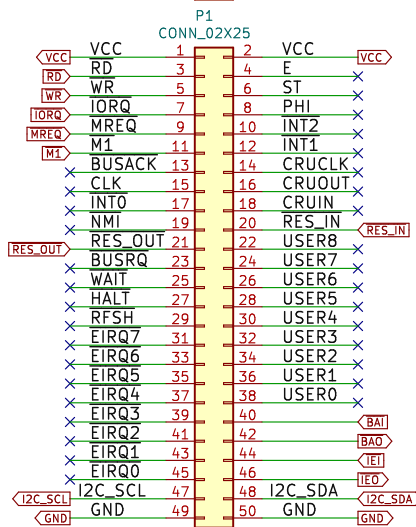
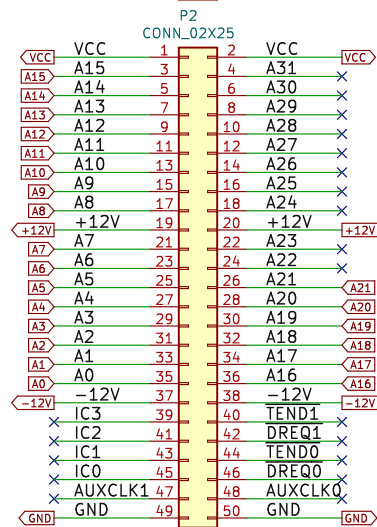
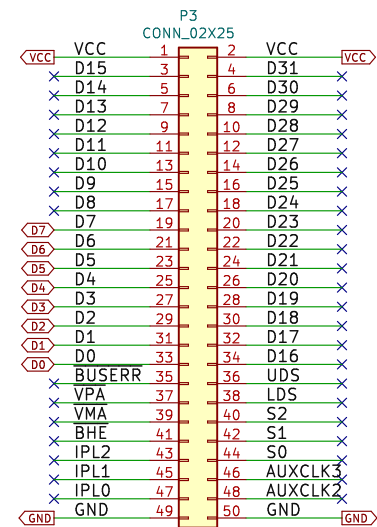




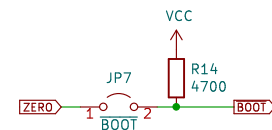
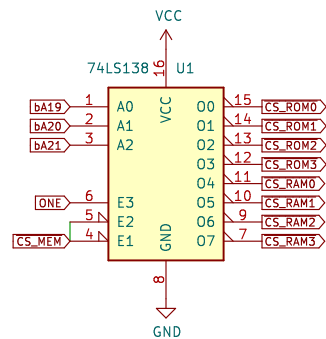
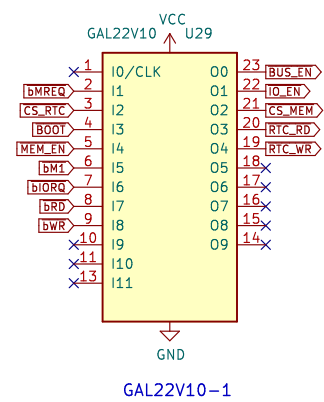
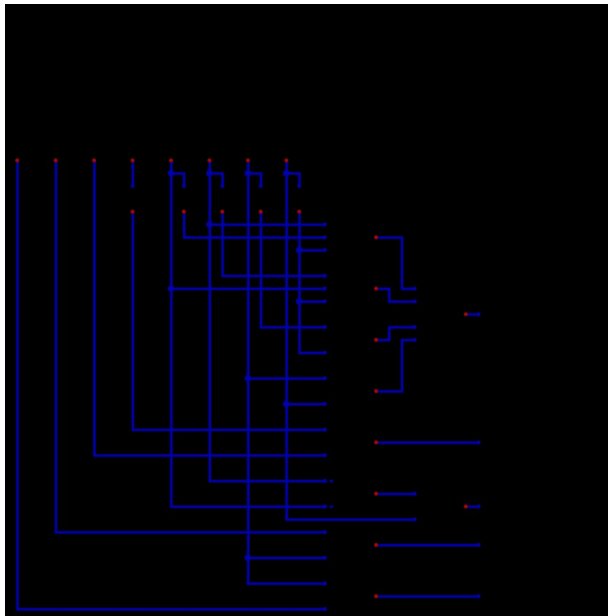












JP7 NOTE: FIRST ROM & RAM BOARD IN SYSTEM MUST HAVE JP7 JUMPER INSTALLED. ALSO MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE. FIRST BOARD ONLY RESPONDS WHEN MEM\_EN LOW (BY DEFAULT).

SECOND ROM & RAM BOARD  
INSTALLED IN SYSTEM MUST HAVE JP7  
JUMPER NOT INSTALLED. ALSO SECOND  
BOARD ONLY RESPONDS WHEN  
MEM\_EN IS HIGH.

ACCESS IS CONTROLLED BY SOFTWARE TO  
ENSURE THAT ONLY ONE OF BOARDS HAS  
MEM\_EN HIGH AT ANY GIVEN TIME OR  
BUS CONTENTION.

