

bus



File: bus.kicad_sch

power



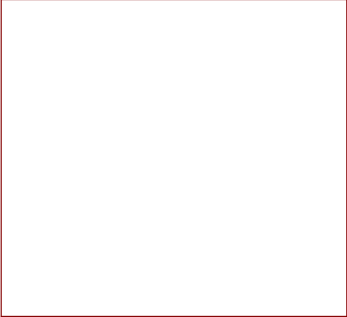
File: power.kicad_sch

Project1



File: Project1.kicad_sch

Project2



File: Project2.kicad_sch

GAL



File: GAL.kicad_sch

buffers



File: buffers.kicad_sch

IO



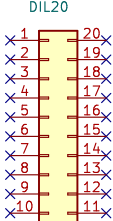
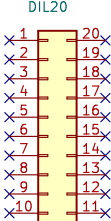
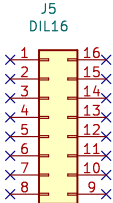
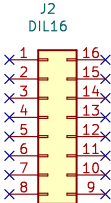
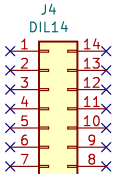
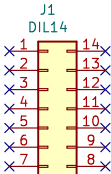
File: IO.kicad_sch

Project3



File: Project3.kicad_sch

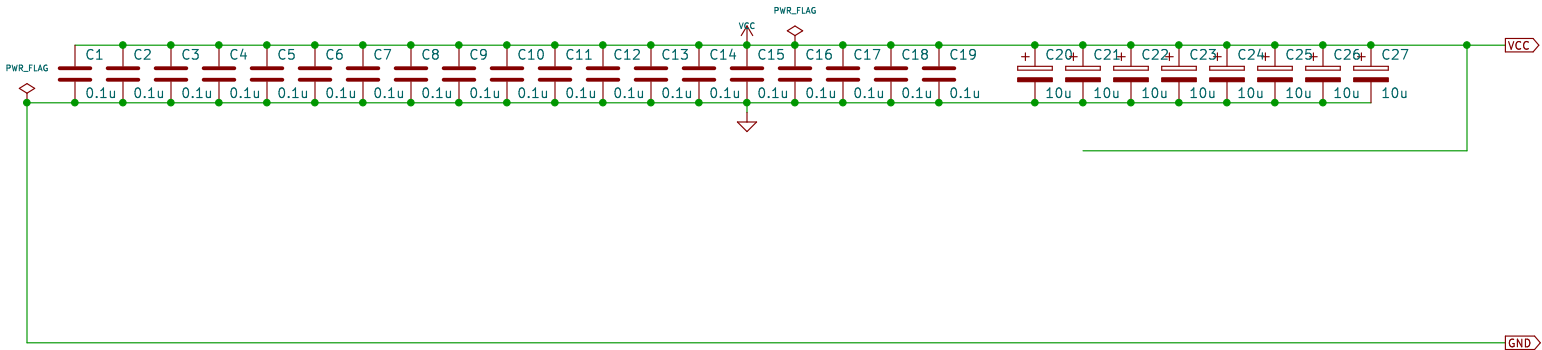
SPARE SOCKETS
FOR DEBUGGING



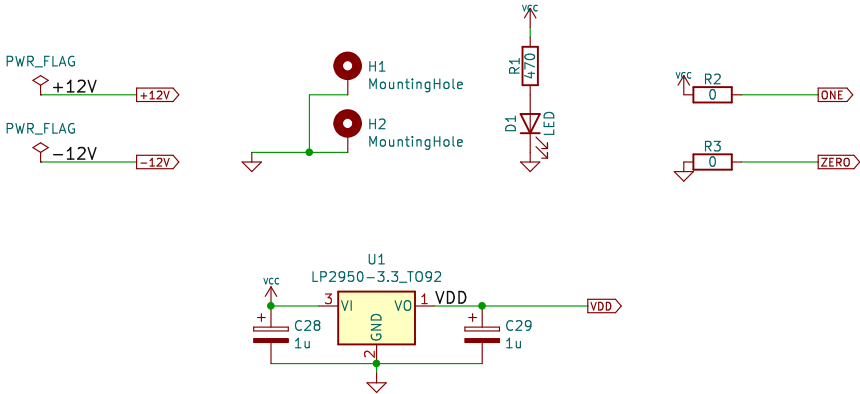
Sheet: /
File: input-output.Blank.kicad_sch

Title:

Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 1/9



Spare Components



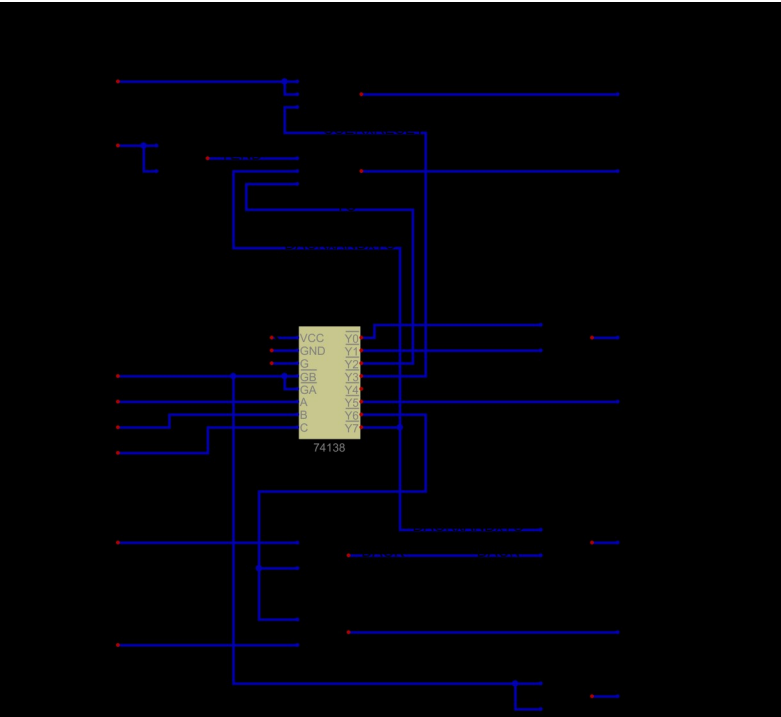
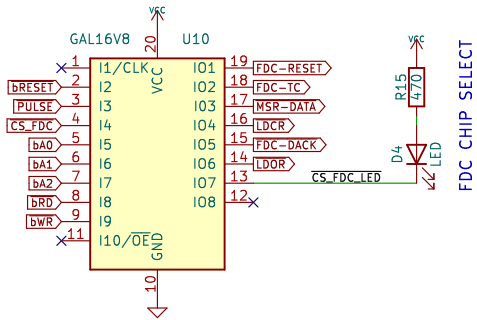
1	2	3	4	5	6	7	8
A							A
B							B
C							C
D							D
E							E
1	2	3	4	5	6	7	8

Sheet: /Project1/ File: Project1.kicad_sch		
Title:		
Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)	Id: 4/9	

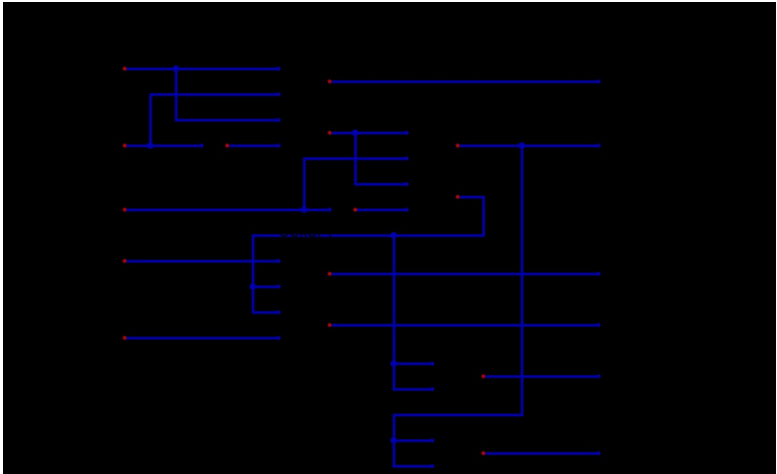
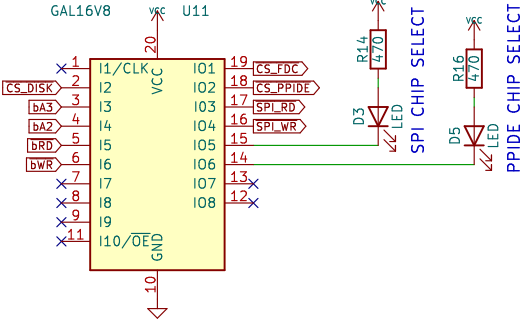
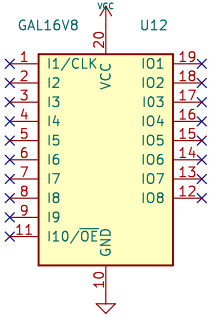
1	2	3	4	5	6	7	8
A							A
B							B
C							C
D							D
E							E
1	2	3	4	5	6	7	8

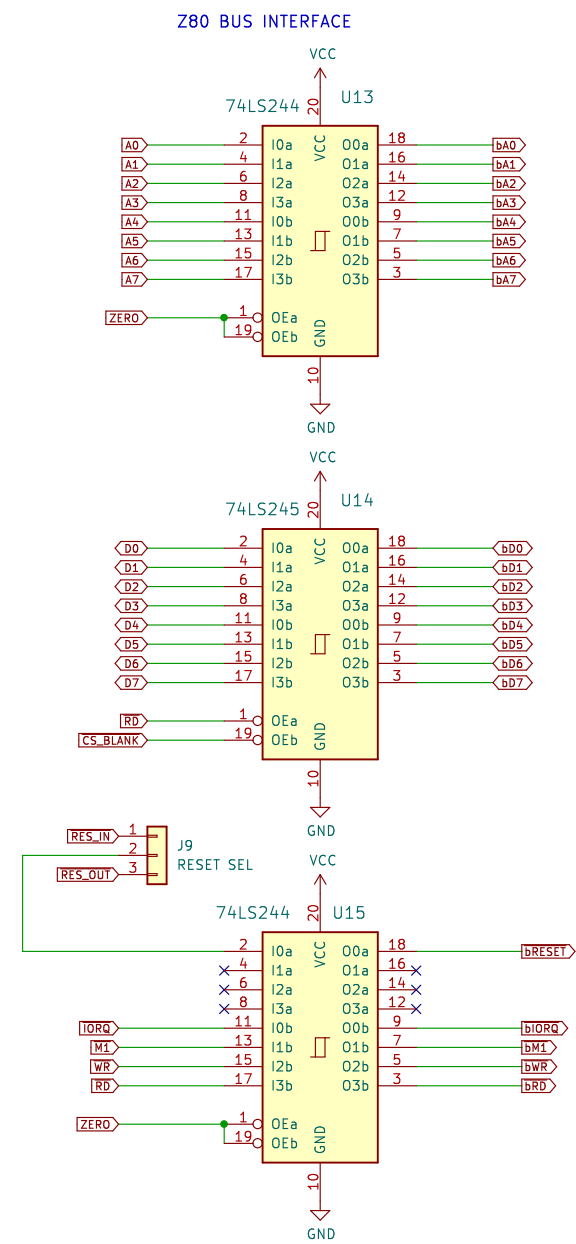
Sheet: /Project2/ File: Project2.kicad_sch		
Title:		
Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)	Id: 5/9	

FDC DMA GAL16V8



SPARE GAL FOR DEBUGGING

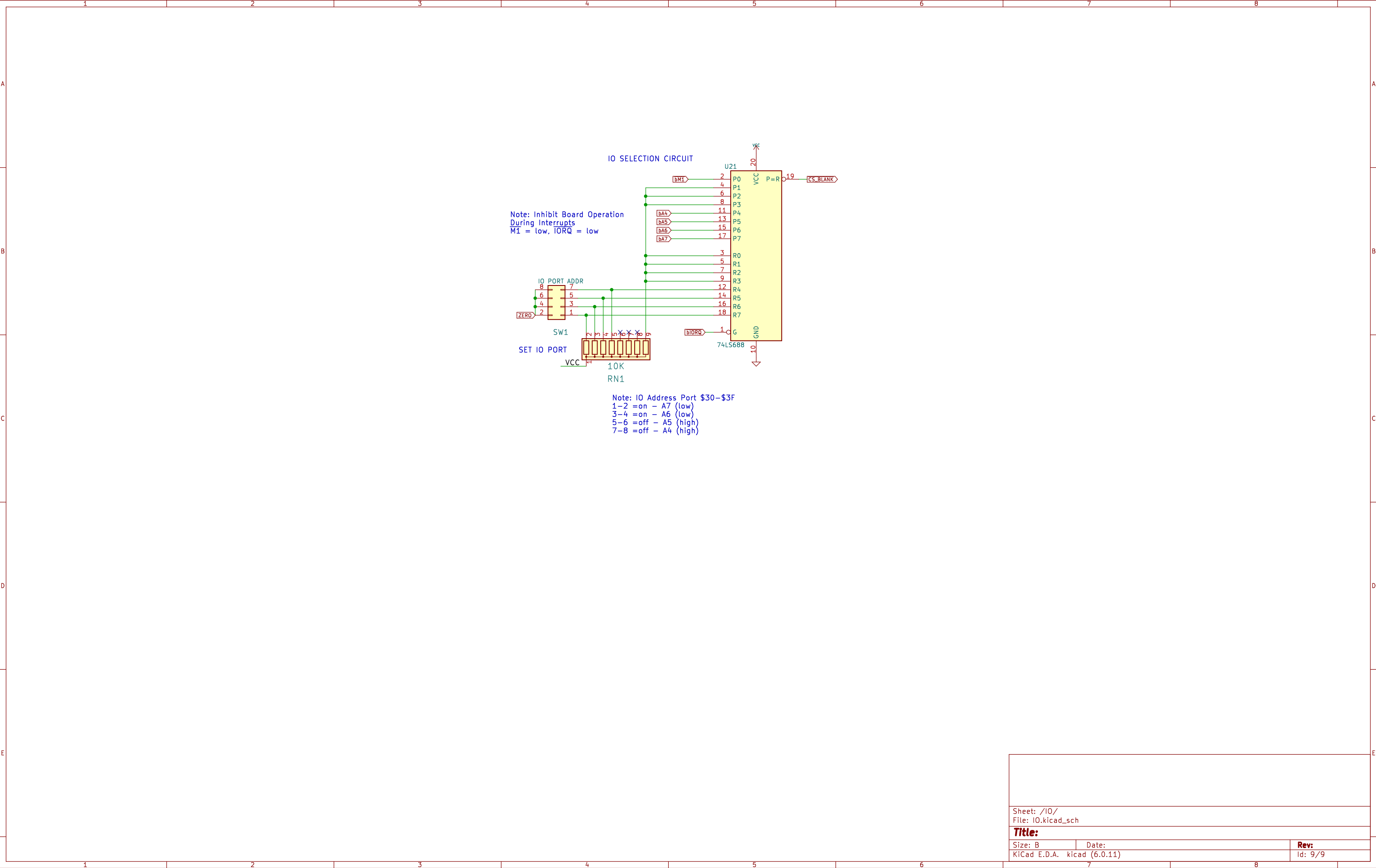




Note: Buffers and Transceivers respond to IO and MEM cycles

1	2	3	4	5	6	7	8
A							A
B							B
C							C
D							D
E							E
1	2	3	4	5	6	7	8

Sheet: /Project3/ File: Project3.kicad_sch		
Title:		
Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)	Id: 8/9	



Sheet: /IO/
File: IO.kicad_sch

Title:

Size: BDate:KiCad E.D.A. kicad (6.0.11)

Rev:Id: 9/9