

12345678

A

bus

power

buffers

fpanel

File: bus.kicad_sch
65816CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 65816CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

B

C

D

E

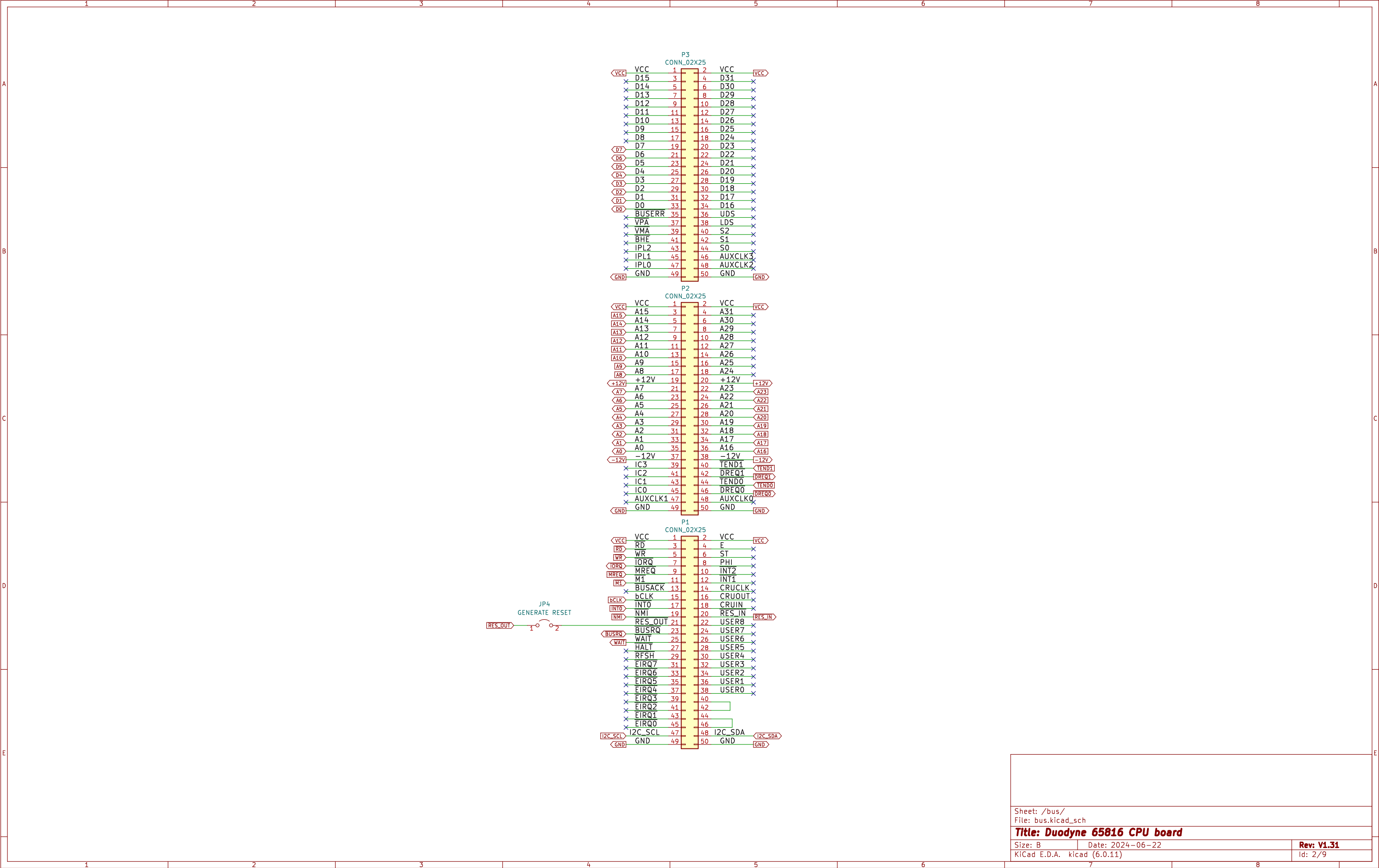
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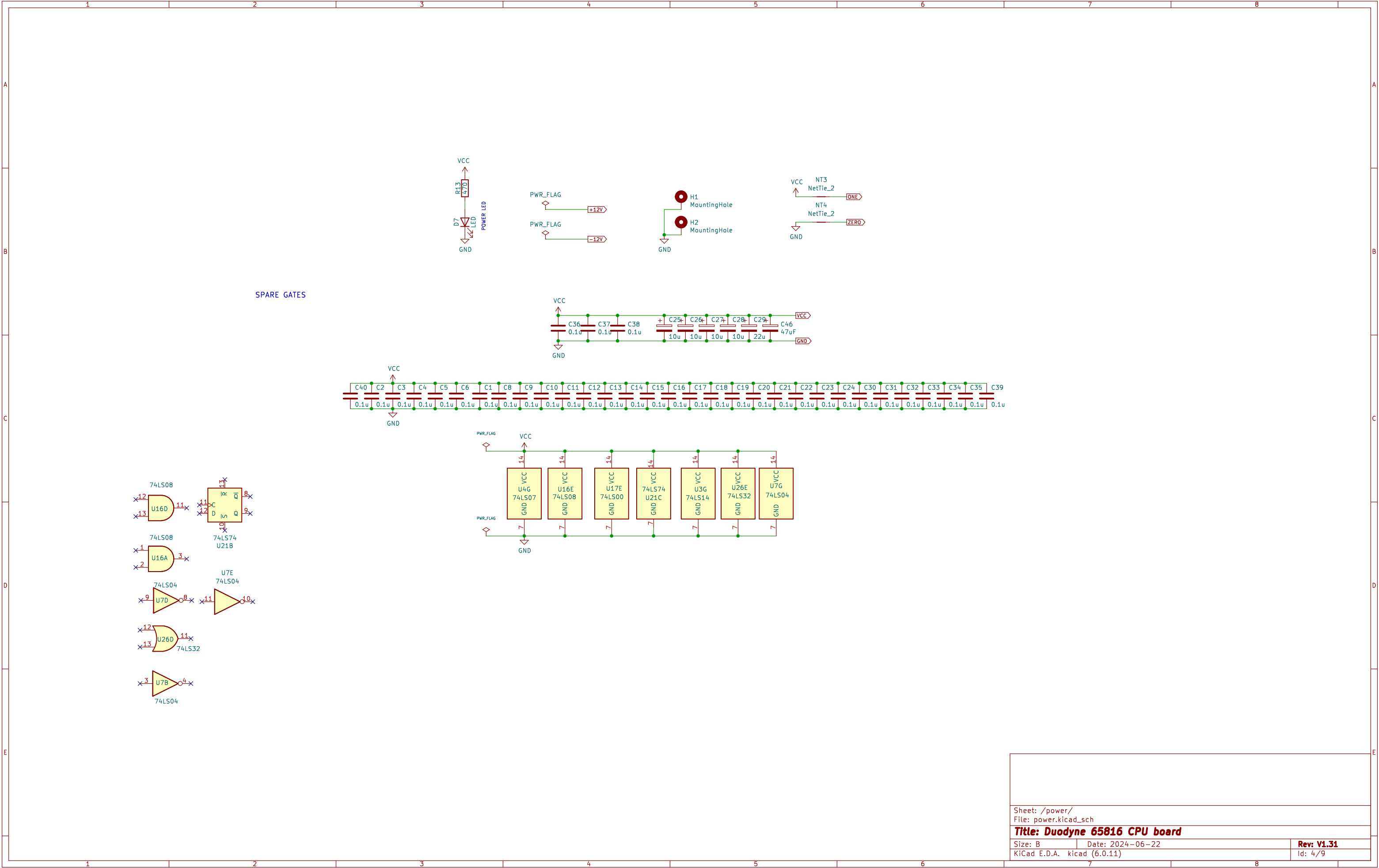
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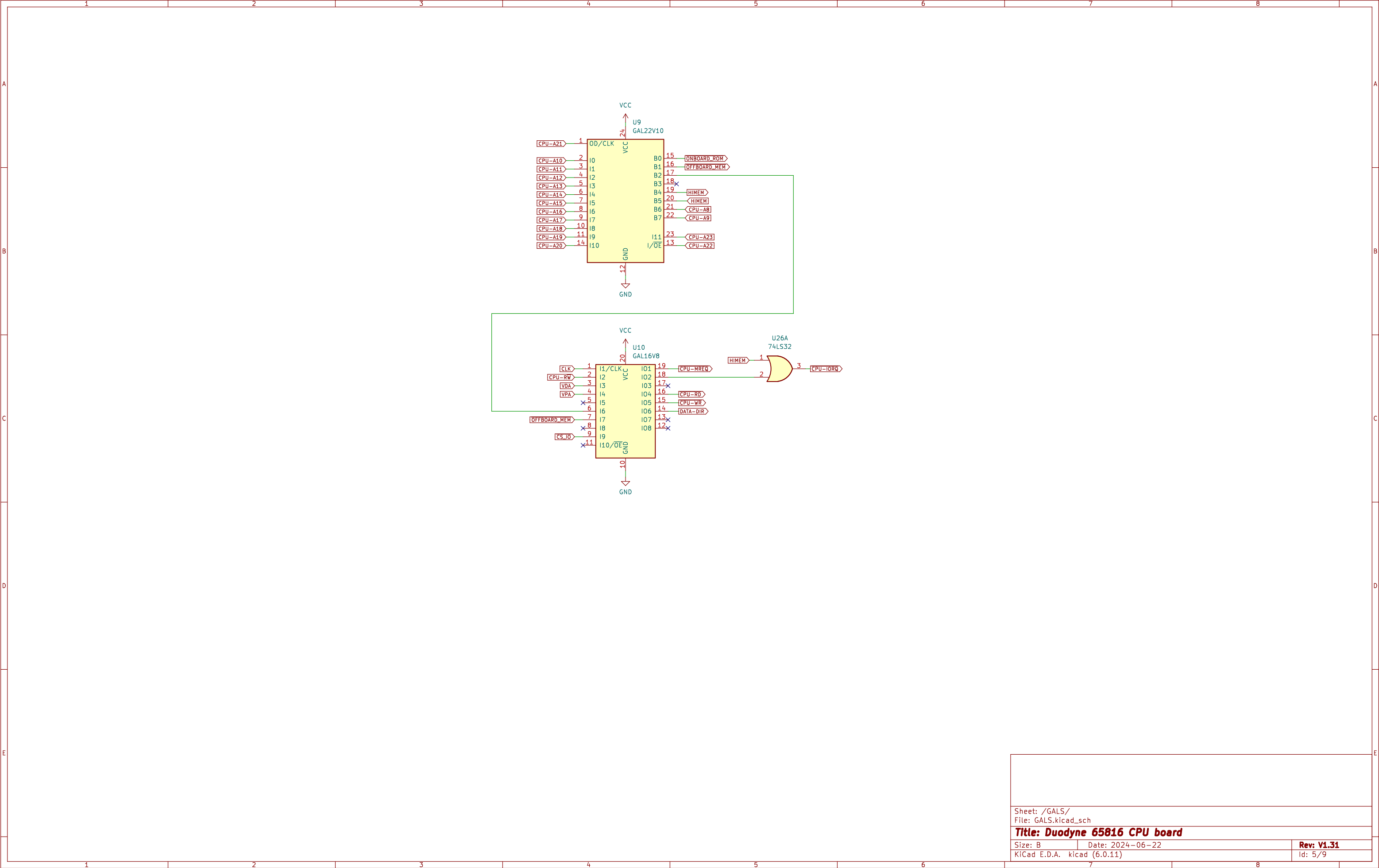
Title: Duodyne 65816 CPU board

Size: BDate: 2024-06-22

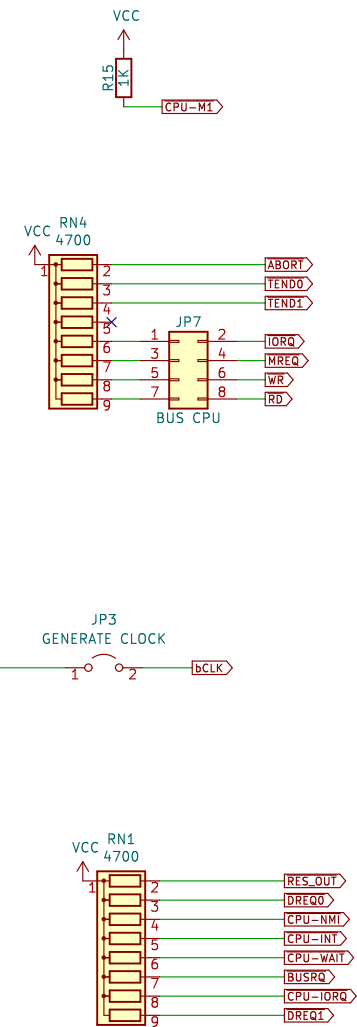
KiCad E.D.A. kicad (6.0.11)Rev: V1.31Id: 1/9







Z80 BUS INTERFACE





PINS 5, 7, 9, 11, 13, 15, 17, 19 ARE FOR LED STATUS
FOR ROMWBW FRONT PANEL INDICATOR

PINS 6, 8, 10, 12, 14, 16, 18, 20 ARE FOR SWITCH
INPUTS FOR ROMWBW FRONT PANEL

PINS 21, 23 ARE FOR I2C CONNECTION (SDA & SCL)

PINS 25, 27, 29, 31, 33, AND 35
ARE FOR TTL SERIAL CONNECTOR TO UART

PINS 29, 37 ARE FOR SERIAL VCC POWER ENABLE

PINS 22, 24 ARE FOR EXTERNAL RESET SWITCH

PINS 26, 28, 30, 32, 34, 36 are aux output bl

