

12345678

bus

power

buffers

fpanel

File: bus.kicad\_sch  
65816CPU

File: power.kicad\_sch  
GALS

File: buffers.kicad\_sch  
bus sharing

File: fpanel.kicad\_sch  
mapper

File: 65816CPU.kicad\_sch

File: GALS.kicad\_sch

File: bussharing.kicad\_sch

File: mapper.kicad\_sch

Sheet: /  
File: processor.65816.kicad\_sch

Title: Duodyne 65816 CPU board

Size: B

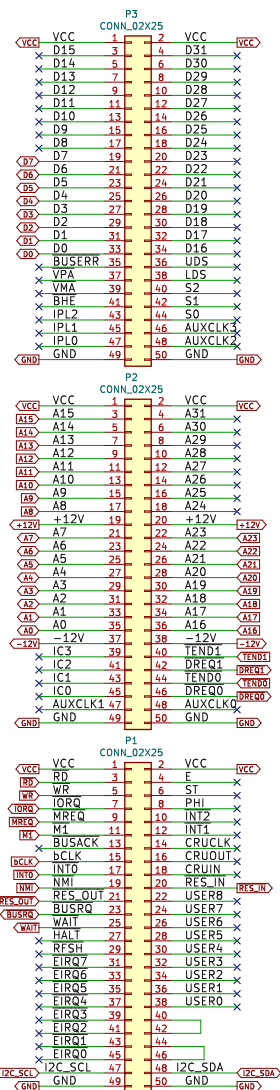
Date: 2023-12-10

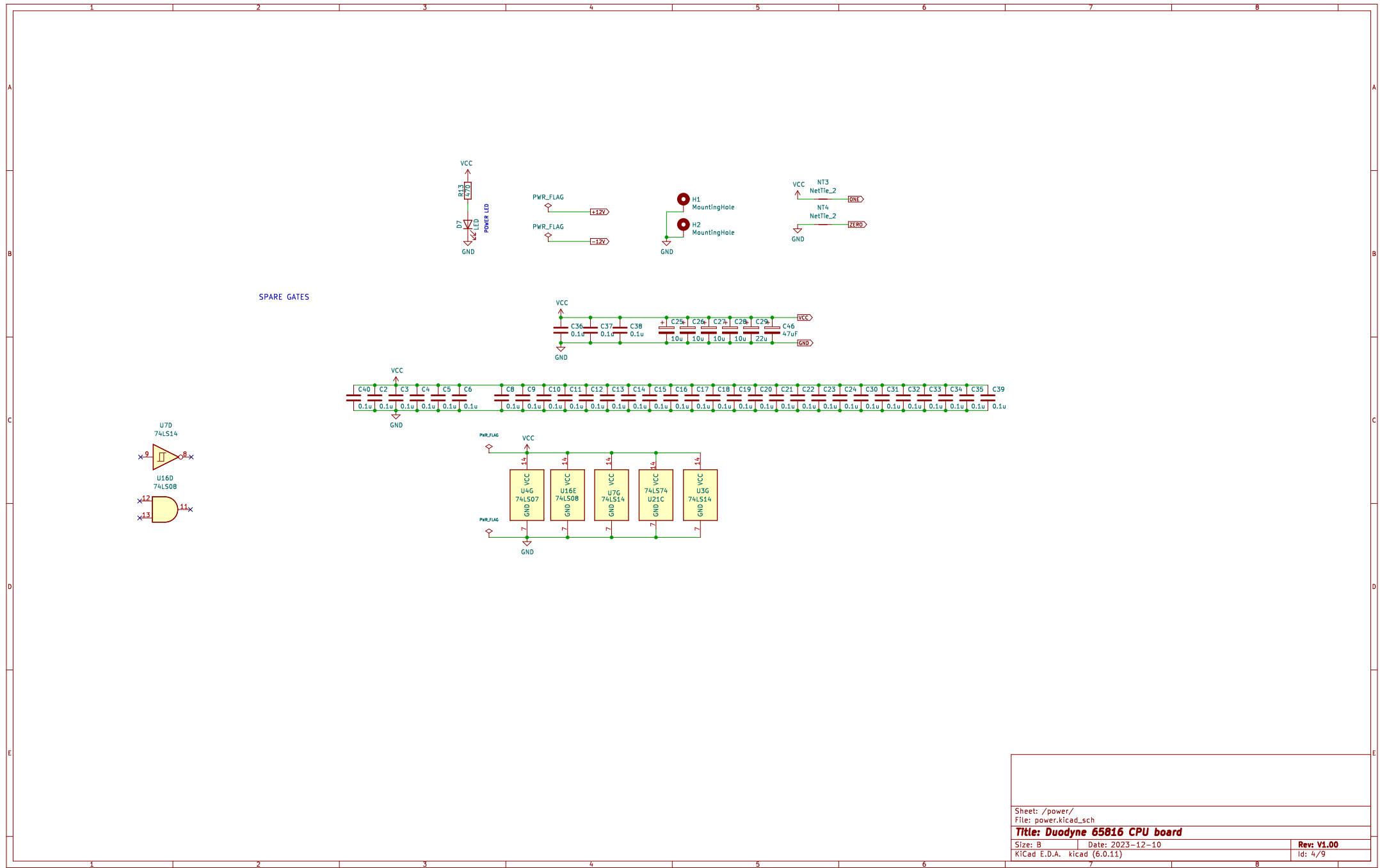
Rev: V1.00

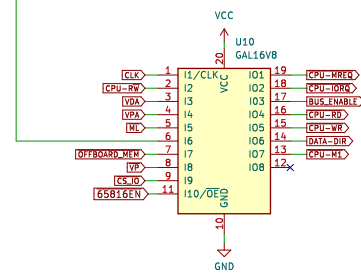
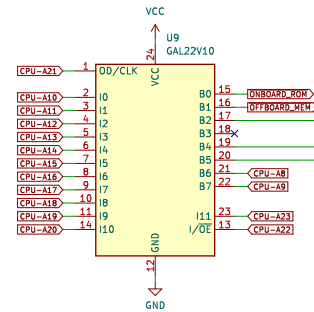
KiCad E.D.A.

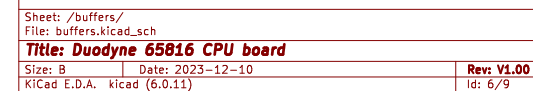
kicad (6.0.11)

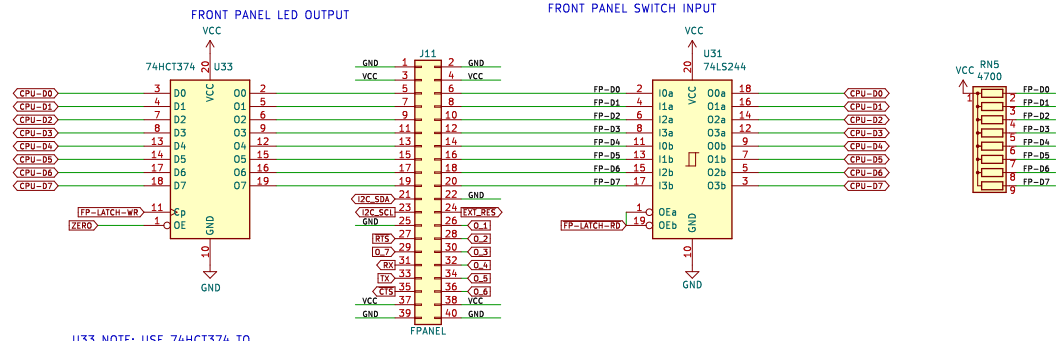
Id: 1/9







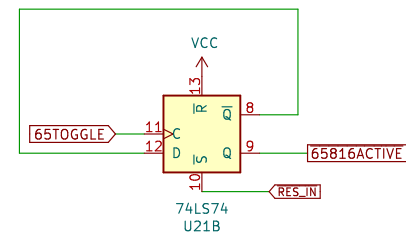
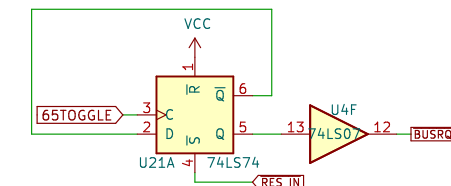
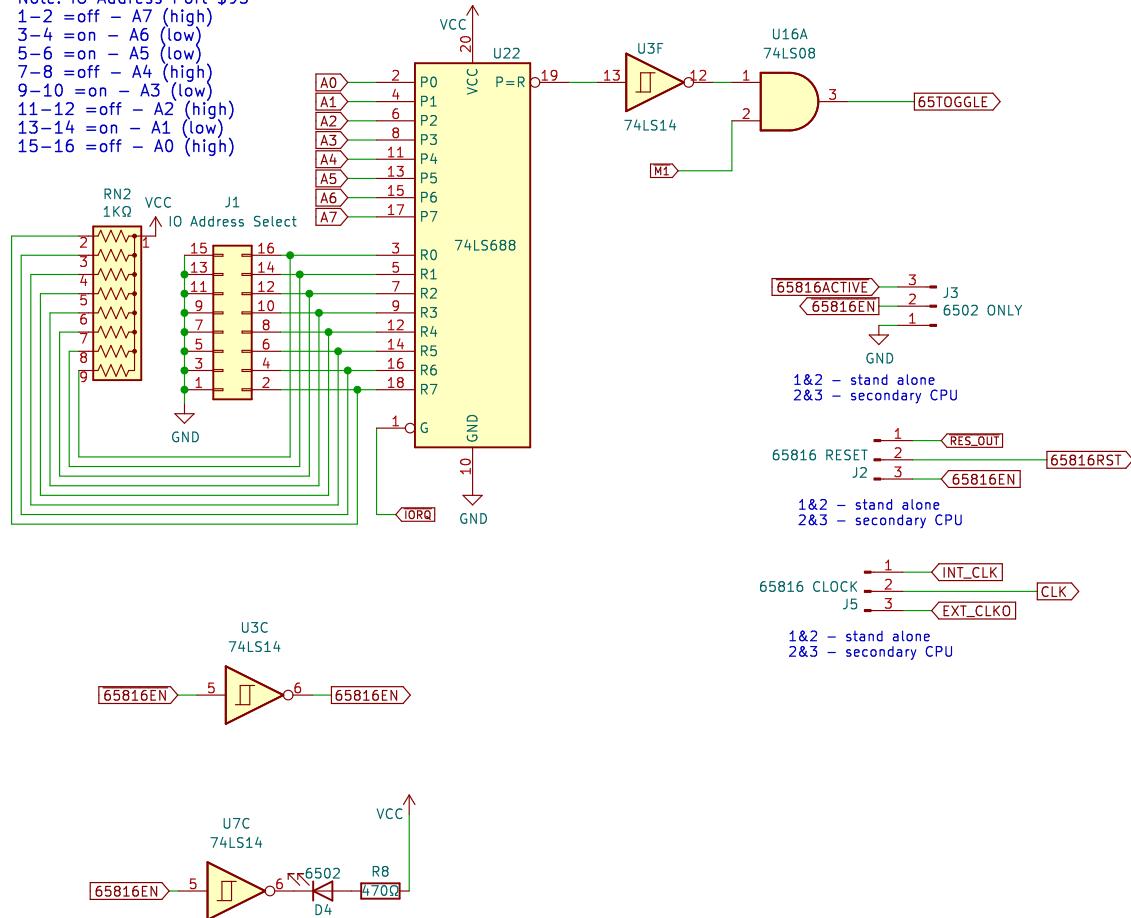






Note: IO Address Port \$95

1-2 = off - A7 (high)  
 3-4 = on - A6 (low)  
 5-6 = on - A5 (low)  
 7-8 = off - A4 (high)  
 9-10 = on - A3 (low)  
 11-12 = off - A2 (high)  
 13-14 = on - A1 (low)  
 15-16 = off - A0 (high)



Sheet: /bus sharing/

File: bussharing.kicad\_sch

**Title: Duodyne 65816 CPU board**

Size: A4 Date: 2023-12-10

KiCad E.D.A. kicad (6.0.11)

Rev: V1.00

Id: 9/9



