

12345678

bus

power

buffers

fpanel

File: bus.kicad_sch
6809CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

File: fpanel.kicad_sch
mapper

File: 6809CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: processor.6809.kicad_sch

Title: **Duodyne 6809 CPU board**

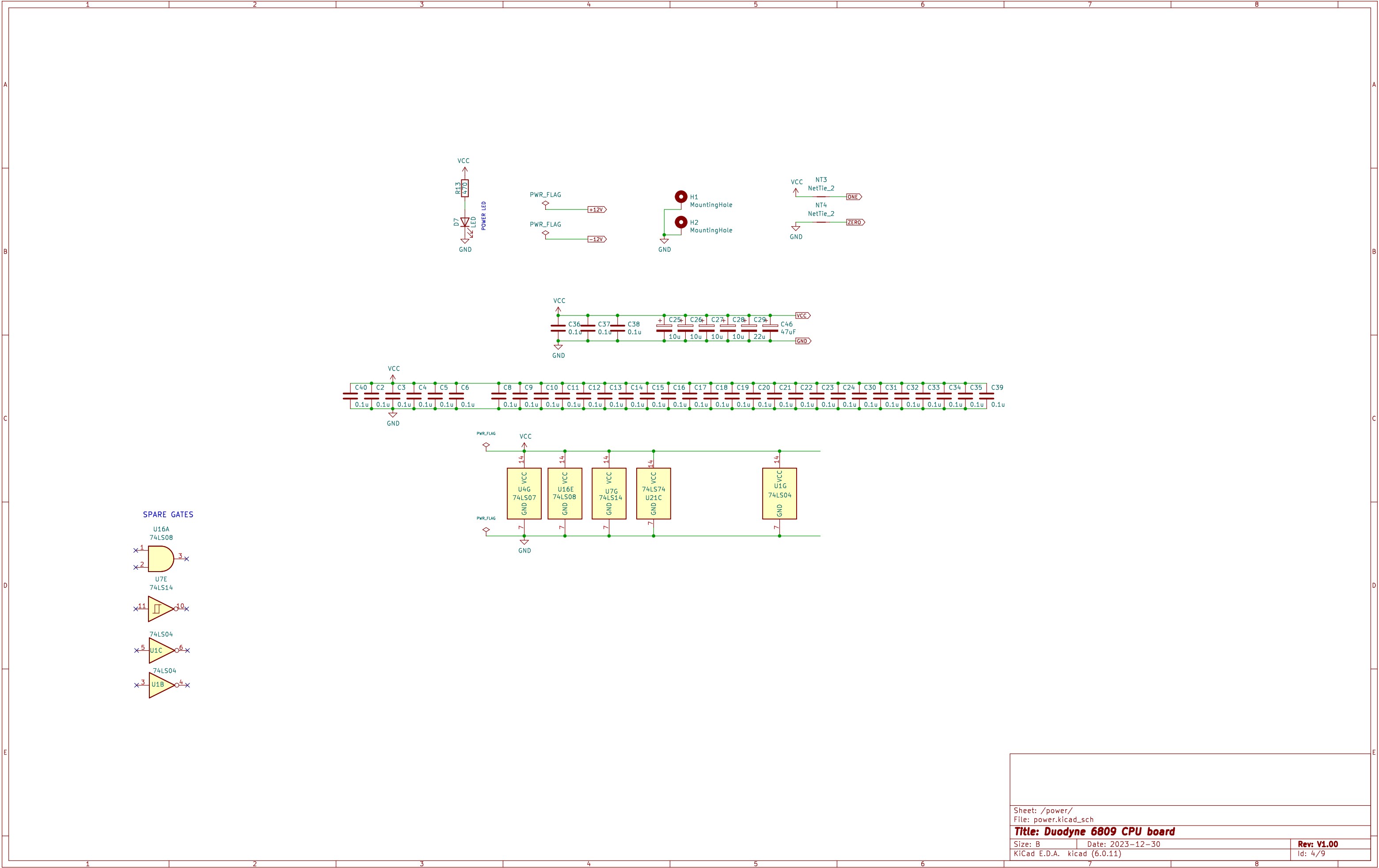
Size: B

Date: 2023-12-30

Rev: **V1.00**

KiCad E.D.A. kicad (6.0.11)

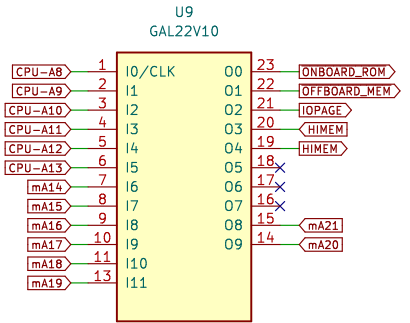
Id: 1/9



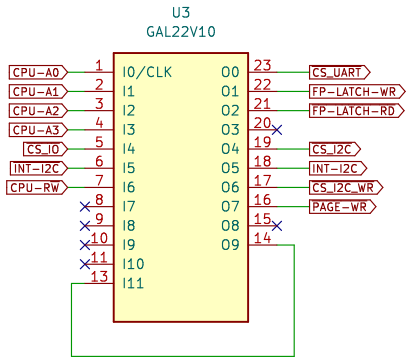
IO Address Port \$50-\$5F

\$50 Front Panel (RW)
\$51
\$52 PAGER (WR)
\$53
\$54
\$55
\$56-\$57 I2C(RW)
\$58-\$5F UART (RW)

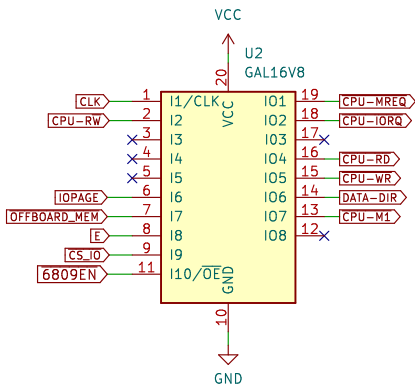
ADDRESS DECODER



IO DECODER



BUS SIGNALS



Sheet: /GALS/
File: GALS.kicad_sch

Title: Duodyne 6809 CPU board

Size: B

Date: 2023-12-30

Rev: V1.00

KiCad E.D.A. kicad (6.0.11)

Id: 5/9

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| KiCad E.D.A. | kiCad (6.0.11) |
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Sheet: /fpanel/
 File: fpanel.kicad_sch
Title: Duodyne 6809 CPU board

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|-----------------------------|------------------|------------|
| Size: B | Date: 2023-12-30 | Rev: V1.00 |
| KiCad E.D.A. kicad (6.0.11) | | Id: 8/9 |

