Board	Device	IO Begin	IO End	Note
Z180 Processor	INTERNAL	$00	$3F	RESERVED
Z80 Processor	DMA	$40	$43	
Processor (Z80, 65C816) *	MAPPER	$50	$57	
Processor (Z80, 65C816) *	UART	$58	$5F	
IO.Zilog	CTC1	$60	$63	
IO.Zilog	SIO1	$64	$67	
IO.Zilog	PIO1	$68	$6B	
IO.Zilog	PIO2	$6C	$6F	
IO.Zilog	CTC2	$44	$47	
IO.Multi	DUART	$70	$7F	
IO.Multi	PARALLEL	$48	$4B	
IO.Multi	PS/2 KBDMSE	$4C	$4D	
IO.Multi	USB-SD	$4E	$4F	
IO.Disk	FDC	$80	$87	
IO.Disk	PPIDE	$88	$8B	
IO.Disk	SPI-SD	$8C	$8F	
IO.Disk	ETHERNET	$90	$93	
ROM & RAM	RTC	$94	$94	
DualESP32	ESP32	$9C	$9D	
DualESP32	ESP32	$9E	$9F	
IO.Media	VDP	$A0	$A3	
IO.Media	PSG	$A4	$A5	
IO.Media	ACR	$A6	$A7	
IO.SelfHost	UART	$A8	$AF	
IO.Voice	TTS	$B8	$BF	
IO.GDC	GDC	$C0	$C3	
IO.GDC	LATCH	$C4	$C7	
IO.GDC	RAMDAC	$C8	$CF	
IO.Text	GDC	$96	$97	
65C816 Processor	Toggle	$95	$95	

x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF

0x
1x
2x
3x
4x
5x
6x
7x
8x
9x
Ax
Bx
Cx
Dx
Ex
Fx
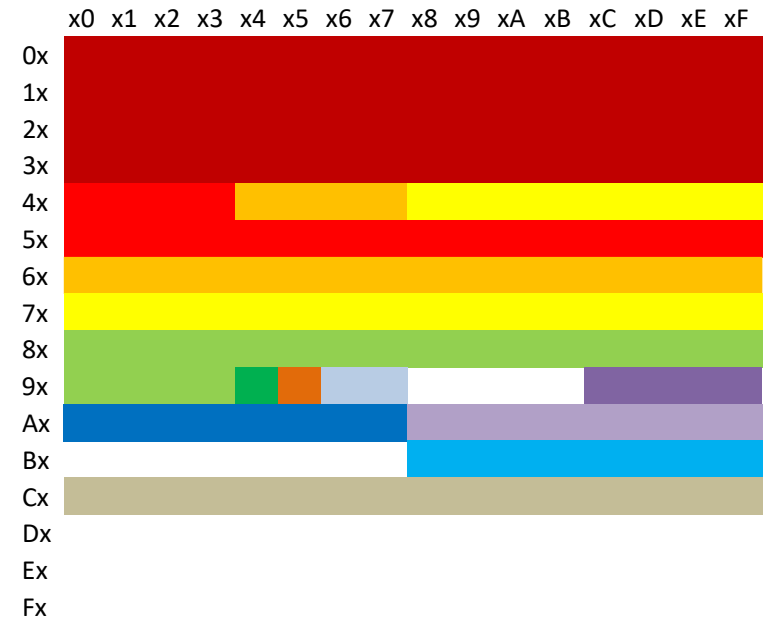
* Z80 and 65C816 Processor board share IO ports. They have mutually exclusive, local IO (Mapper & UART) accessible only to the CPU