

buffers

File: buffers.kicad\_sch  
RTC

power

File: power.kicad\_sch  
battery

bus

File: bus.kicad\_sch  
GALS

File: RTC.kicad\_sch  
IO

File: battery.kicad\_sch  
RAM

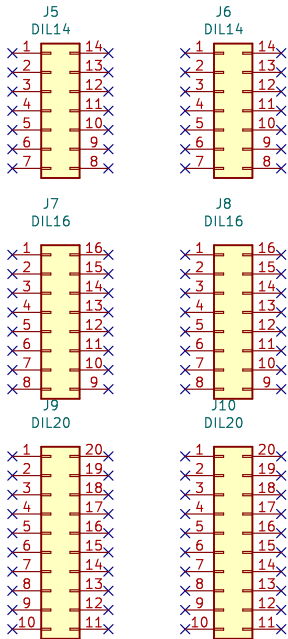
File: GALS.kicad\_sch  
ROM

File: IO.kicad\_sch

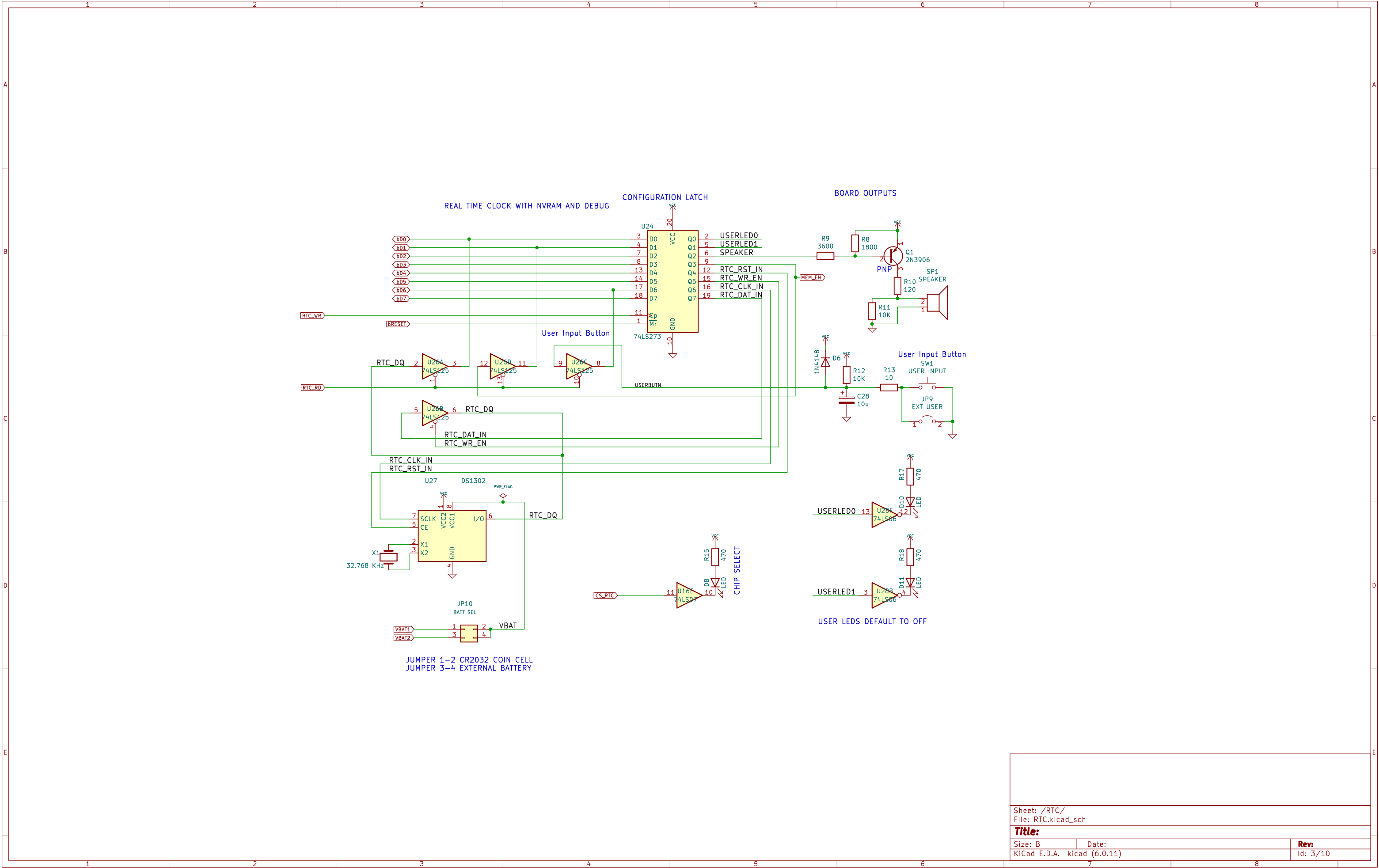
File: RAM.kicad\_sch

File: ROM.kicad\_sch

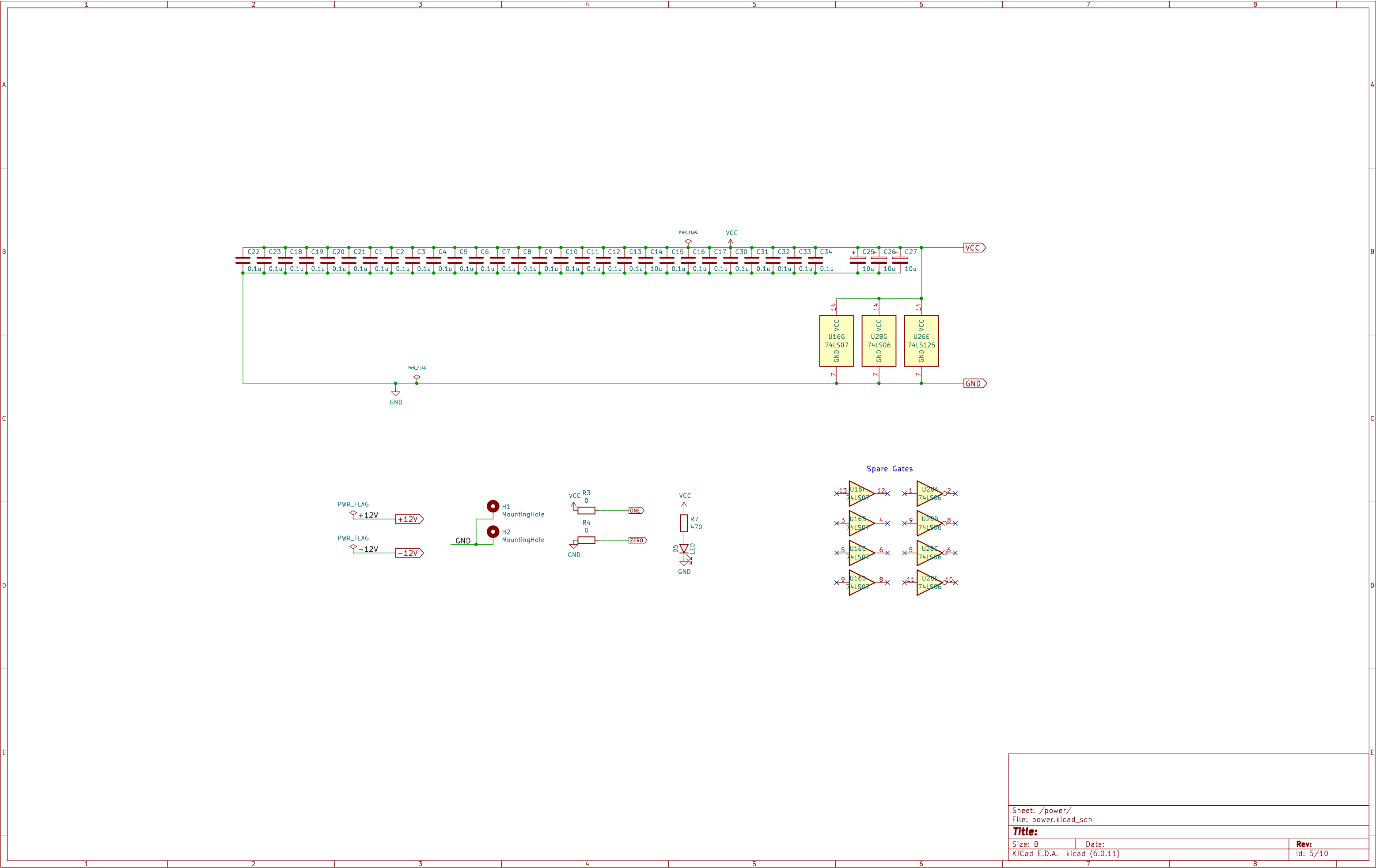
SPARE SOCKETS  
FOR DEBUGGING

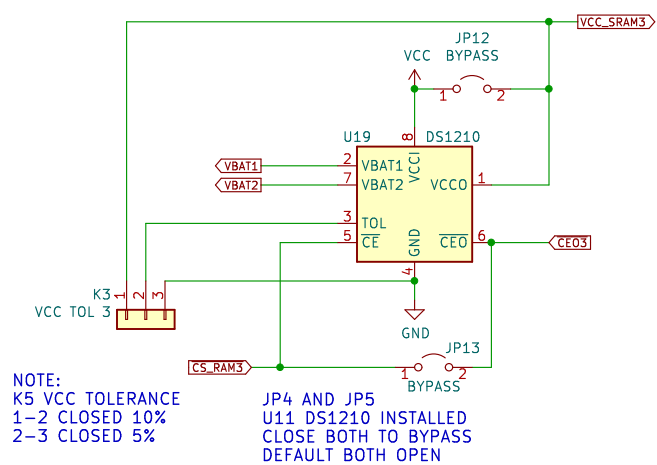
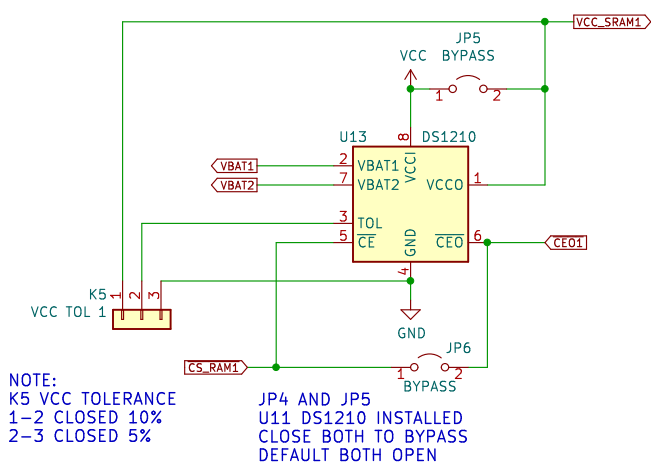
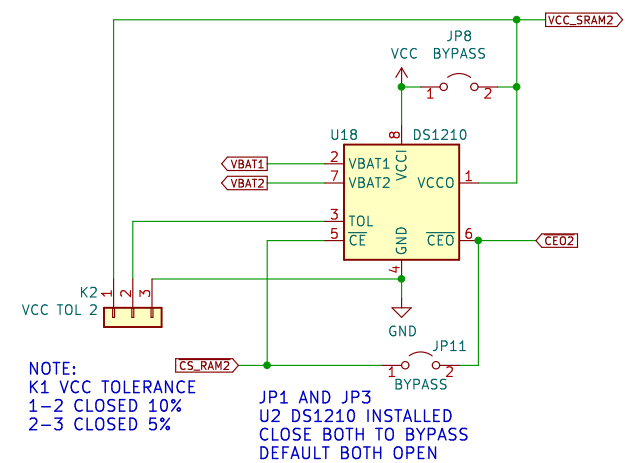
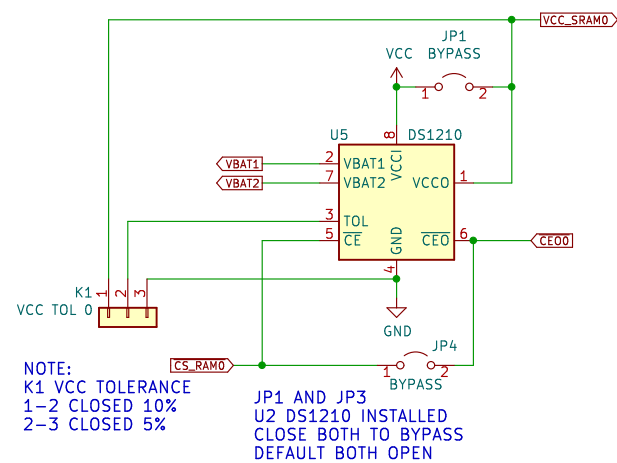
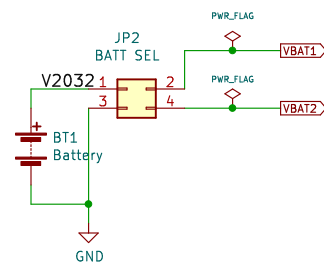


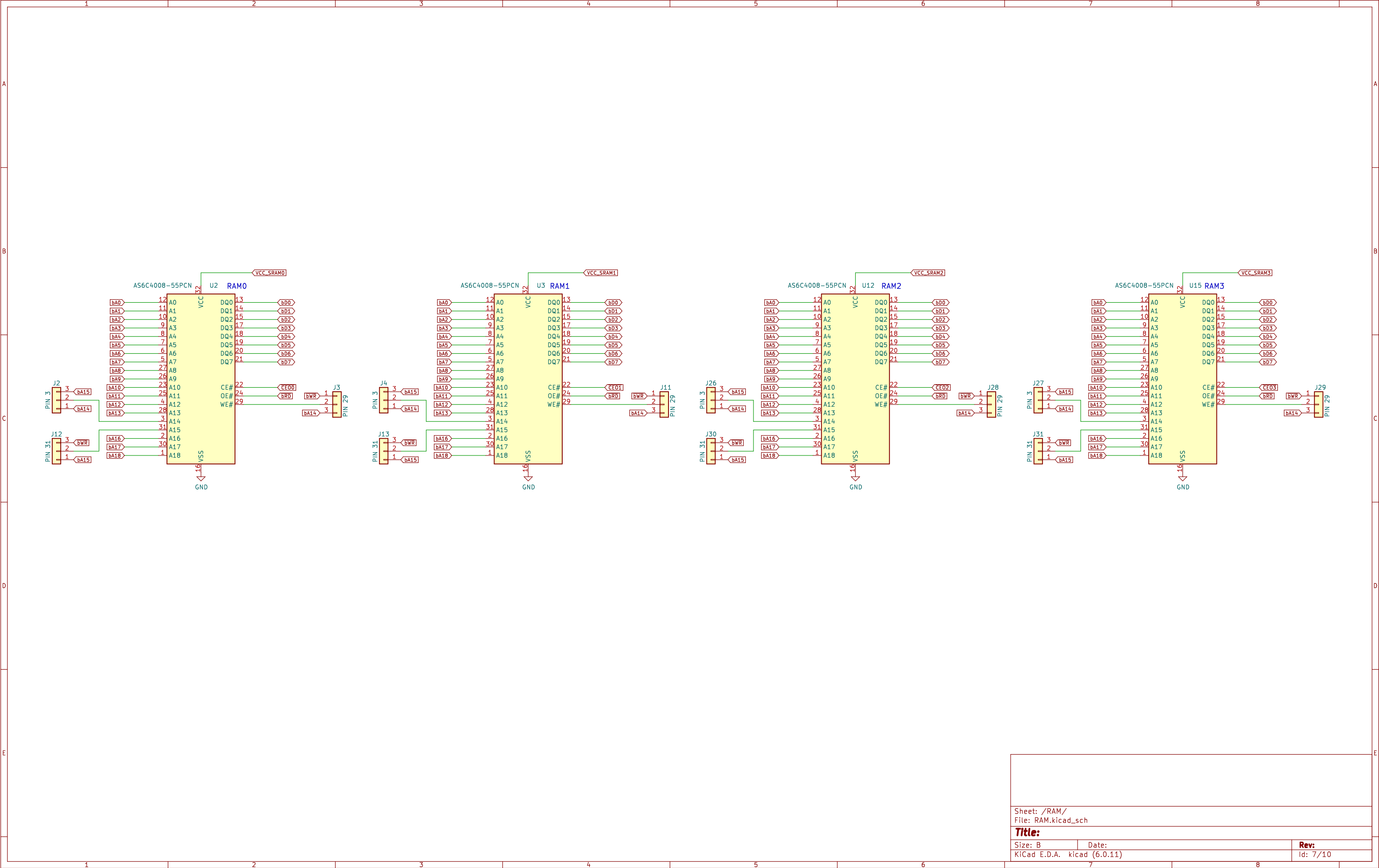
1	2	3	4	5	6	7	8	
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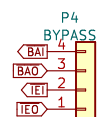
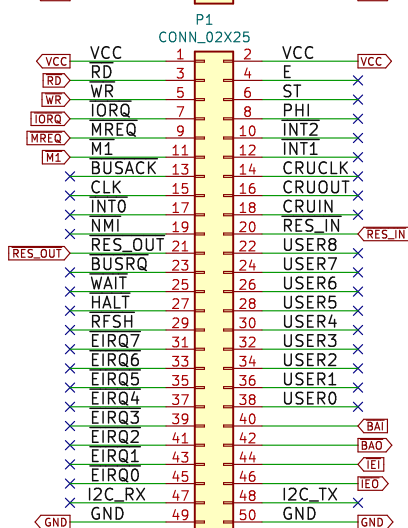
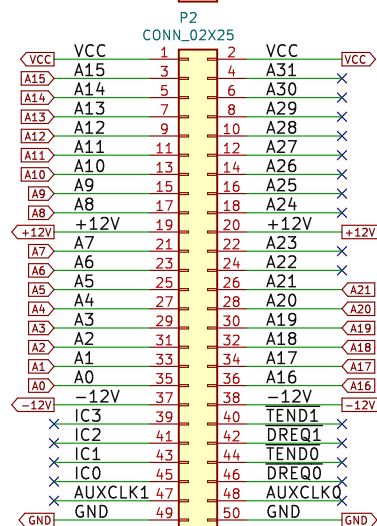
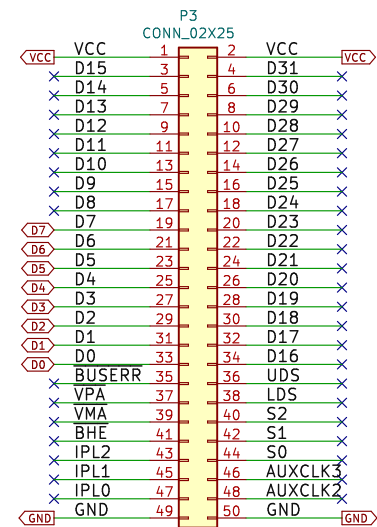




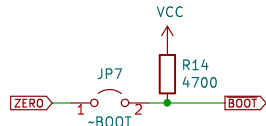
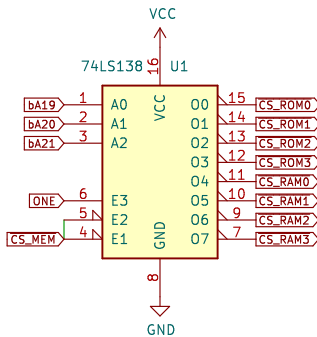
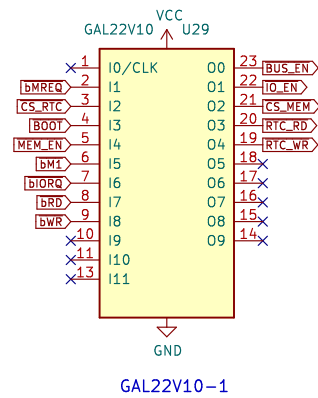
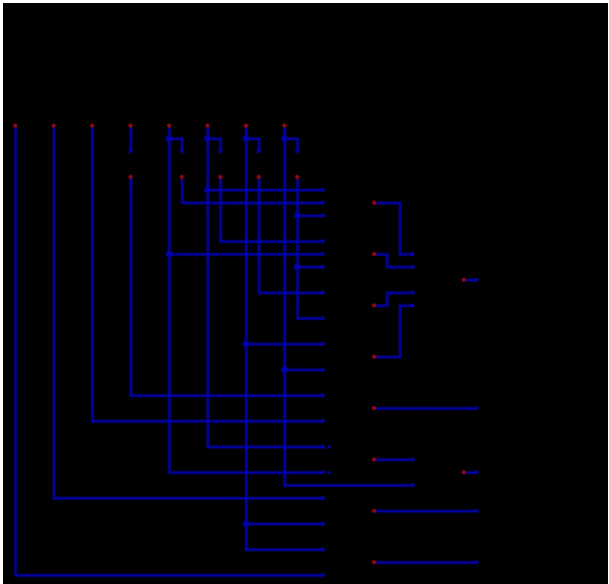








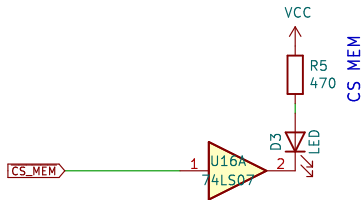
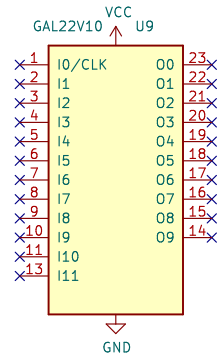




JP7 NOTE: FIRST ROM & RAM BOARD IN SYSTEM MUST HAVE JP7 JUMPER INSTALLED. ALSO MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE. FIRST BOARD ONLY RESPONDS WHEN MEM\_EN LOW (BY DEFAULT).

SECOND ROM & RAM BOARD INSTALLED IN SYSTEM MUST HAVE JP7 JUMPER NOT INSTALLED. ALSO SECOND BOARD ONLY RESPONDS WHEN MEM\_EN IS HIGH.

ACCESS IS CONTROLLED BY SOFTWARE TO ENSURE THAT ONLY ONE OF BOARDS HAS MEM\_EN HIGH AT ANY GIVEN TIME OR BUS CONTENTION.



Sheet: /GALS/  
File: GALS.kicad\_sch

**Title:**

Size: B Date:  
KiCad E.D.A. kicad (6.0.11)

**Rev:**  
Id: 9/10

