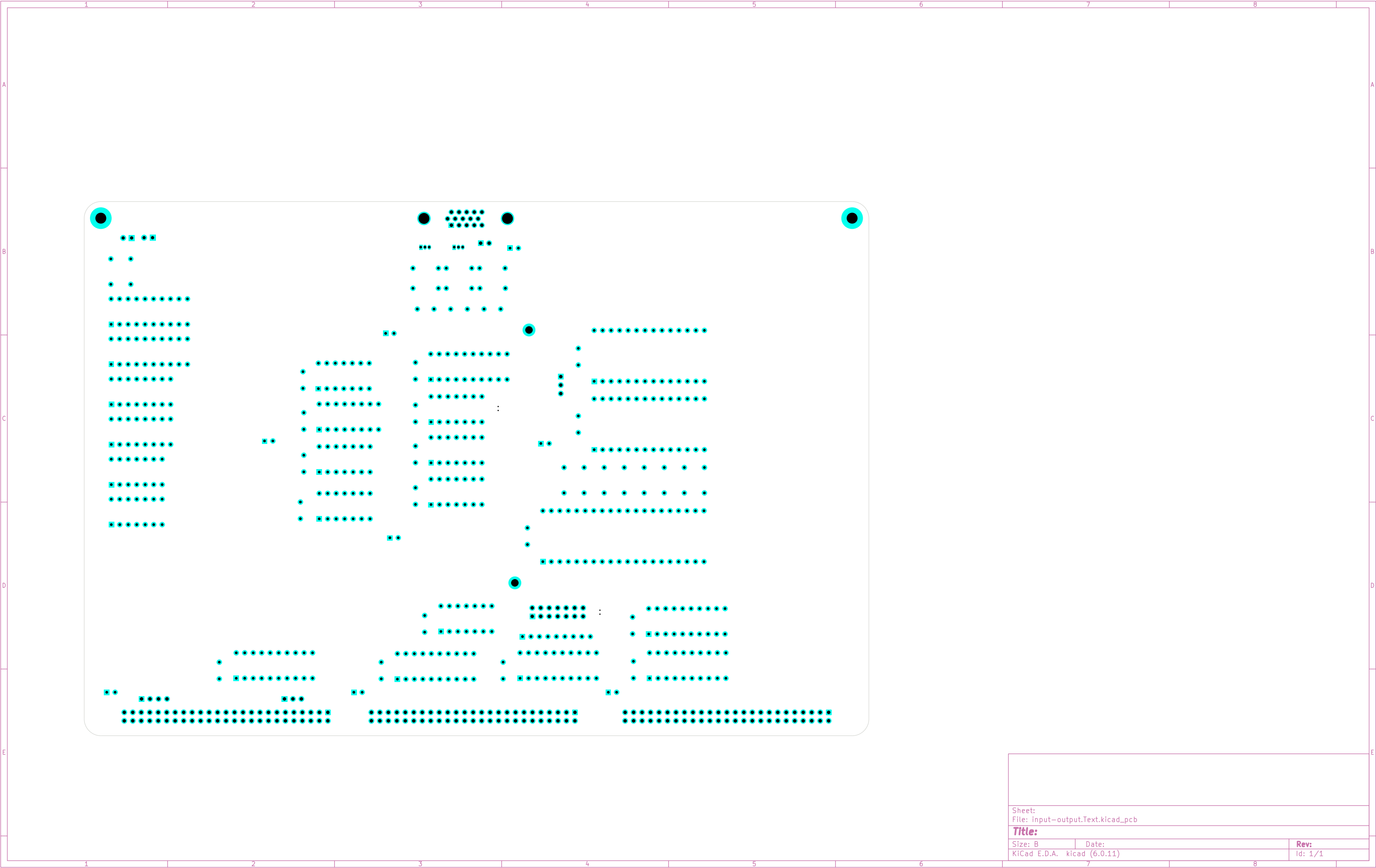
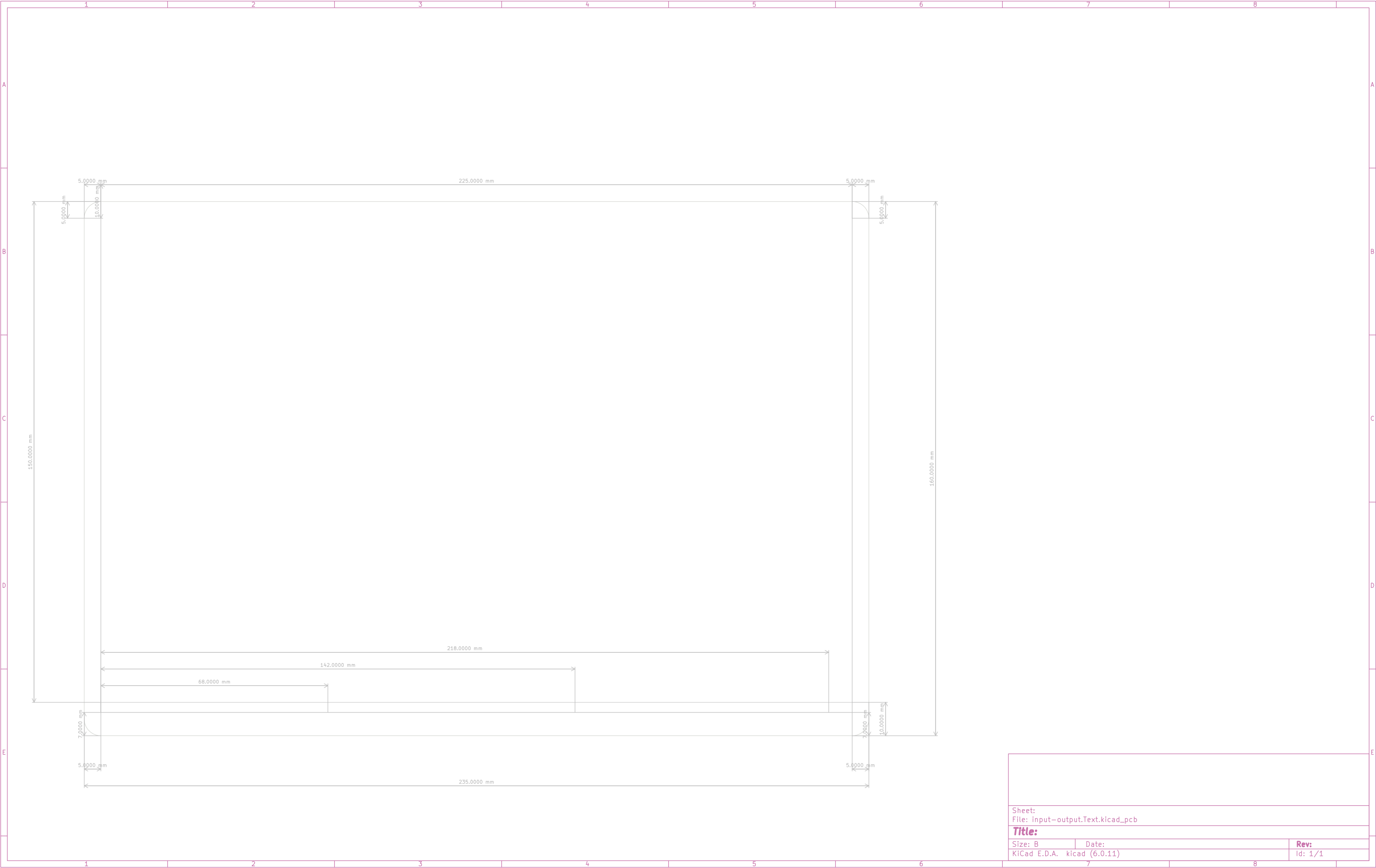


This image displays a PCB layout for a 4-layer board. The top layer (Layer 1) is populated with various components, including a large black rectangular component at the top center, several smaller black rectangular components, and numerous small circular vias. The bottom layer (Layer 4) features a dense grid of vias, indicating a high-density design. The layout is enclosed in a rectangular frame with rounded corners. In the bottom right corner, there is a title block containing the following information:

Sheet:		
File: input-output.Text.kicad_pcb		
<b>Title:</b>		
Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 1/1



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Sheet:  
File: input-output.Text.kicad\_pcb

Title:

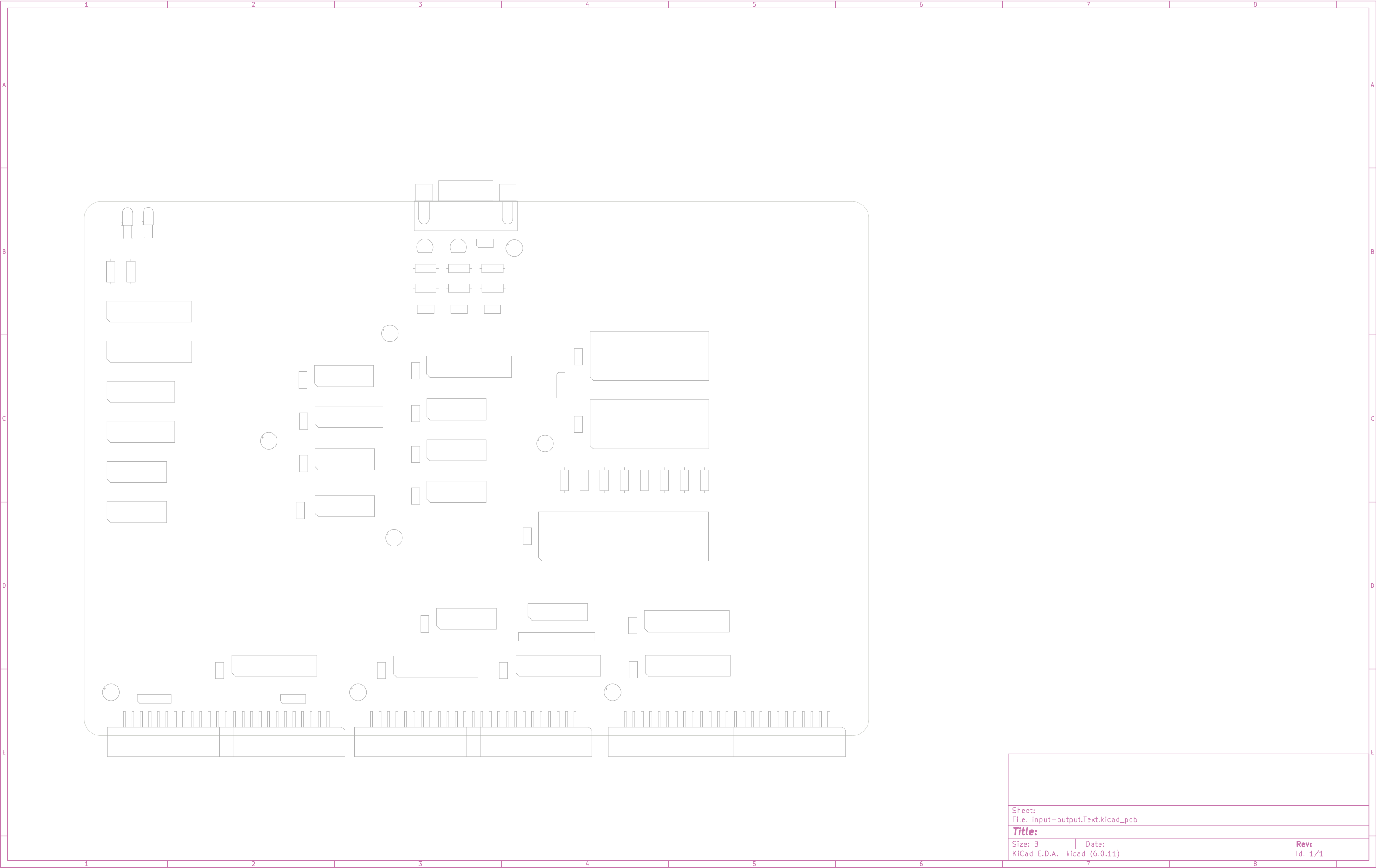
Size: BDate:

KiCad E.D.A. kicad (6.0.11)

Rev:

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Sheet:		
File: input-output.Text.kicad_pcb		
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KiCad E.D.A. kicad (6.0.11)	Id: 1/1	

