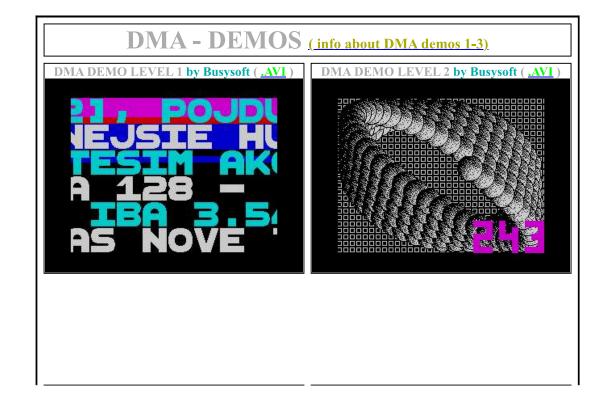
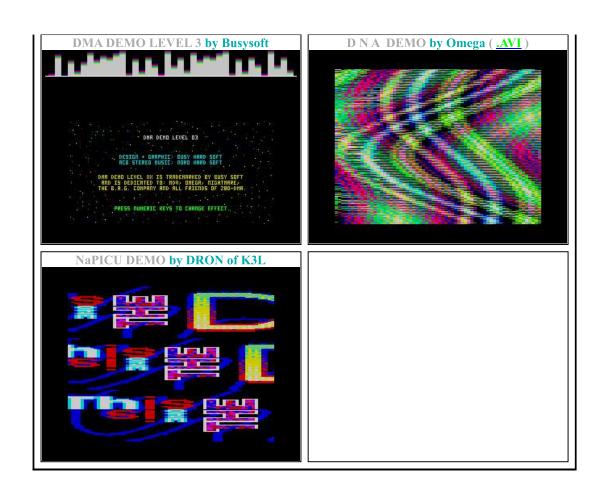


DATA-GEAR is DMA interface for 8-bit computers with Z80CPU. Original schematic is DMA part of interface MB02+. DATA-GEAR is connected to socket for Z80CPU, there's no need any hardware modifications in your computer. DMA chip use dirrect access to memory or port. Transfer of linear data block (max. 64kB) memory to memory / memory to port / port to memory / port to port. For computers with max. CPU speed 4MHz is used Z80DMA chip, but for faster computers (SAM COUPE=6MHz, ZS SCORPION TURBO=7MHz ...) is need use DMA chip at 6-8MHz. DMA chip is programmed via port 11(#0B) = MB02 compatible. On board is jumper JP1 for change programming port to 107(#6B). If JP1 is connect, then port 107(#6B) is used.

Max. speed of data transfer on ZX128+ is 17.3 kB(17727 bytes) / frame = 865.6 kB(886350 bytes) / second

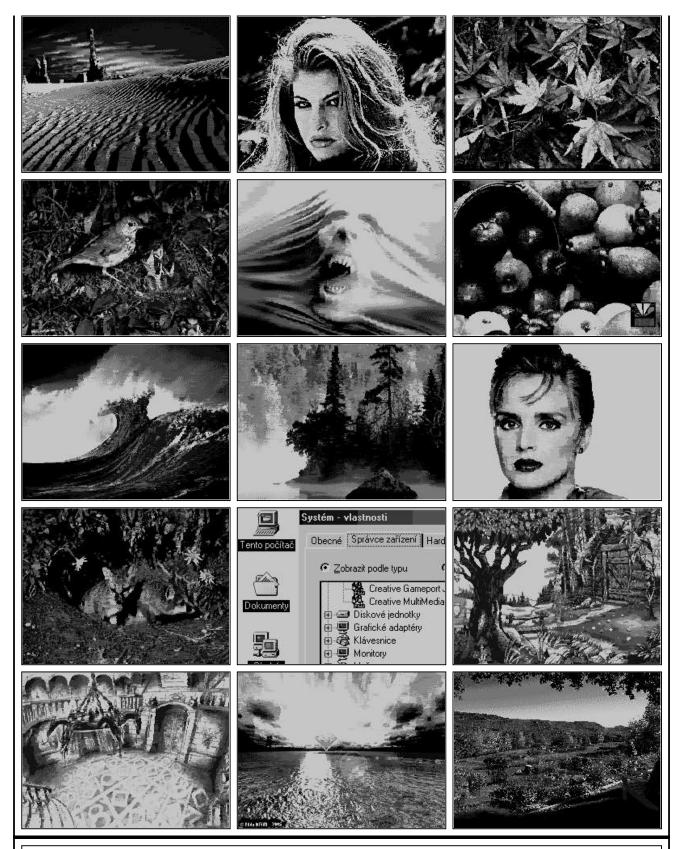
DATA-GEAR - DOWNLOAD
PCB + Schematic for Eagle
<u>GAL equations + JEDec</u>
INFO ABOUT DMA
<u>DMA - info</u>
Czech scripts(page 225-243)
Z80 peripherals(page 77-94)
<u>DMA - CP/M</u>
VIDEO EXAMPLES
Example of DMA transfer





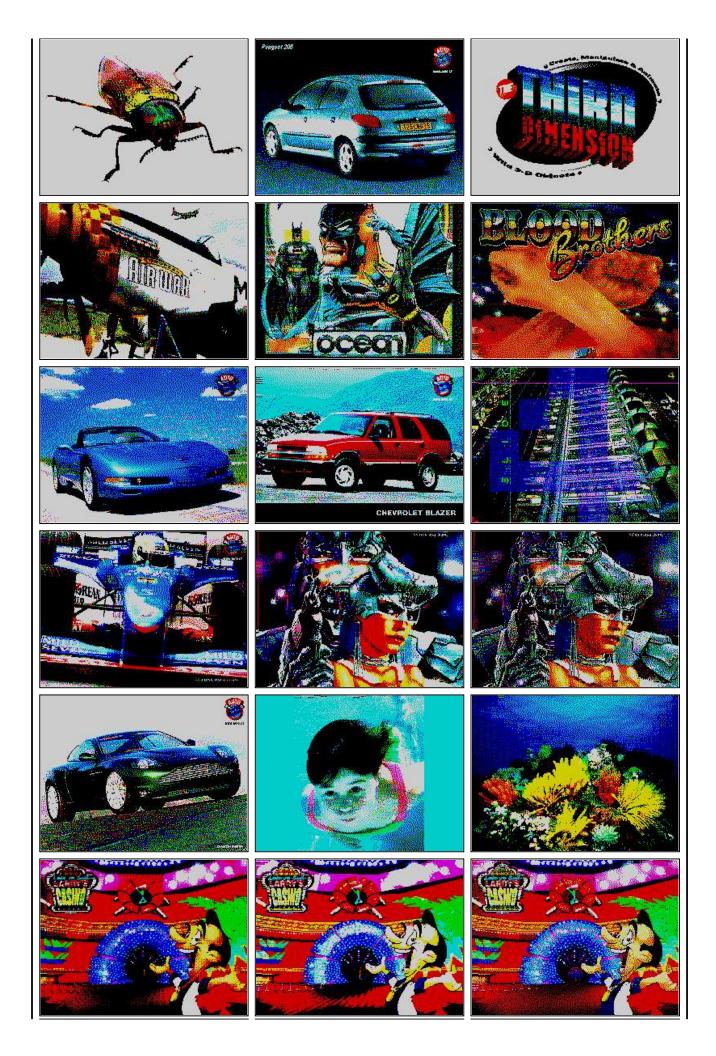


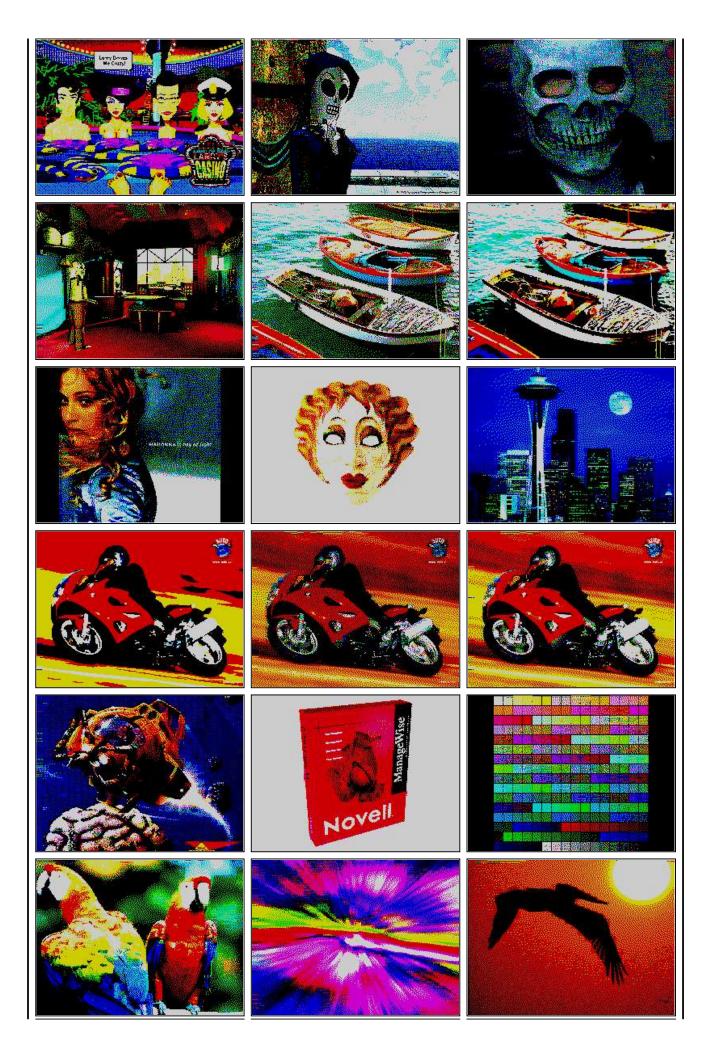


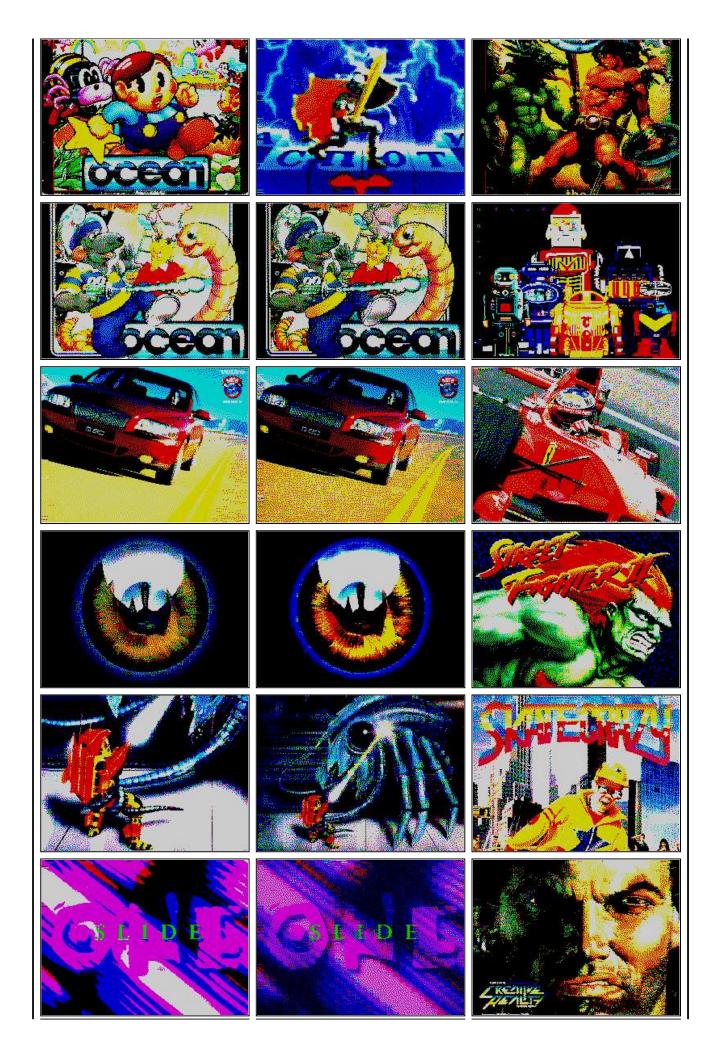


COLOR MULTITECH GRAPHICS

FULLSCREEN MULTICOLOR PICTURES by Omega









DOWNLOAD

GRAPHICS

MULTITECH SCREEN VIEWER + GREYSCALE PICTURES 1
MULTITECH SCREEN VIEWER + GREYSCALE PICTURES 2
MULTITECH SCREEN VIEWER + COLOR PICTURES
MULTITECH SCREEN VIEWER - SOURCE CODE
MULTITECH DMA EFFECTS (from DNA demo by Omega) - SOURCE CODE

MULTITECH SCREEN VIEWER for TIMEX / PENTAGON without DMA + GREYSCALE PICTURES 1
MULTITECH SCREEN VIEWER for TIMEX / PENTAGON without DMA + GREYSCALE PICTURES 2
MULTITECH SCREEN VIEWER for TIMEX / PENTAGON without DMA + COLOR PICTURES
MULTITECH SCREEN VIEWER for TIMEX / PENTAGON without DMA - SOURCE CODE

GRAPHIC CONVERTORS

ZXS MULTITECH CONVERTOR for PC (link to original 8BC webpage)

BMP2SCR EXP 2.11a

BMP2SCR EXP 2.10b

BMP2SCR PRO 2.01

BMP2SCR 1.751

BMP2SCR v1.75

BMP2SCR v1.74

BMP2SCR v1.73

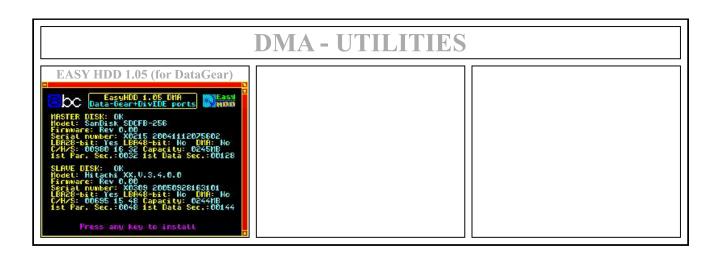
BMP2SCR v1.70

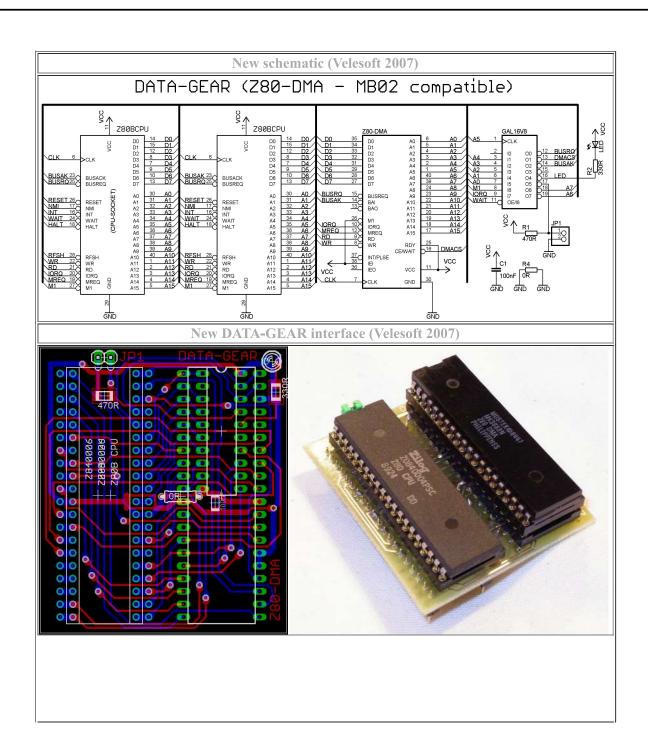
BMP2SCR v1.62

BMP2SCR INSTR

BMP2SCR PRO 2.00c INSTR

RETRO X - alpha 5



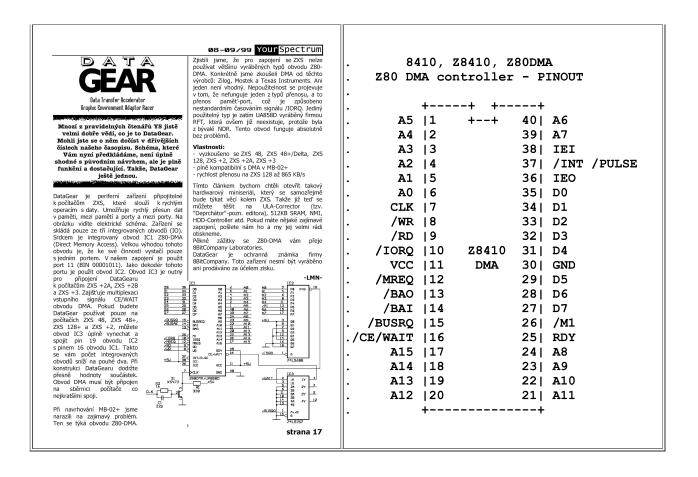




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Original schematic (8BC Company)

Z80 DMA - PINOUT



LIST OF Z80DMA MANUFACTURERS

ZILOG: (tested with Data-Gear)

- -Z841004 = 4MHz
- Z84C1006 = 6MHz 6 MHz version ideal for SAM COUPE or ZX
- Z84C1008 = 8MHz faster type of DMA, ideal for fast Russian ZX clones with Z80CPU at 7MHz (too for SAM or ZX)

RFT: (tested with Data-Gear)

- UA858D = 4MHz - German DMA clone used in MB02+ interface

MOSTEK: (tested with Data-Gear)

- MK3883N4 = 4MHz
- MK3883N = 2.5MHz tested on ZX, work correct on 3.58 MHz

ST: (tested with Data-Gear)

- Z8410AB1 (4MHz)

TOSHIBA:

- TMPZ84C10AP-6 = 6MHz

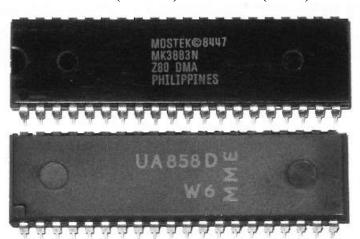
SHARP:

- LH0083A - Japan Z80DMA clone (4MHz?)

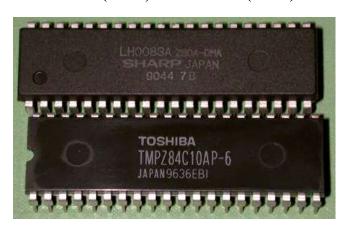
SGS:

- Z8410AB1 - Italian Z80DMA clone (4MHz?)

MOSTEK(2.5 MHz) and UA858D(4MHz)



SHARP(4MHz) and TOSHIBA(6MHz)









EXAMPLE OF DMA MULTICOLOR IN BORDER AREA

(transfer mode 2T read data from memory + 2T write data to port #FE)