

bus

File: bus.kicad\_sch

DUART

File: DUART.kicad\_sch

kbdmse

File: kbdmse.kicad\_sch

GAL

File: GAL.kicad\_sch

power

File: power.kicad\_sch

parallel

File: parallel.kicad\_sch

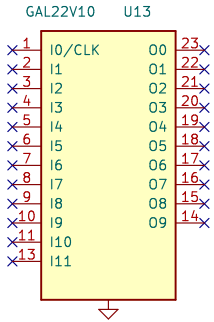
USB-SD

File: USB-SD.kicad\_sch

buffers

File: buffers.kicad\_sch

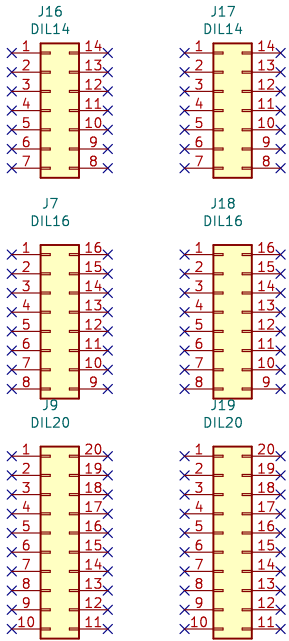
SPARE GAL FOR  
DEBUGGING

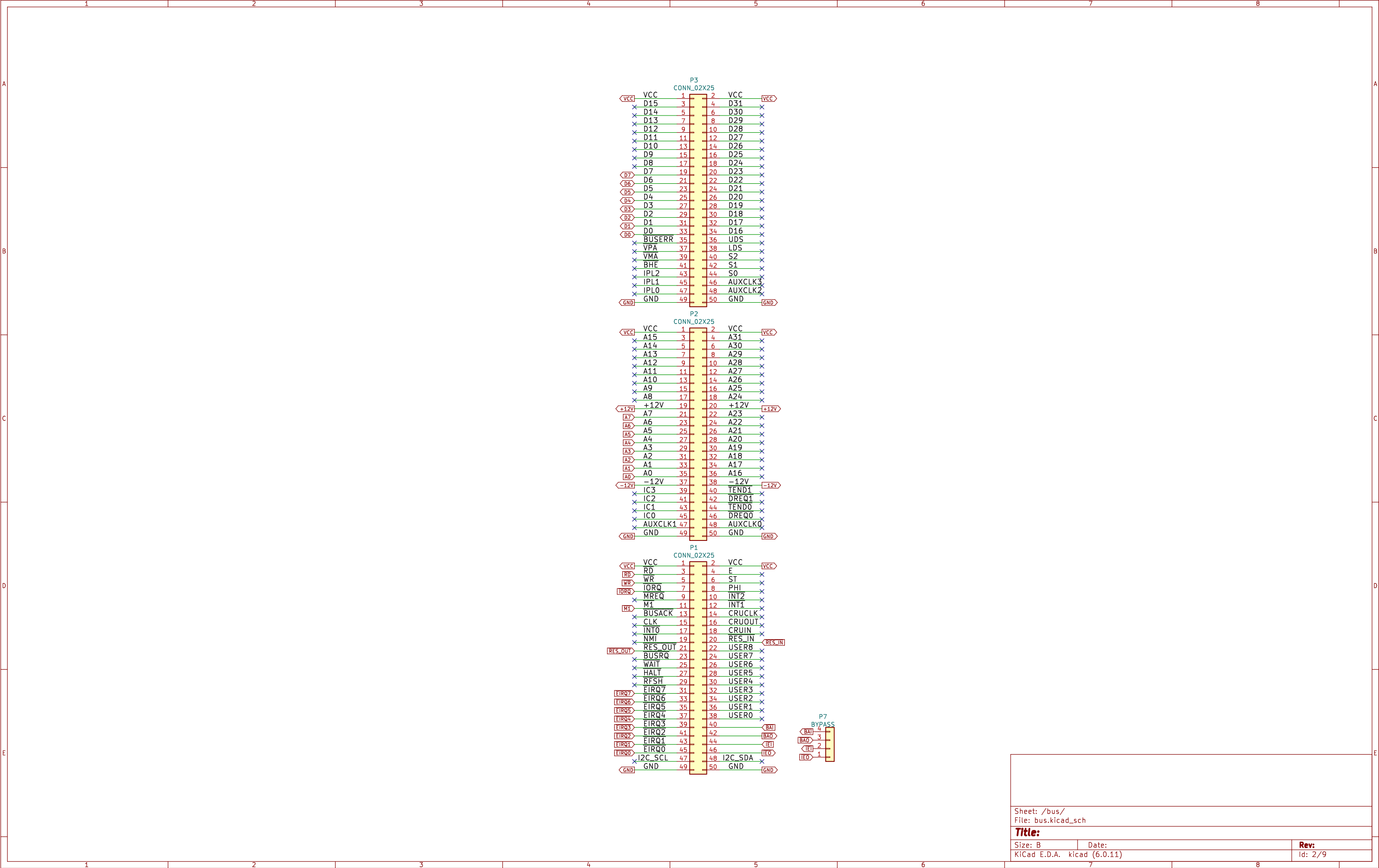


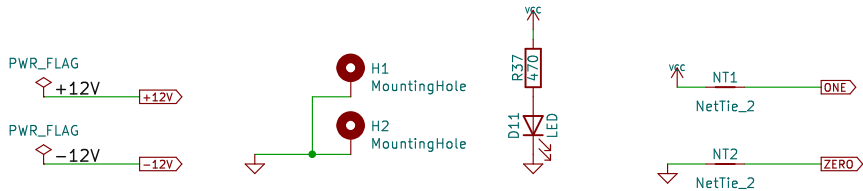
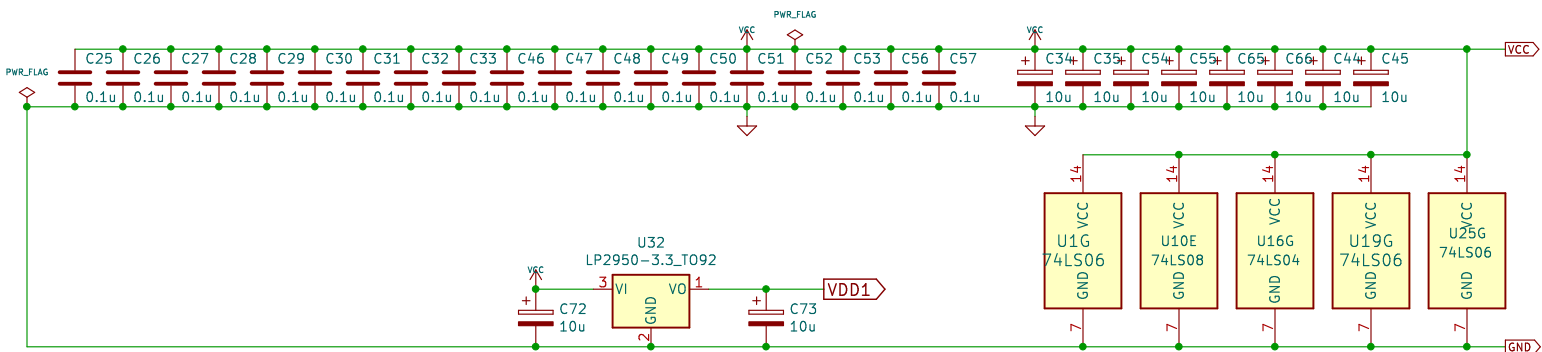
THROUGH HOLES FOR  
PATCH WIRE ROUTING  
ACROSS SIDES



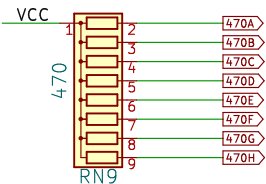
SPARE SOCKETS  
FOR DEBUGGING







Spare Components



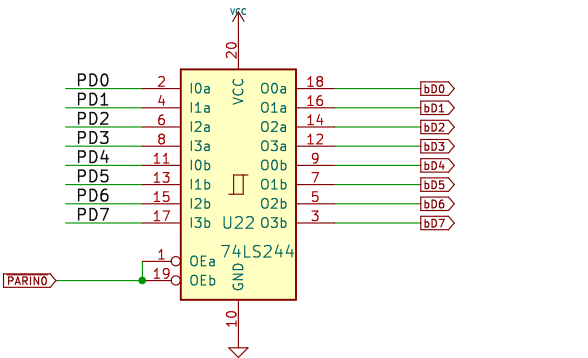
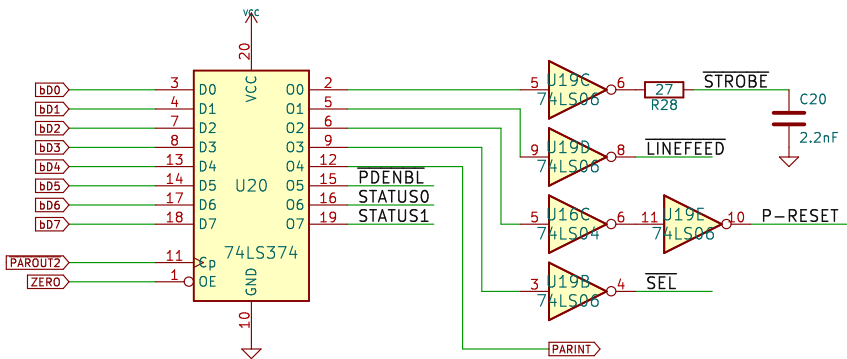
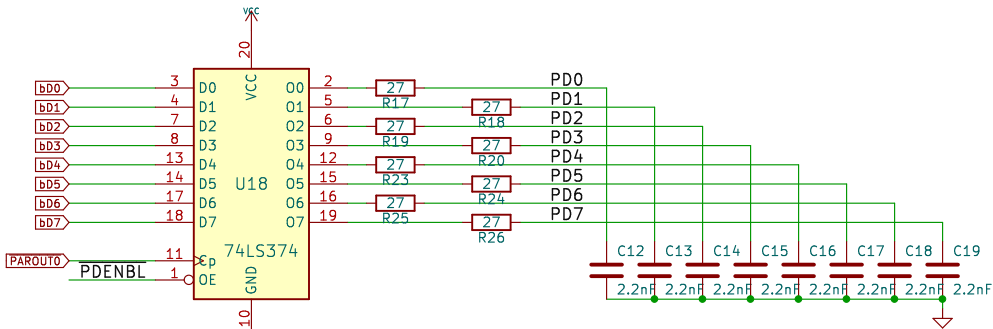
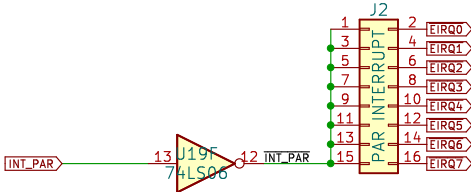
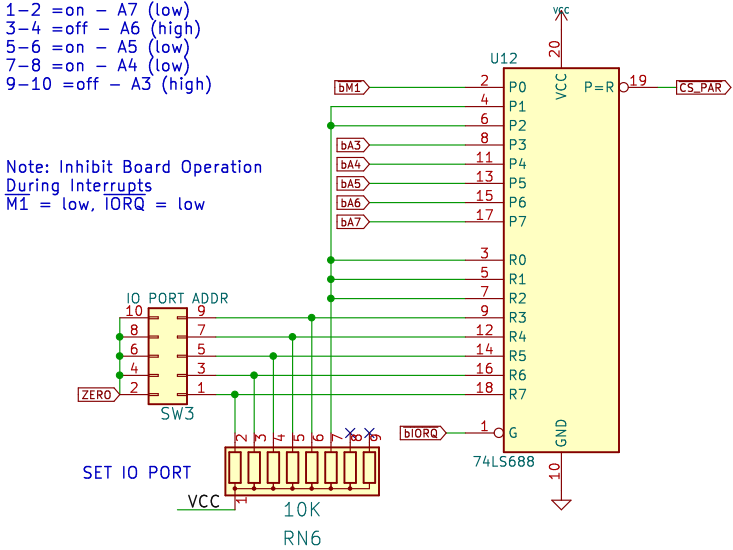
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|---|-------|---------|
| Sheet: /power/<br>File: power.kicad_sch |       |         |
| <b>Title:</b>                           |       |         |
| Size: B                                 | Date: | Rev:    |
| KiCad E.D.A. kicad (6.0.11)             |       | Id: 3/9 |



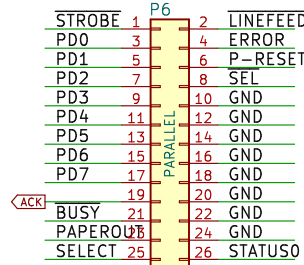
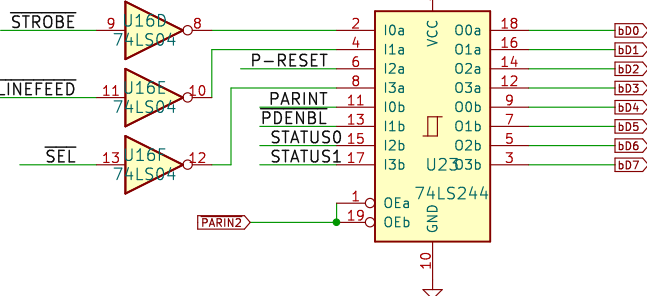
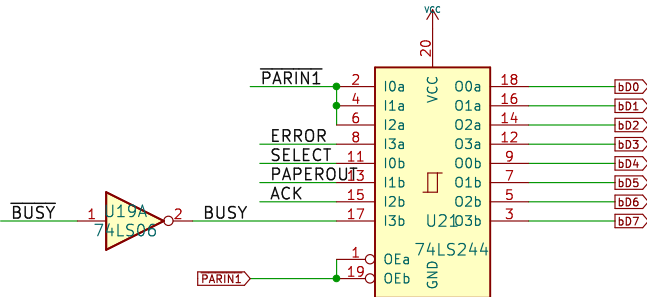
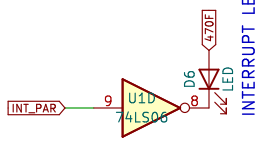
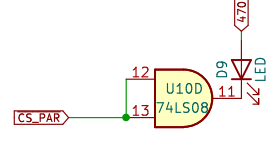
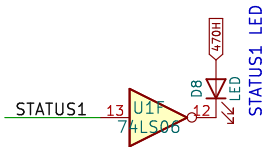
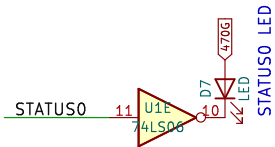
IO SELECTION CIRCUIT  
PARALLEL PORT, PS/2 KEYBOARD & MOUSE, USB-SD

Note: IO Address Port \$48-\$4F  
1-2 =on - A7 (low)  
3-4 =off - A6 (high)  
5-6 =on - A5 (low)  
7-8 =on - A4 (low)  
9-10 =off - A3 (high)

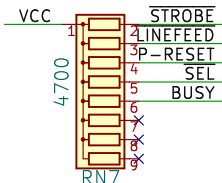
Note: Inhibit Board Operation  
During Interrupts  
M1 = low, IORQ = low



PARALLEL PORT BASED ON SCHEMATIC AT  
<http://www.linux-cae.net/Projects/Parallel/Dage/parport.html>



NOTE: STATUS0 WILL NOT APPEAR  
ON IBM PC COMPATIBLE DB25  
FEMALE CONNECTOR



Sheet: /parallel/  
File: parallel.kicad\_sch

**Title:**

Size: B

Date:

KiCad E.D.A. kicad (6.0.11)

**Rev:**

Id: 5/9

