

12345678

A

bus

File: bus.kicad_sch
6809CPU

power

File: power.kicad_sch
GALS

buffers

File: buffers.kicad_sch
bus sharing

fpanel

File: fpanel.kicad_sch
mapper

B

C

D

E

12345678

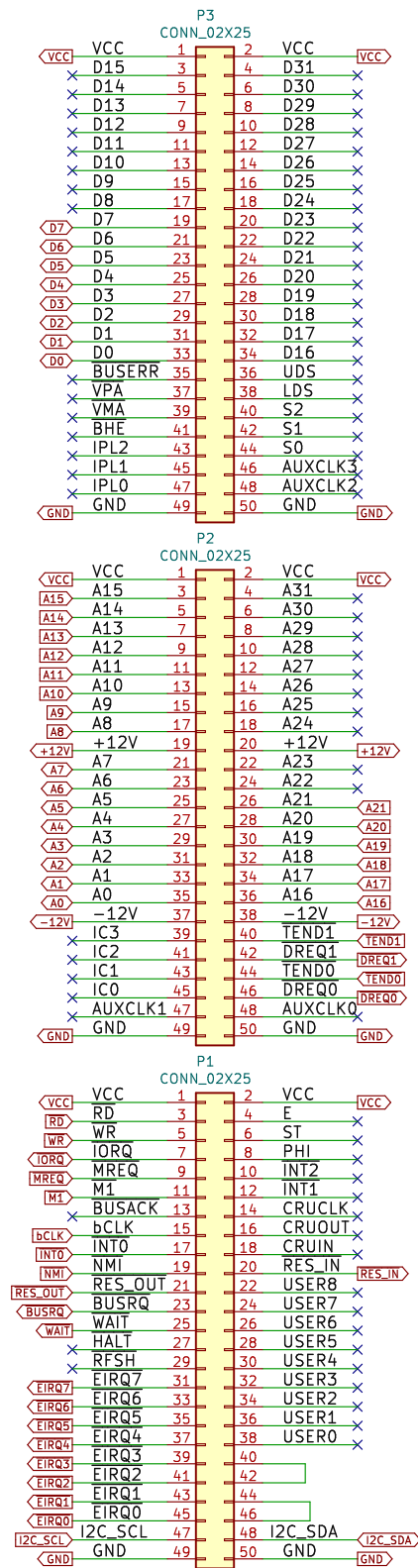
E

Sheet: /
File: processor.6809.kicad_sch

Title: Duodyne 6809 CPU board

Size: BDate: 2023-12-30Rev: V1.00

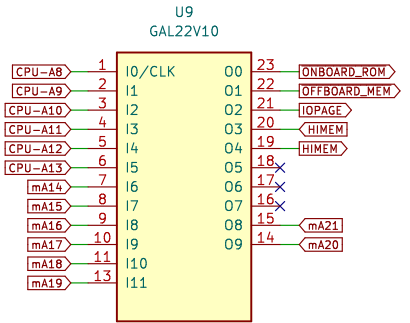
KiCad E.D.A. kicad (6.0.11)Id: 1/9



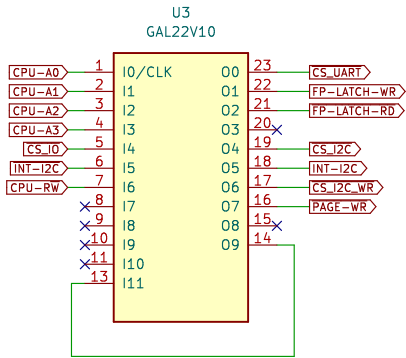
IO Address Port \$50-\$5F

\$50 Front Panel (RW)
\$51
\$52 PAGER (WR)
\$53
\$54
\$55
\$56-\$57 I2C(RW)
\$58-\$5F UART (RW)

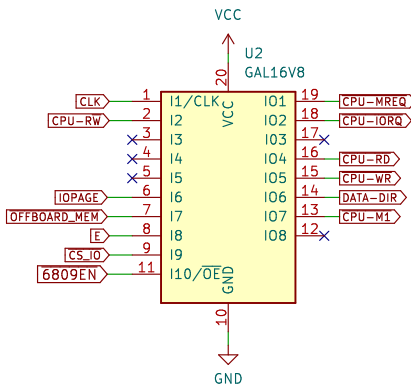
ADDRESS DECODER



IO DECODER



BUS SIGNALS



Sheet: /GALS/
File: GALS.kicad_sch

Title: Duodyne 6809 CPU board

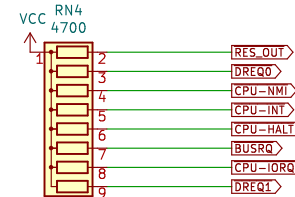
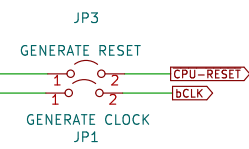
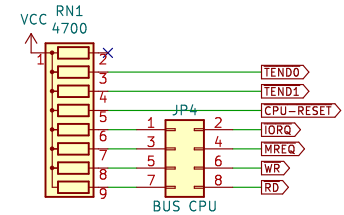
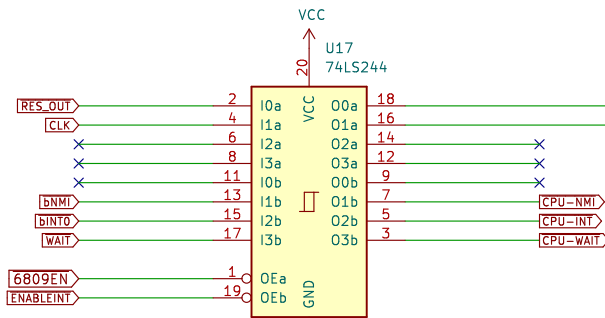
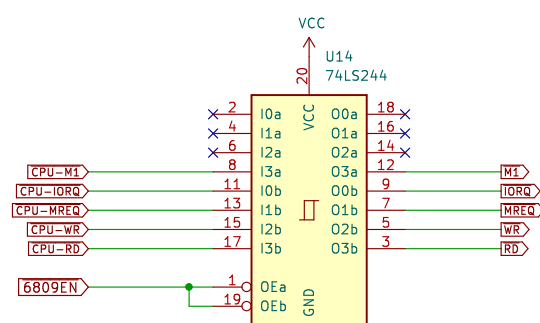
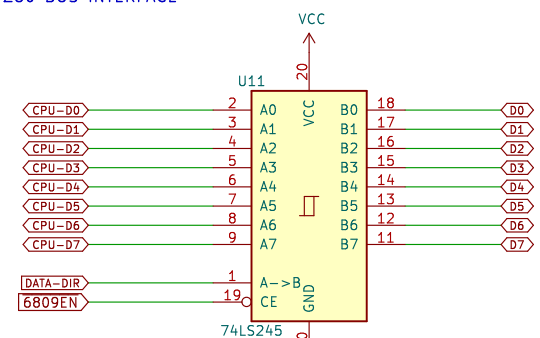
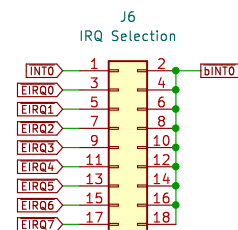
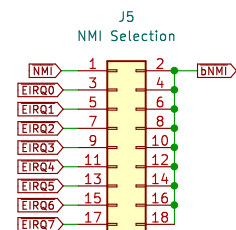
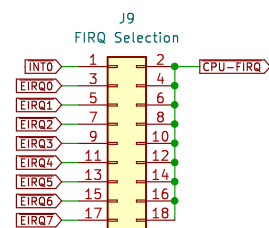
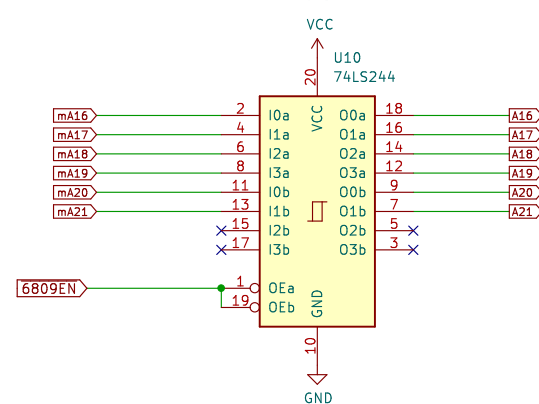
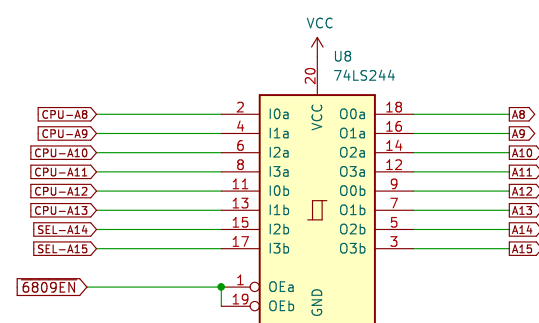
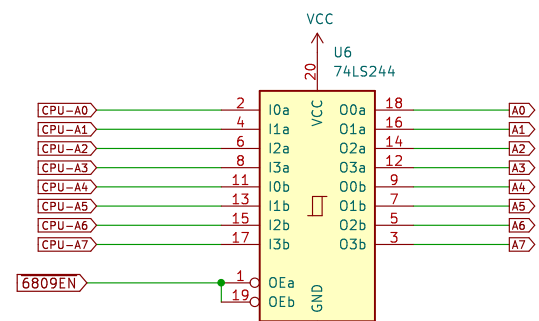
Size: B

Date: 2023-12-30

Rev: V1.00

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Id: 5/9





PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE

