

12345678

bus

power

buffers

fpanel

Z80CPU

File: bus.kicad_sch
DMA

File: power.kicad_sch
CPLDs

File: buffers.kicad_sch
IM2

File: fpanel.kicad_sch
waitstate

File: Z80CPU.kicad_sch
mapper

File: DMA.kicad_sch

File: CPLDs.kicad_sch

File: IM2.kicad_sch

File: waitstate.kicad_sch

File: mapper.kicad_sch

Sheet: /
File: Slick.kicad_sch

Title:

Size: B

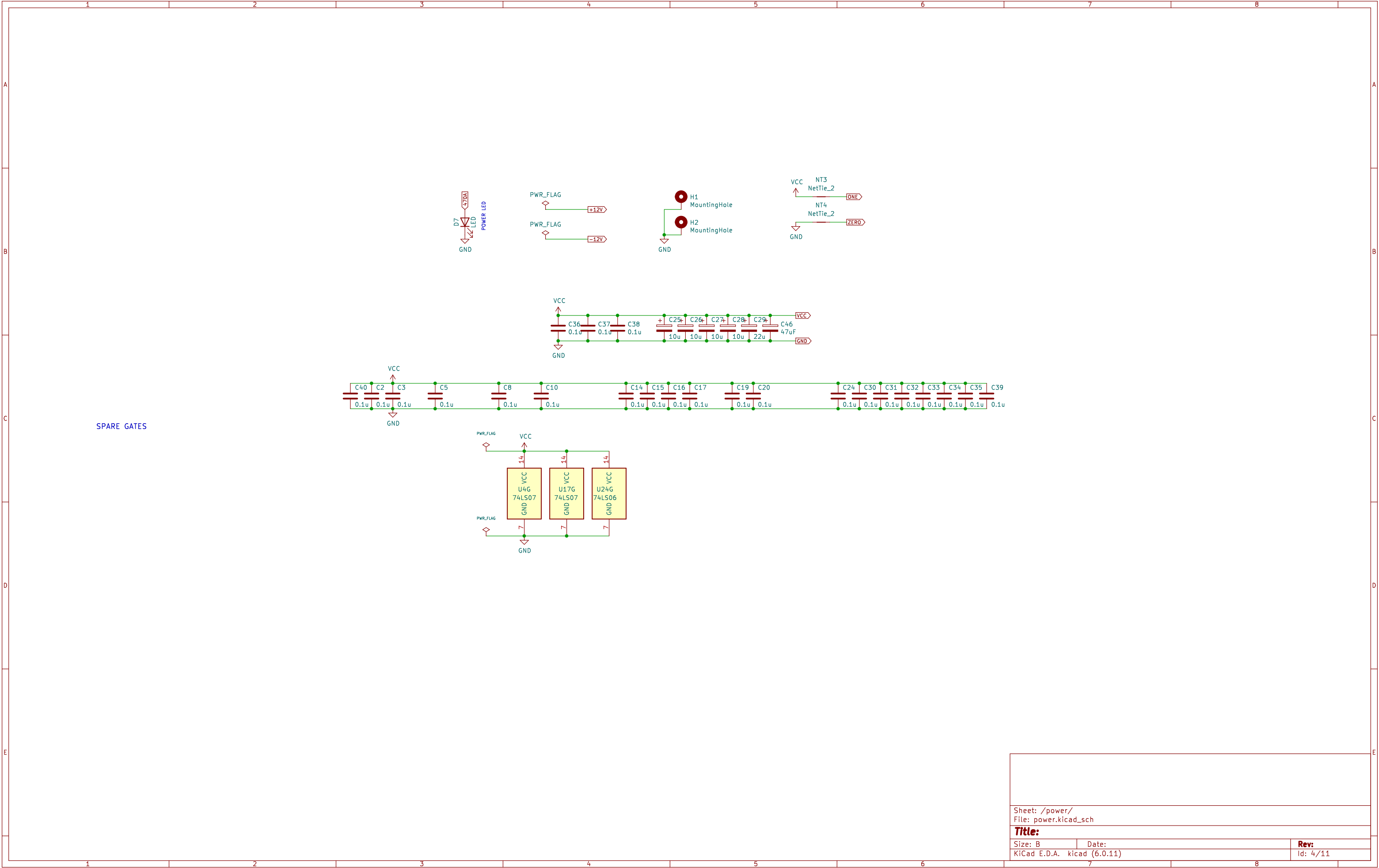
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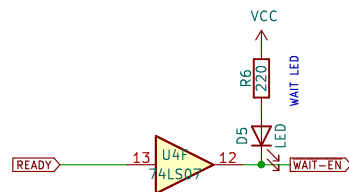
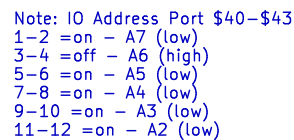
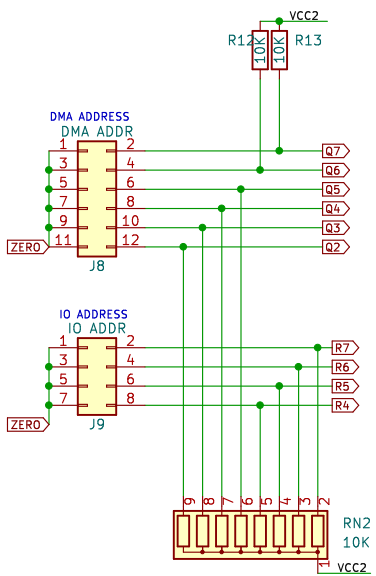
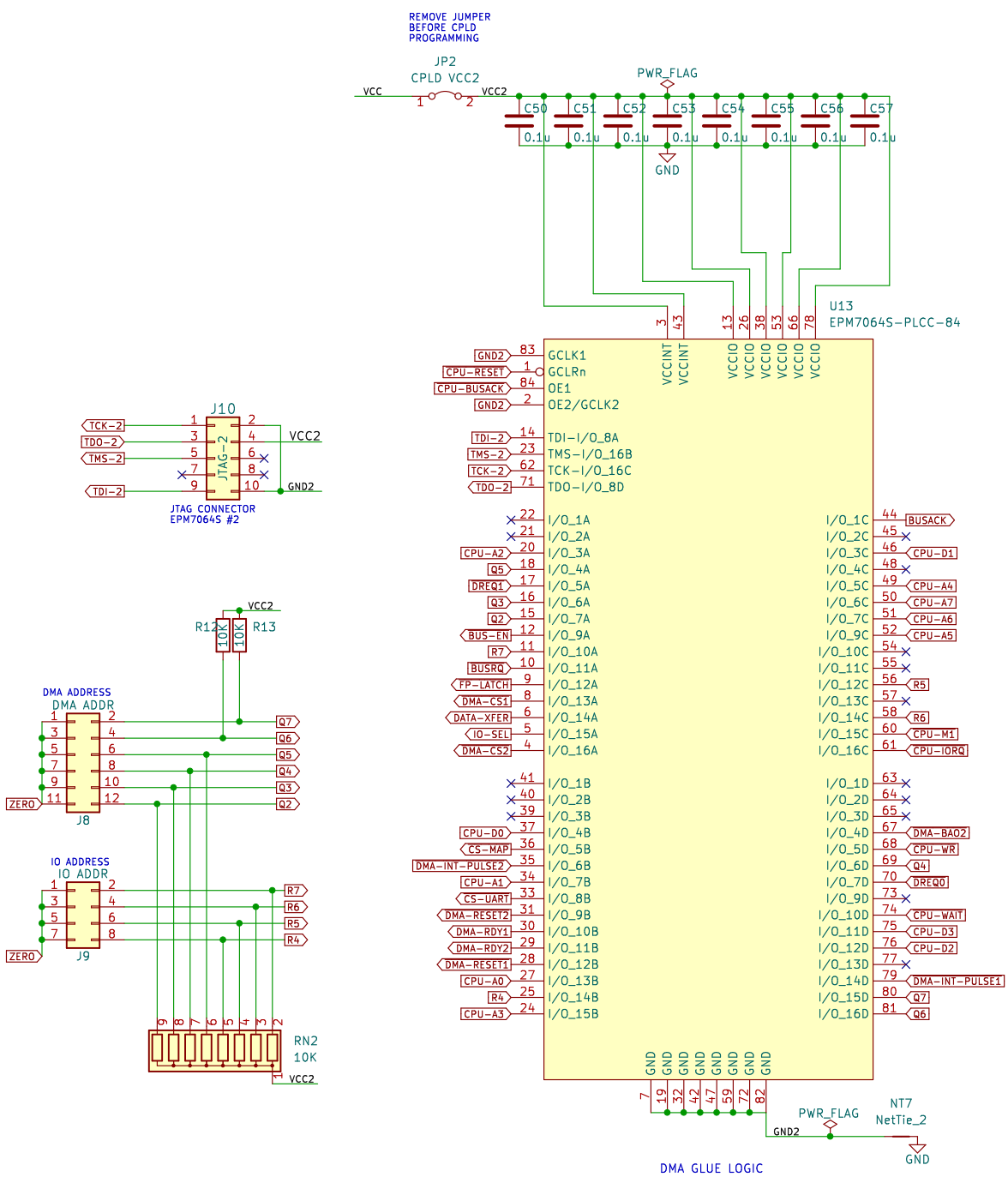
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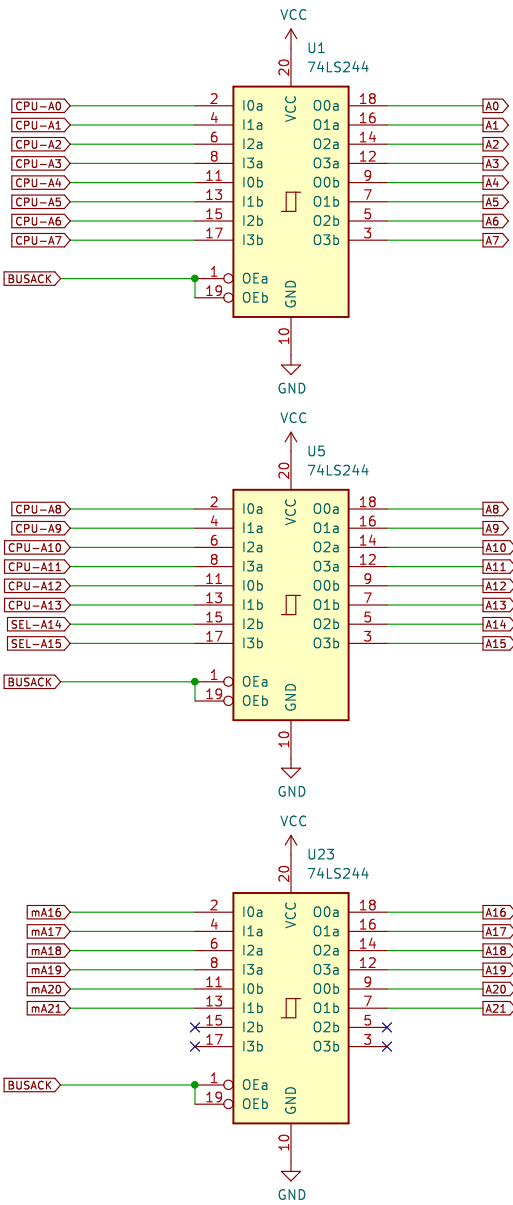
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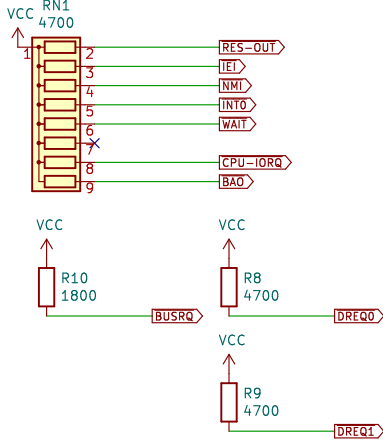
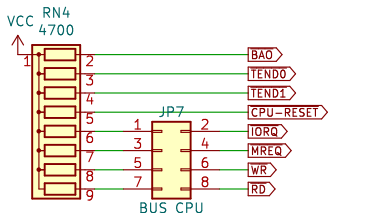
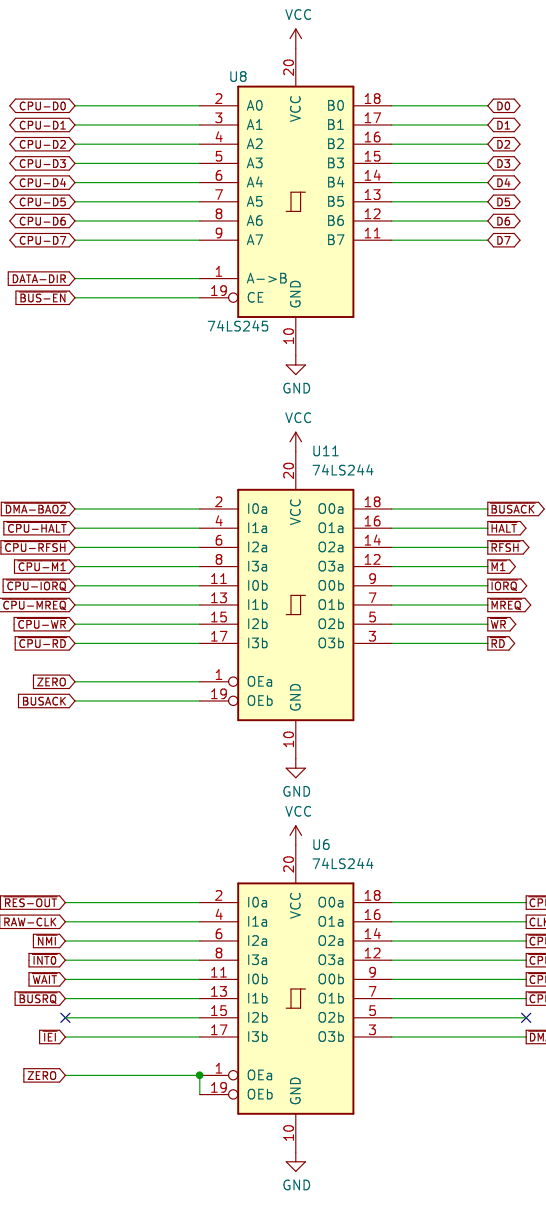


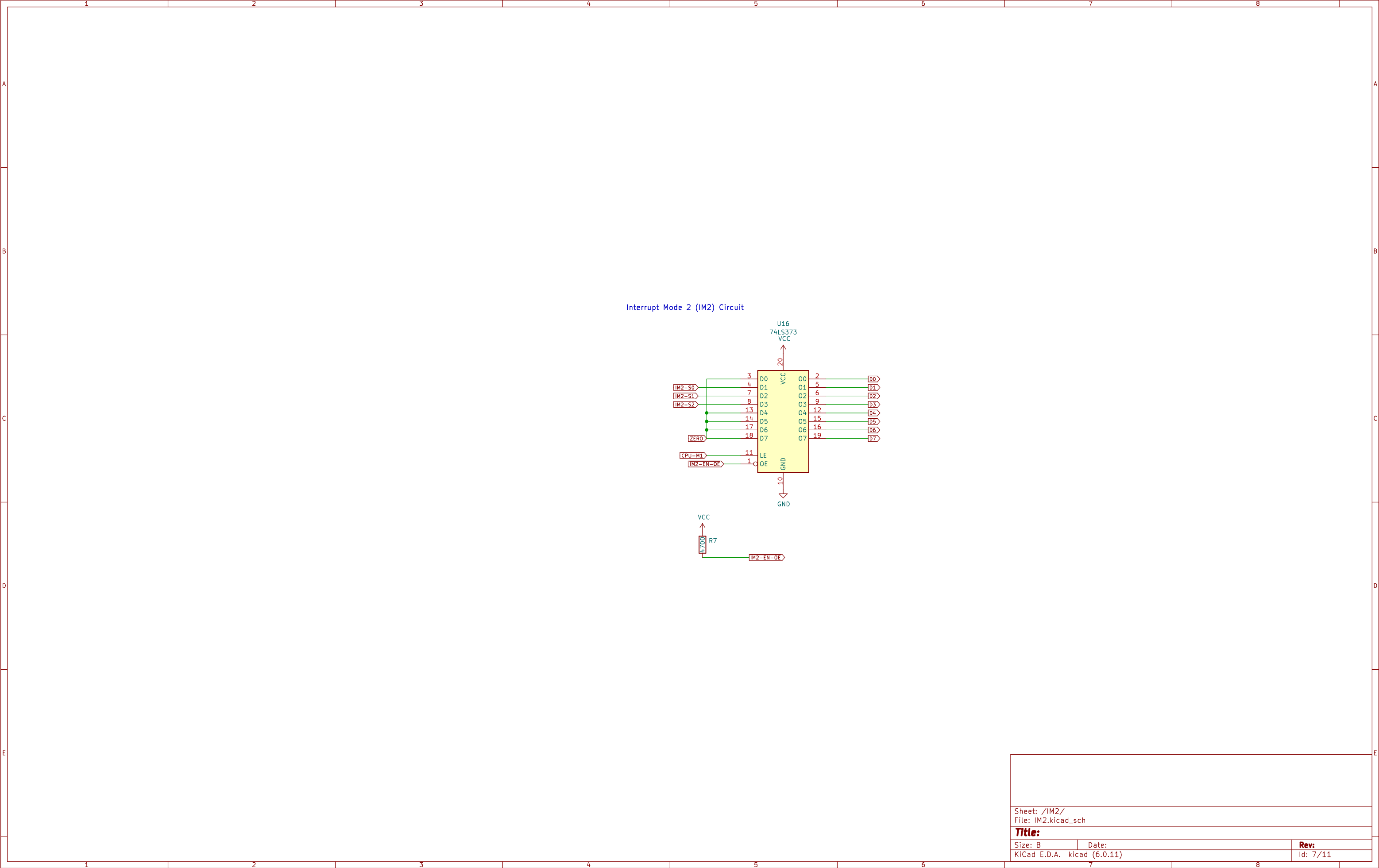




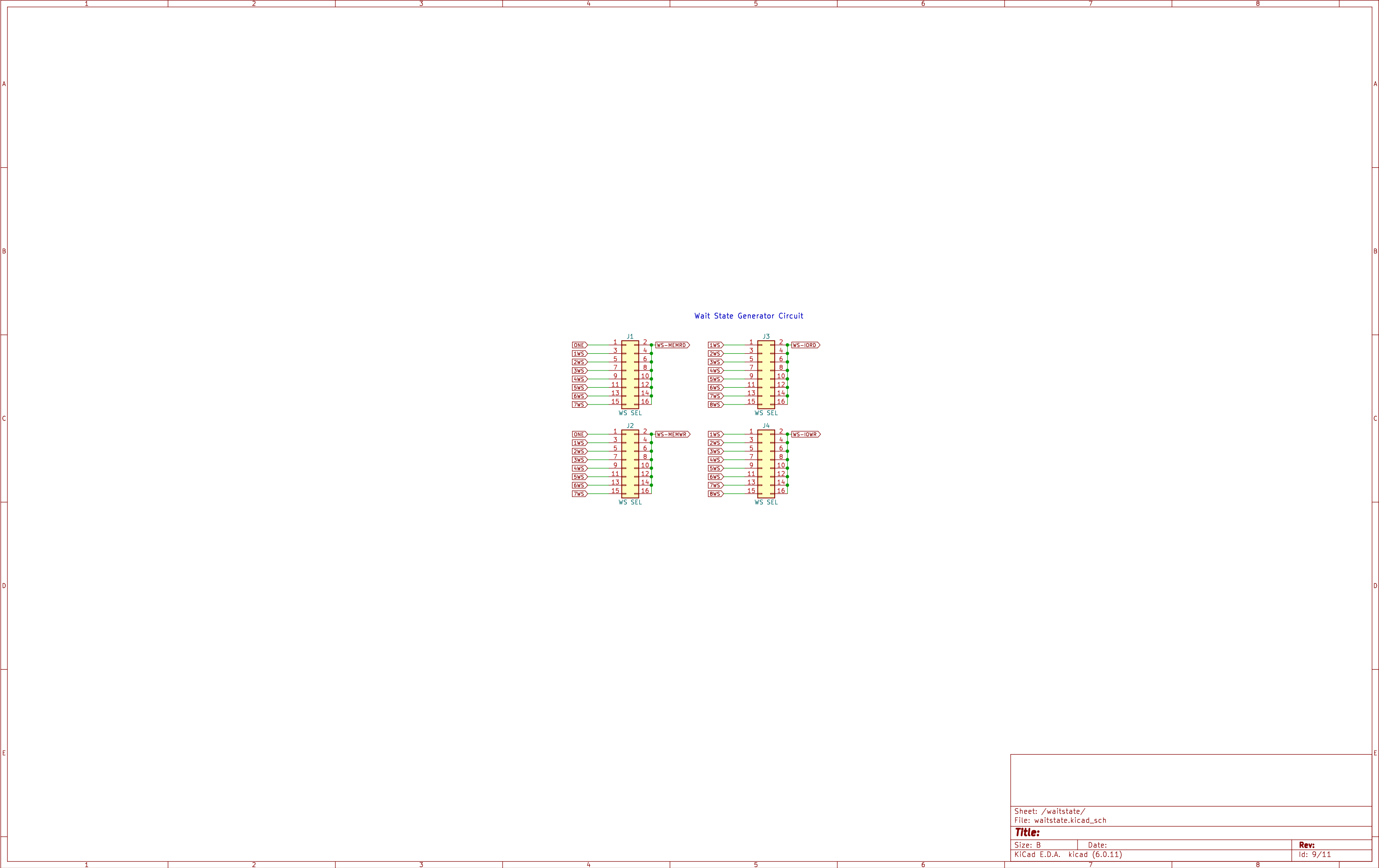


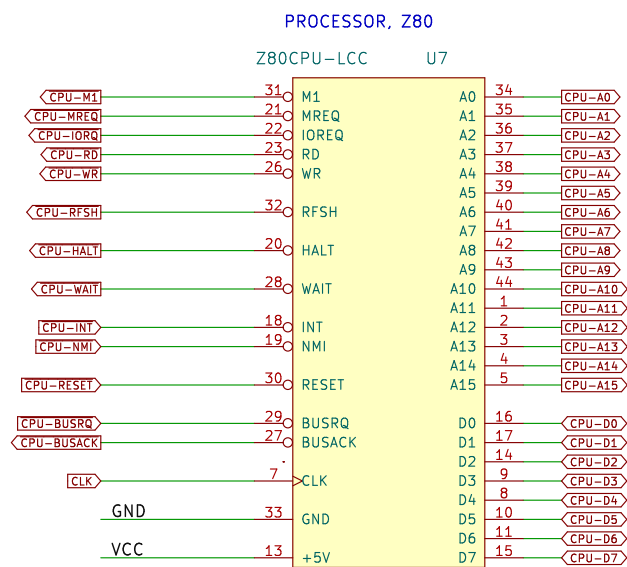
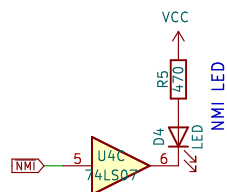
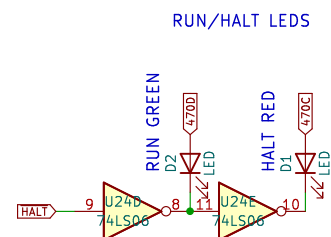
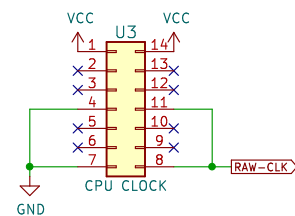
Z80 BUS INTERFACE











Tri-state address, data, & control bus during DMA

