

12345678

A

bus

File: bus.kicad_sch
DMA

power

File: power.kicad_sch
CPLDs

buffers

File: buffers.kicad_sch
IM2

fpanel

File: fpanel.kicad_sch
waitstate

Z80CPU

File: Z80CPU.kicad_sch
mapper

B

C

D

E

12345678

Sheet: /
File: Slick.kicad_sch

Title:

Size: B

Date:

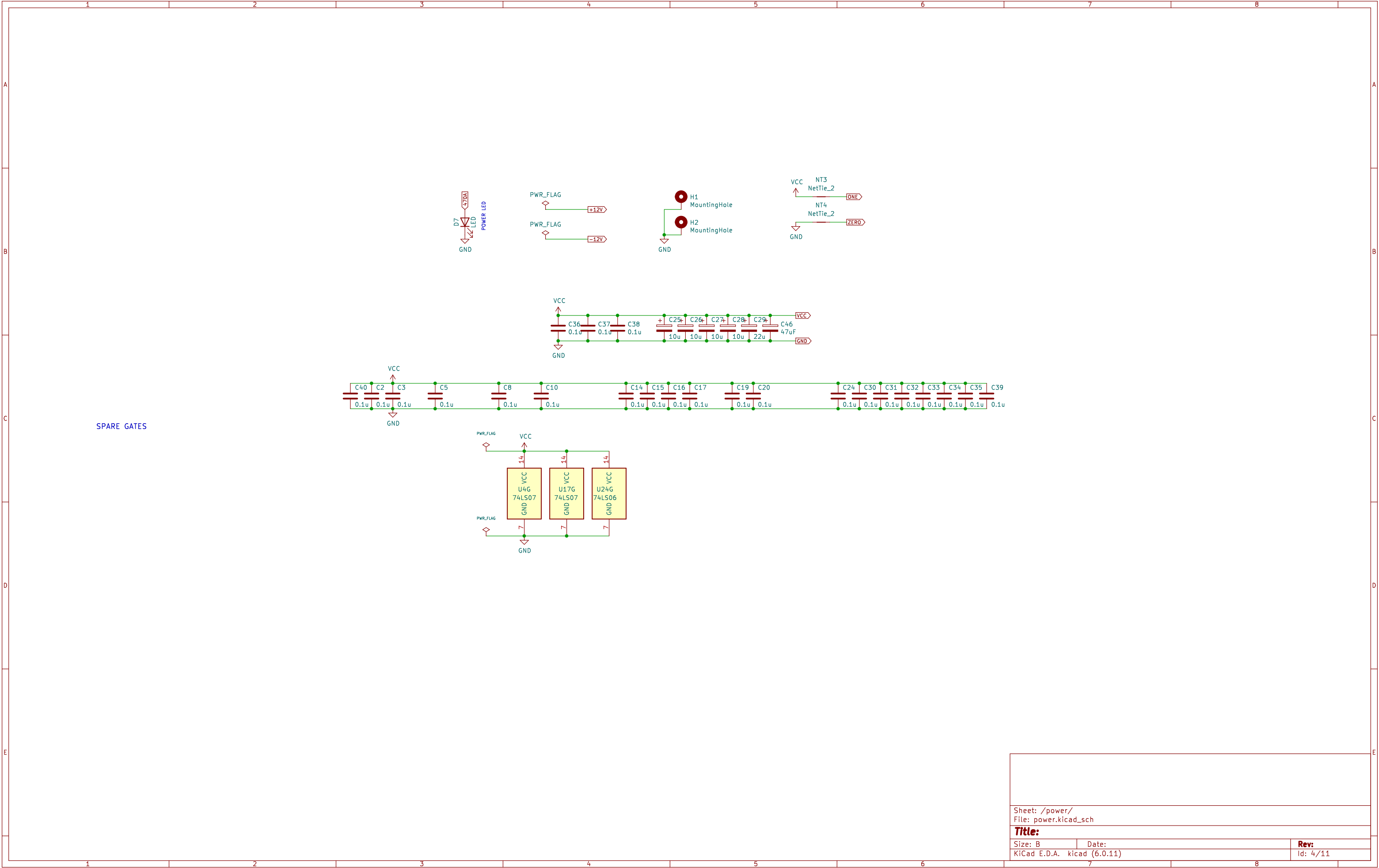
Rev:

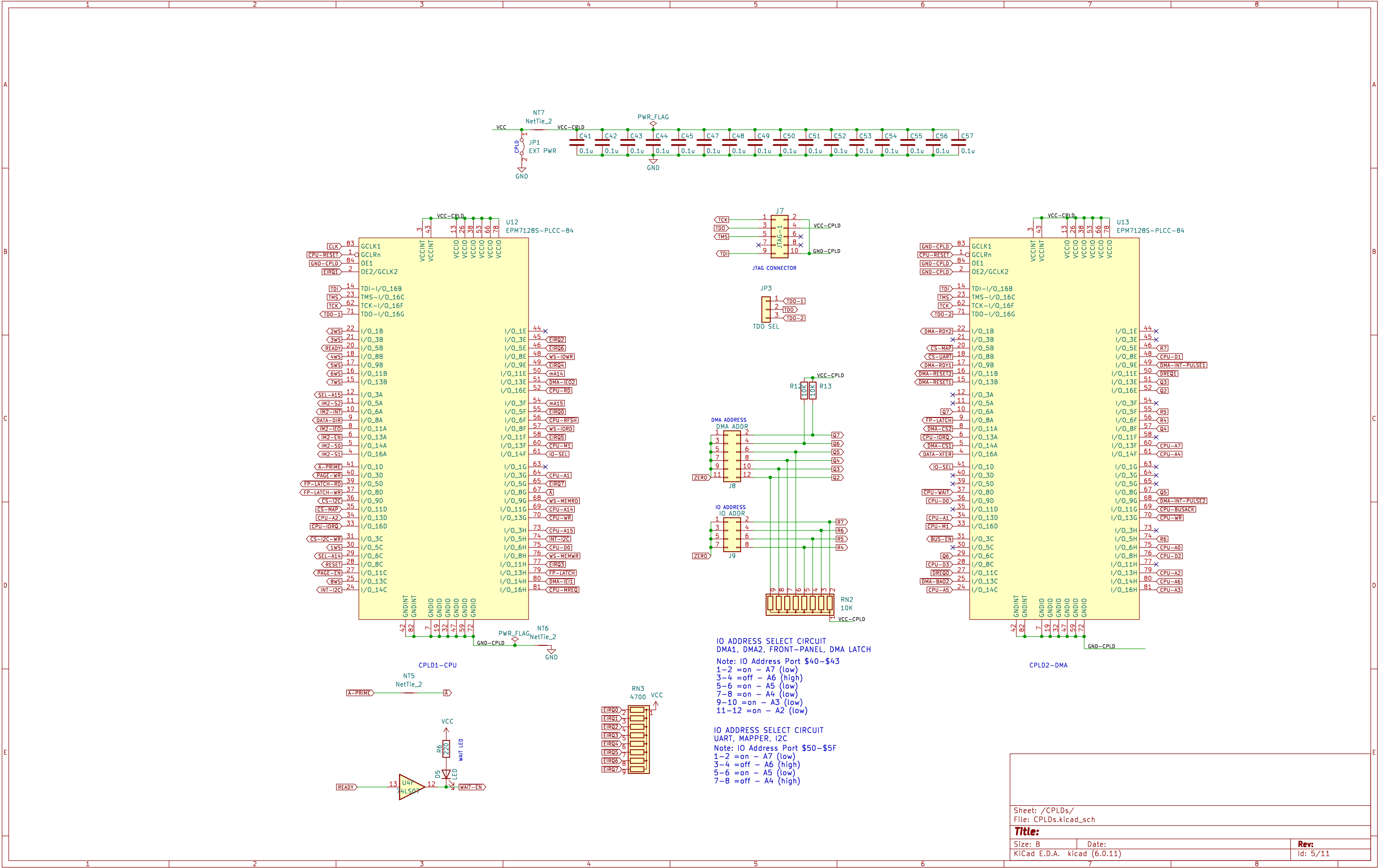
KiCad E.D.A. kicad (6.0.11)

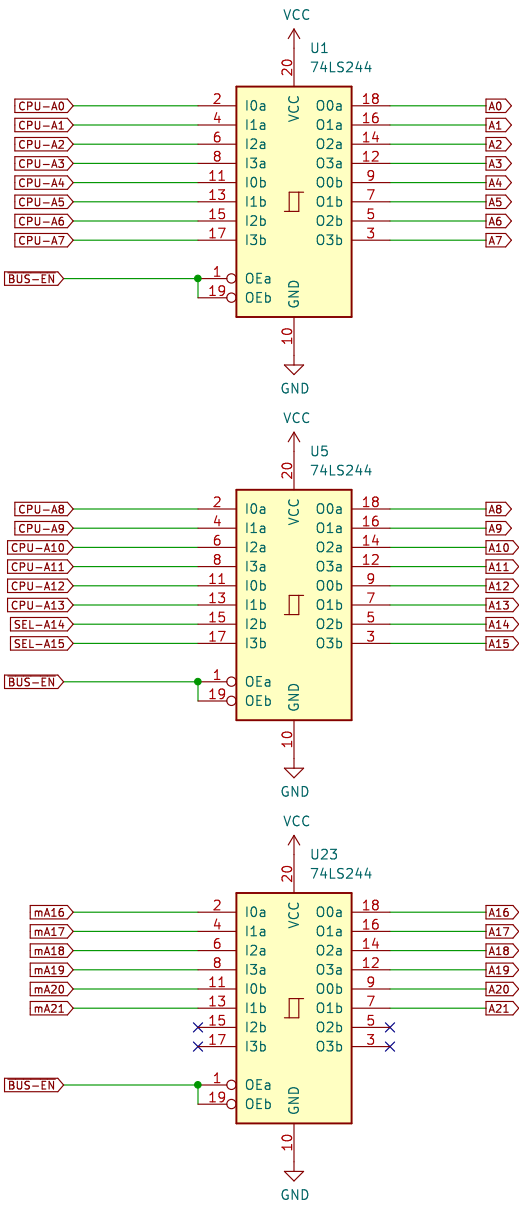
Id: 1/11



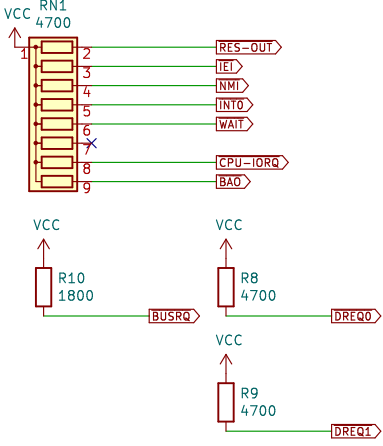
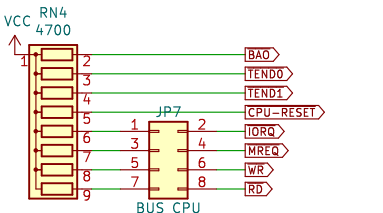
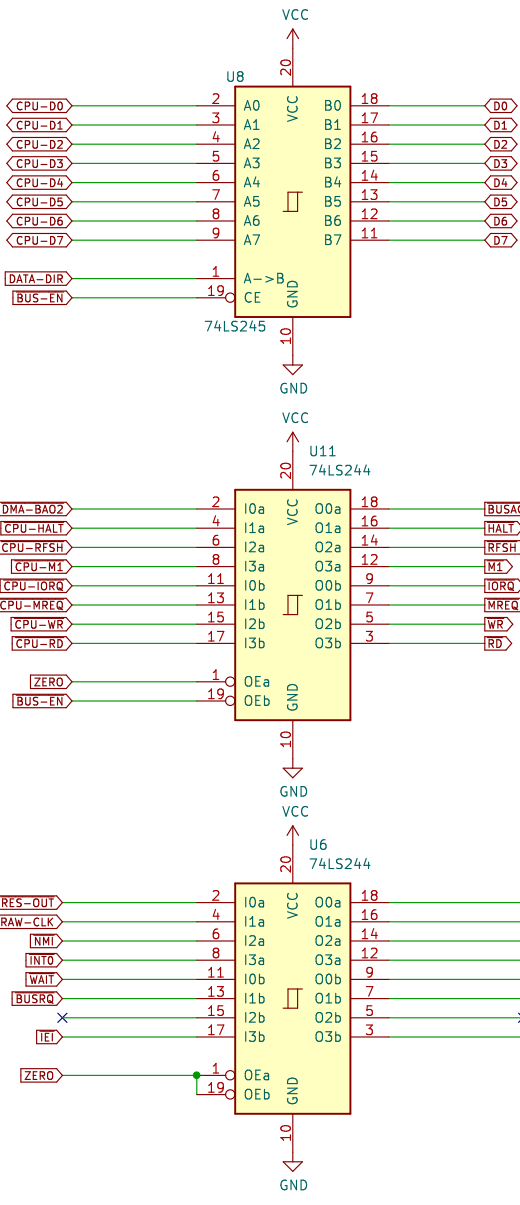
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| Size: B | Date: | Rev: |
| KiCad E.D.A. kicad (6.0.11) | | Id: 2/11 |



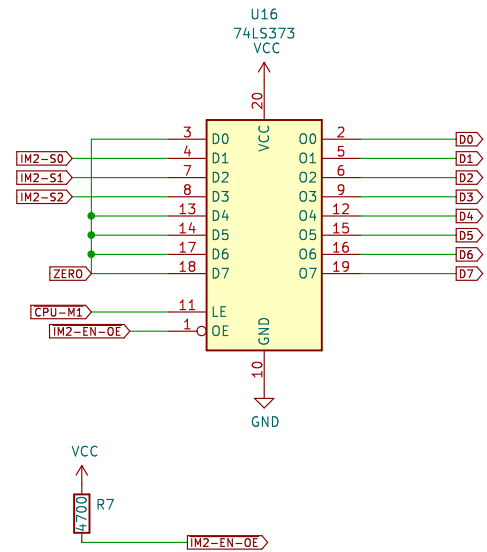




Z80 BUS INTERFACE



Interrupt Mode 2 (IM2) Circuit



Sheet: /IM2/
File: IM2.kicad_sch

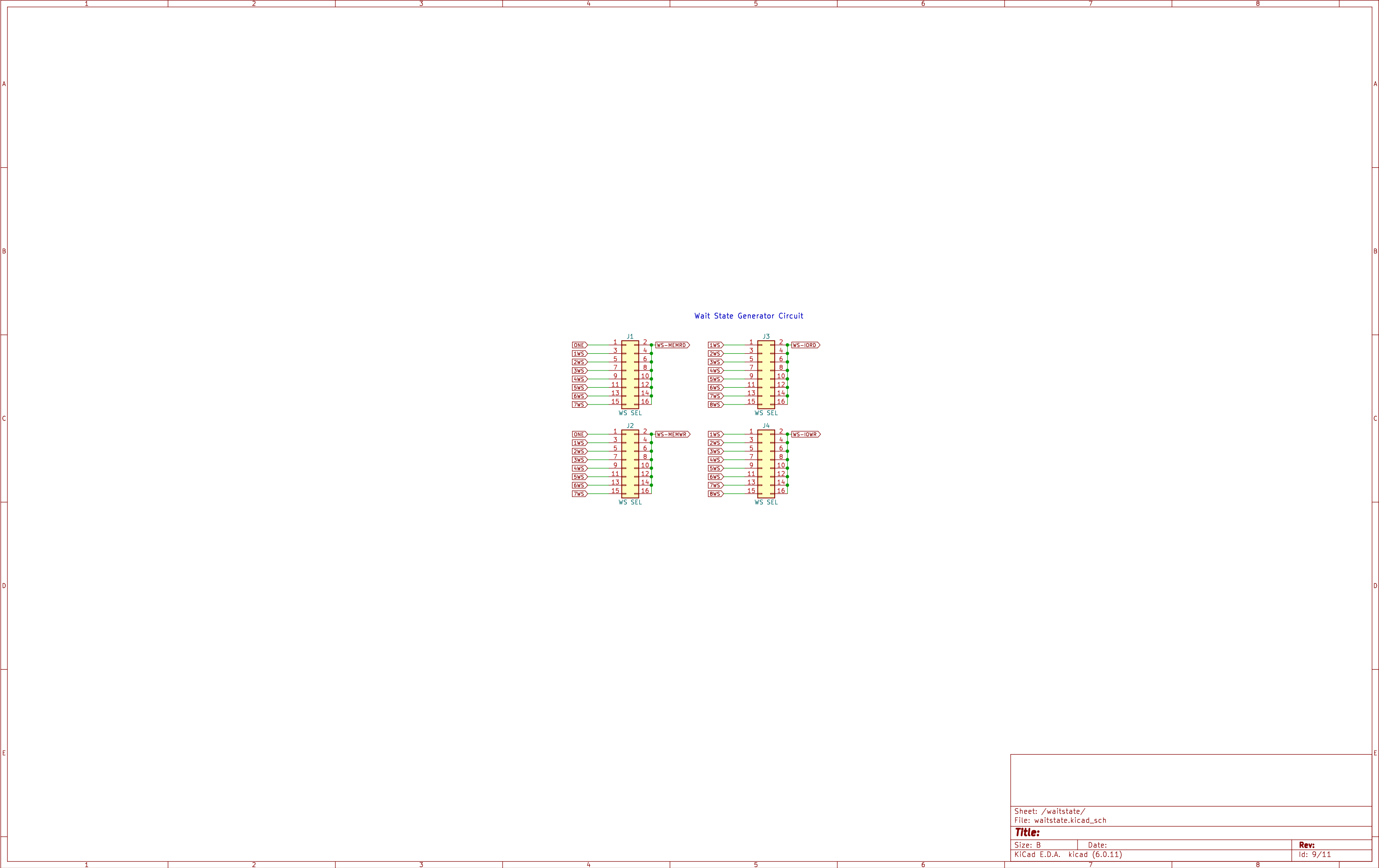
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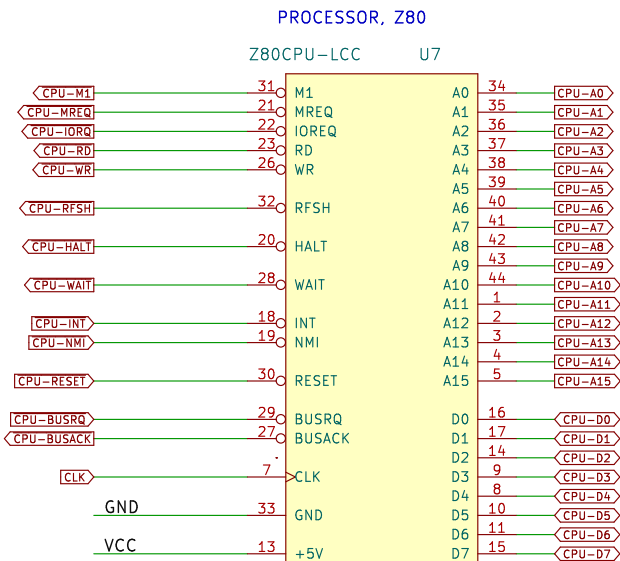
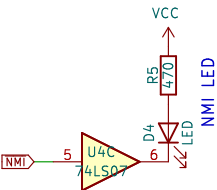
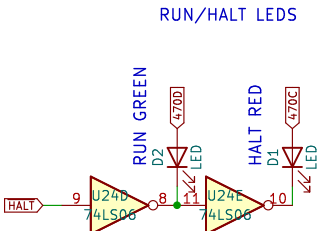
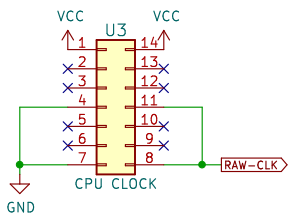
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Rev: 7/11

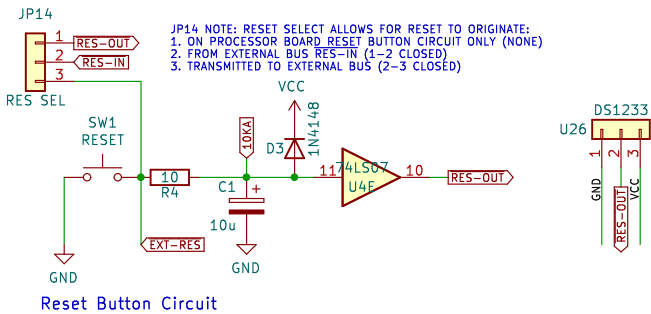


PINS 34, 36 ARE FOR WAIT STATE CIRCUIT ENABLE





Tri-state address, data, & control bus during DMA



Sheet: /Z80CPU/
File: Z80CPU.kicad_sch

Title:

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Date:

Rev:

KiCad E.D.A. kicad (6.0.11)

Id: 10/11

