

By Dan Werner, GAL modifications by Andrew Lynch

Sheet: /bus/  
File: bus.kicad\_sch

**Title: Dual ESP32 IO**

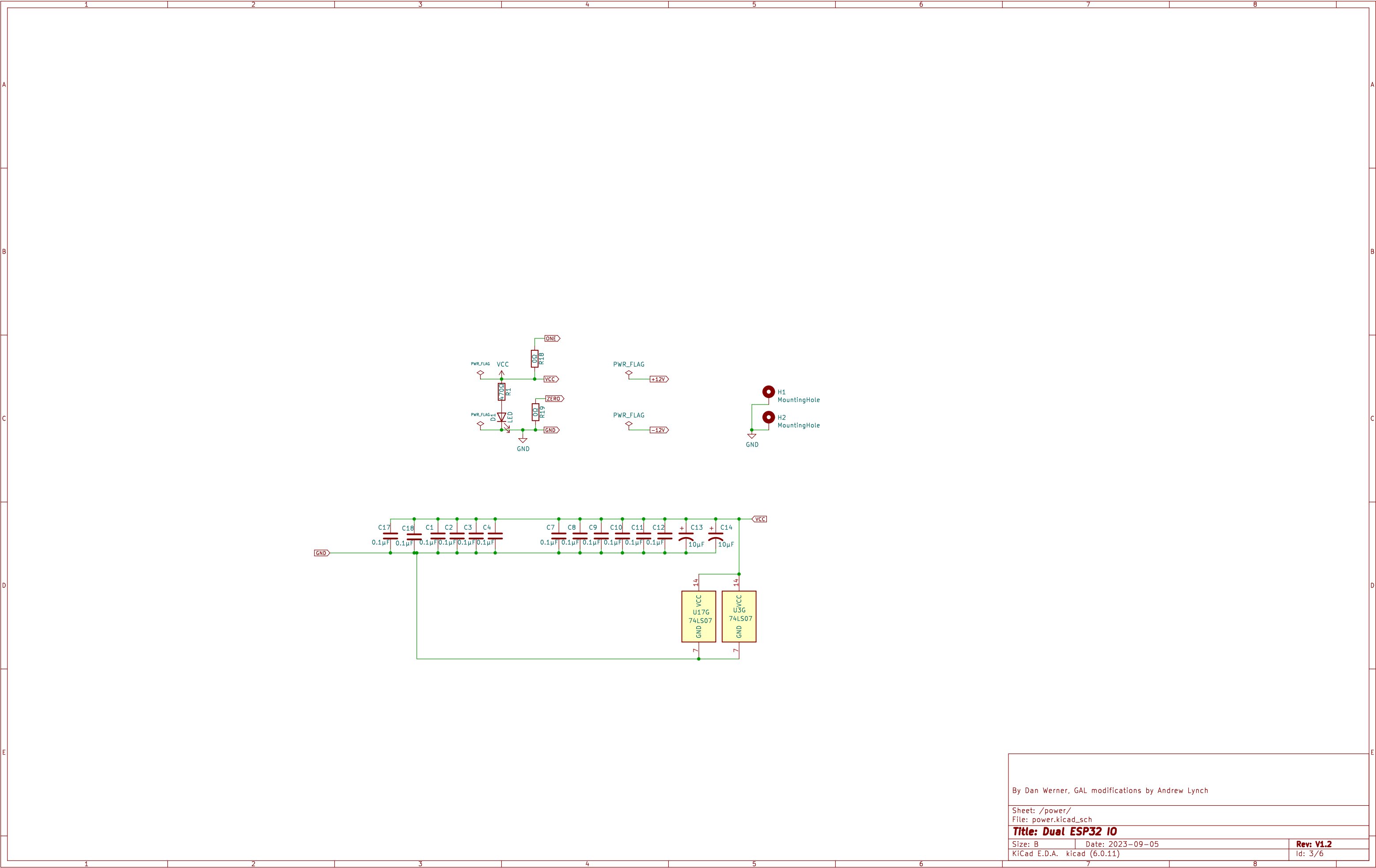
Size: B

Date: 2023-09-05

Rev: V1.2

Size: 5	Date: 202
KiCad E.D.A.	kicad (6.0.11)

Id: 2/6

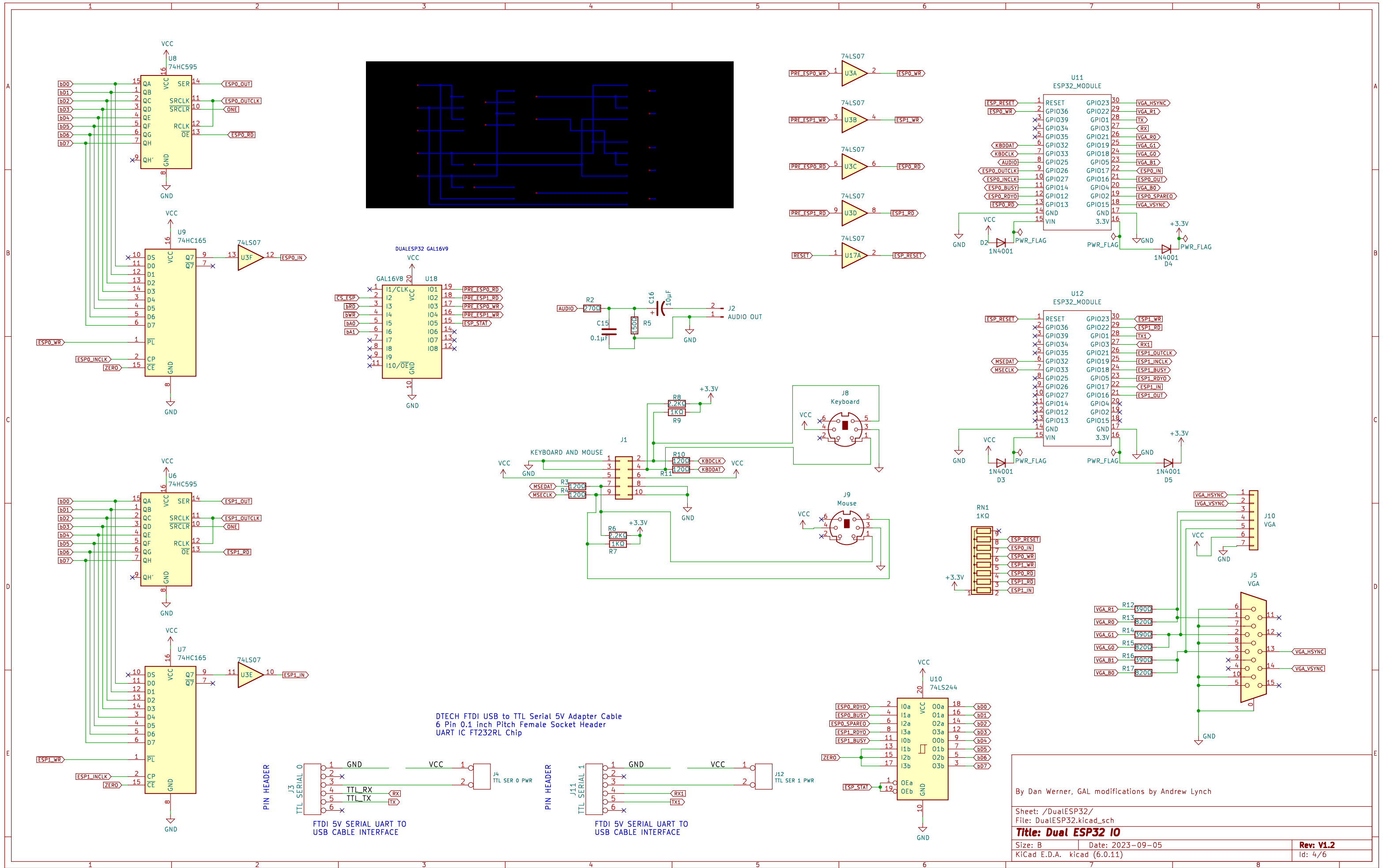


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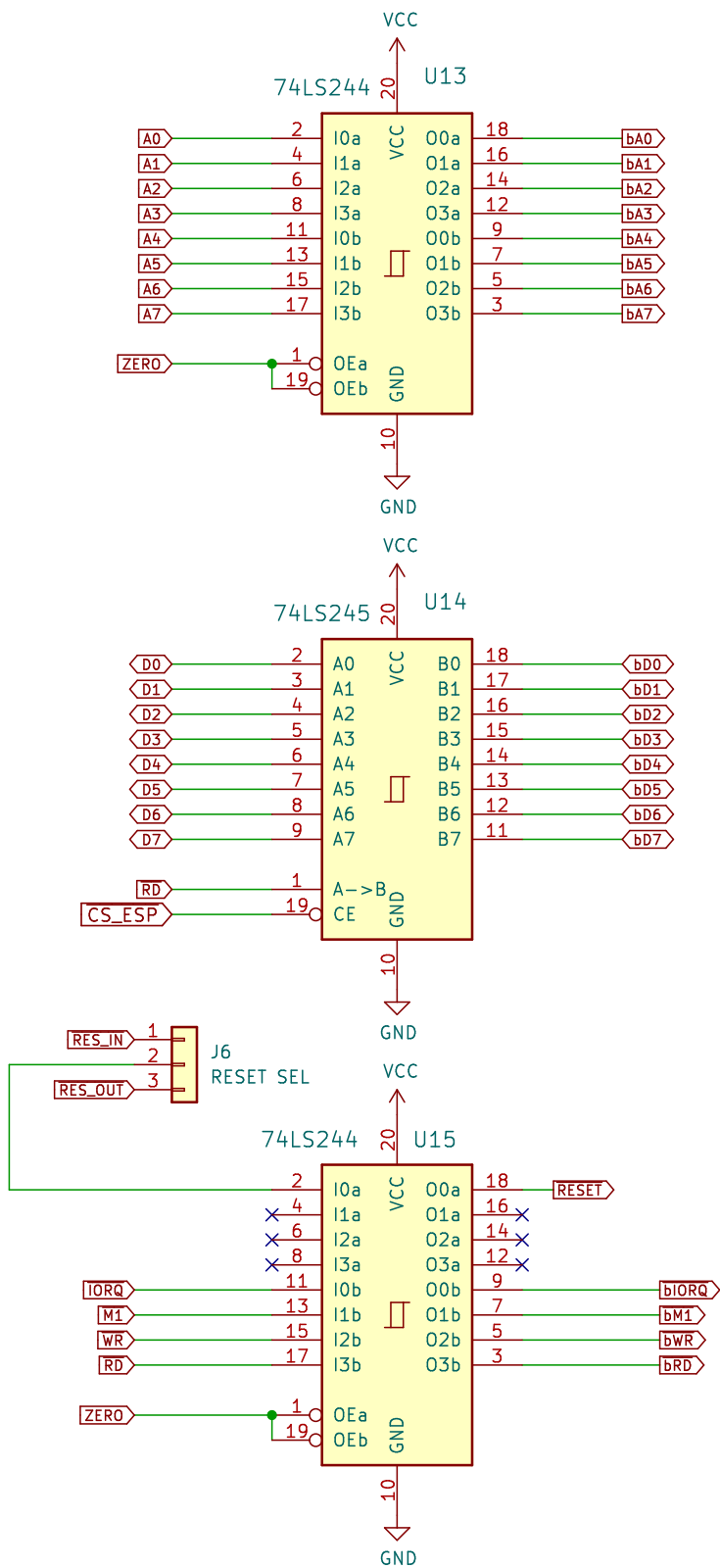
Sheet: /power/  
File: power.kicad\_sch

**Title: Dual ESP32 IO**

Size: B	Date: 2023-09-05	Rev: V1.2
KiCad E.D.A.	kicad (6.0.11)	Id: 3/6



Z80 BUS INTERFACE



Note: Buffers and Transceivers respond to IO and MEM cycles

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Sheet: /buffers/  
File: buffers.kicad\_sch

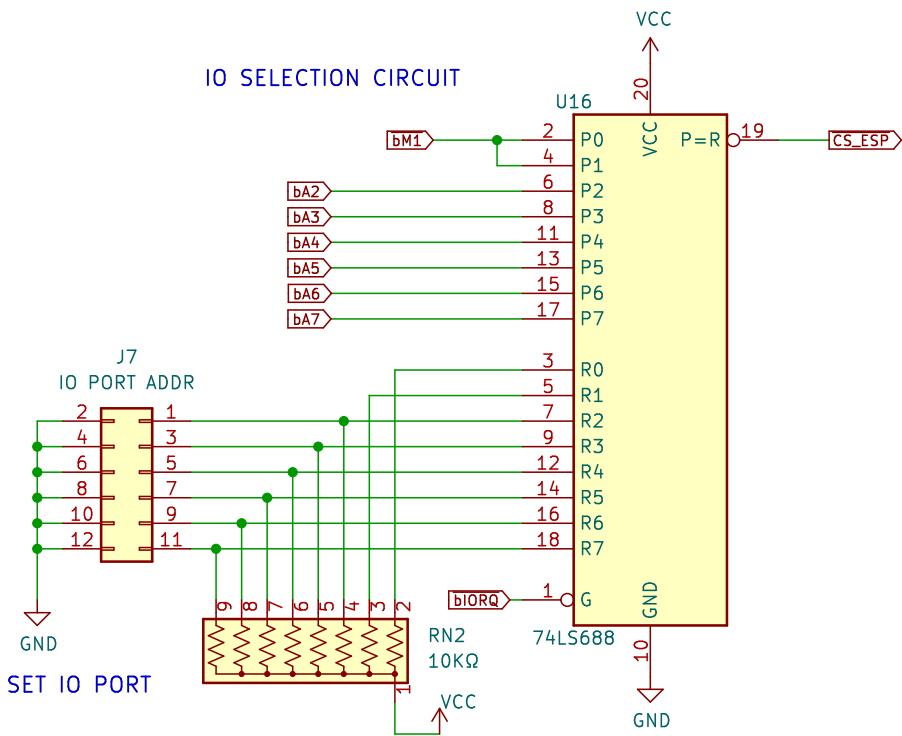
**Title: Dual ESP32 IO**

Size: A4 Date: 2023-09-05

**Rev: V1.2**

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Id: 5/6



Note: Inhibit Board Operation  
During Interrupts  
M1 = low, IORQ = low

Note: IO Address Port \$9C-\$9F  
1=off - A2 (high)  
3=off - A3 (high)  
5=off - A4 (high)  
7=on - A5 (low)  
9=on - A6 (low)  
11=off - A7 (high)

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Sheet: /IOSelection/  
File: IOSelection.kicad\_sch

**Title: Dual ESP32 IO**

Size: A4 Date: 2023-09-05

KiCad E.D.A. kicad (6.0.11)

**Rev: V1.2**

Id: 6/6