	1 2	3	4 5	6	7 8	
	bus	power	buffers	fpanel	Z80CPU	
A						A
В						В
	File: bus.kicad_sch DMA	File: power.kicad_sch CPLDs	File: buffers.kicad_sch IM2	File: fpanel.kicad_sch waitstate	File: Z80CPU.kicad_sch mapper	
	File: DMA.kicad_sch	File: CPLDs.kicad_sch	File: IM2.kicad_sch	File: waitstate.kicad_sch	File: mapper.kicad_sch	
D						D
H						
E						E
				Shee File:	t: / Slick.kicad_sch	
H				Title		
	4	7		Size: KiCa	B Date: Re d E.D.A. kicad (6.0.11) Id:	: 1/11
	1 2	5	4 5	b	/ 8	



















