

buffers



File: buffers.kicad_sch
RTC

power



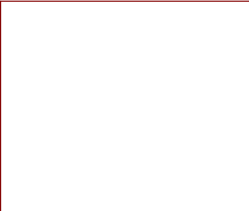
File: power.kicad_sch
battery

bus

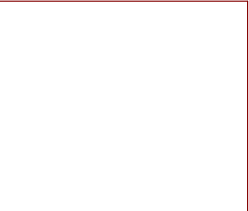


File: bus.kicad_sch
GALS

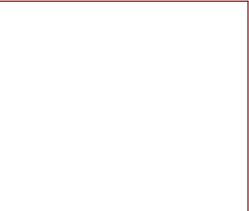
File: RTC.kicad_sch
IO



File: battery.kicad_sch
RAM



File: GALS.kicad_sch
ROM

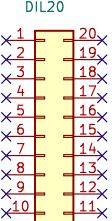
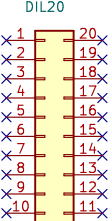
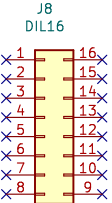
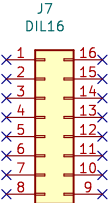
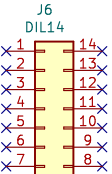
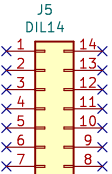


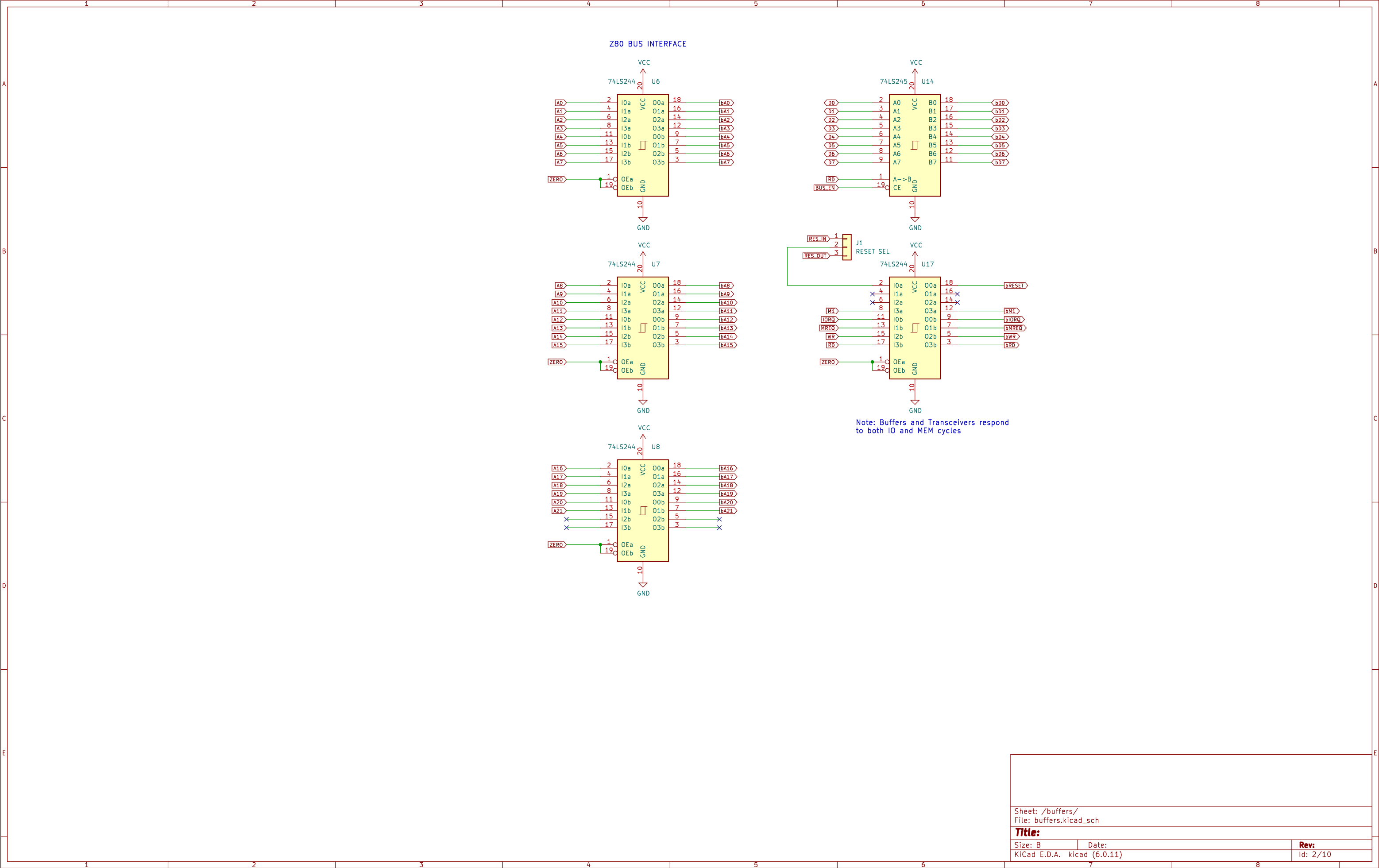
File: IO.kicad_sch

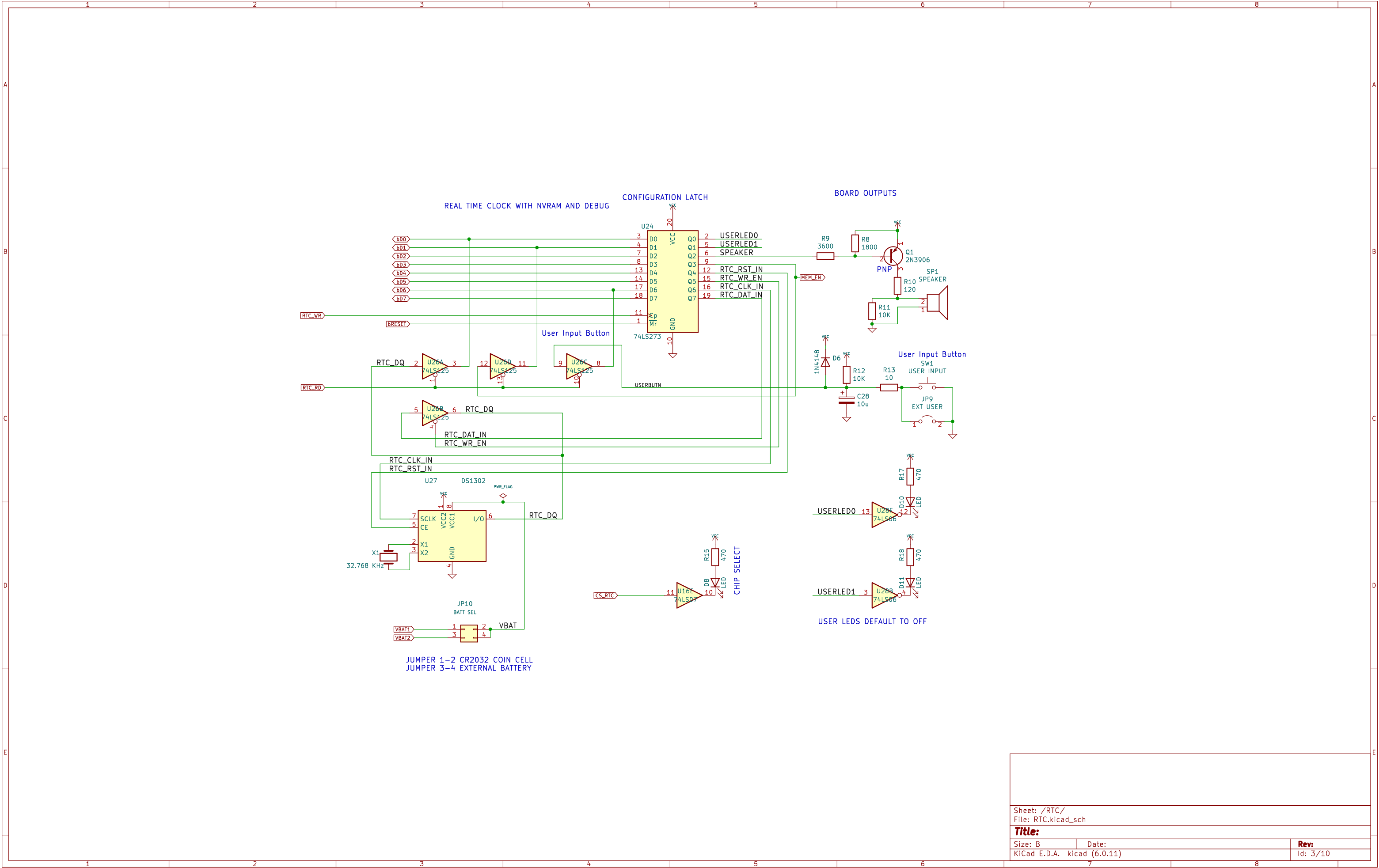
File: RAM.kicad_sch

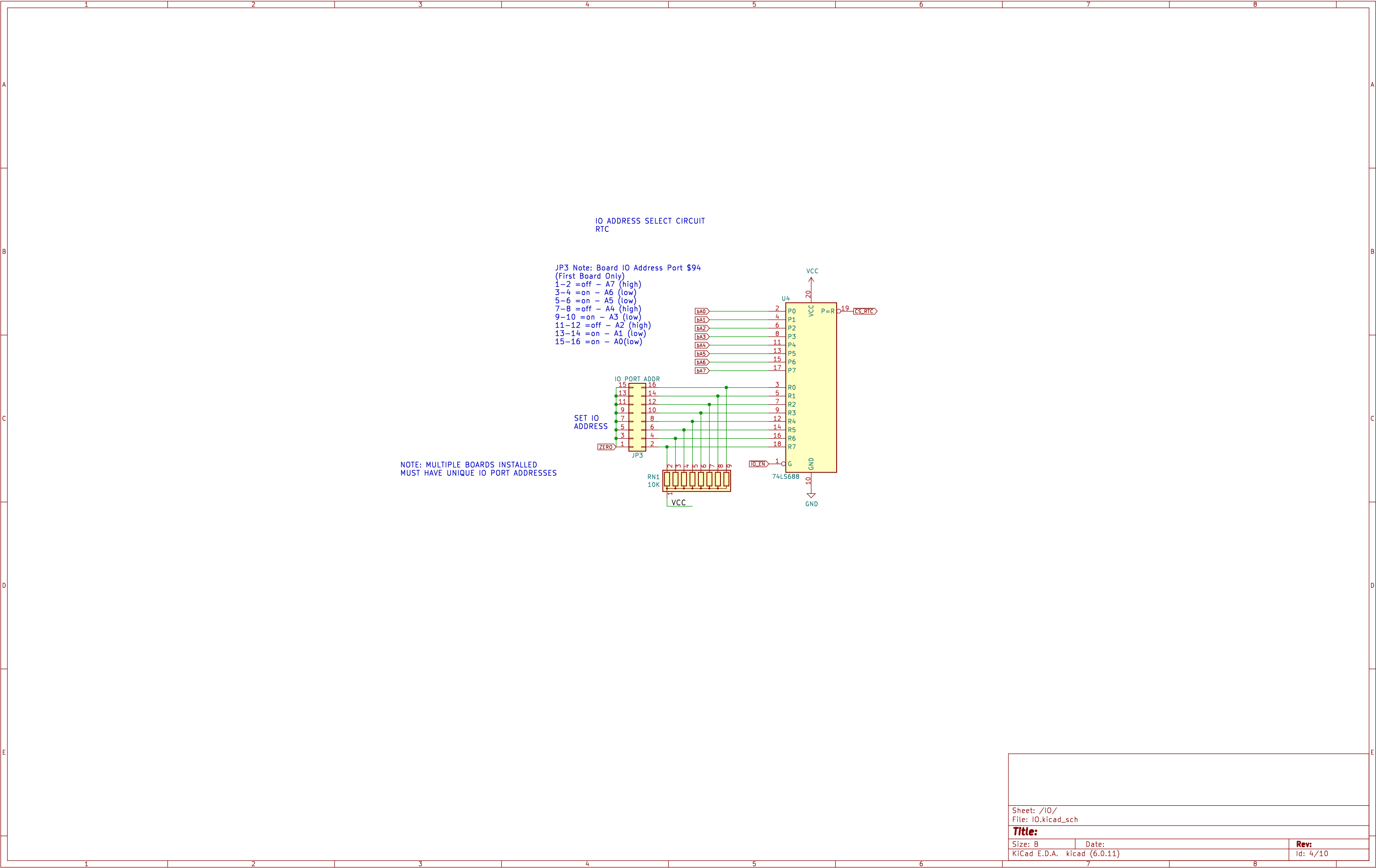
File: ROM.kicad_sch

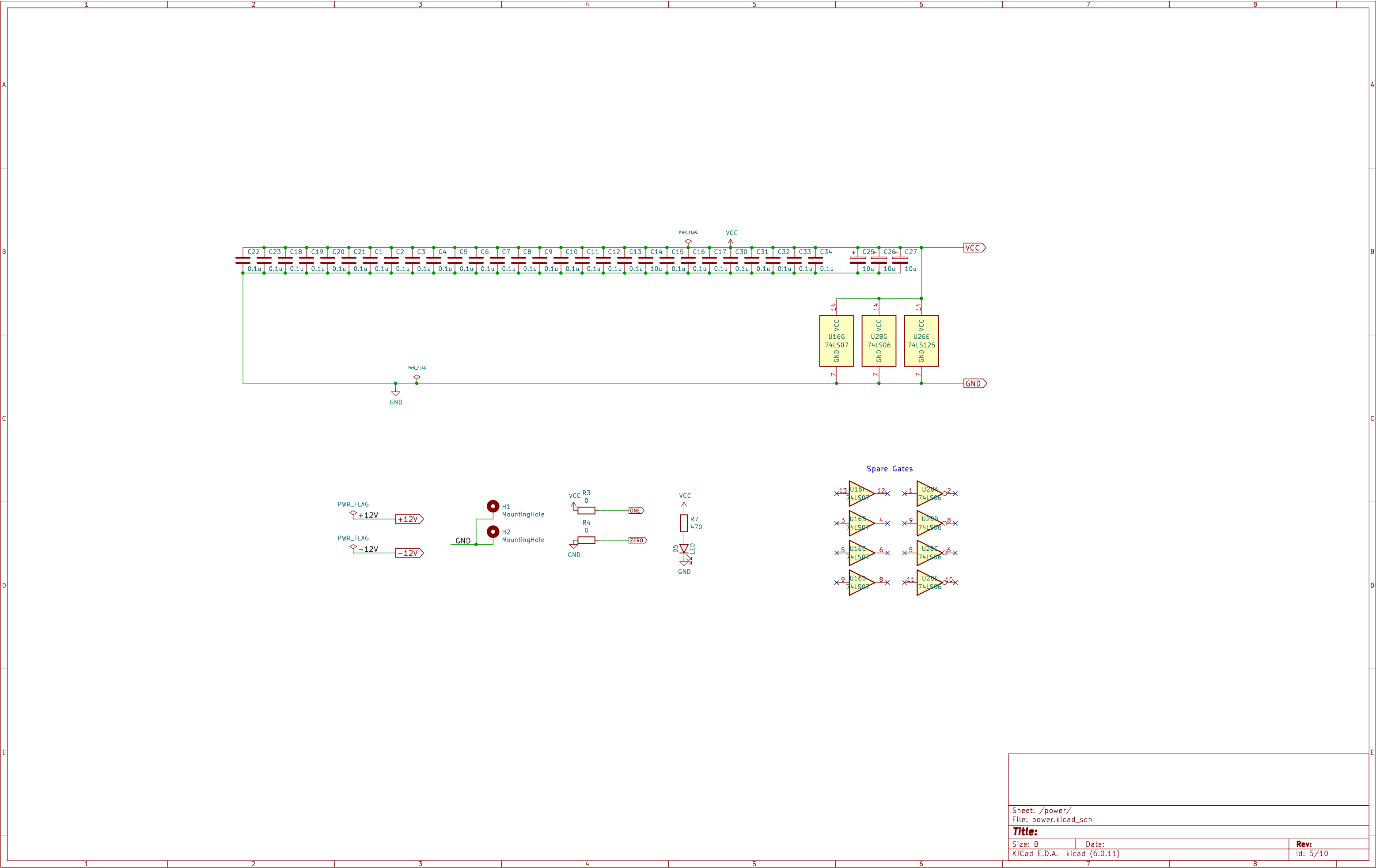
SPARE SOCKETS
FOR DEBUGGING

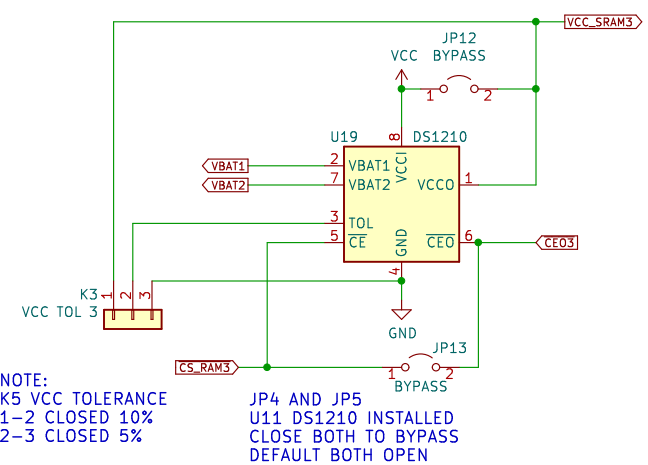
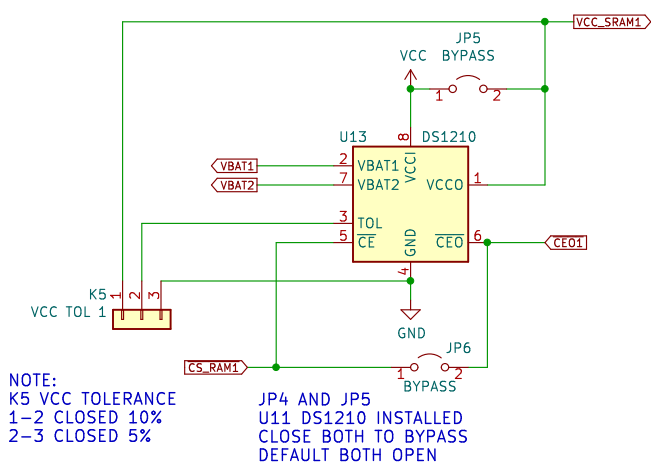
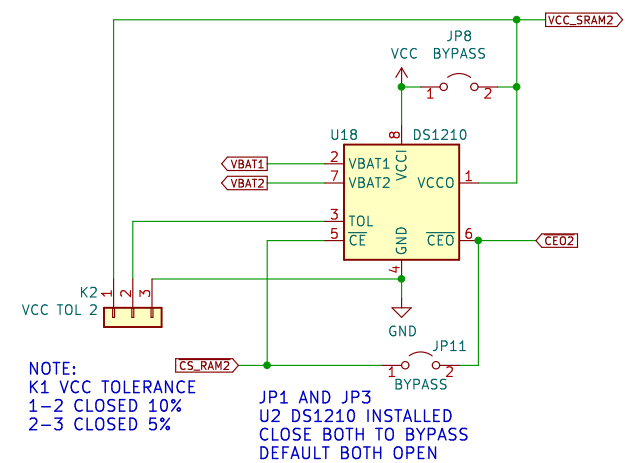
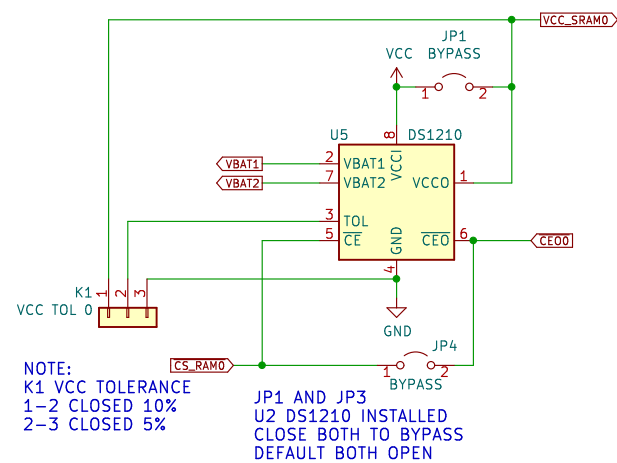
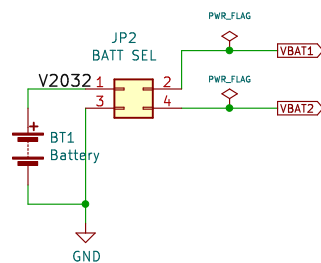


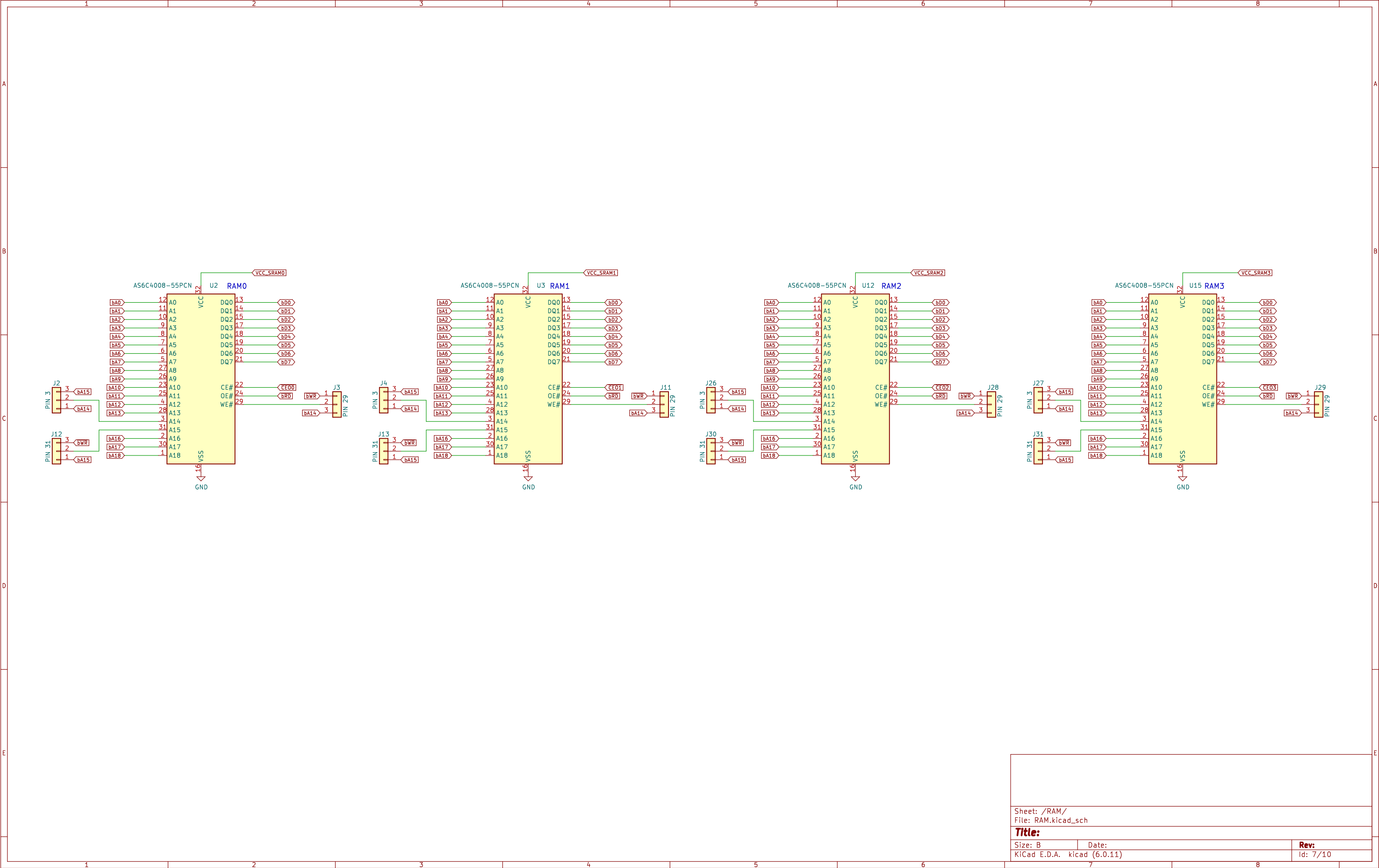


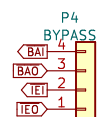
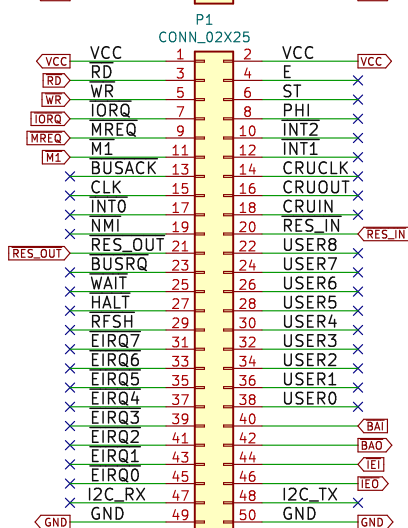
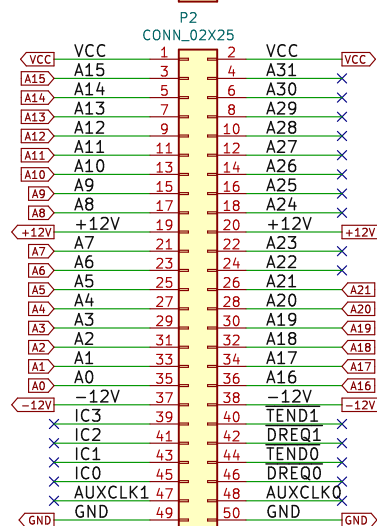
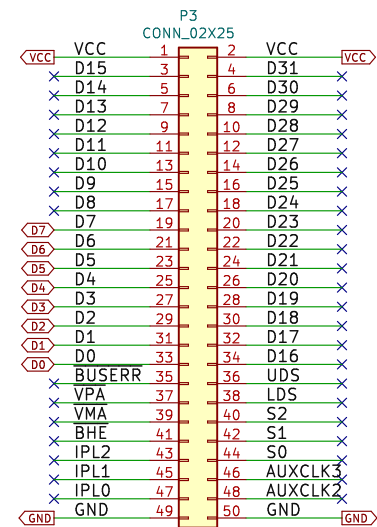


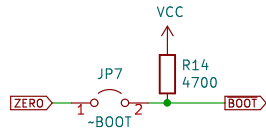
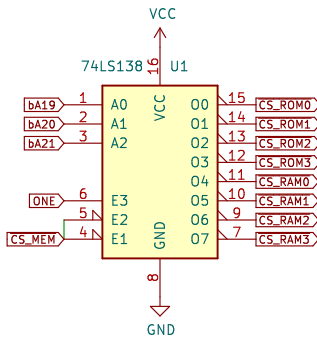
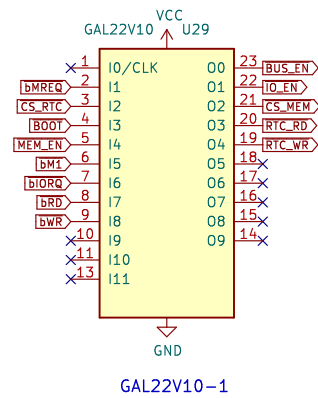
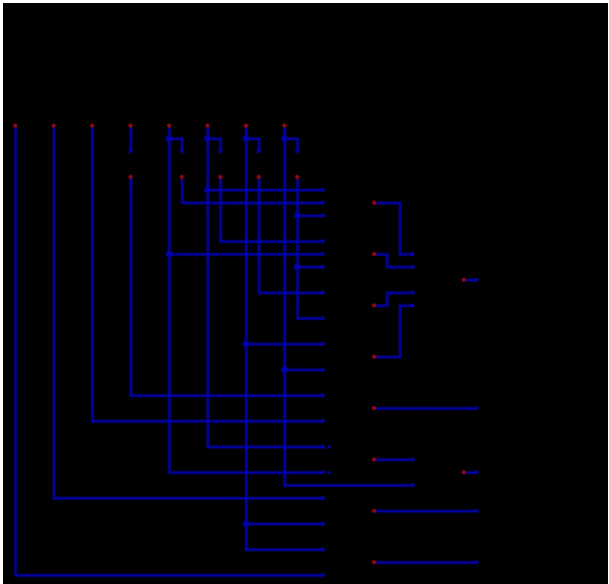








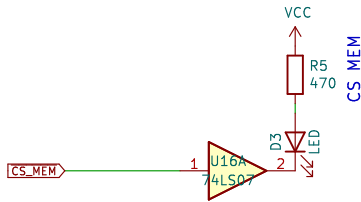
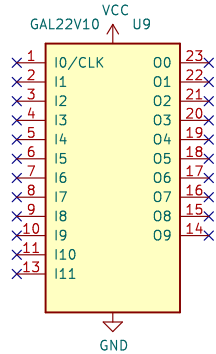




JP7 NOTE: FIRST ROM & RAM BOARD IN SYSTEM MUST HAVE JP7 JUMPER INSTALLED. ALSO MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE. FIRST BOARD ONLY RESPONDS WHEN MEM_EN LOW (BY DEFAULT).

SECOND ROM & RAM BOARD INSTALLED IN SYSTEM MUST HAVE JP7 JUMPER NOT INSTALLED. ALSO SECOND BOARD ONLY RESPONDS WHEN MEM_EN IS HIGH.

ACCESS IS CONTROLLED BY SOFTWARE TO ENSURE THAT ONLY ONE OF BOARDS HAS MEM_EN HIGH AT ANY GIVEN TIME OR BUS CONTENTION.

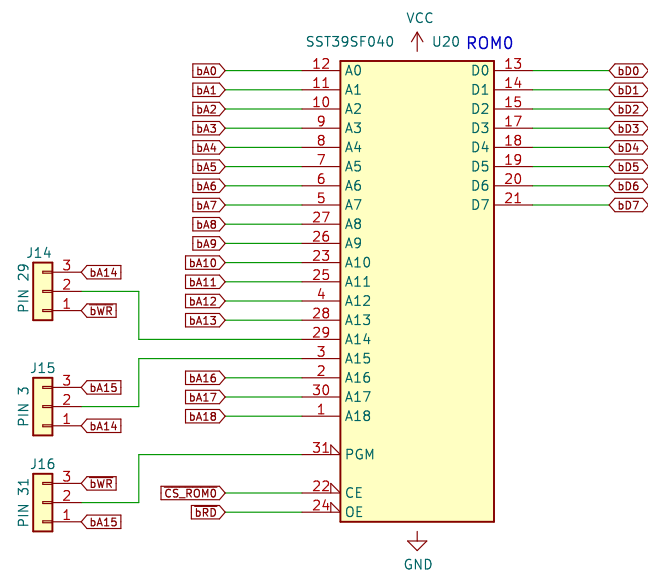


Sheet: /GALS/
File: GALS.kicad_sch

Title:

Size: B Date:
KiCad E.D.A. kicad (6.0.11)

Rev:
Id: 9/10



U20 NOTE: FIRST BOARD IN SYSTEM MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE AND JP7 INSTALLED FOR PROPER BOOT SEQUENCE.

