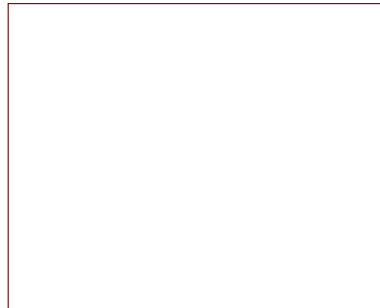
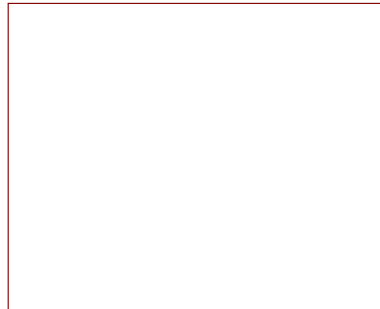


power



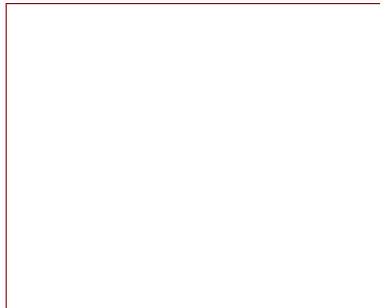
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bus



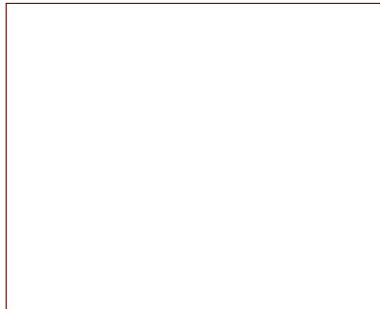
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buffers



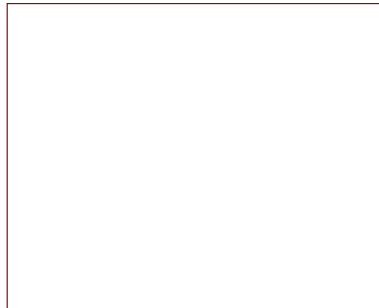
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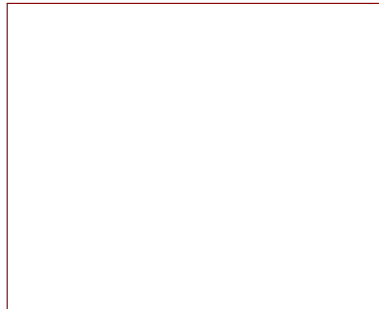
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dualPIO



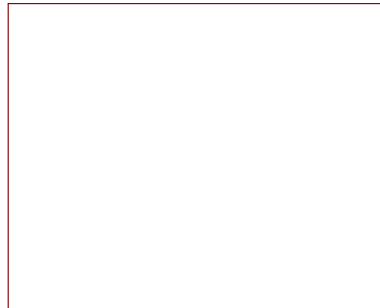
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GAL



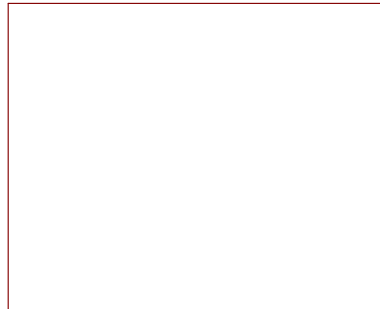
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CTC-INT



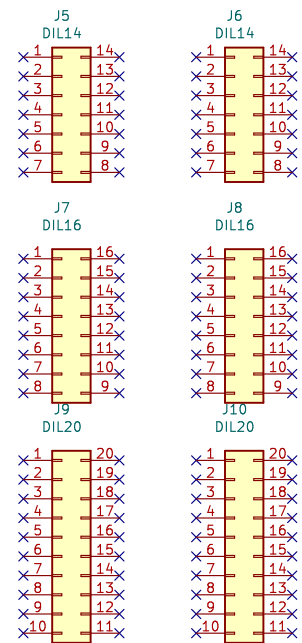
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CTC-SIO



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SPARE SOCKETS FOR DEBUGGING



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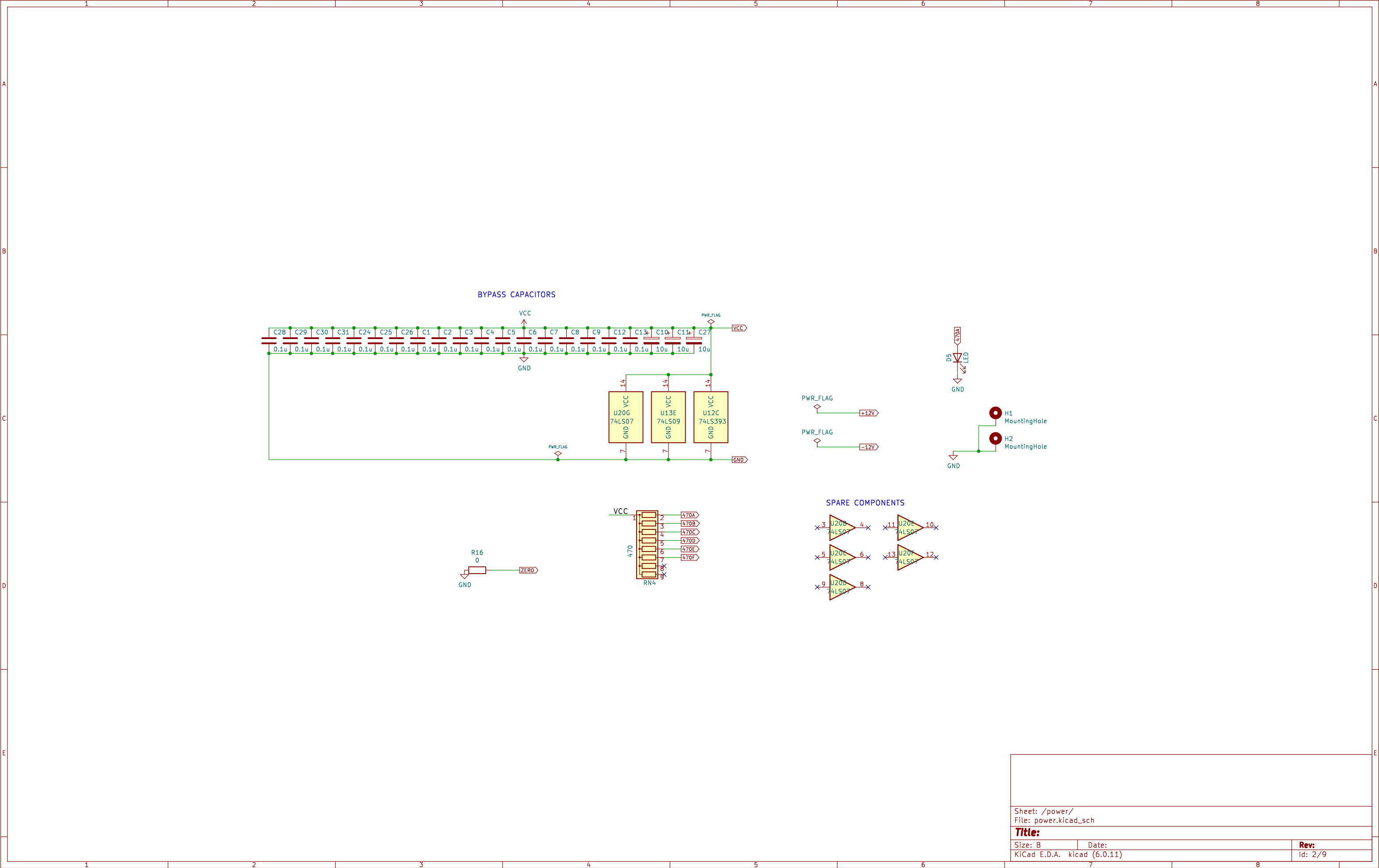
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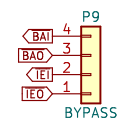
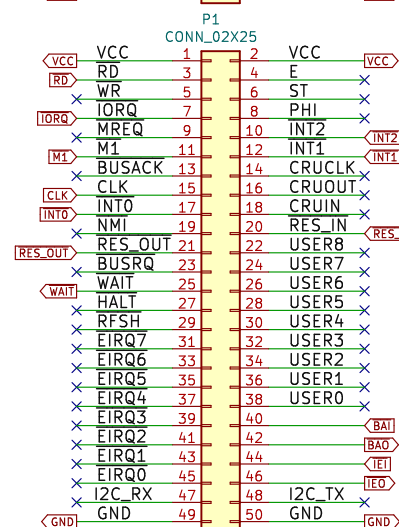
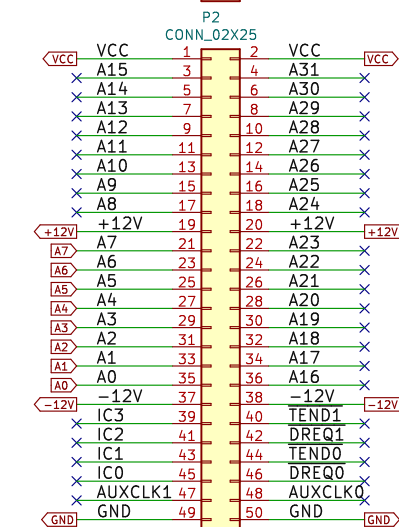
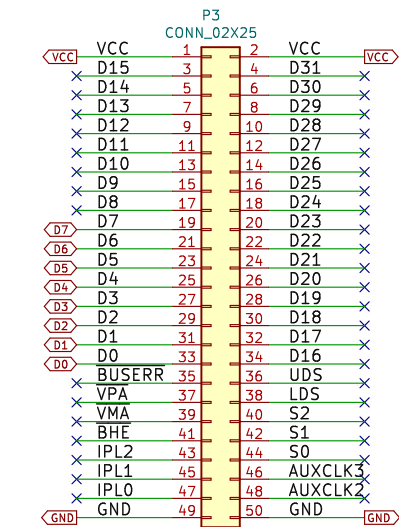
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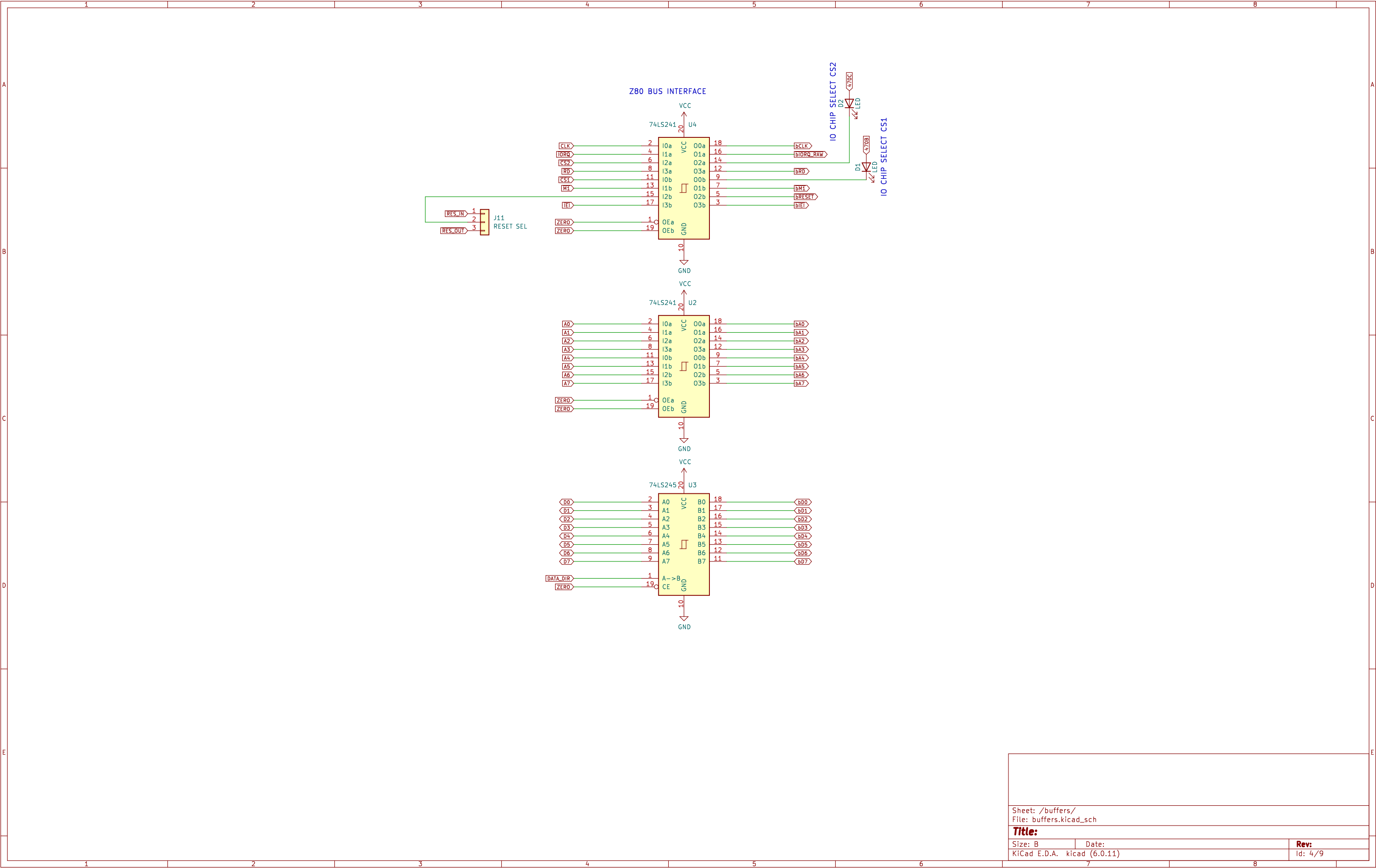
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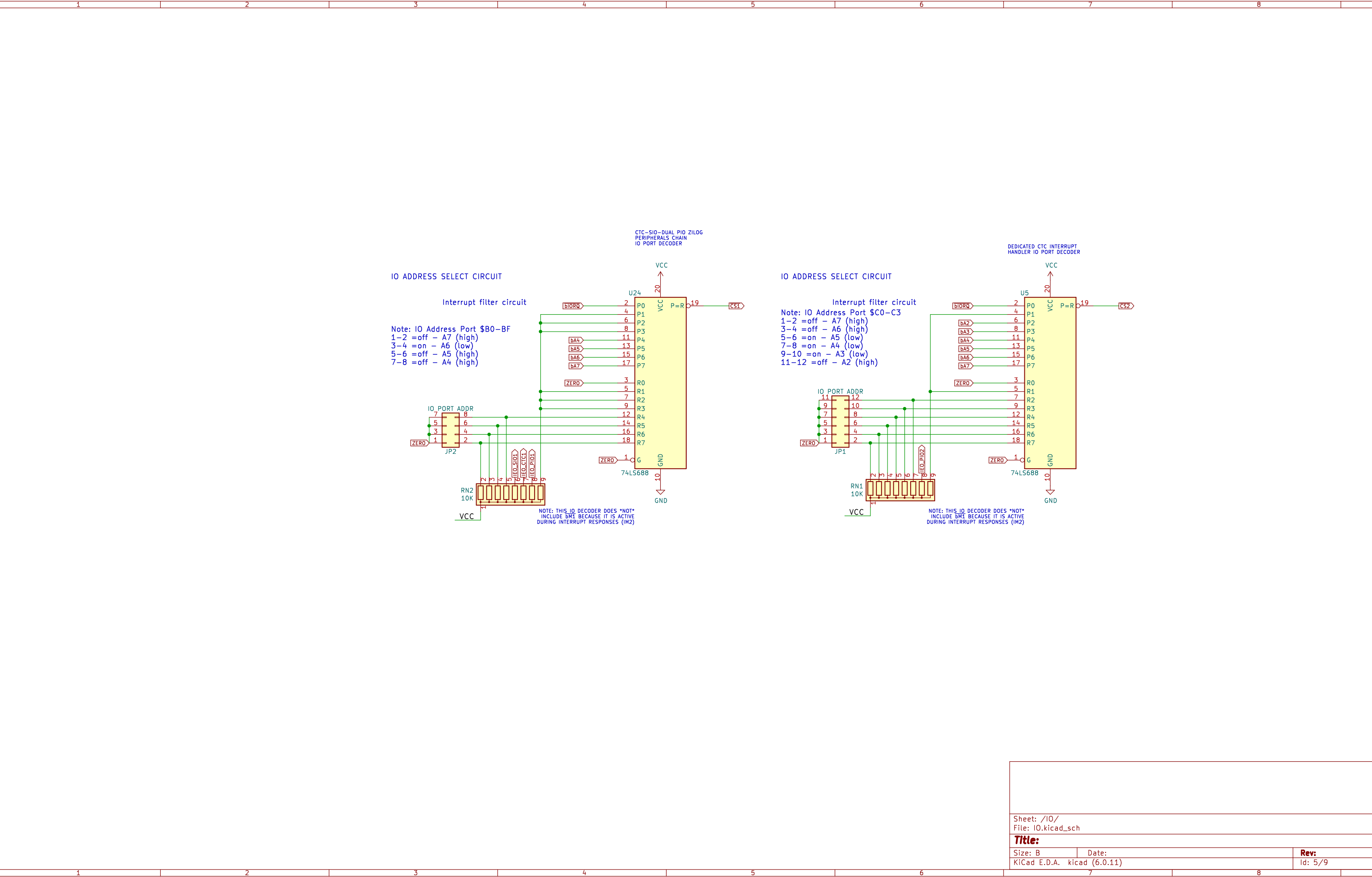
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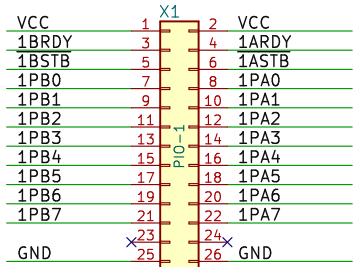
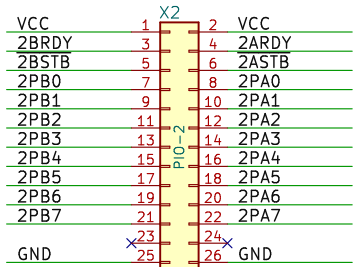
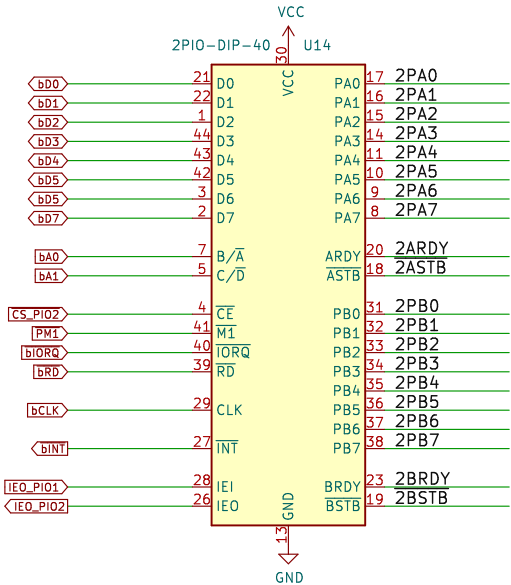
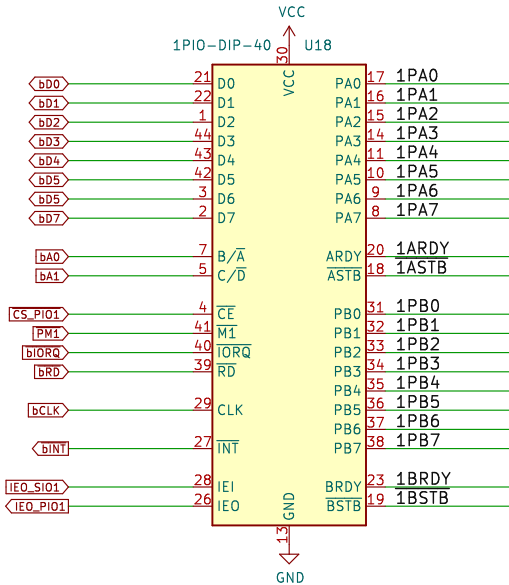
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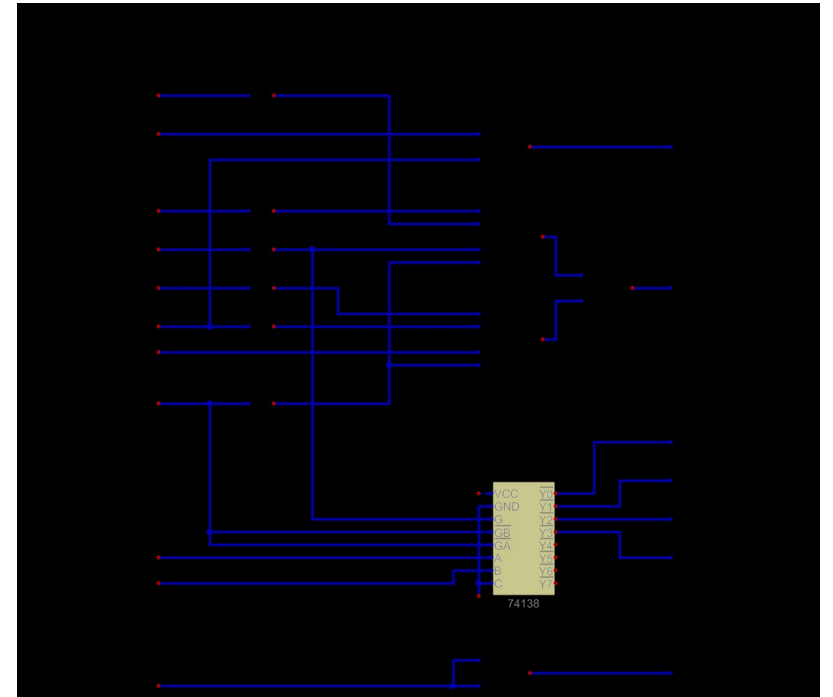
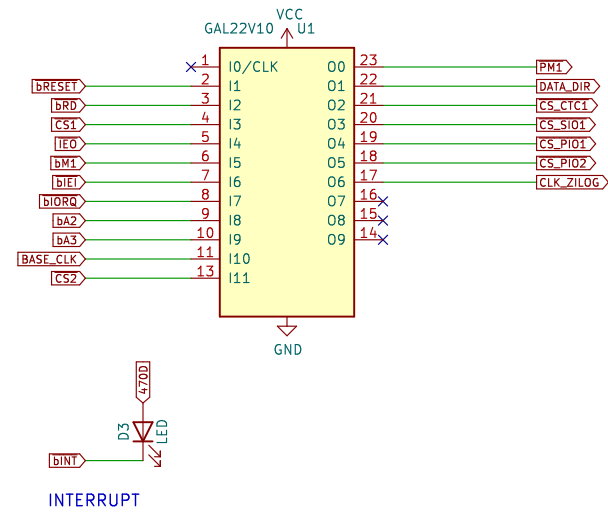
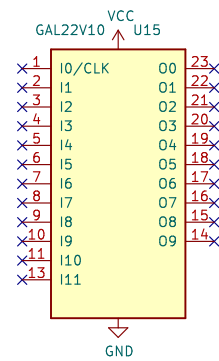
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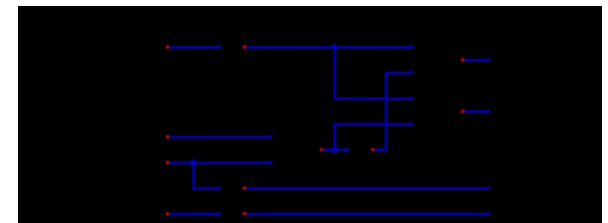
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[illegible]

Pin configuration diagram for the GAL16V8 U21. The chip is shown with pins 1 through 19. Pin 1 is CLK, Pin 2 is VCC, Pin 3 is DEL_CNT, Pin 4 is BM1, Pin 5 is bCLK, Pin 6 is an unconnected input, Pin 7 is an unconnected input, Pin 8 is an unconnected input, Pin 9 is an unconnected input, Pin 10 is OE, Pin 11 is an unconnected input, Pin 12 is an unconnected input, Pin 13 is an unconnected input, Pin 14 is an unconnected input, Pin 15 is an unconnected input, Pin 16 is bCLK, Pin 17 is BM1, Pin 18 is bWAIT, Pin 19 is BIORQ. The chip is connected to VCC and GND.



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