

NOTE: I2C TO SD CARD CIRCUIT BASED ON  
<http://www.technoblogy.com/show?3XEP>

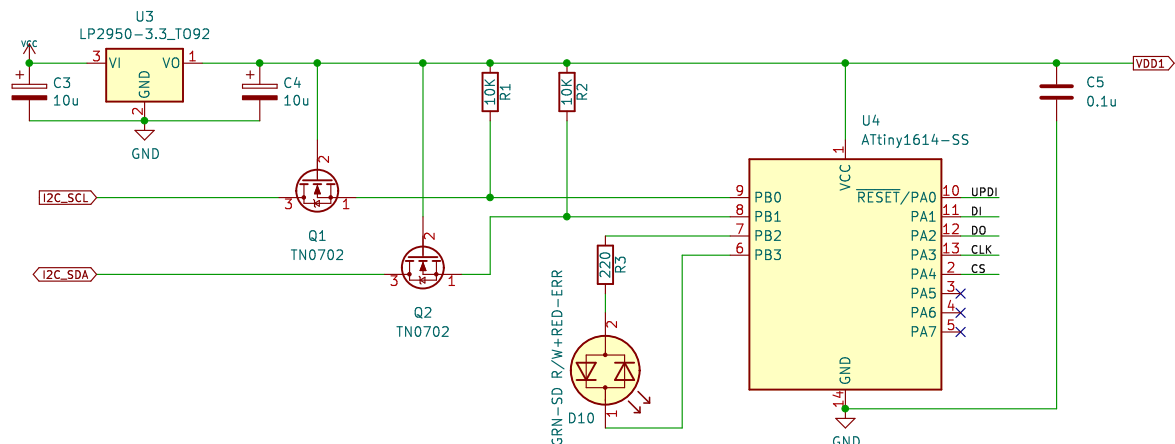
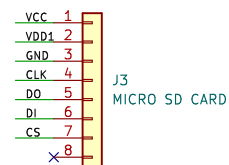
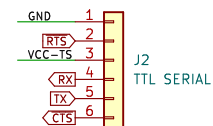


Diagram showing 14 SPDT switches (SW1 to SW14) connected to various pins and GND. The switches are arranged in two columns. Each switch has a common terminal (1) connected to GND, a normally open terminal (2) connected to a signal pin, and a normally closed terminal (3) connected to GND. The signal pins are labeled as follows:

- SW1: FP-D0
- SW2: FP-D1
- SW3: FP-D2
- SW4: FP-D3
- SW5: FP-D4
- SW6: FP-D5
- SW7: FP-D6
- SW8: FP-D7
- SW9: EXT\_RES
- SW10: IM2-EN
- SW11: INTO-EN
- SW12: WAIT-EN
- SW13: VCC-T5
- SW14: VCC-T5

NOTE: USE MTS-102 SPDT SWITCHES



UPDI TO UPDI INTERFACE

The diagram shows the connection between the UPDI pin of the NT1 NetTie\_2 module and the UPDI pin of the UPDI ONLY module. The NT1 module has pins for GND (1), VCC-TS (3), and UPDI-TX (5). The UPDI ONLY module has pins for GND (1), VCC-TS (3), and UPDI-TX (5). The UPDI pin of the NT1 module is connected to the UPDI-TX pin of the UPDI ONLY module via a 470Ω resistor (R4).

NOTE: ONLY ATTEMPT UPDI PROGRAMMING  
WITH FRONT PANEL DISCONNECTED FROM  
Z80 PROCESSOR BOARD. SW13 CONTROLS  
VCC POWER SUPPLIED TO ATTINY 1614  
VIA THE USB TO UPDI INTERFACE.  
REMOVE UNNECESSARY COMPONENTS TO  
MINIMIZE US CURRENT DRAW. (SD CARD  
ADAPTER, SSD1306 DISPLAY, AND 74LS240)

