

Sheet: buffers



File: buffers.sch

Sheet: RTC



File: RTC.sch

Sheet: IO



File: IO.sch

Sheet: power



File: power.sch

Sheet: battery



File: battery.sch

Sheet: RAM



File: RAM.sch

Sheet: bus



File: bus.sch

Sheet: GALS



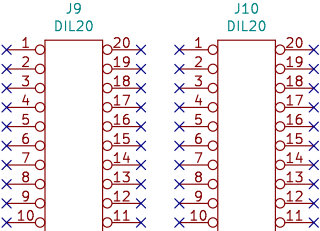
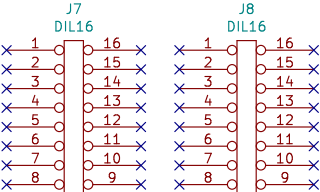
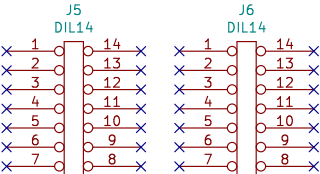
File: GALS.sch

Sheet: ROM



File: ROM.sch

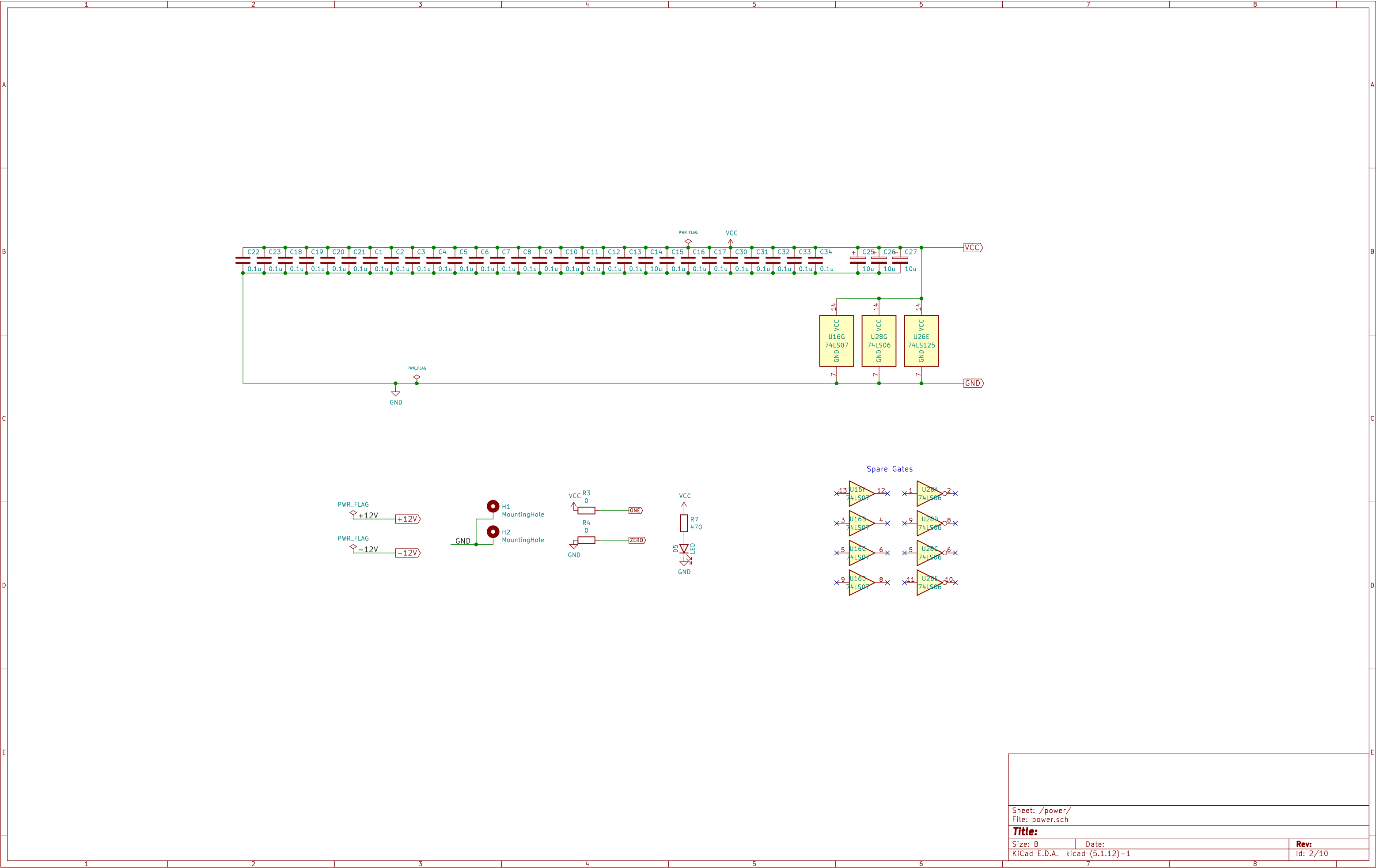
SPARE SOCKETS
FOR DEBUGGING

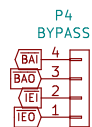
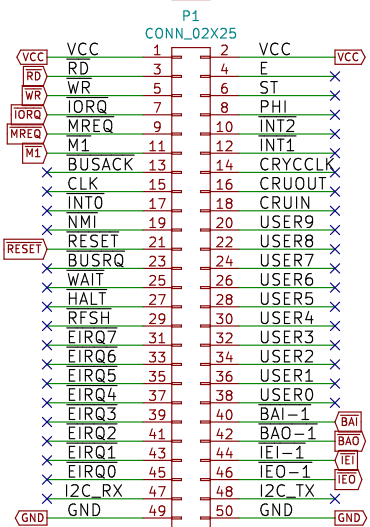
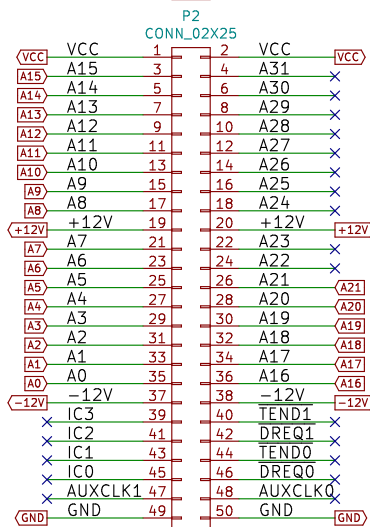
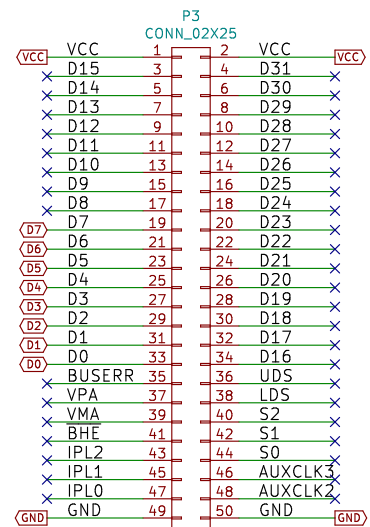


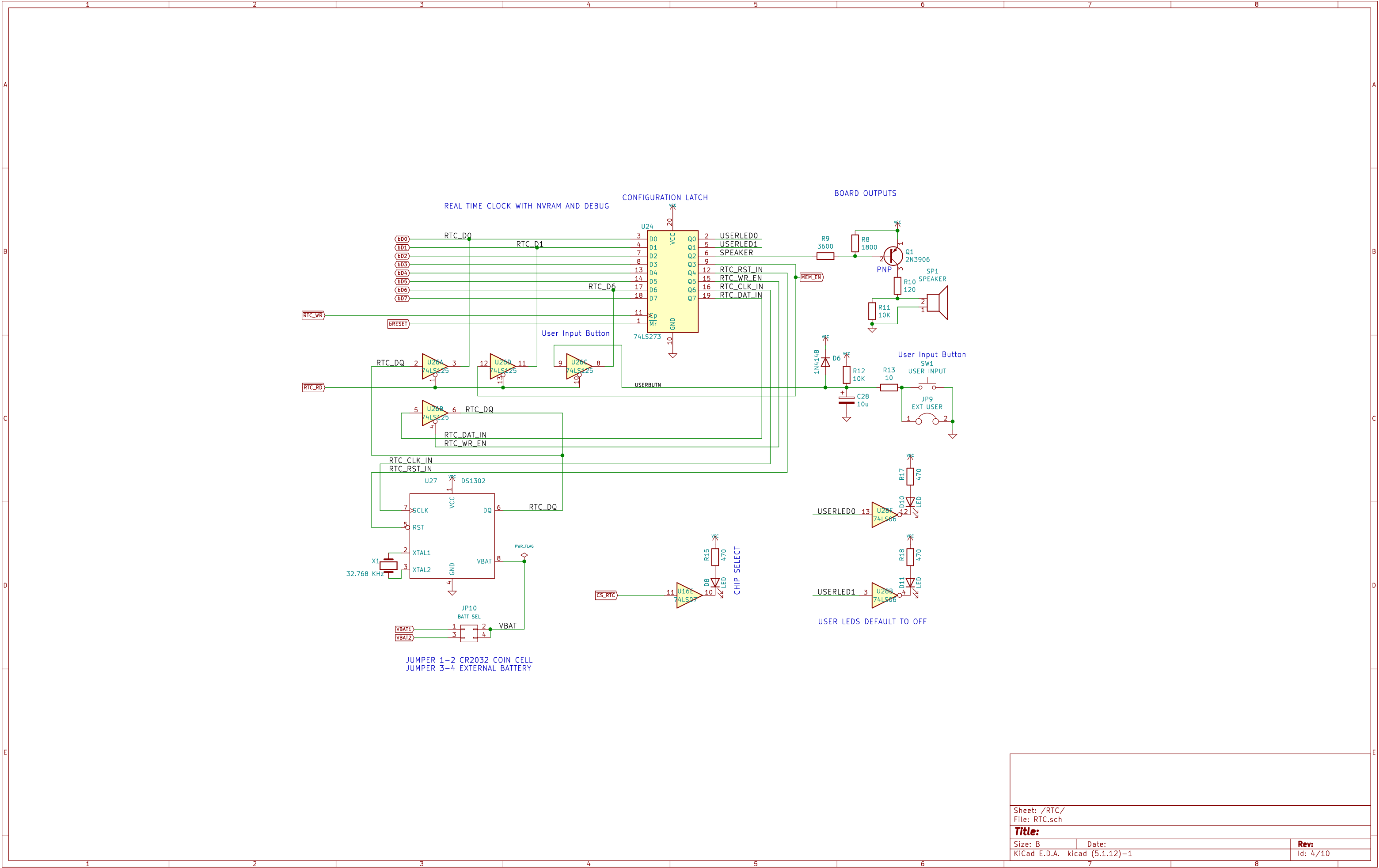
Sheet: /
File: memory.ROMRAM.sch

Title:

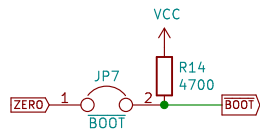
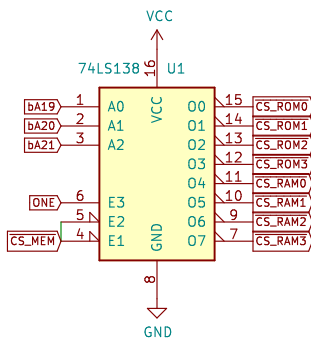
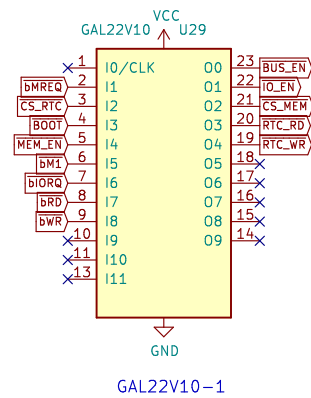
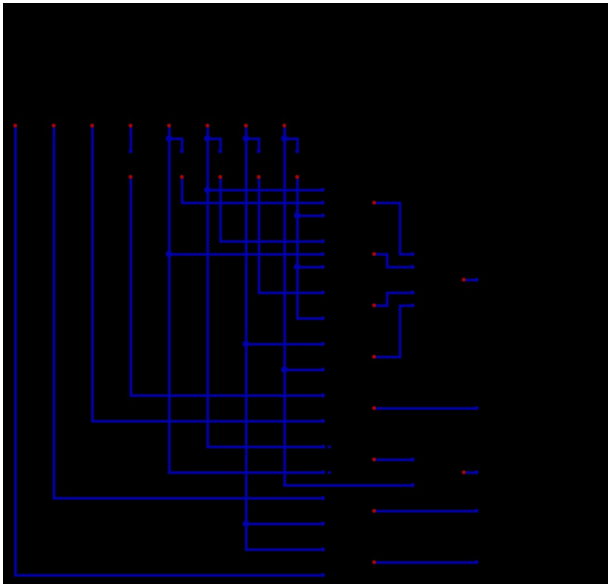
Size: B	Date:	Rev:
KiCad E.D.A.	kiCad (5.1.12)–1	Id: 1/10







1	2	3	4	5	6	7	8	
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JP7 NOTE: FIRST ROM & RAM BOARD IN SYSTEM MUST HAVE JP7 JUMPER INSTALLED. ALSO MUST HAVE ROM INSTALLED IN U20 WITH BOOT CODE. FIRST BOARD ONLY RESPONDS WHEN MEM_EN LOW (BY DEFAULT).

SECOND ROM & RAM BOARD INSTALLED IN SYSTEM MUST HAVE JP7 JUMPER NOT INSTALLED. ALSO SECOND BOARD ONLY RESPONDS WHEN MEM_EN IS HIGH.

ACCESS IS CONTROLLED BY SOFTWARE TO ENSURE THAT ONLY ONE OF BOARDS HAS MEM_EN HIGH AT ANY GIVEN TIME OR BUS CONTENTION.

