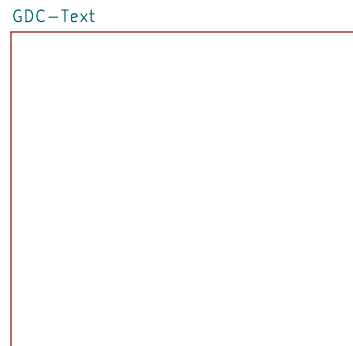
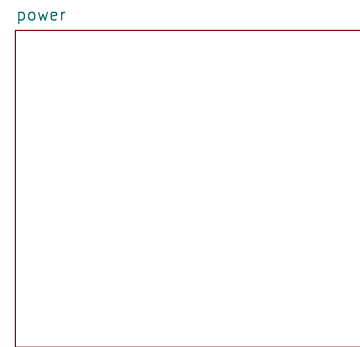


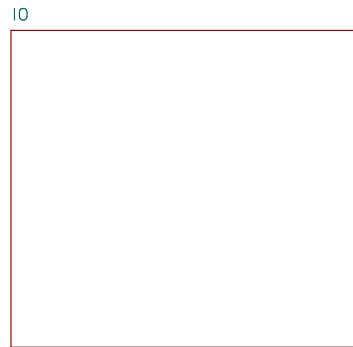
File: bus.kicad_sch



File: GDC-Text.kicad_sch



File: power.kicad_sch

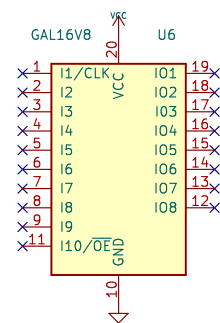


File: IO.kicad_sch

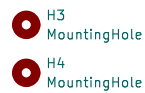


File: buffers.kicad_sch

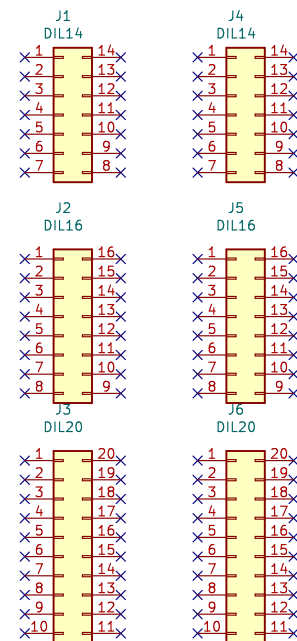
SPARE GAL FOR DEBUGGING

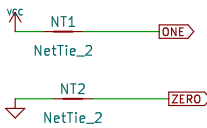
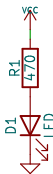
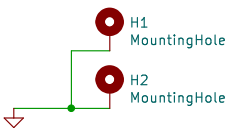
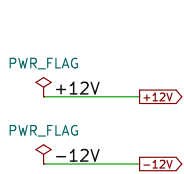
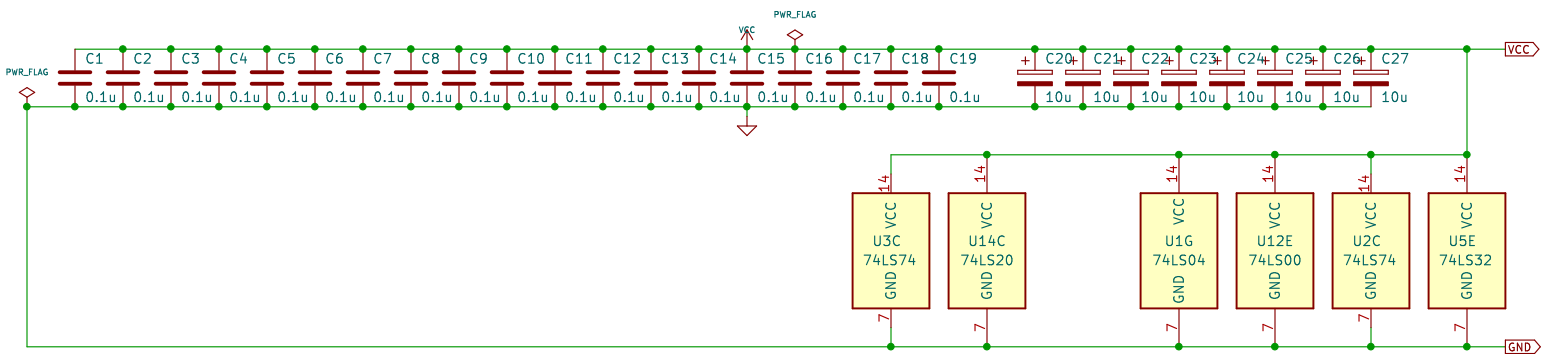


THROUGH HOLES FOR
PATCH WIRE ROUTING
ACROSS SIDES



SPARE SOCKETS FOR DEBUGGING





Spare Components

