

bus

File: bus.kicad\_sch

power

File: power.kicad\_sch

VDP

File: VDP.kicad\_sch

PSG

File: PSG.kicad\_sch

GAL

File: GAL.kicad\_sch

buffers

File: buffers.kicad\_sch

10

File: IO.kicad\_sch

ACR

File: ACR.kicad\_sch

Amplifier

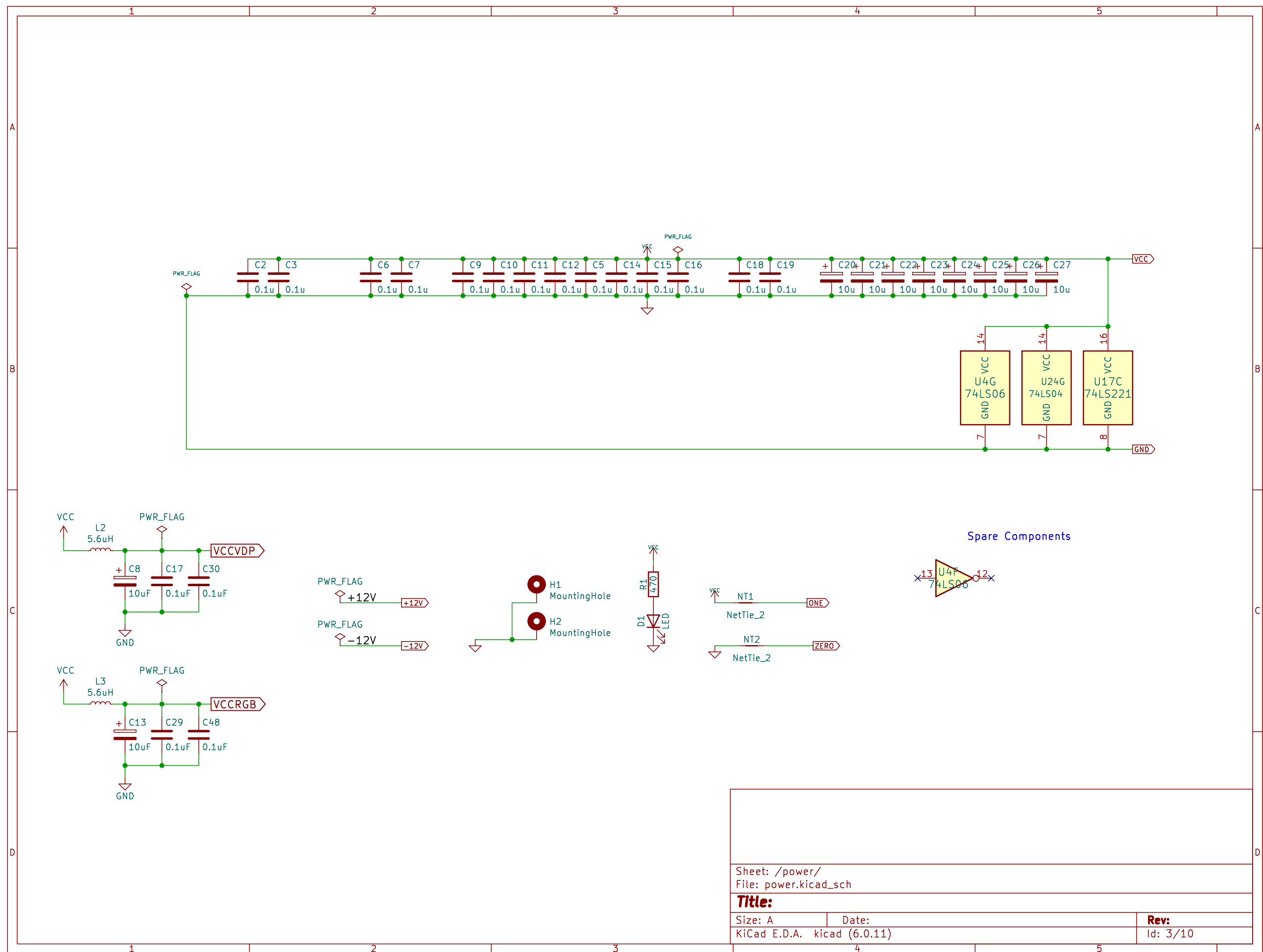
File: Amplifier.kicad\_sch

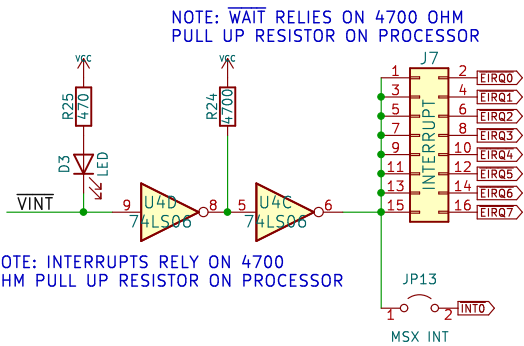
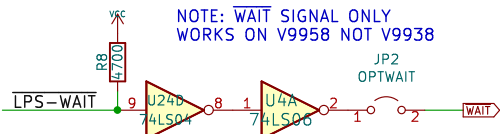
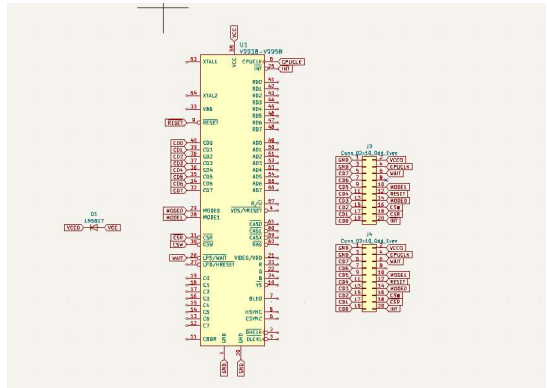
Sheet: /  
File: input-output.Media.kicad\_sch

**Title:**

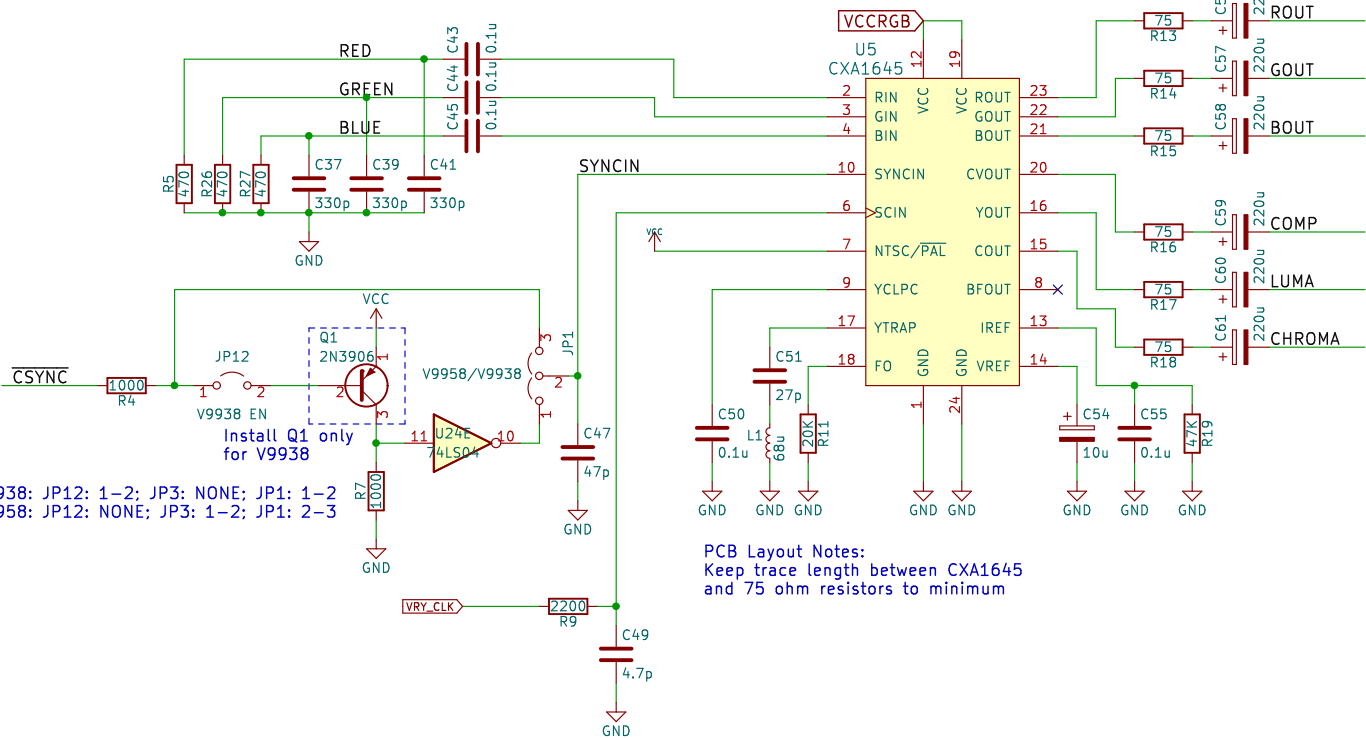
Size: B	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 1/10



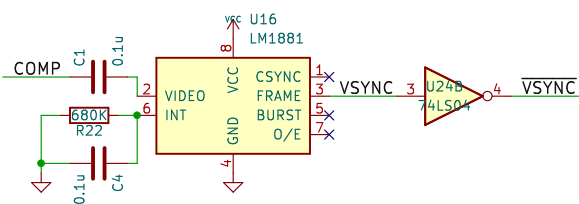




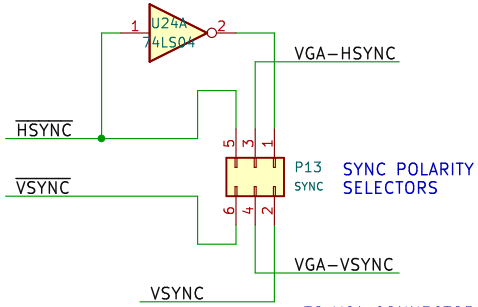
NOTE: INTERRUPTS RELY ON 4700 OHM PULL UP RESISTOR ON PROCESSOR



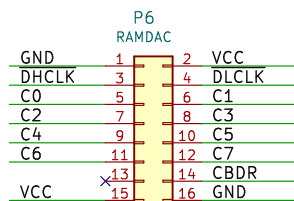
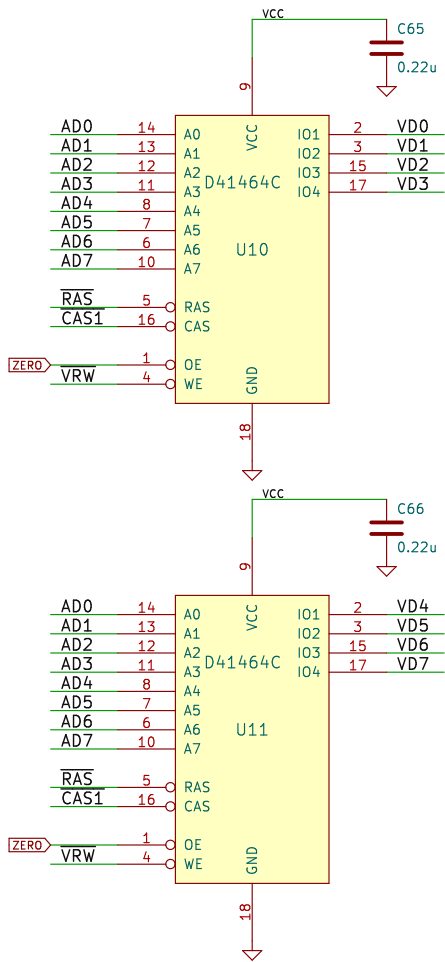
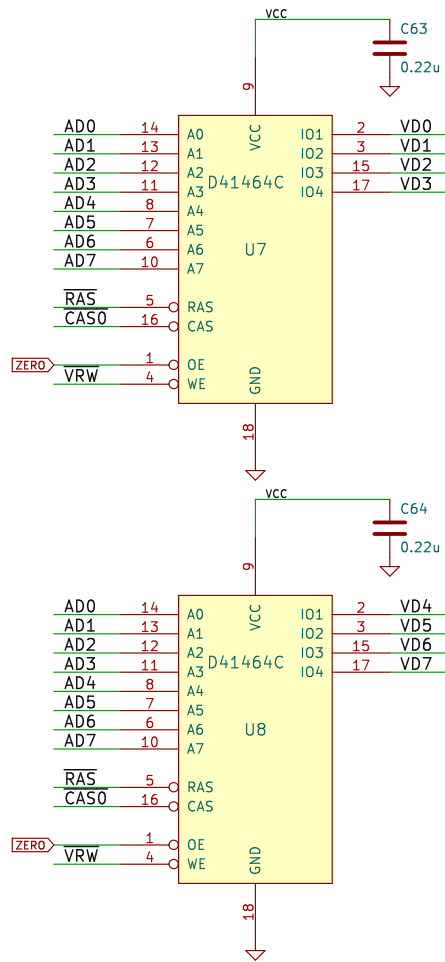
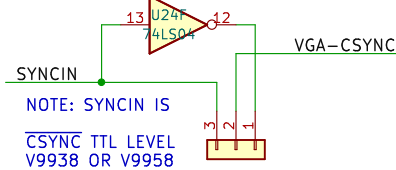
PCB Layout Notes:  
Keep trace length between CXA1645 and 75 ohm resistors to minimum



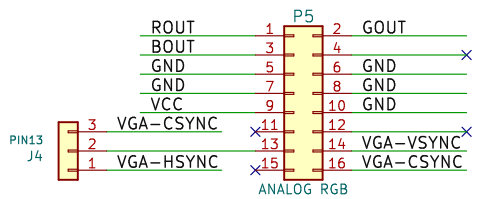
Vertical and Horizontal Sync Circuit



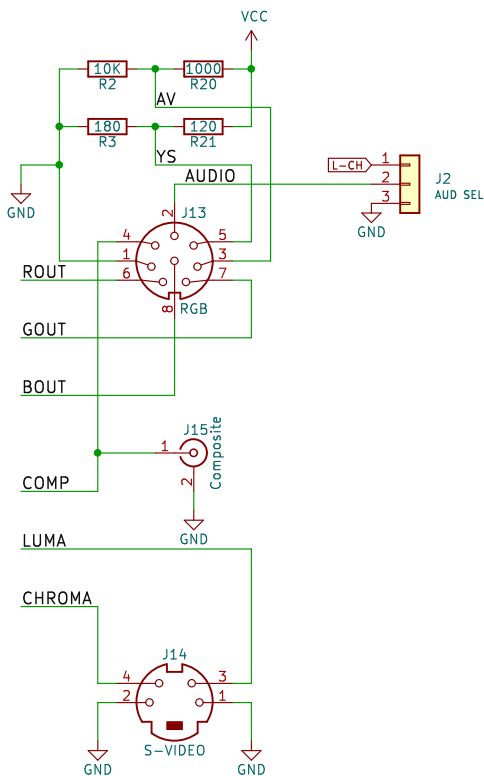
Composite Sync Circuit



NOTE: P6 FOR RAMDAC EXPANSION OPTION



NOTE: P5 FOR VGA HD15F OPTION



Sheet: /VDP/  
File: VDP.kicad\_sch

Title:

Size: B

Date:

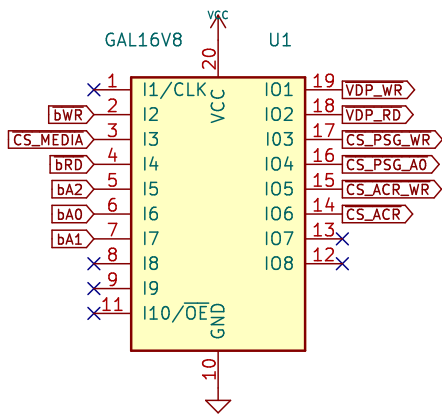
KiCad E.D.A. kicad (6.0.11)

Rev:

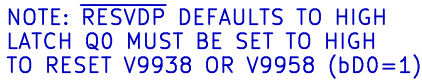
Id: 4/10



MEDIA CS V3 GAL16V8







Sheet: /ACR/ File: ACR.kicad_sch		
<b>Title:</b>		
Size: A	Date:	<b>Rev:</b>
KiCad E.D.A. kicad (6.0.11)		Id: 8/10



