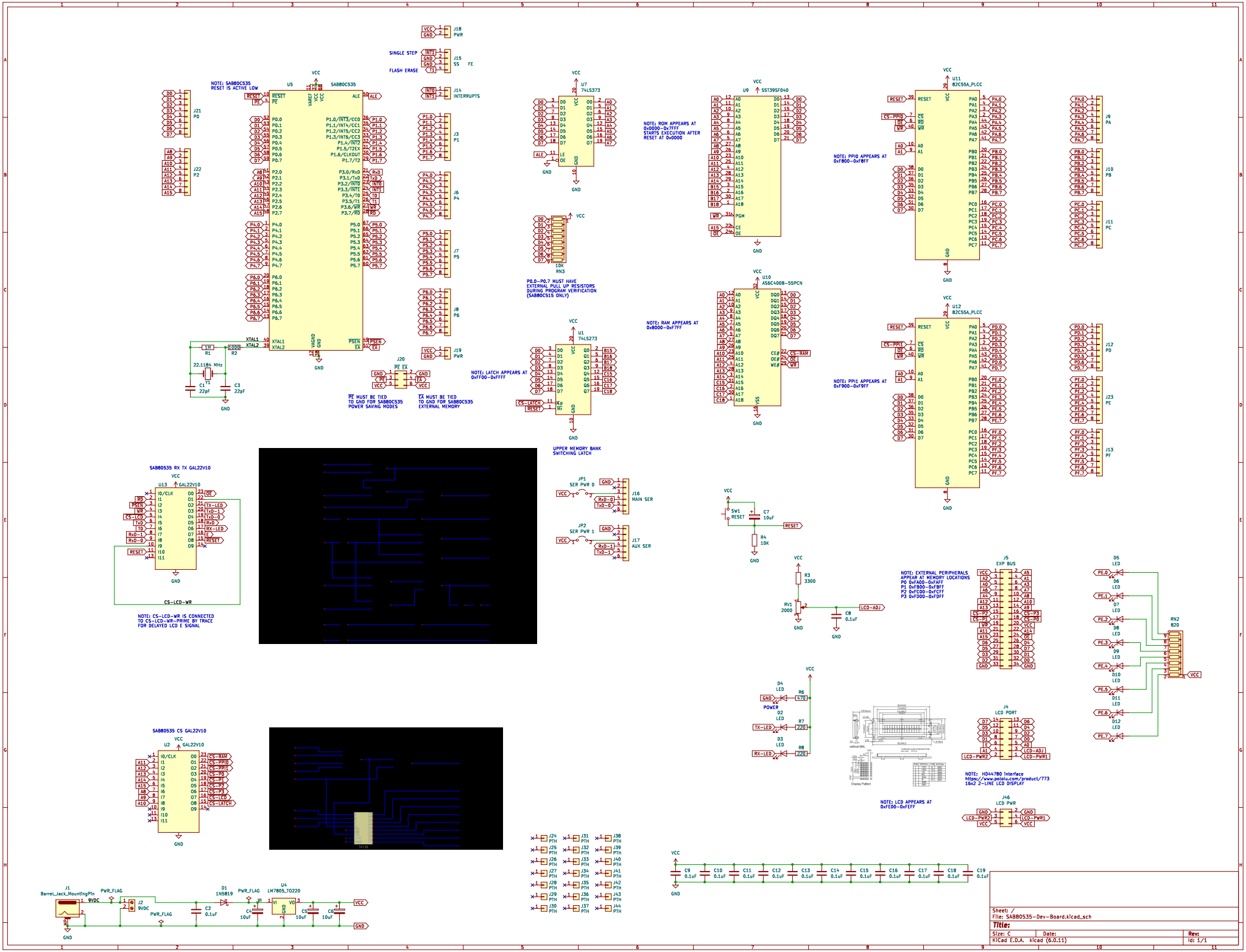
NOTE: SAB80C535 RESET IS ACTIVE LOW
U5 SAB80C535
J18 PWR
SINGLE STEP
FLASH ERASE
J15 SS FE
J14 INTERRUPTS
J21 P0
J22 P2
J3 P1
J6 P4
J7 P5
J8 P6
J19 PWR
U7 74LS373
U9 SST39SF040
U11 82C55A_PLCC
NOTE: ROM APPEARS AT 0x0000-0x7FFF STARTS EXECUTION AFTER RESET AT 0x0000
NOTE: PPIO APPEARS AT 0xF800-0xFBFF
J9 PA
J10 PB
J11 PC
RN3 10K
P0.0-P0.7 MUST HAVE EXTERNAL PULL UP RESISTORS DURING PROGRAM VERIFICATION (SAB80C515 ONLY)
U10 AS6C4008-55PCN
NOTE: RAM APPEARS AT 0x8000-0xF7FF
U12 82C55A_PLCC
NOTE: PPI1 APPEARS AT 0xF900-0xF9FF
J12 PD
J23 PE
J13 PF
U1 74LS373
NOTE: LATCH APPEARS AT 0xF100-0xFFFF
PE MUST BE TIED TO GND FOR SAB80C535 POWER SAVING MODES
EX MUST BE TIED TO GND FOR SAB80C535 EXTERNAL MEMORY
UPPER MEMORY BANK SWITCHING LATCH
22.1184 MHz
SAB80535 RX TX GAL22V10
U13 GAL22V10
NOTE: CS-LCD-WR IS CONNECTED TO CS-LCD-WR-PRIME BY TRACE FOR DELAYED LCD E SIGNAL
CS-LCD-WR
JP1 SER PWR 0
JP2 SER PWR 1
J16 MAIN SER
J17 AUX SER
SW1 RESET
C7 10uF
R4 10K
R3 3300
RV1 2000
C8 0.1uF
LCD-ADJ
NOTE: EXTERNAL PERIPHERALS APPEAR AT MEMORY LOCATIONS
P0 0xFA00-0xFAFF
P1 0xFB00-0xFBFF
P2 0xFC00-0xFCFF
P3 0xFD00-0xFDFF
J5 EXP BUS
D5 LED
D6 LED
D7 LED
D8 LED
D9 LED
D10 LED
D11 LED
D12 LED
RN2 820
D4 LED POWER
D2 LED TX-LED
D3 LED RX-LED
R6 470
R7 220
R8 220
J4 LCD PORT
NOTE: HD44780 Interface https://www.pololu.com/product/773 16x2 2-LINE LCD DISPLAY
NOTE: LCD APPEARS AT 0xF000-0xFEFF
J46 LCD PWR
SAB80535 CS GAL22V10
U2 GAL22V10
74138
J24 PTH
J25 PTH
J26 PTH
J27 PTH
J28 PTH
J29 PTH
J30 PTH
J31 PTH
J32 PTH
J33 PTH
J34 PTH
J35 PTH
J36 PTH
J37 PTH
J38 PTH
J39 PTH
J40 PTH
J41 PTH
J42 PTH
J43 PTH
J44 PTH
C9 0.1uF
C10 0.1uF
C11 0.1uF
C12 0.1uF
C13 0.1uF
C14 0.1uF
C15 0.1uF
C16 0.1uF
C17 0.1uF
C18 0.1uF
C19 0.1uF
J1 Barrel_Jack_MountingPin
J2 9VDC
PWR_FLAG
D1 1N5819
U4 LM7805_TO220
C2 0.1uF
C4 10uF
C5 10uF
C6 10uF
Sheet: /
File: SAB80535-Dev-Board.kicad_sch
Title:
Size: C
Date:
Rev:
KiCad E.D.A. kicad (6.0.11)
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