

	1	2	3	4	5	6	7	8	
A	bus		power		buffers		fpanel		
B	File: bus.kicad_sch 65816CPU		File: power.kicad_sch GALS		File: buffers.kicad_sch bus sharing		File: fpanel.kicad_sch mapper		
C	File: 65816CPU.kicad_sch		File: GALS.kicad_sch		File: bussharing.kicad_sch		File: mapper.kicad_sch		
D									
E									
	1	2	3	4	5	6	7	8	

Sheet: /

File: processor.65816.kicad_sch

Title: **Duodyne 65816 CPU board**

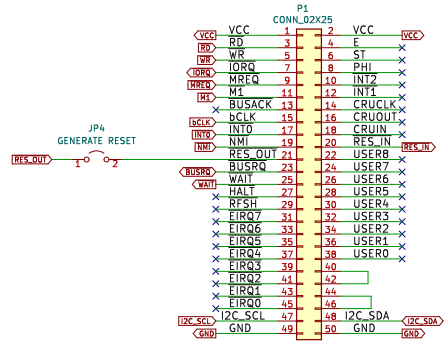
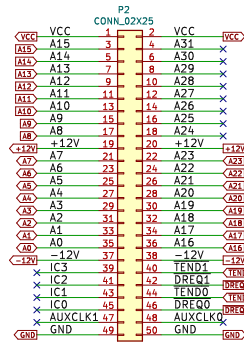
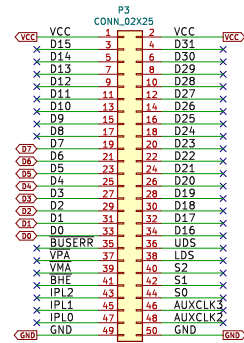
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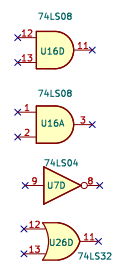
Date: 2023-12-28

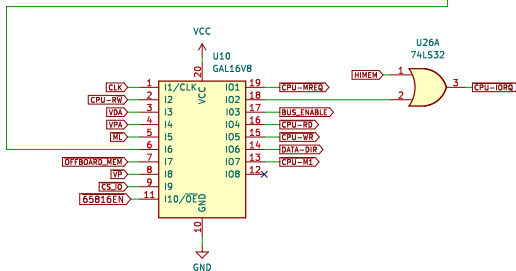
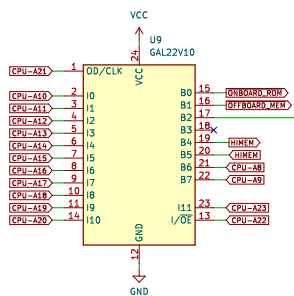
Rev: **V1.10**

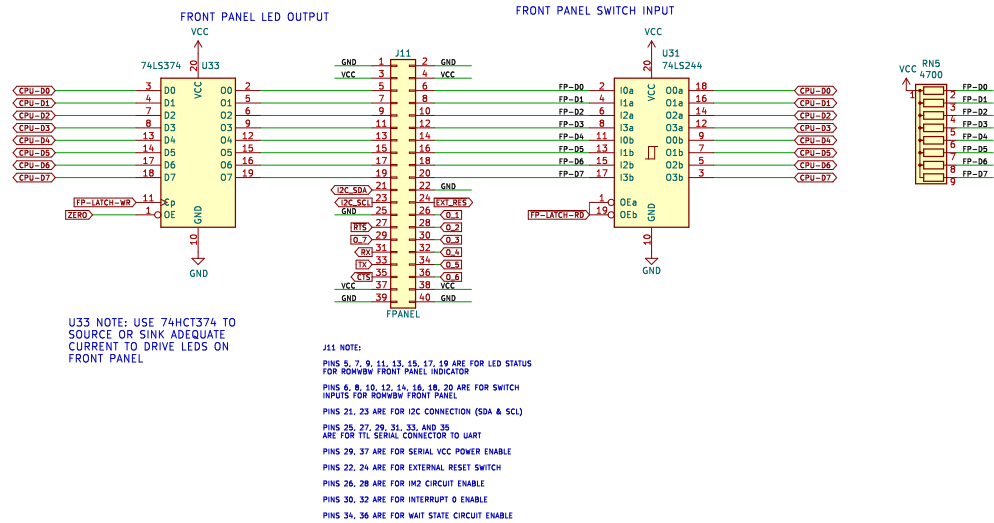
KiCad E.D.A. kicad (6.0.11)

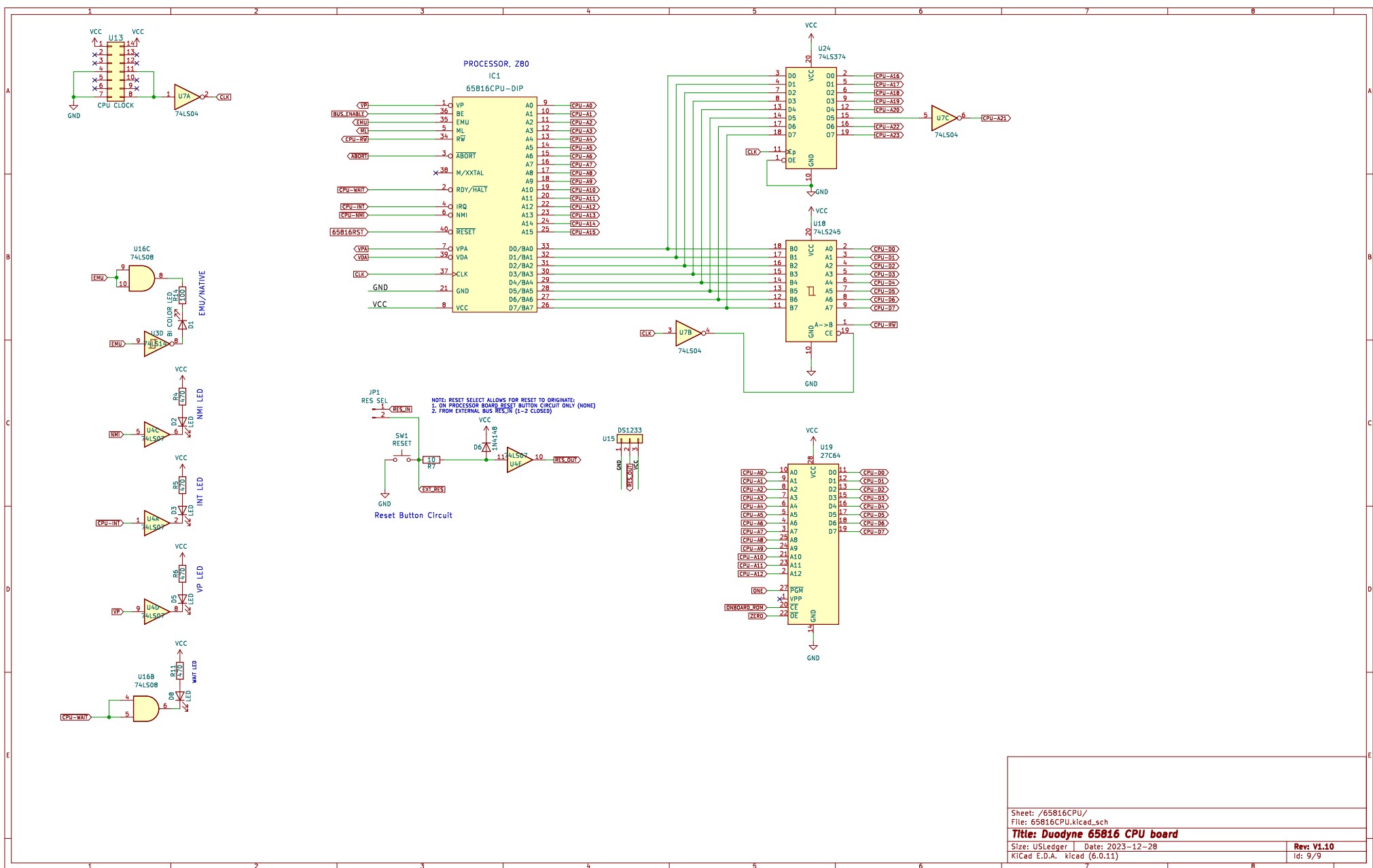
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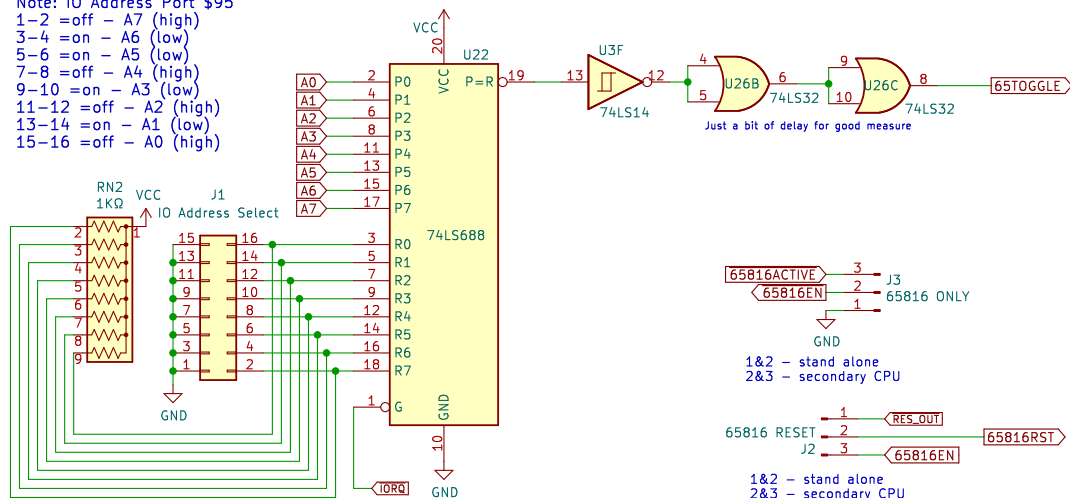








Note: IO Address Port \$95
 1-2 = off - A7 (high)
 3-4 = on - A6 (low)
 5-6 = on - A5 (low)
 7-8 = off - A4 (high)
 9-10 = on - A3 (low)
 11-12 = off - A2 (high)
 13-14 = on - A1 (low)
 15-16 = off - A0 (high)

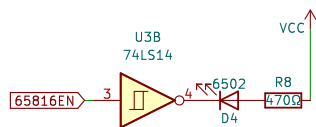
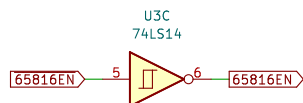
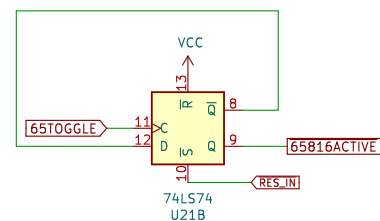
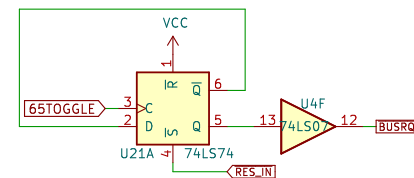


65816ACTIVE 3 J3
 65816EN 2
 1 65816 ONLY

1&2 - stand alone
 2&3 - secondary CPU

65816 RESET 1 RES_OUT
 J2 2
 3 65816EN 65816RST

1&2 - stand alone
 2&3 - secondary CPU

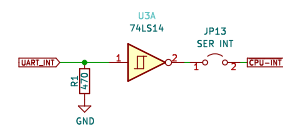
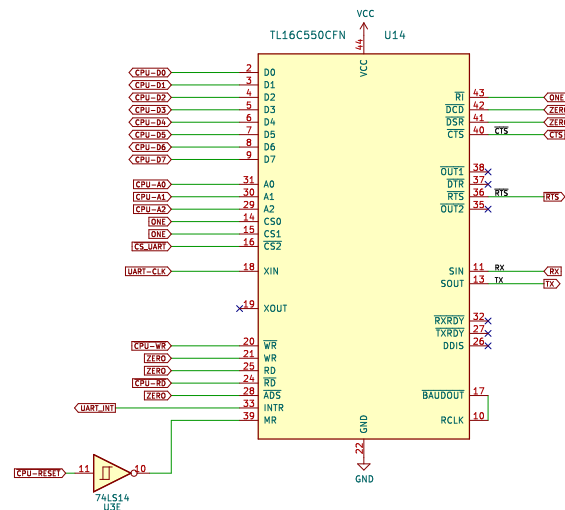
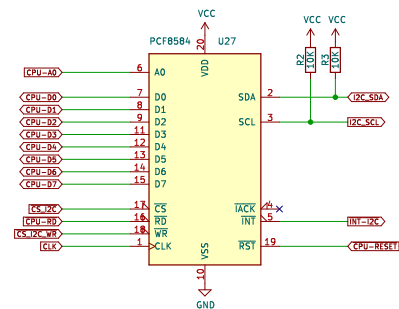
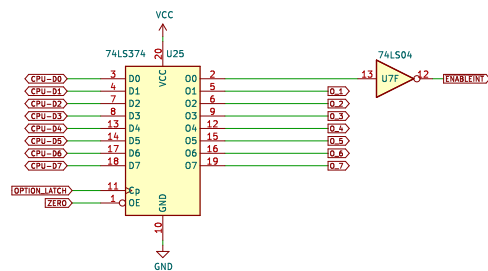
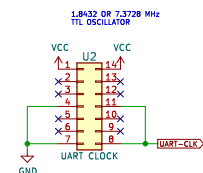
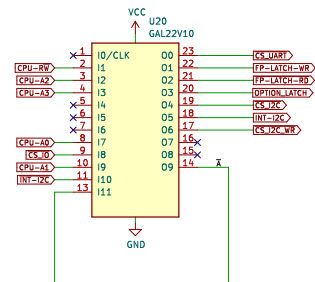
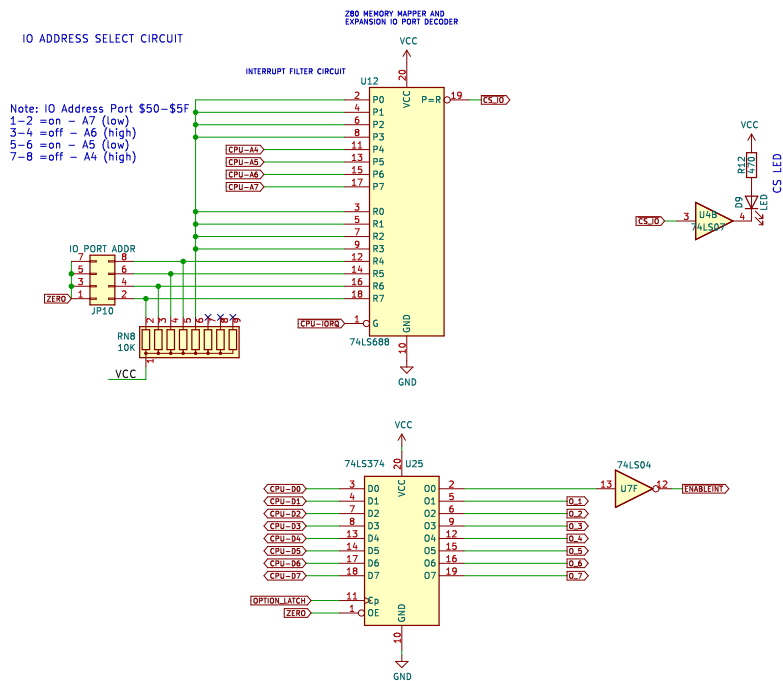


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 File: bussharing.kicad_sch

Title: Duodyne 65816 CPU board

Size: A4 Date: 2023-12-28
 KiCad E.D.A. kicad (6.0.11)

Rev: V1.10
 Id: 9/9



NOTE: INTERRUPTS RELY ON 4700 OHM PULL UP RESISTOR ON PROCESSOR