

12345678

A

bus

File: bus.kicad_sch

VDP

File: VDP.kicad_sch

GAL

File: GAL.kicad_sch

IO

File: IO.kicad_sch

Amplifier

File: Amplifier.kicad_sch

power

File: power.kicad_sch

PSG

File: PSG.kicad_sch

buffers

File: buffers.kicad_sch

ACR

File: ACR.kicad_sch

B

C

D

E

12345678

Sheet: /

File: Input-output,Media.kicad_sch

Title:

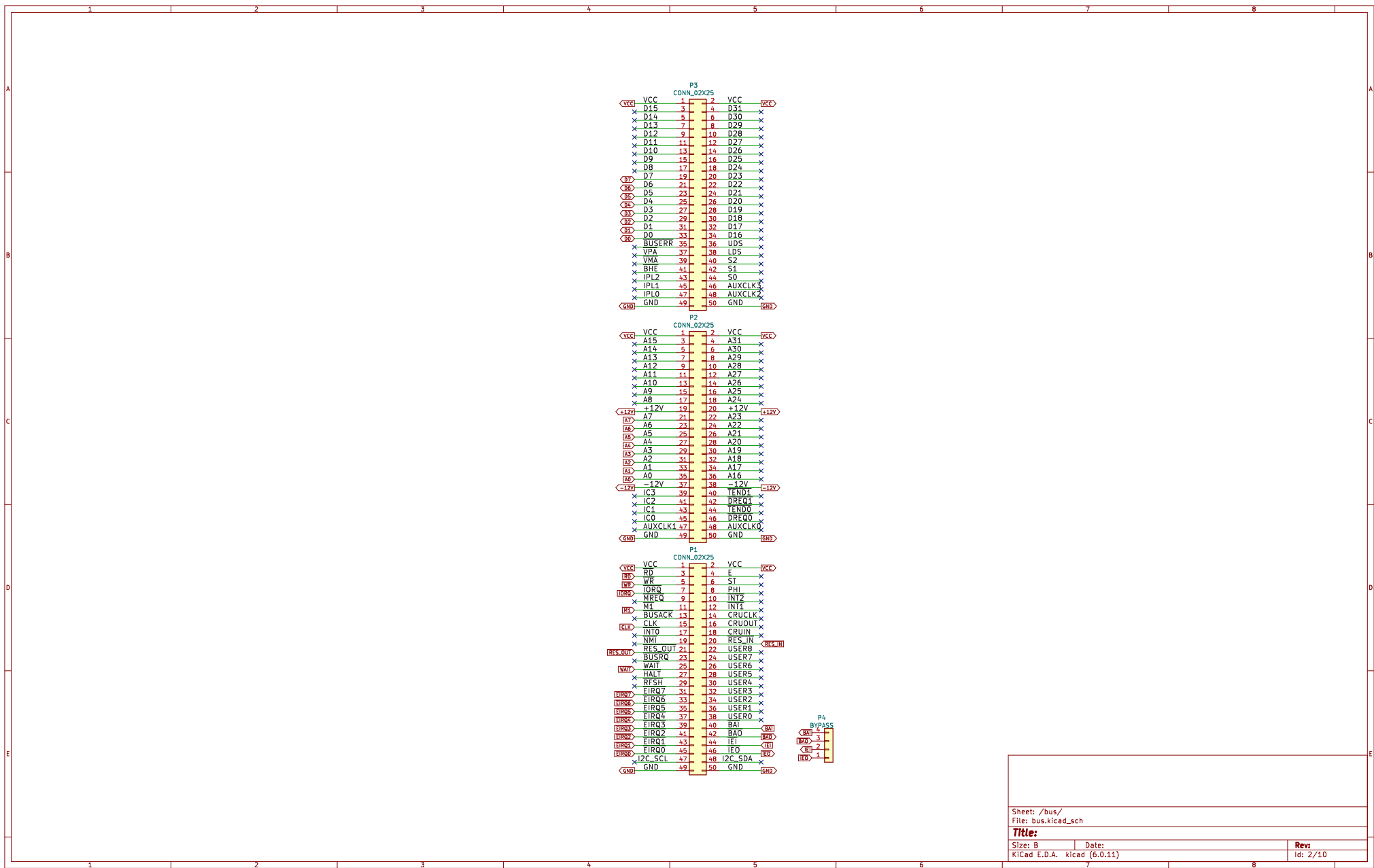
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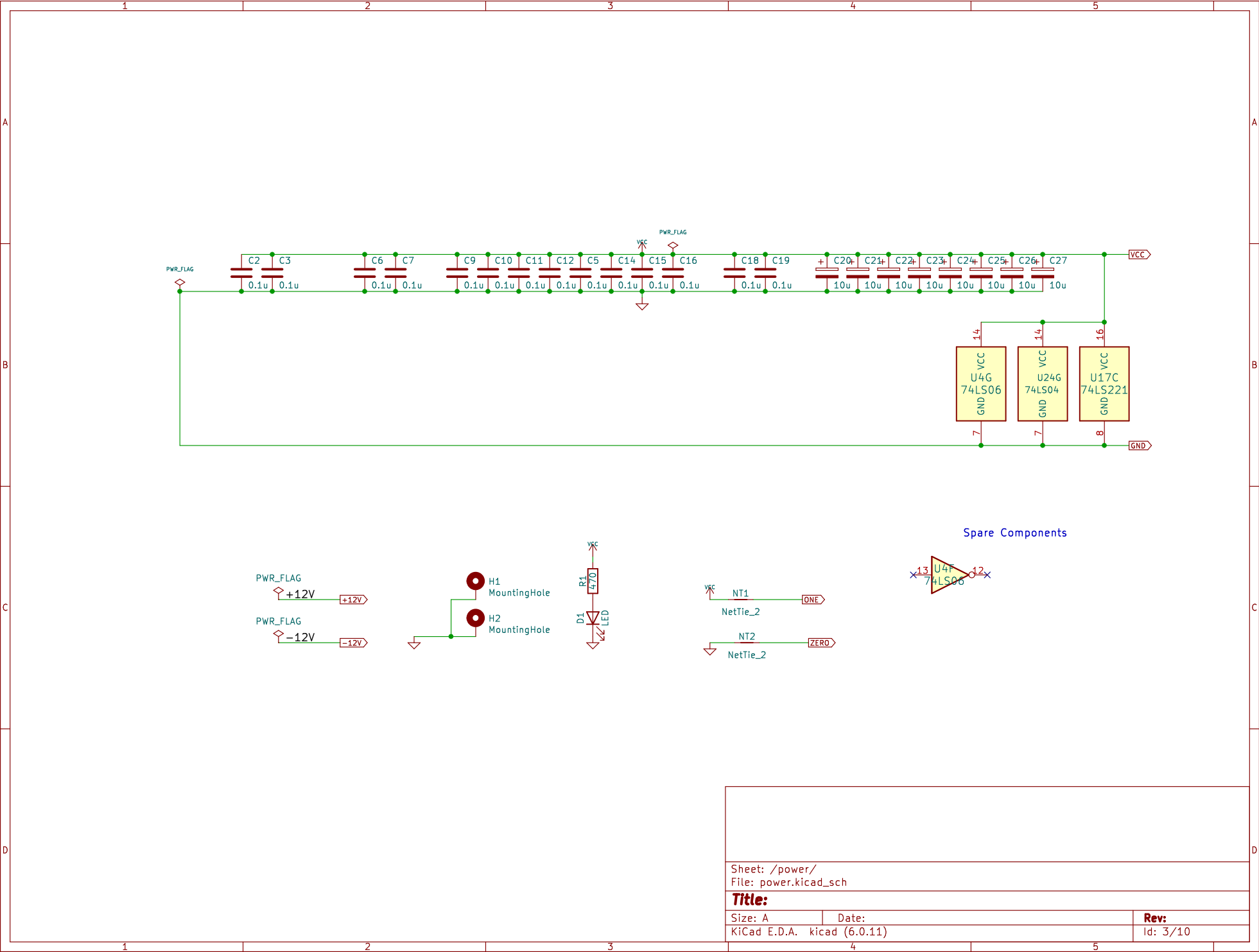
Date:

KiCad E.D.A. kicad (6.0.11)

Rev:

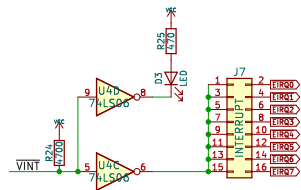
Id: 1/10



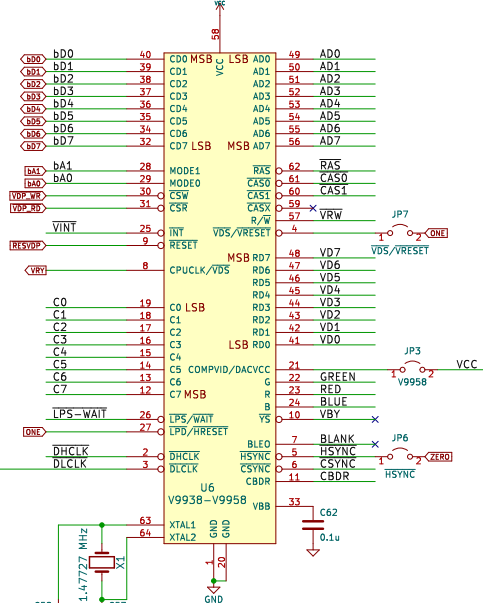


NOTE: WAIT SIGNAL ONLY WORKS ON V9958 NOT V9938

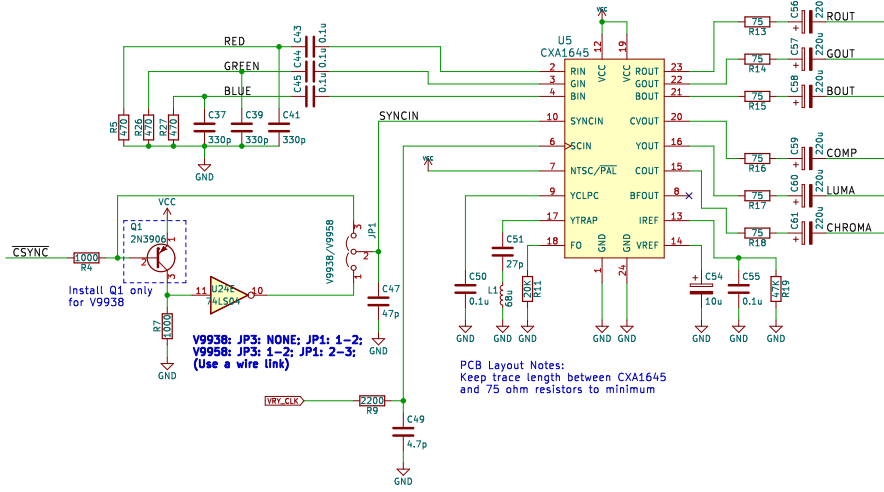
NOTE: WAIT RELIES ON 4700 OHM PULL UP RESISTOR ON PROCESSOR



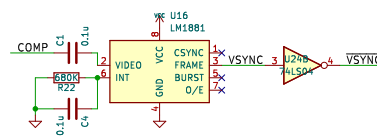
NOTE: INTERRUPTS RELY ON 4700 OHM PULL UP RESISTOR ON PROCESSOR



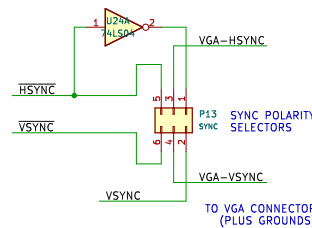
WARNING: DO NOT SET JP3 PINS 1-2 FOR V9938 OR WILL DAMAGE/DESTROY V9938. JP3 PINS 1-2 FOR V9958 ONLY



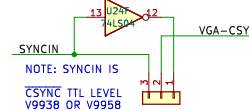
PCB Layout Notes:
Keep trace length between CXA1645 and 75 ohm resistors to minimum



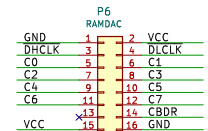
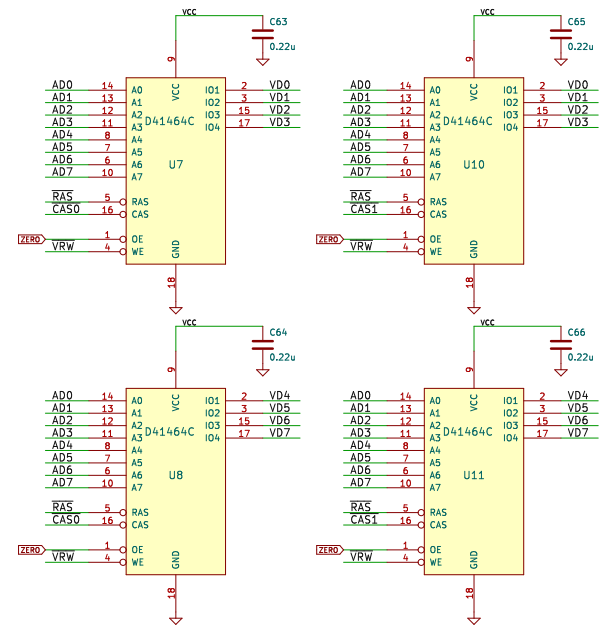
Vertical and Horizontal Sync Circuit



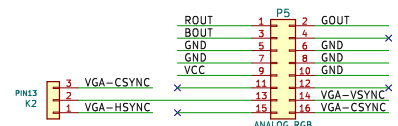
Composite Sync Circuit



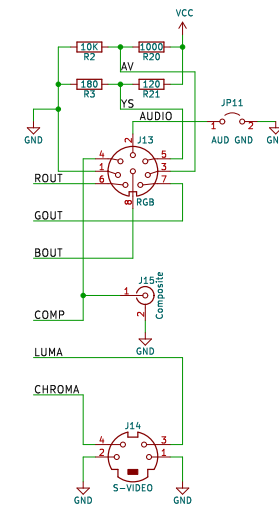
NOTE: SYNCIN IS CSYNC TTL LEVEL V9938 OR V9958

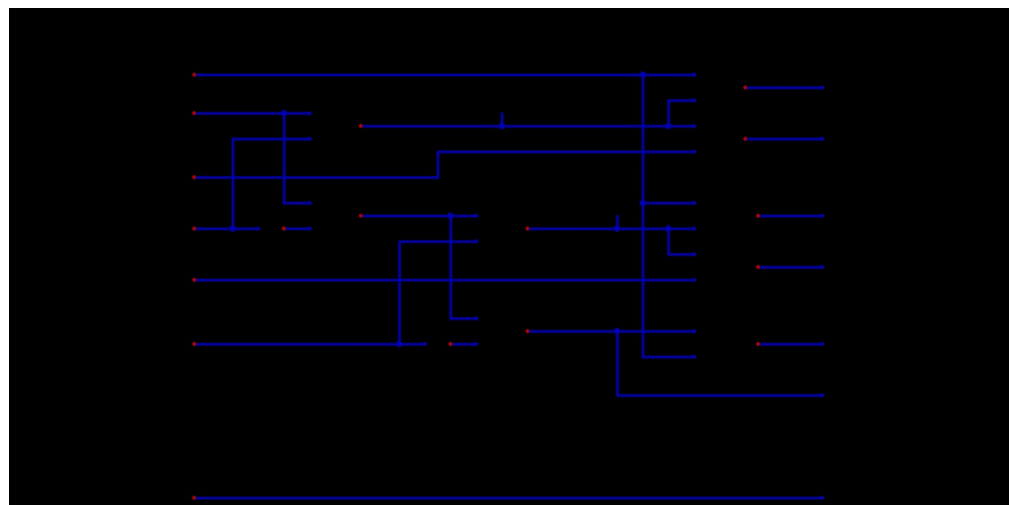


NOTE: P6 FOR RAMDAC EXPANSION OPTION

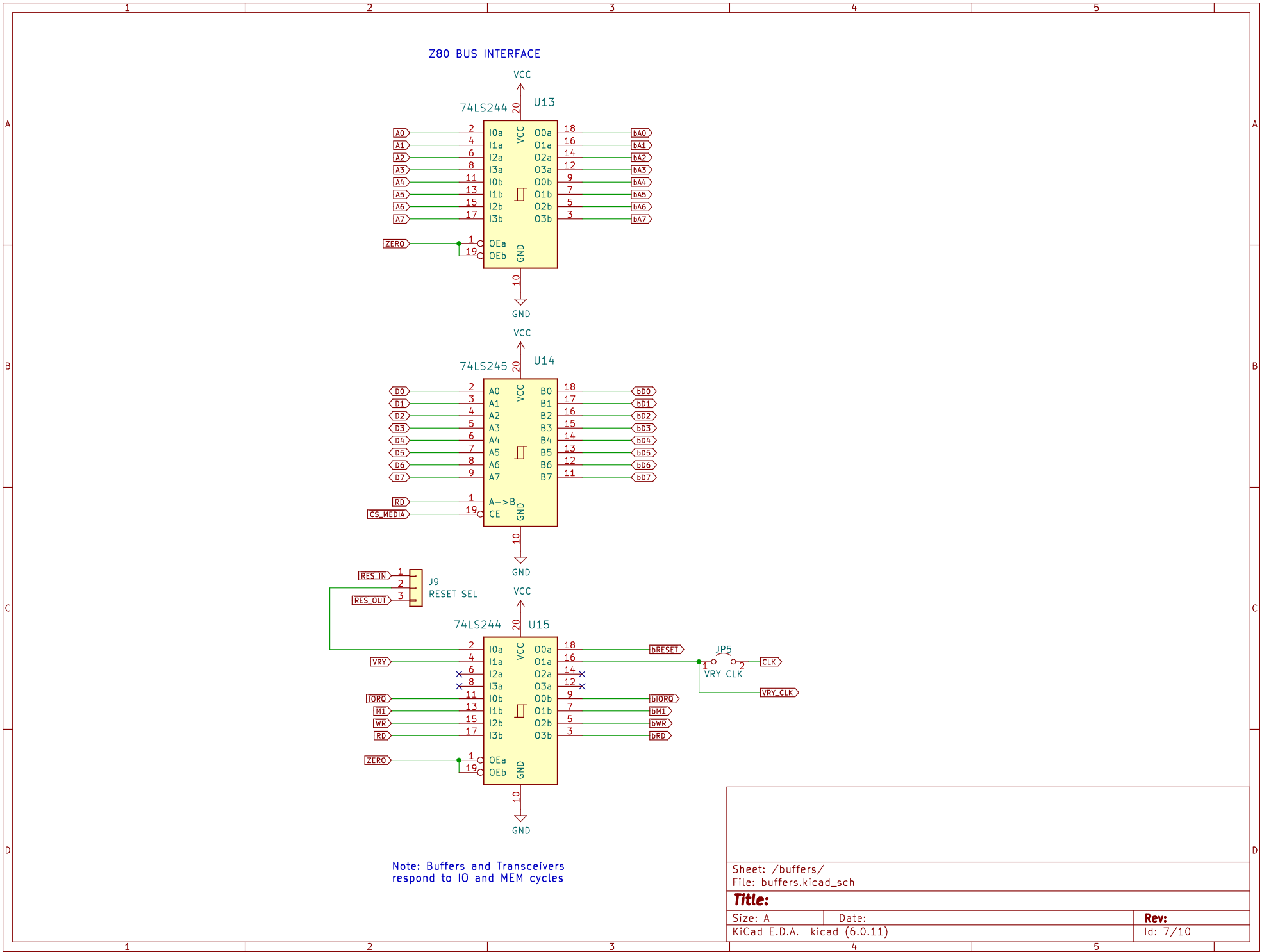


NOTE: P5 FOR VGA HD15F OPTION

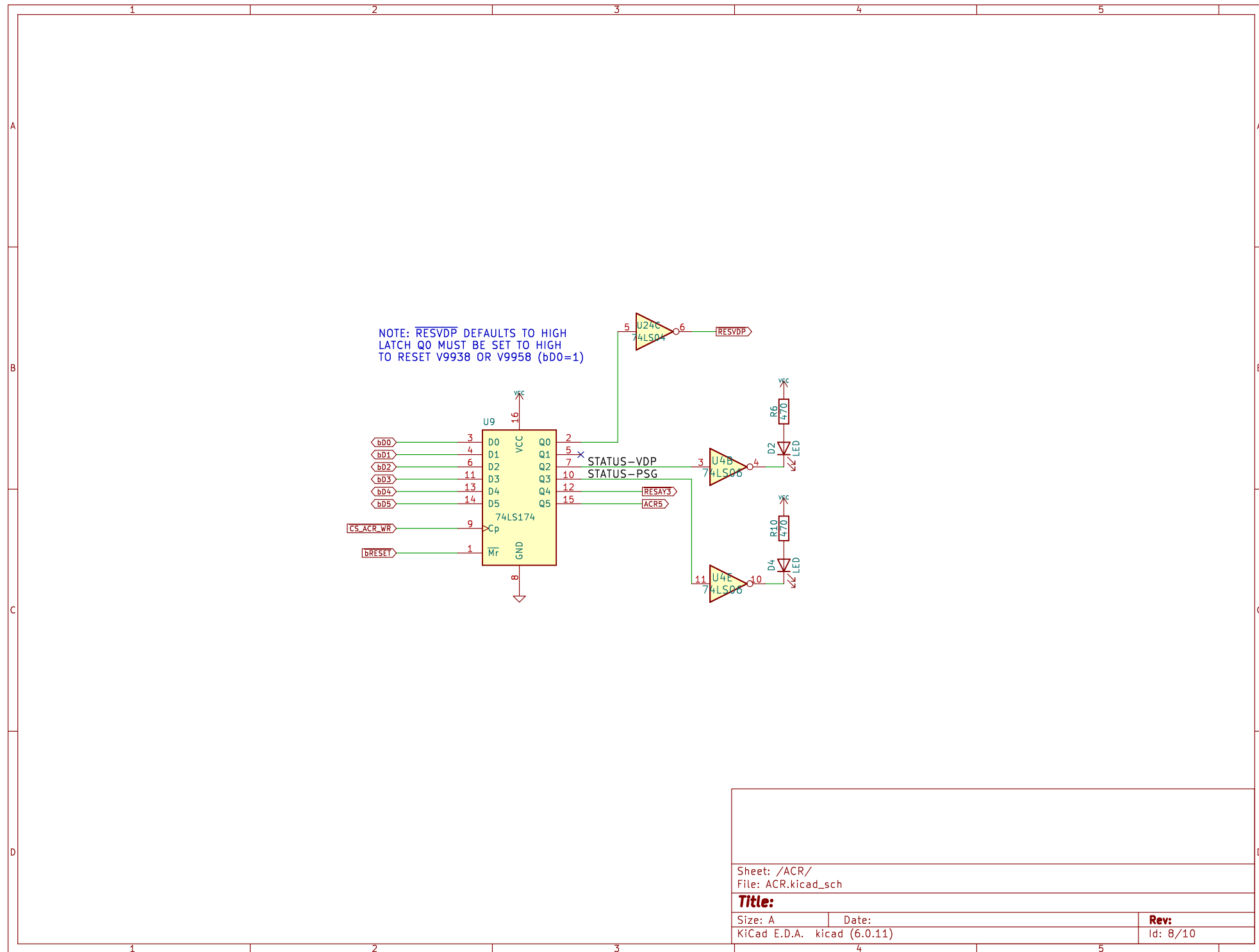


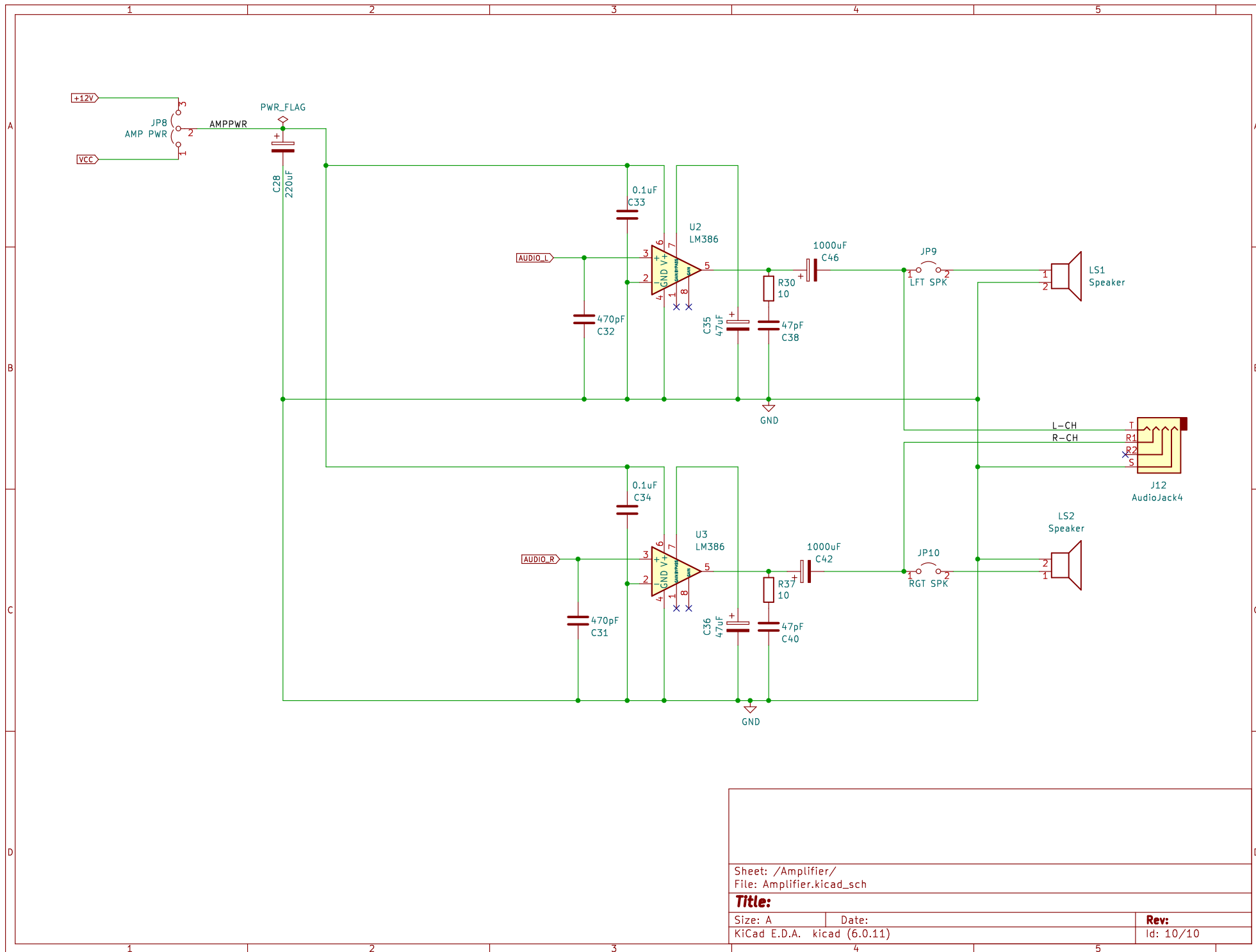


Rev:
Id: 6/10



Sheet: /buffers/ File: buffers.kicad_sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad (6.0.11)		Id: 7/10





Sheet: /Amplifier/
File: Amplifier.kicad_sch

Title:

Size: A Date:
KiCad E.D.A. kicad (6.0.11)

Rev:
Id: 10/10