

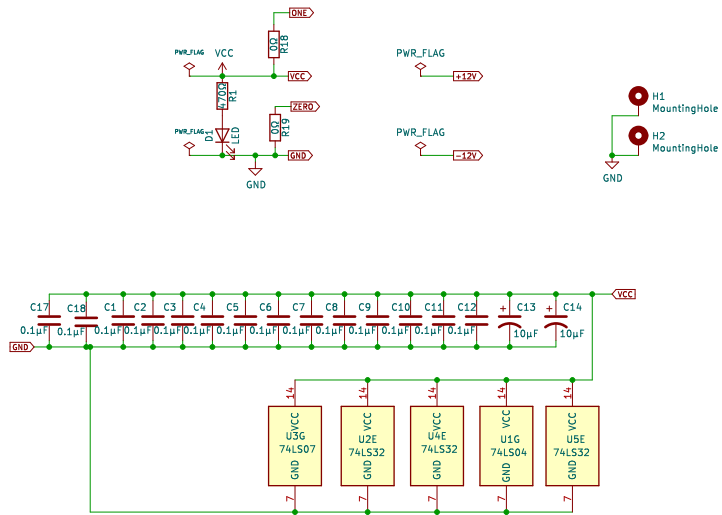
By Dan Werner

Sheet: /bus/  
File: bus.kicad\_sch

**Title: Dual ESP32 IO**

Size: B | Date: 2023-08-06  
KiCad E.D.A. kicad (6.0.11)

Rev: V1.0  
Id: 2/6



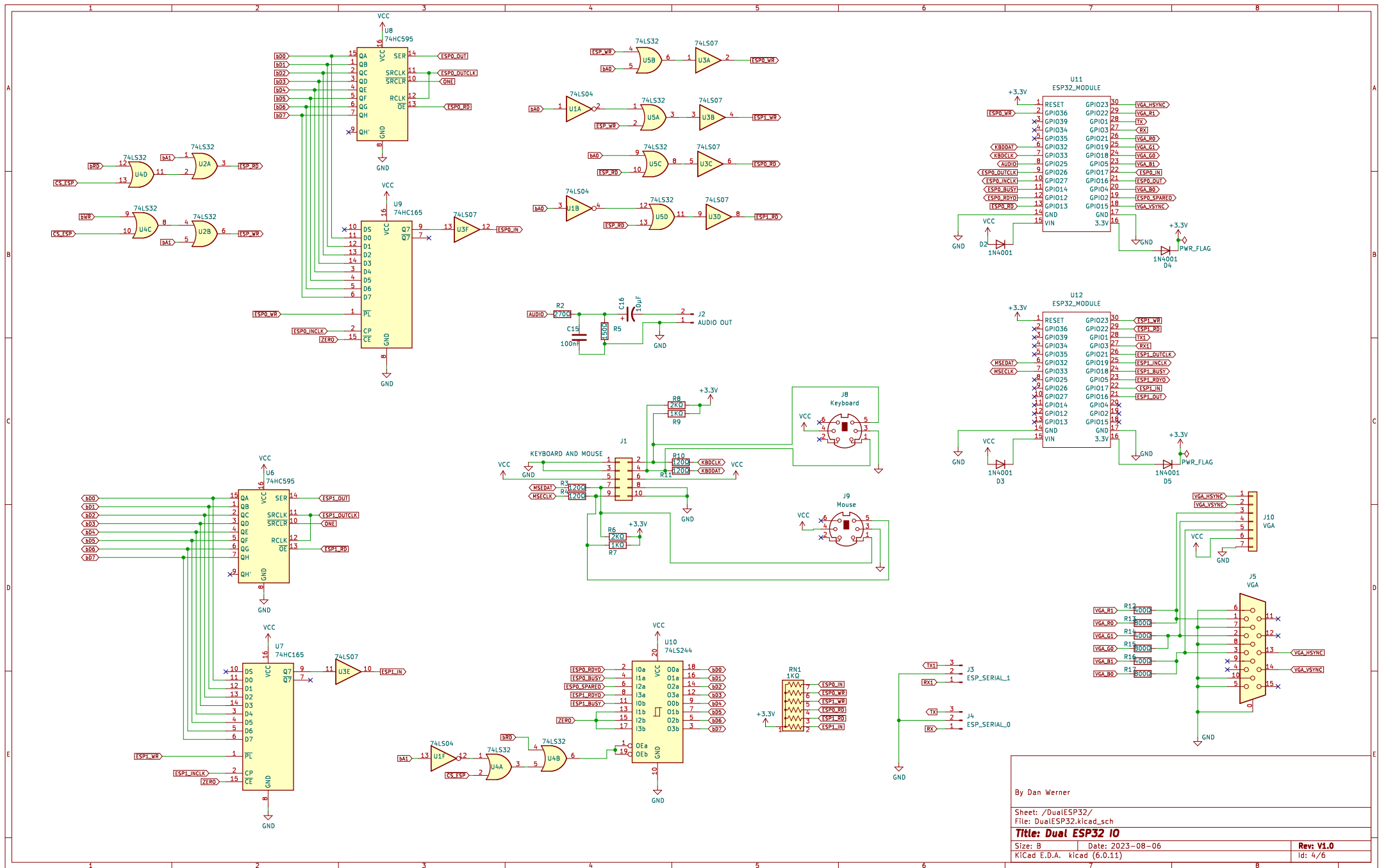
By Dan Werner

Sheet: /power/  
File: power.kicad\_sch

**Title: Dual ESP32 IO**

Size: B | Date: 2023-08-06  
KiCad E.D.A. kicad (6.0.11)

Rev: V1.0  
Id: 3/6



By Dan Werner

Sheet: /DualESP32/  
File: DualESP32.kicad\_sch

**Title: Dual ESP32 IO**

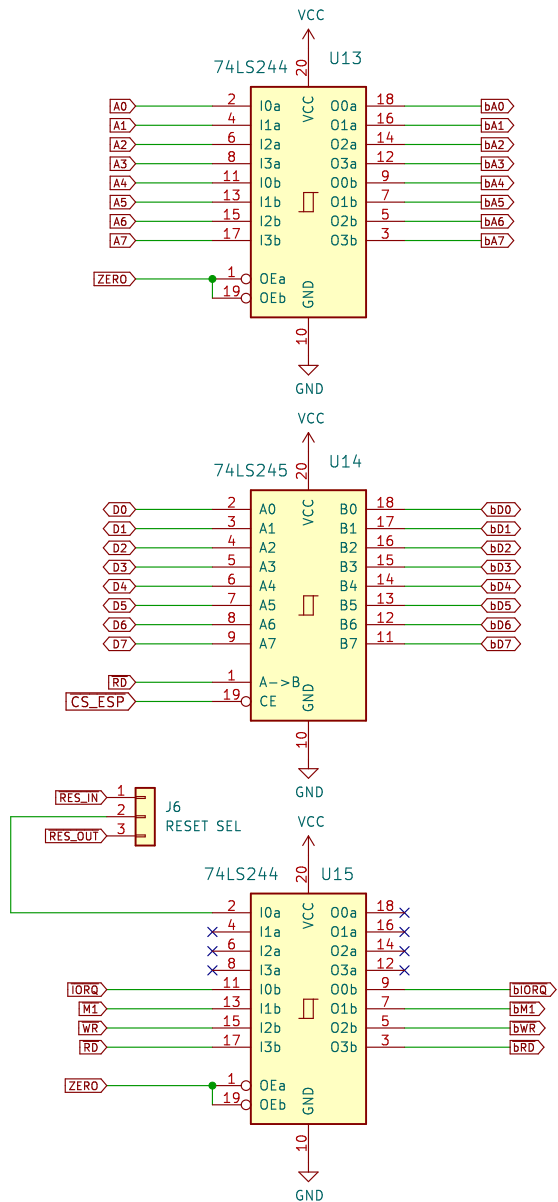
Size: B Date: 2023-08-06

KiCad E.D.A. kicad (6.0.11)

Rev: V1.0

Id: 4/6

# Z80 BUS INTERFACE



Note: Buffers and Transceivers respond to IO and MEM cycles

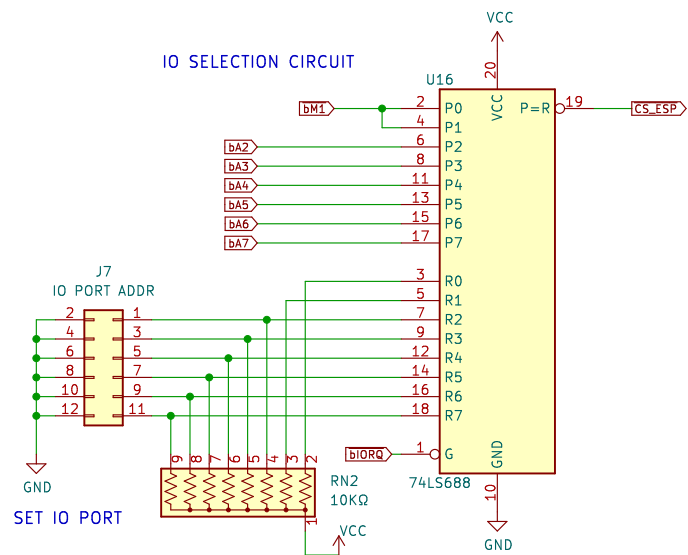
By Dan Werner

Sheet: /buffers/  
File: buffers.kicad\_sch

**Title: Dual ESP32 IO**

Size: A4 Date: 2023-08-06  
KiCad E.D.A. kicad (6.0.11)

**Rev: V1.0**  
Id: 5/6



Note: Inhibit Board Operation  
During Interrupts  
M1 = low, IORQ = low

Note: IO Address Port \$9C-\$9F  
1=off - A2 (high)  
3=off - A3 (high)  
5=off - A4 (high)  
7=on - A5 (low)  
9=on - A6 (low)  
11=off - A7 (high)

By Dan Werner

Sheet: /IOSelection/  
File: IOSelection.kicad\_sch

**Title: Dual ESP32 IO**

Size: A4 Date: 2023-08-06

KiCad E.D.A. kicad (6.0.11)

**Rev: V1.0**

Id: 6/6