

12345678

A

bus

power

buffers

fpanel

B

File: bus.kicad\_sch  
80C188CPU

File: power.kicad\_sch  
Memory

File: buffers.kicad\_sch  
bus sharing

File: fpanel.kicad\_sch  
mapper

C

D

E

12345678

RetroBrew Computer Group  
Based on a design by John R Coffman.

Sheet: /  
File: processor.80C188.kicad\_sch

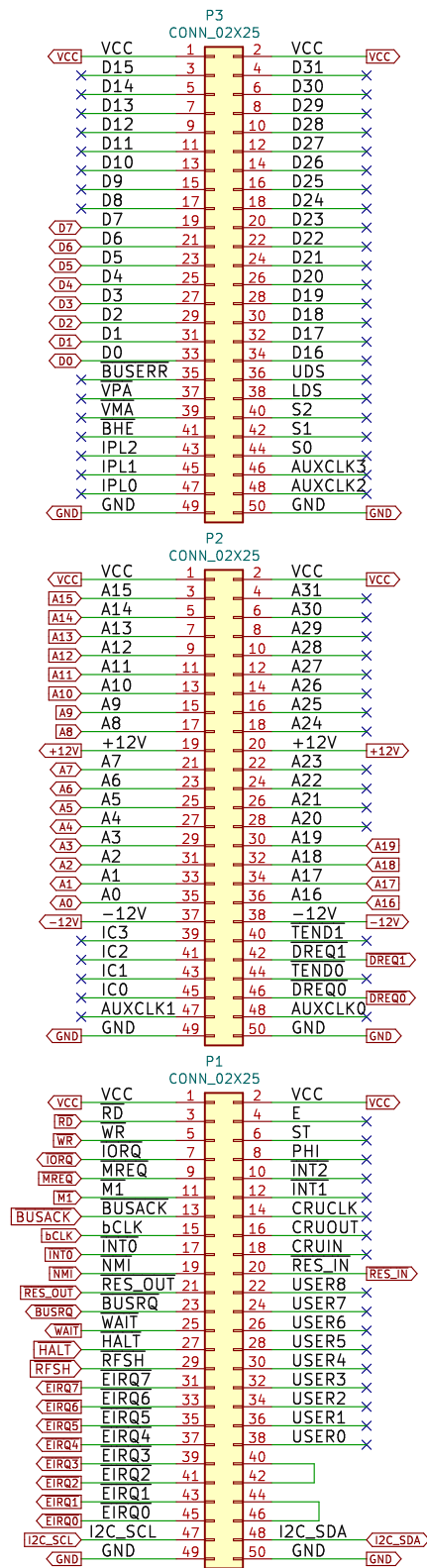
Title: Duodyne 80C188 CPU board

Size: BDate: 2024-07-05

KiCad E.D.A. kicad (6.0.11)

Rev: V0.5

Id: 1/9

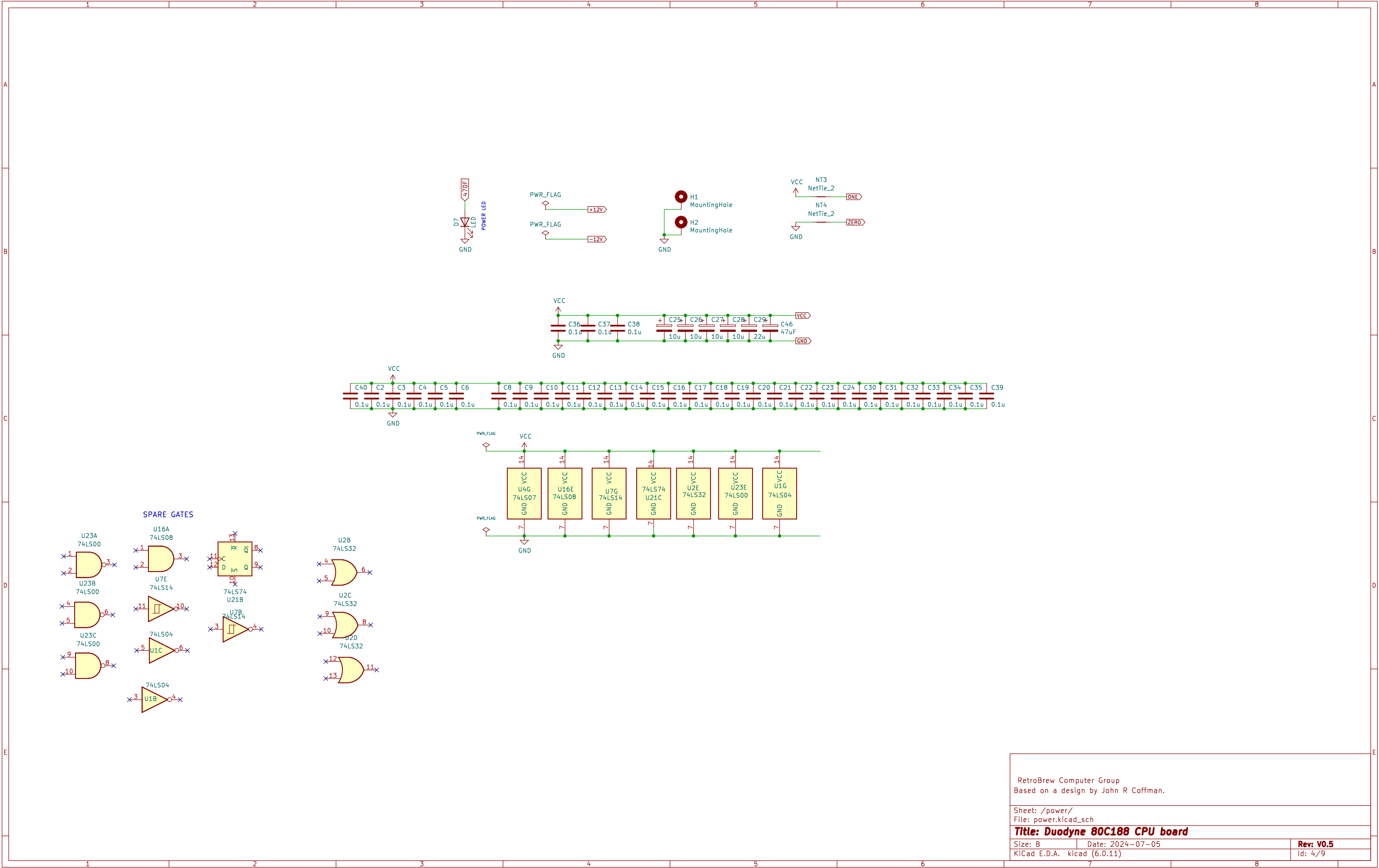


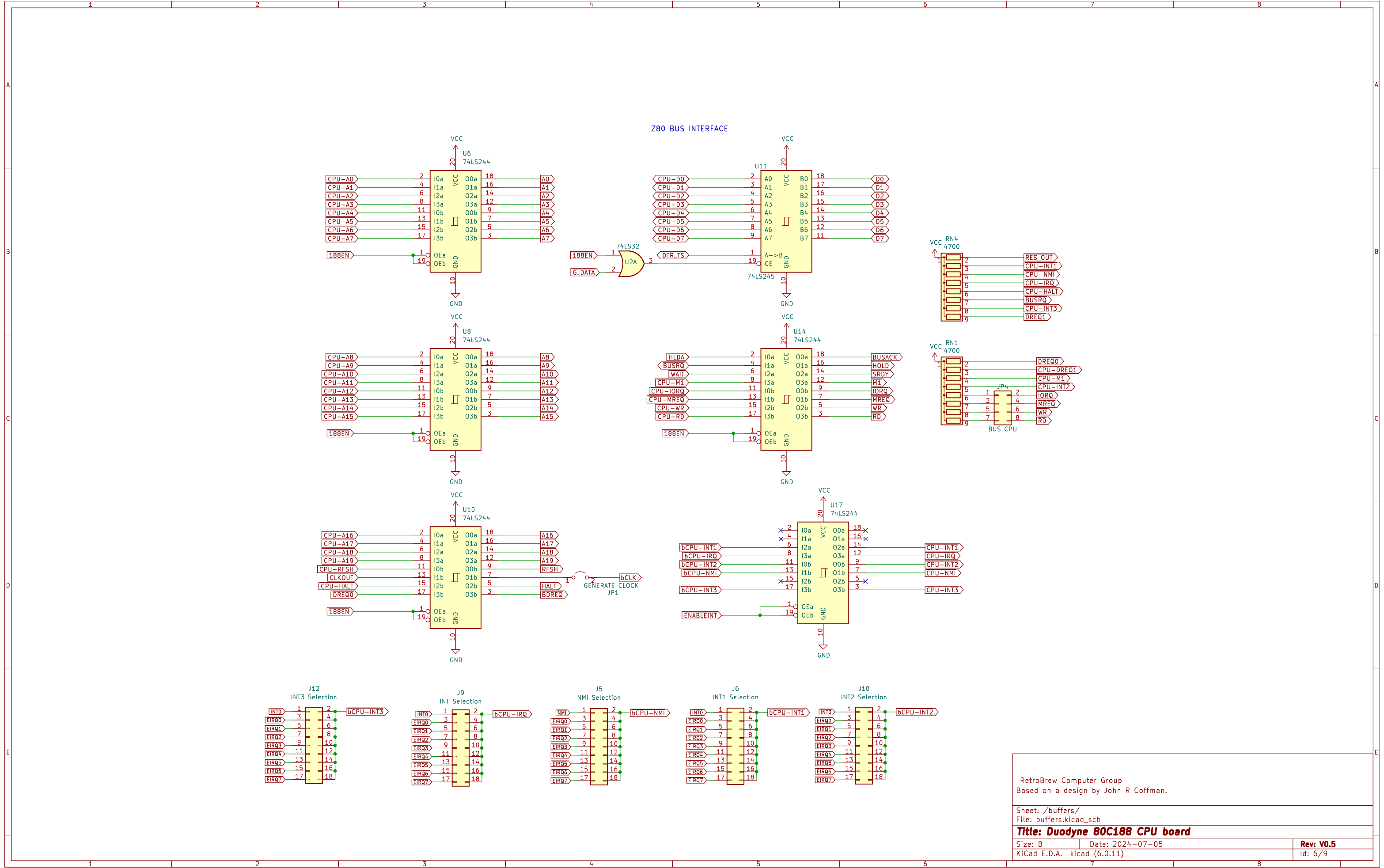
RetroBrew Computer Group  
Based on a design by John R Coffman.

Sheet: /bus/  
File: bus.kicad\_sch

**Title: Duodyne 80C188 CPU board**

|                             |                  |           |
|-----------------------------|------------------|-----------|
| Size: B                     | Date: 2024-07-05 | Rev: V0.5 |
| KiCad E.D.A. kicad (6.0.11) |                  | Id: 2/9   |





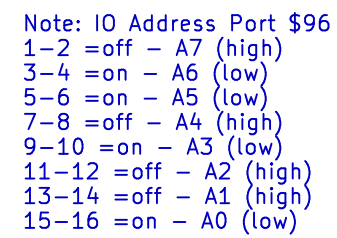
RetroBrew Computer Group  
Based on a design by John R Coffman.

Sheet: /buffers/  
File: buffers.kicad\_sch

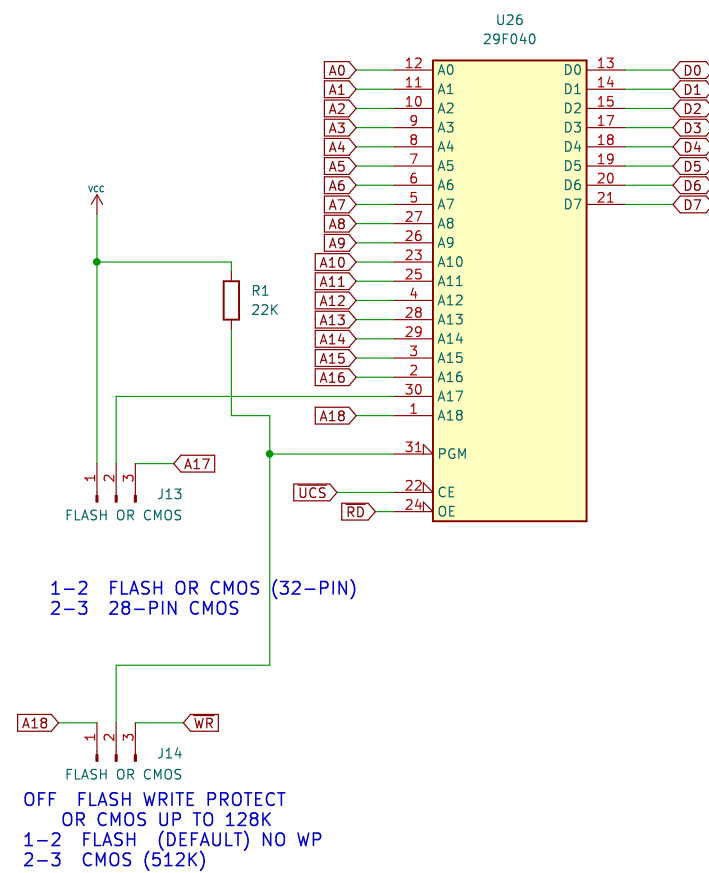
**Title: Duodyne 80C188 CPU board**

|                             |                  |           |
|-----------------------------|------------------|-----------|
| Size: B                     | Date: 2024-07-05 | Rev: V0.5 |
| KiCad E.D.A. kicad (6.0.11) |                  | Id: 6/9   |









## ROM

FLASH 39SF010 (128K)  
ALTERNATES: 39SF020, 29F020, 29F010  
39SF040, 29F040 (512K) NOT RECOMMENDED

CMOS 27C010 (128K, 32pin)  
ALTERNATE 27C020  
28pin: 27C512(64K), 27C256(32K)

DEFAULT CHIP: 39SF010 (128K FLASH)  
DEFAULT JUMPERS: 1-2, 1-2 (BOTH)

29F040 SHOWS ALL PIN ASSIGNMENTS



