

# **Jacobs University Bremen**

# **Digital Signal Processing and**

# **Communications Lab**

**300231 – EEDSPL**

**Spring semester 2013**

Instructor: Dr. Fangning Hu

# SCHEDULE – WHEN AND WHERE

- Tuesday's from 14:15 - 18:30
- Research I, EE Teaching Lab (Rm. 71 and 72)
- Please try to be on time!

# LECTURE PLAN

- Lab #1: Simulink TX
- Lab #2: Simulink RX
- Lab #3: Block Simulation in MATLAB
- Lab #4: DSP Block Design
- Lab #5: PLL Simulation in MATLAB
- Lab #6: DSP PLL Design
- Lab #7: DDC Simulation in MATLAB
- Lab #8: STR and Receiver in MATLAB

# GRADING

- At the end of the course, you will be assigned a grade based on the following distribution:
- Labs - 20%
- Attendance - 20%
- Final Exam - 60%
- Each lab is graded 50% for the pass-off and 50% for the write-up

# RESOURCES

- Main website of the course (serves as lab manual):
- <http://www.xwallace.com/courses/dsp>
- Instructor: Dr. Fangning Hu
- TA's:
  - Ahsan, Javed, email: [a.javed@jacobs-university.de](mailto:a.javed@jacobs-university.de);
  - Catalin Andrei Voinea, email: [c.voinea@jacobs-university.de](mailto:c.voinea@jacobs-university.de);