■ Disassembly 🛞	Optimizer Assi	Memory Brow	🚝 Memory	Alloc 🛭 🗀	
Project 'FLC_Express'				* E	\triangle
DSPL2ROM				1,049k	
DSPL2RAM				262k	
DSPL1PRAM				32,768	
DSPL1DRAM				32,768	
SHDSPL2ROM				1,049k	
SHDSPL2RAM				262k	
SHDSPL1PRAM				32,768	
SHDSPL1DRAM				32,768	
EMIFACS0				537M	
EMIFACS2				33,554k	
EMIFACS3				33,554k	
EMIFACS4				33,554k	
EMIFACS5				33,554k	
		80,888 (6	51%)	131k	
.data					
⇒ .text		39,328			
⇒ .far	Ja 200	34,192			
> .const	2,200				
⊳ .stack	2,048				
> .sysmem	2,048				
> .cinit	756				
⊳ .bss	4				
⇒ .cio	288				
> .switch	24			E271.4	
DDR2				537M	