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## Summary

Industry experience in microprocessor design, held roles in ASIC physical design and SoC architecture design. Interests in developing software/hardware systems to improve our capabilities, productivity, and resource allocation. Fields of interest include software development, FPGA prototyping, and efficient machine learning.

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## Selected Publications/Awards

Architectural Analysis of Deep Learning on Edge Devices. 2019, in pre-print.  
Physical Design of a 3D-stacked Heterogeneous Multi-core Processor. IEEE 3D-IC 2016.  
[Rationale for a 3D Heterogeneous Multi-core Processor](#). IEEE ICCD 2013.  
Ranked 34<sup>th</sup> in USA, [IEEEExtreme 24-hour Programming Competition, 2014](#). Team of 2.  
Best FPGA Implementation at [International LSI Design Contest, Japan 2009](#). Xilinx Award. Team of 3.

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## Experience

### Intel

AUSTIN, TEXAS

#### SoC Performance Architect

2017-

Developed and enhanced tools/simulator infrastructure to validate and analyze performance of device/server SoCs. Conducted power & performance analysis of server SoCs. Delivered and accelerated workload projections for internal/external customers.

#### Design Automation Engineer

2016

Developed and enabled place & route, timing flows for a CPU design and met scheduled tape-in.

### Qualcomm Research

SAN DIEGO, CALIFORNIA

#### Research Intern

Summer 2013

Performed mixed-signal circuit design verification and FPGA prototyping.

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## Education

### North Carolina State University

RALEIGH, NORTH CAROLINA

#### Ph.D. in Computer Engineering

2016

Dissertation: Three-Dimensional Integration of Heterogeneous Multi-Core Processors.  
Developed RTL-GDS2 flow for academic 3D-IC processor tape-outs with IBM/GlobalFoundries/Ziptronix.  
Teaching assistant for graduate-level digital design and computer architecture courses.

Software Engineering

Advanced Microarchitecture

ASIC Design

VLSI Systems Design

Computer Networks

Parallel Computer Architecture

ASIC Verification

VLSI System Testing (Duke Univ.)

Memory Systems

Computer Design &amp; Technology

Integrated Circuits Technology &amp;

Electronic System Level &amp; Physical

Embedded Systems Design

Digital Electronics

Fabrication

Design

### Institut Teknologi Bandung

INDONESIA

#### B.S. in Electrical Engineering

2009

Thesis: Implemented various machine learning algorithms on a multi-core microcontroller.

### Oita University

JAPAN

#### Exchange Student, Research & Coursework

2007-2008

Project: Implemented face tracking on a panning camera using neural networks.

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## Skills

### C++, Python

PyTorch, Tensorflow Lite

SystemC, Tcl, Clojure

Platform Architect, Simics

### Python Data Science/ML

Pandas, Jupyter

Scikit-learn

Dash, Streamlit, d3.js

### Verilog, SystemVerilog

QuestaSim, VCS

HTML+CSS, Javascript

NodeJS, SQLite

### Design Compiler

PrimeTime, IC Compiler

[Online Courses](#)

Startup Engineering

Distributed ML with Spark