|                   | Microprocessor (KCS403)  | *                               |
|-------------------|--|---------------------------------|
|                   | Course Outcome (CO)  Bloom's Knowledge Leve  | l (KL)                          |
|                   | At the end of course, the student will be able to understand   |                                 |
| CO 1              | A dead by the second of Edital Gorden parties to Migroprocessor based personal computer  | K <sub>3</sub> , K <sub>4</sub> |
| CO 2              | system.  | K <sub>2</sub> ,K <sub>4</sub>  |
| CO 3              | 1 (2005/2007) 'A C and with Microprocessor   | K <sub>3</sub>                  |
| CO 4              | (ONOT IONOT)   | K <sub>4</sub>                  |
| CO 5              | Evaluate the data transfer information through serial & parallel ports.  | K <sub>5</sub>                  |
| DETAILED SYLLABUS |  | 3-1-0                           |
| Unit              | Topic  | Proposed<br>Lecture             |
| I '               | Microprocessor evolution and types, microprocessor architecture and operation of its components, addressing modes, interrupts, data transfer schemes, instruction and data flow, timer and timing diagram, Interfacing devices.  | 08                              |
| П                 | Pin diagram and internal architecture of 8085 microprocessor, registers, ALU, Control & status, interrupt and machine cycle. Instruction sets. Addressing modes. Instruction formats Instruction Classification: data transfer, arithmetic operations, logical operations, branching operations, machine control and assembler directives. | 08                              |
| Ш                 | Architecture of 8086 microprocessor: register organization, bus interface unit, execution unit, memory addressing, and memory segmentation. Operating modes. Instruction sets, instruction   |                                 |
| IV                | Assembly language programming based on intel 8085/8086. Instructions, data transfer, arithmetic, logic, branch operations, looping, counting, indexing, programming techniques, counters and time  | 08                              |
| V                 | Peripheral Devices: 8237 DMA Controller, 8255 programmable peripheral interface, 8253/8254programmable timer/counter, 8259 programmable interrupt controller, 8251 USART and RS232C.   | 08                              |

## Text books:

- Gaonkar, Ramesh S, "Microprocessor Architecture, Programming and Applications with
- 8085", Penram International Publishing.
- Ray A K, Bhurchandi K M, "Advanced Microprocessors and Peripherals", TMH 3.
- 4. Hall D V,"Microprocessor Interfacing', TMH
- 5. Liu and, "Introduction to Microprocessor", TMH
- Brey, Barry B, "INTEL Microprocessors", PHI
- 7. Renu Sigh & B.P. Gibson G A, "Microcomputer System: The 8086/8088 family", PHI
- Aditya P Mathur Sigh, "Microprocessor, Interfacing and Applications M Rafiqzzaman, "Microprocessors, Theory and Applications
- J.L. Antonakos, An Introduction to the Intel Family of Microprocessors, Pearson, 1999