LALITHA SREE CHINTAPALLI

+91-9848315411| lalitchintapalli479@gmail.com | Visakhapatnam, India 530001

OBJECTIVE

Electronics and Communication Engineering student with hands-on experience in AI&ML VLSI Design and Embedded Systems. Proven expertise in FPGA implementation and IoT development. Seeking internships and full-time opportunities to apply technical skills in cutting-edge technology projects. Successfully improved product reliability and reduced production costs in previous roles through effective design modifications and troubleshooting techniques.

SKILLS

Programming Languages: Python, C, Verilog, HTML/CSS.
Hardware & Tools: PCB Design, Arduino.
Soft Skills: Team Collaboration, Technical Communication, Problem-Solving, Time Management

INTERNSHIPS

AI/ML Intern |[Excelr]|[May 2024-June 2024]
Developed predictive models in Python using Scikit-learn & Pandas; improved accuracy by 15% in dataset analysis.

VLSI Intern |[Abhyasa Semicon Technologies] |[April 2025 – June 2025]
Designed and verified digital circuits using Verilog; implemented FPGA-based modules for real-time applications.

EDUCATION

B.Tech, Electronics & Communication Engineering – Vignan's Institute of Engineering for Women, Visakhapatnam (2022–Present)
Intermediate (MPC) -SRI VISWA Junior College, Visakhapatnam (2020–2022)
ICSE (10th Class) – Bethany School, Visakhapatnam (2020)

PROFILES

https://www.linkedin.com/in/lalitha-sree-chintapalli-b50127289/

CERTIFICATIONS

PCB Design Workshop | [Andhra Pradesh State Skill Development Corporation] | [2024]
Robotics Workshop | [TECK TEAM SOLUTIONS] | [2023]
C&MS Office |[GIT Institute Of Advanced Technologies] | [2022]

PROJECTS

Smart Blind Stick:
Developed an IoT-based smart walking stick that detects obstacles using ultrasonic sensors and alerts the user through vibration and buzzer signals, enhancing mobility and safety for visually impaired individuals.