六顆邏輯閘

學生:李俊逸

2024 Advanced Mixed-Operation System (AMOS) Lab.



Tamkang University

Department of Electrical and Computer Engineering

No.151, Yingzhuan Rd., Tamsui Dist., New Taipei City 25137, Taiwan (R.O.C.)



Inverter

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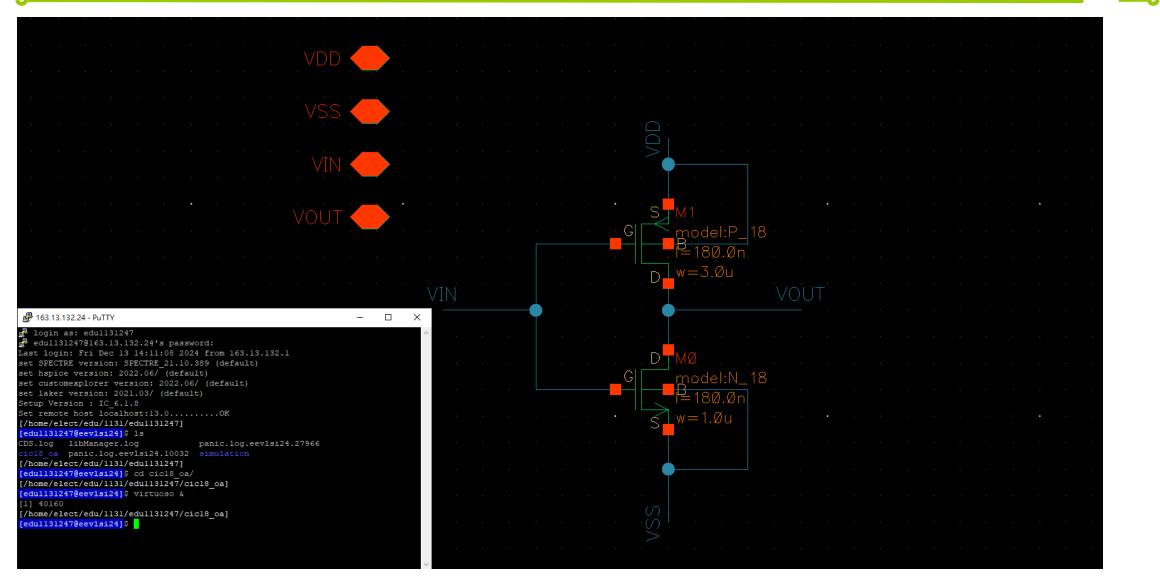
Tamkang University
Department of Electrical and Computer Engineering
No.151, Yingzhuan Rd., Tamsui Dist., New Taipei City 25137, Taiwan (R.O.C.)





Inverter Schematic

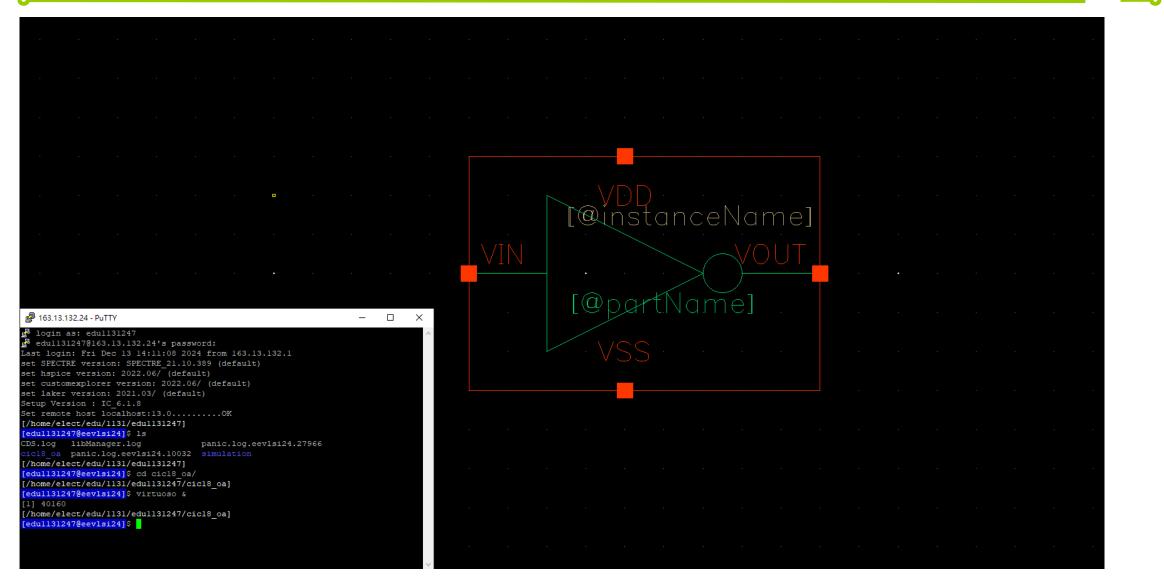






Symbol

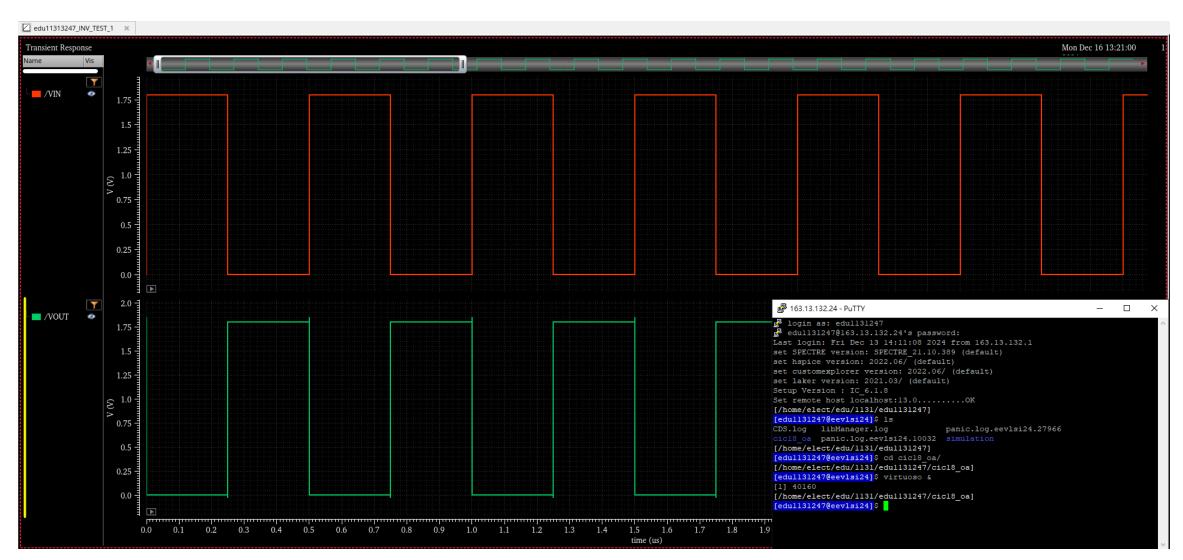






Pre-Sim

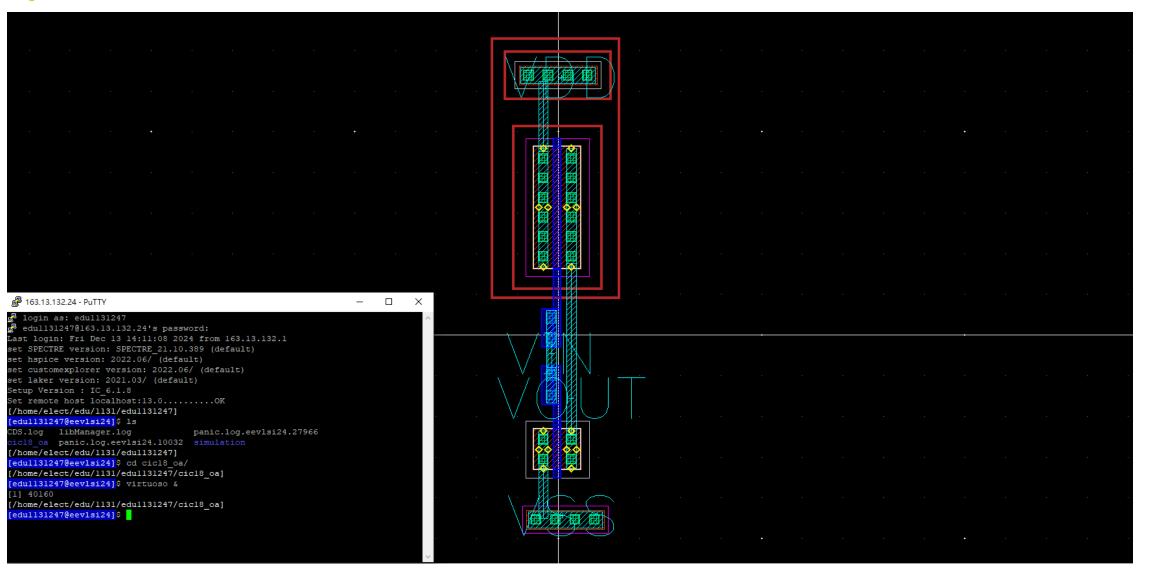






Inverter Layout

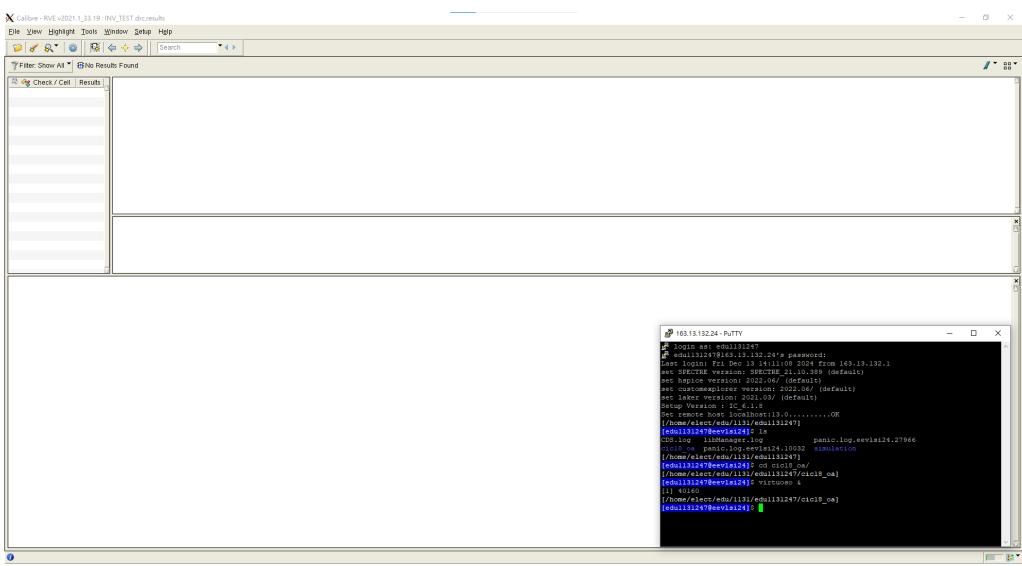






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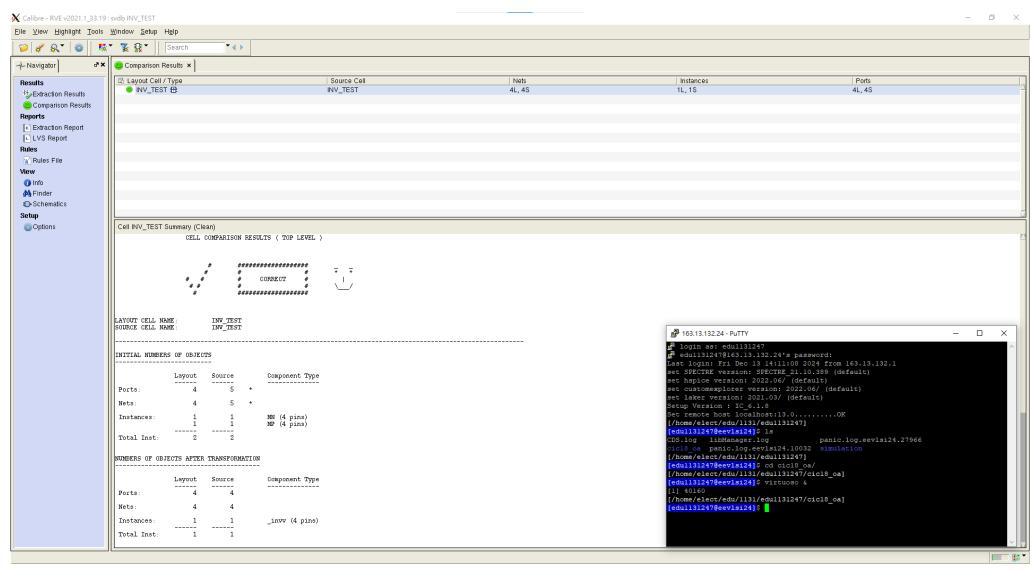






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NAND

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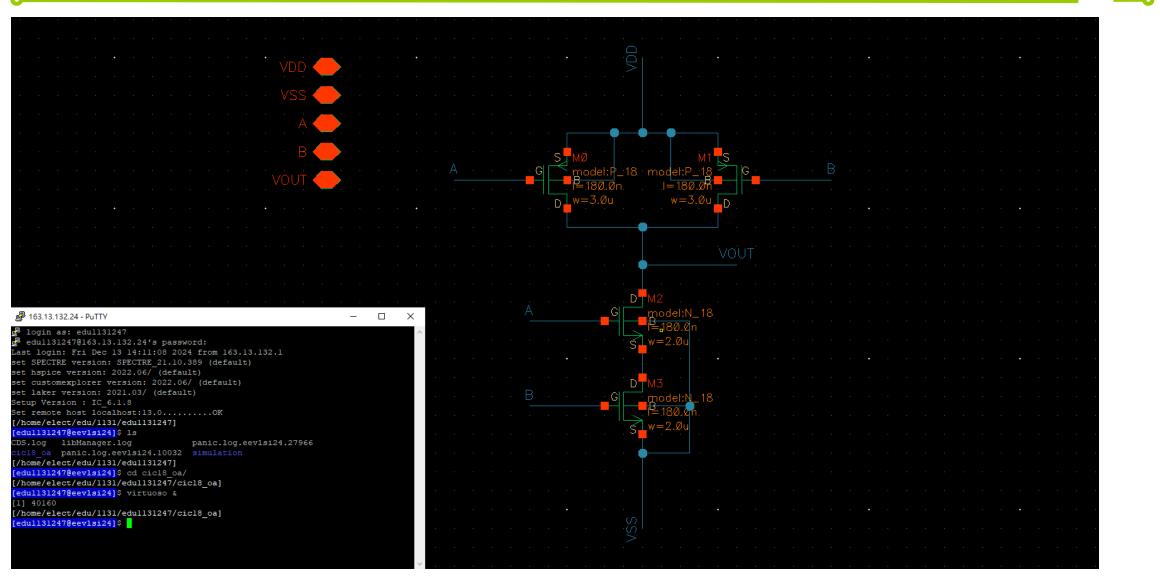
Tamkang University
Department of Electrical and Computer Engineering
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NAND Schematic

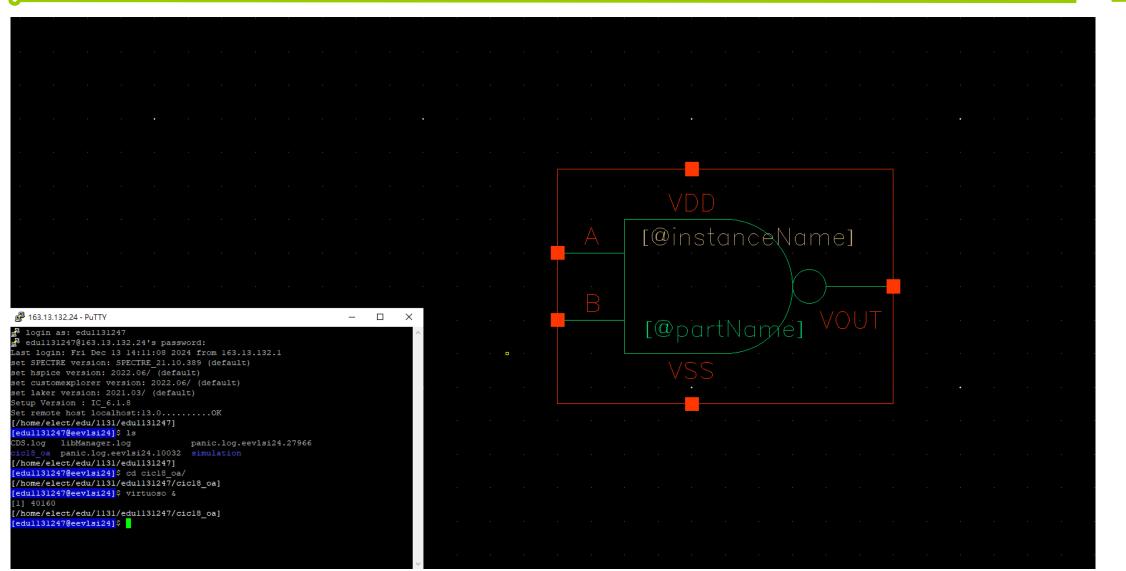






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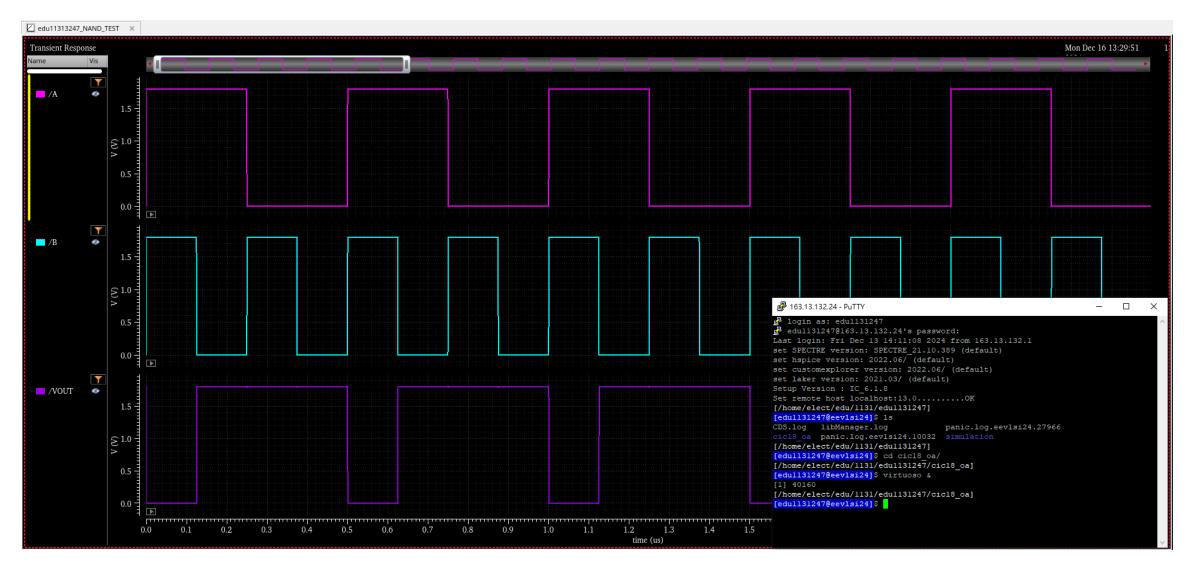






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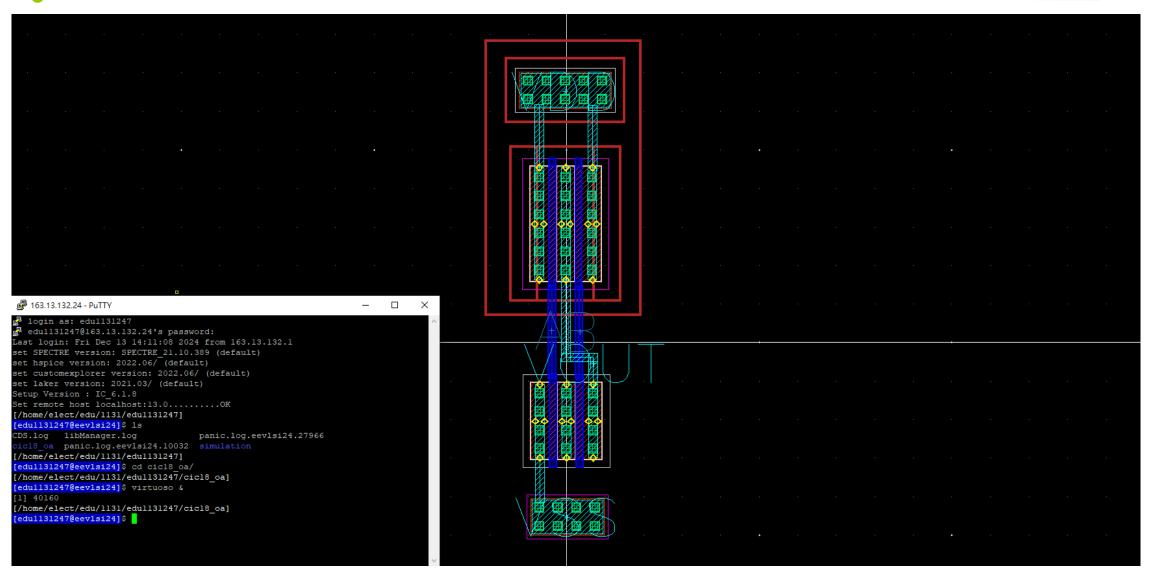






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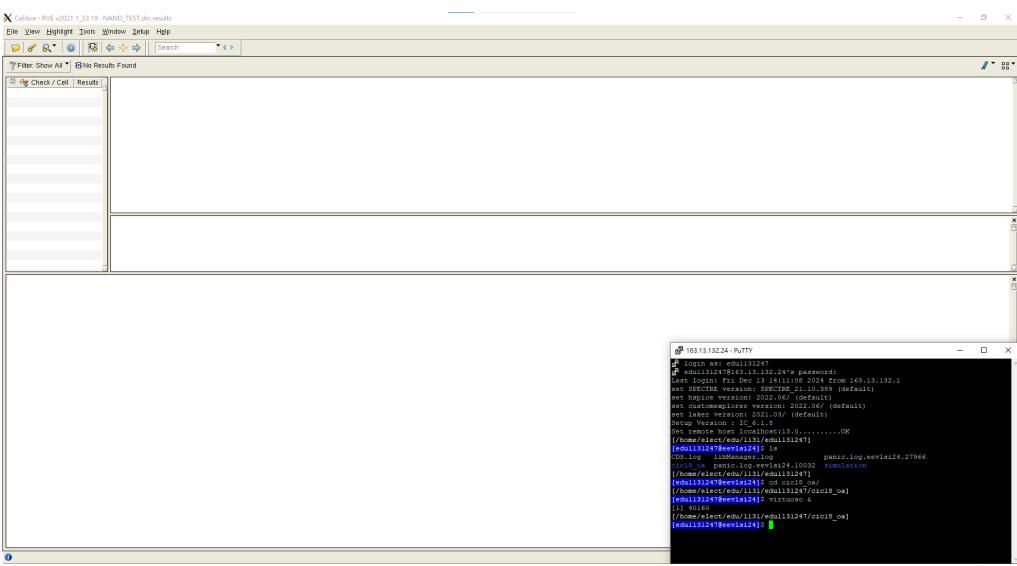






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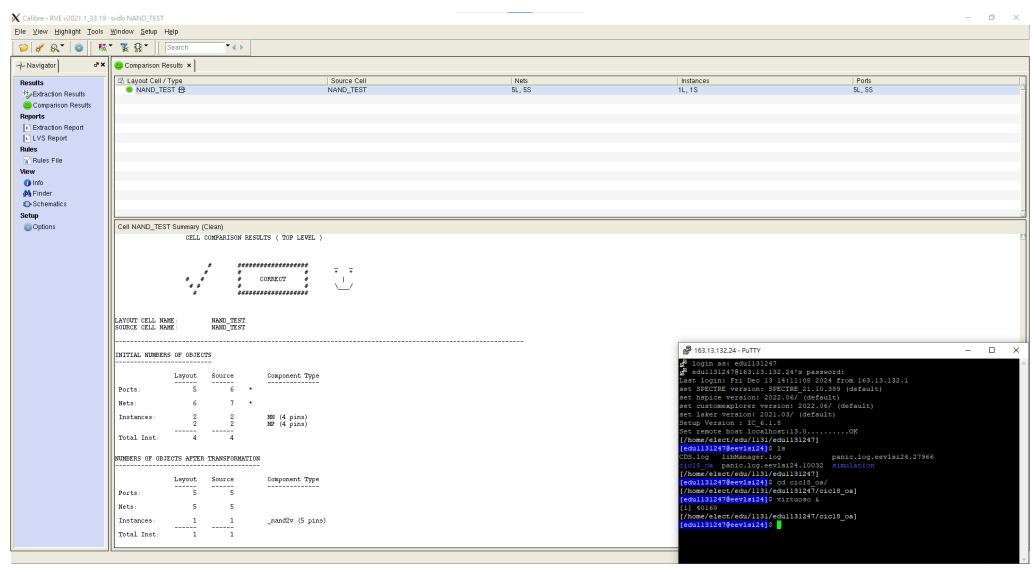






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NOR

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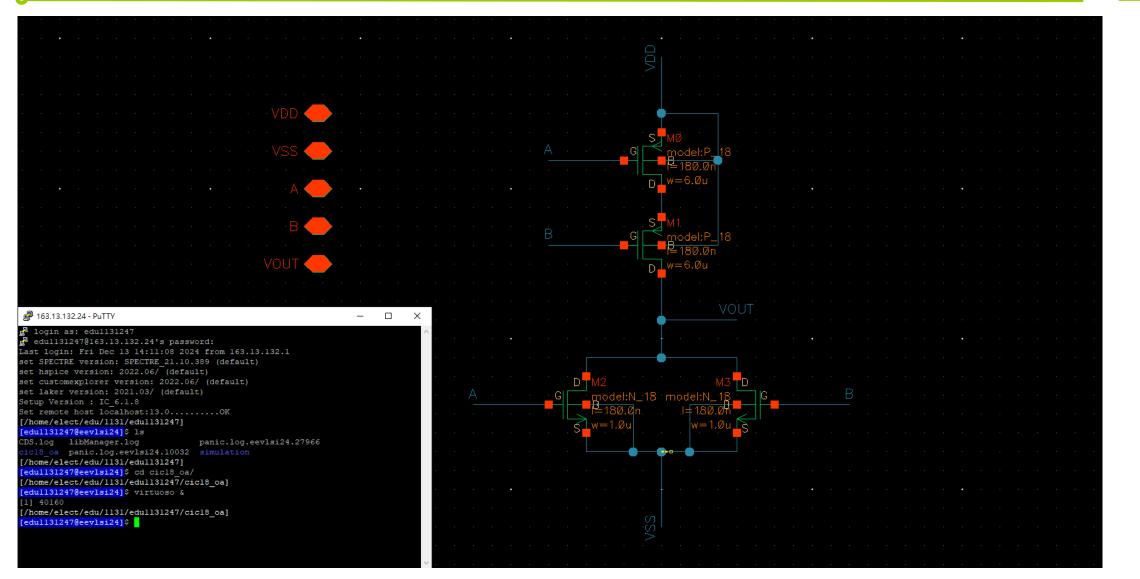
Tamkang University
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No.151, Yingzhuan Rd., Tamsui Dist., New Taipei City 25137, Taiwan (R.O.C.)





NOR Schematic

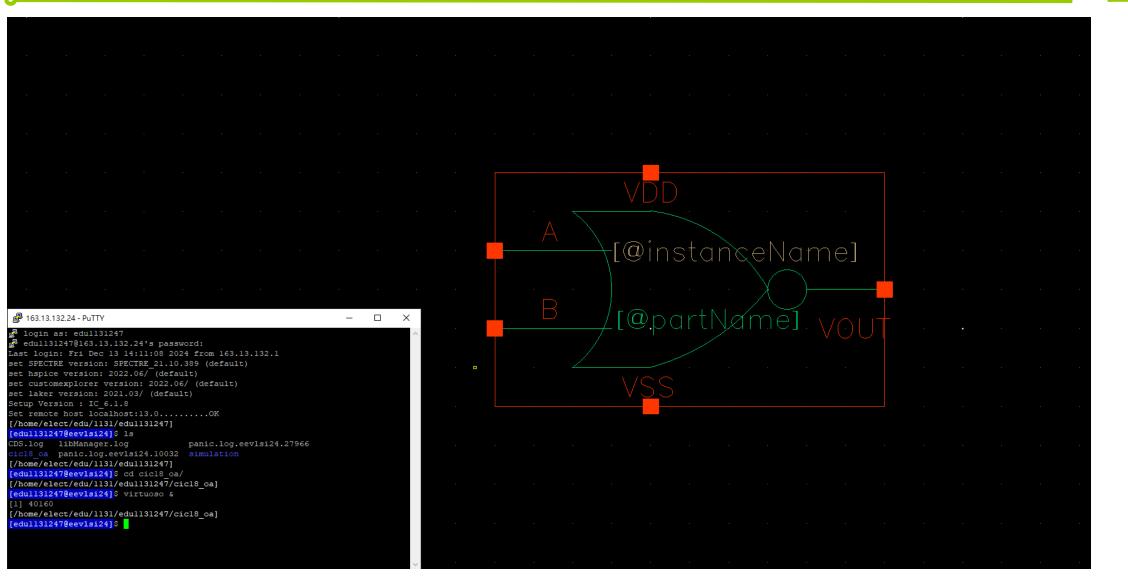






Symbol

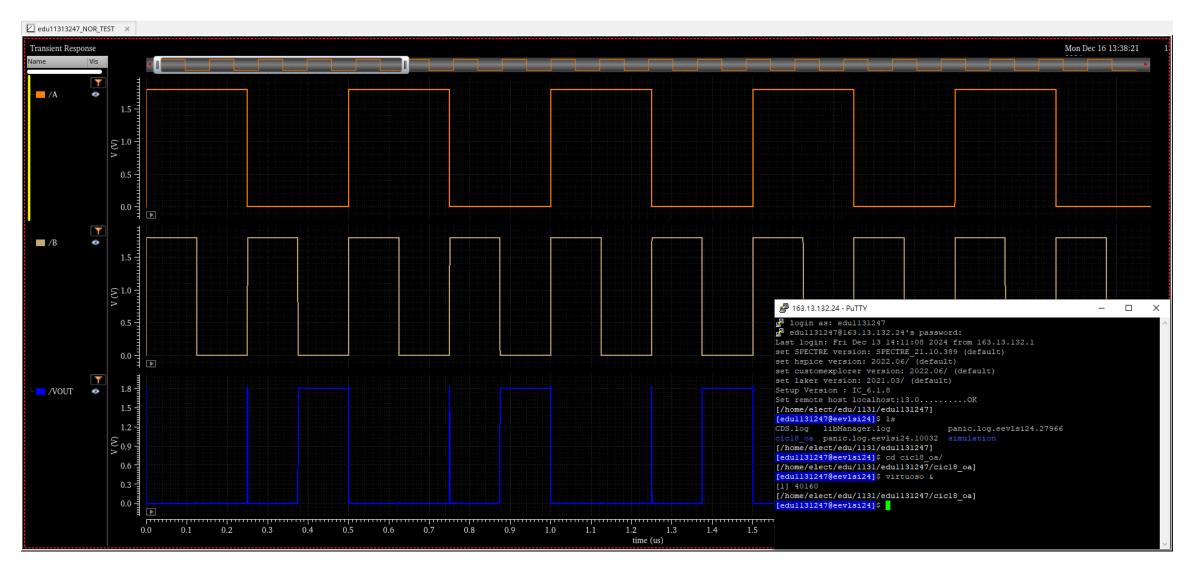






Pre-Sim

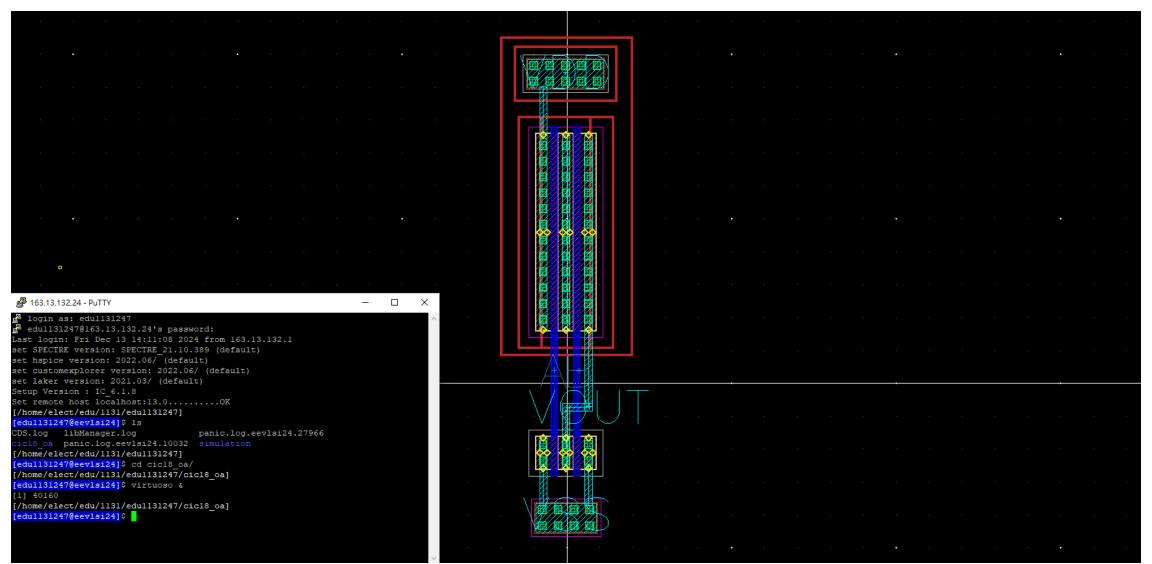






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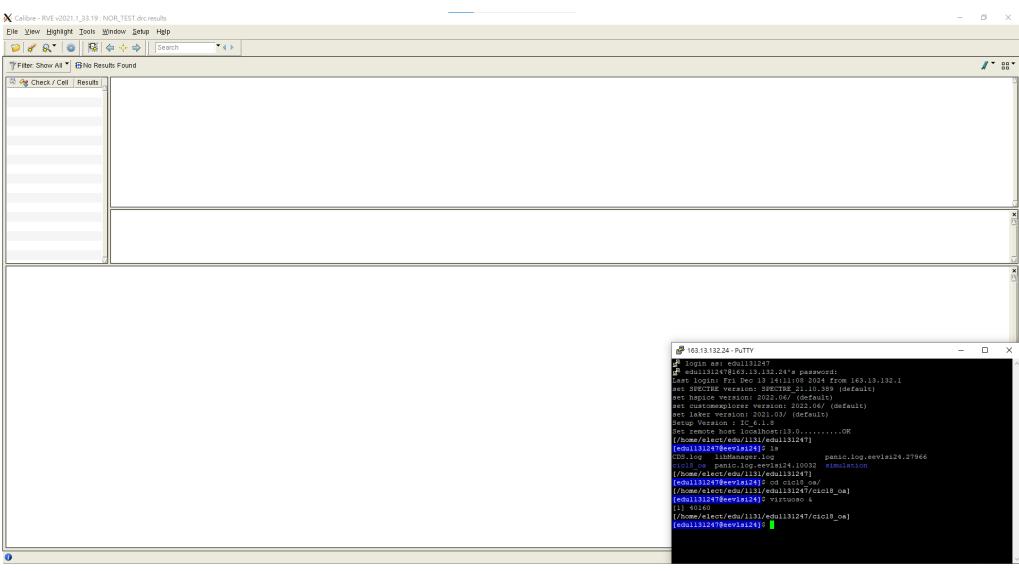






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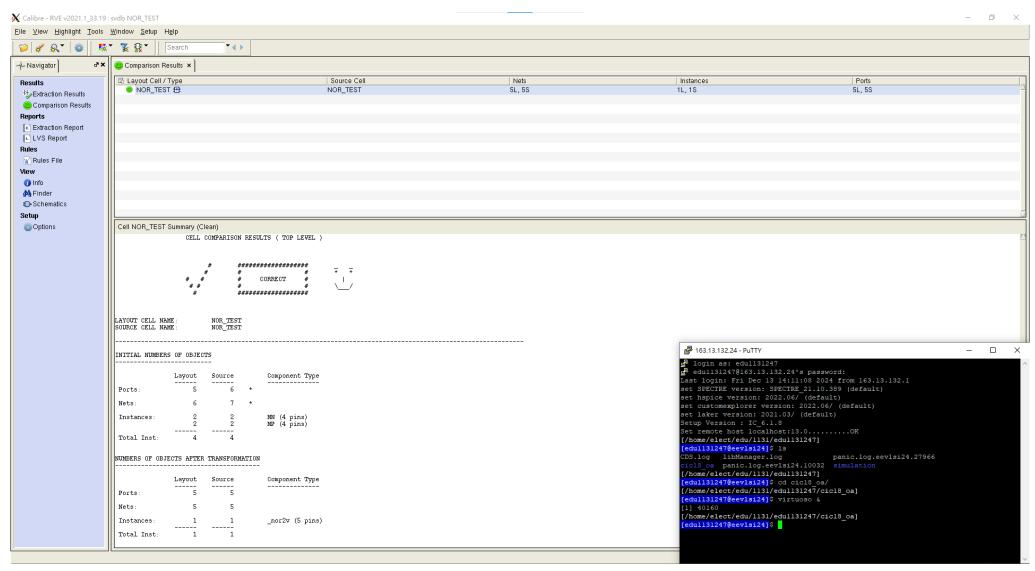






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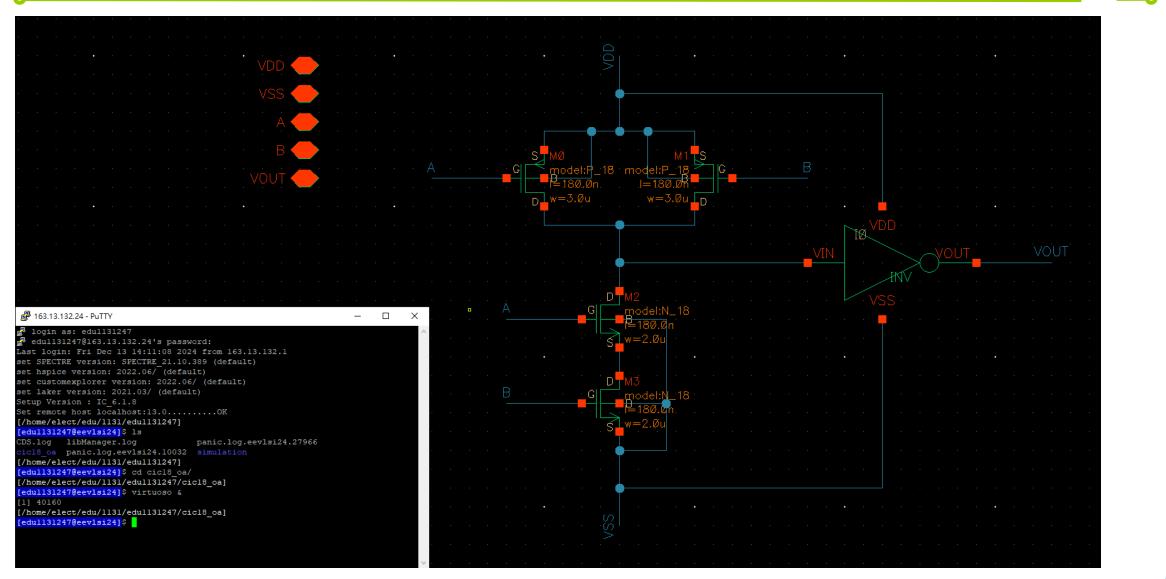
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AND Schematic

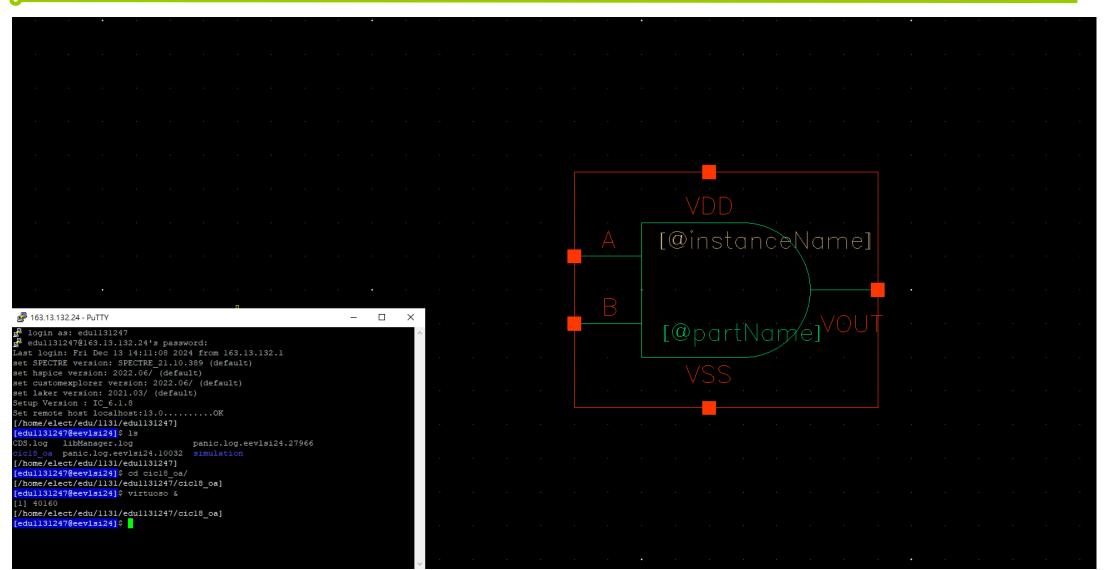






Symbol







Pre-Sim

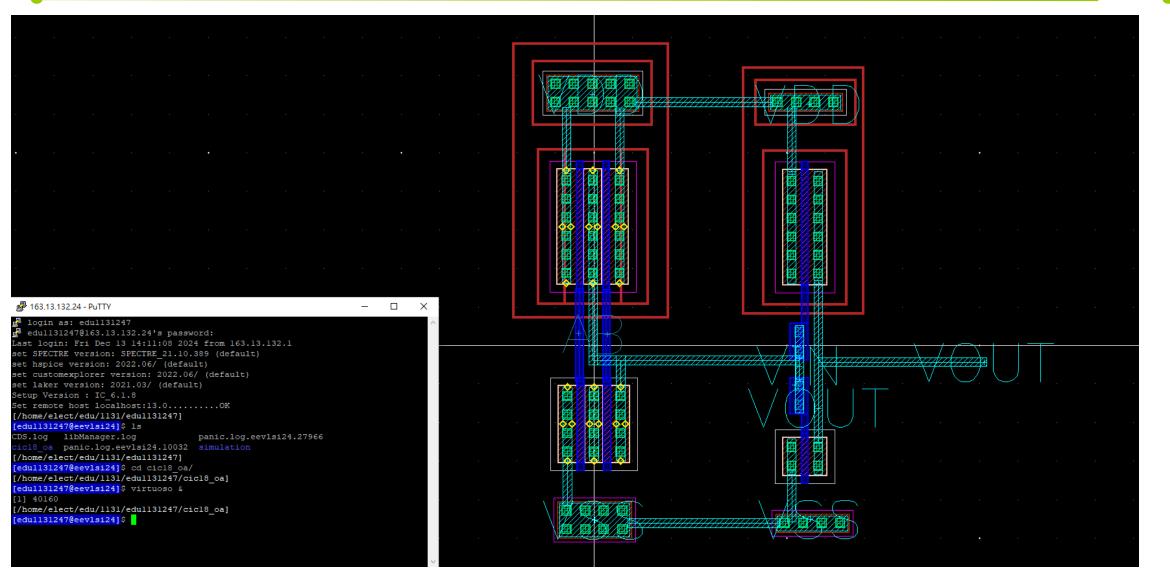






AND Layout

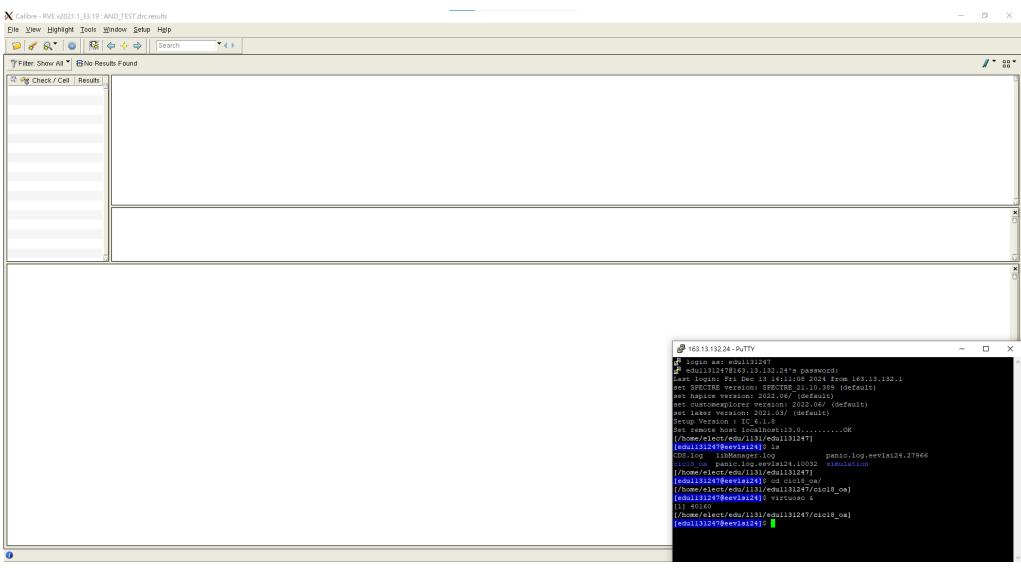






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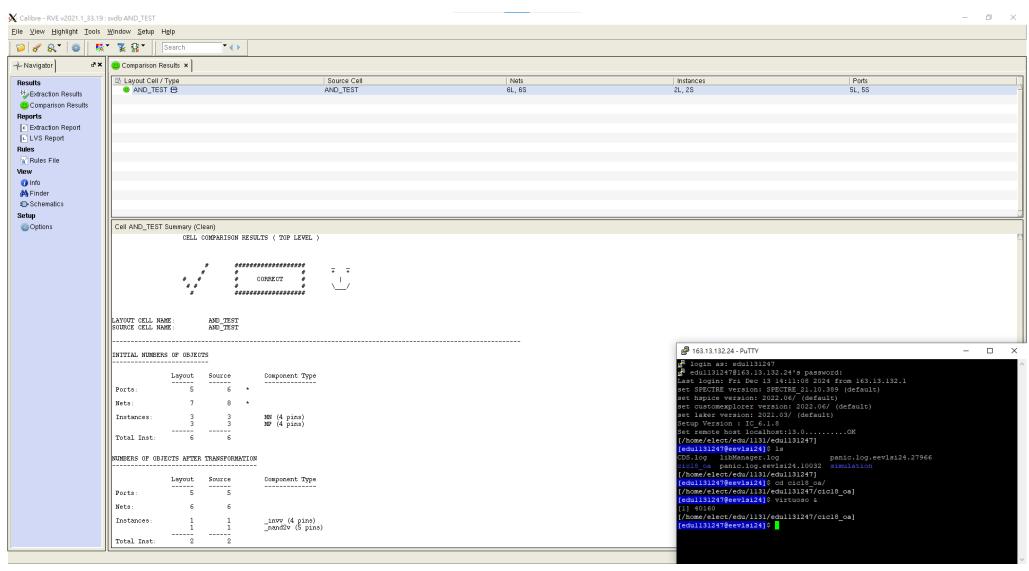






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OR

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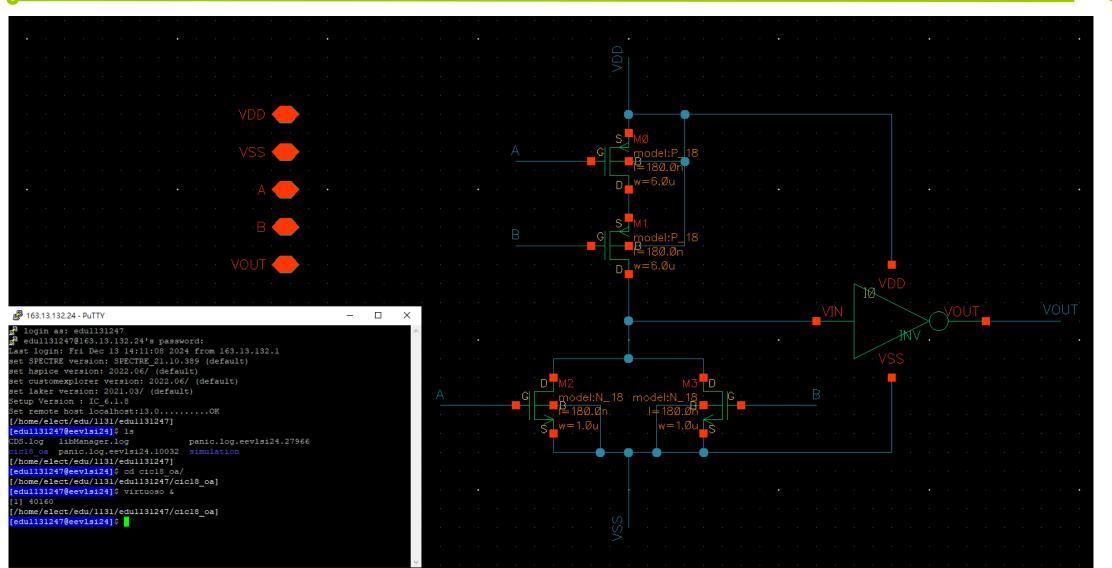
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OR Schematic

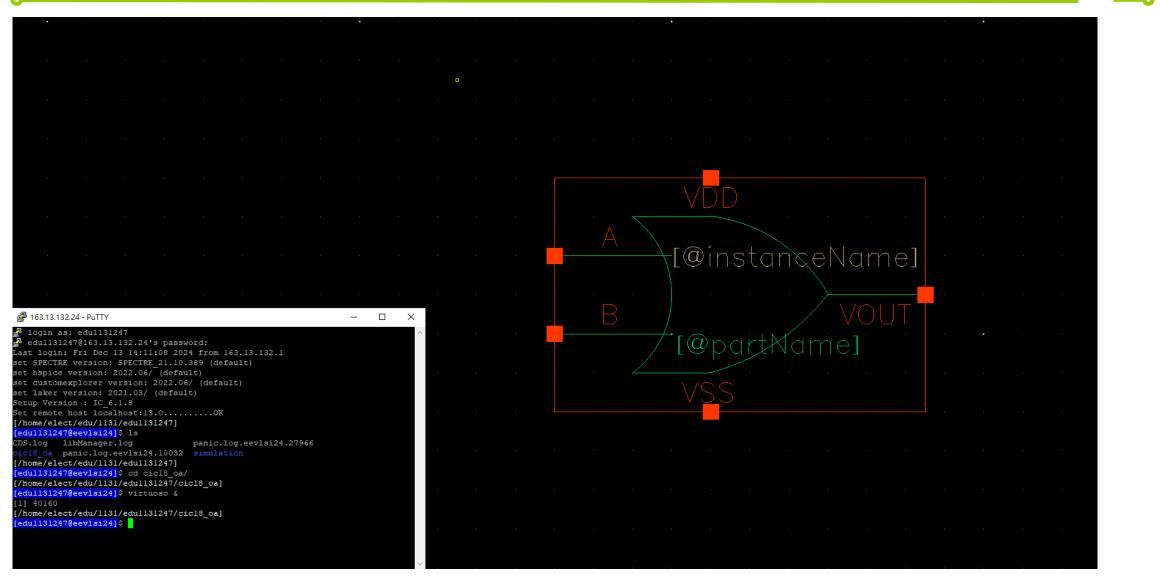






Symbol

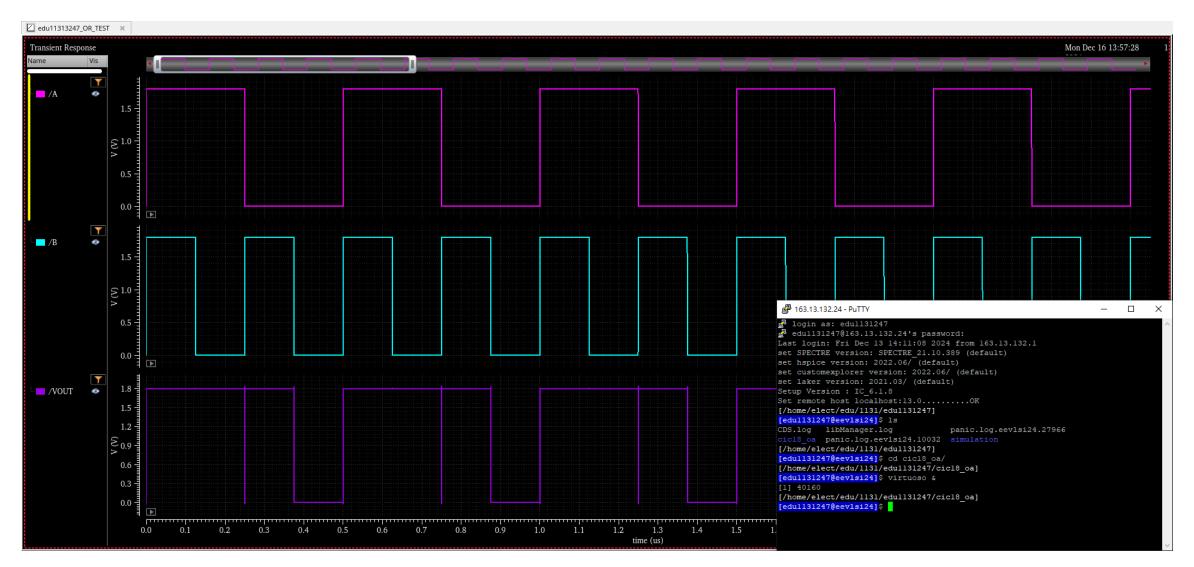






Pre-Sim

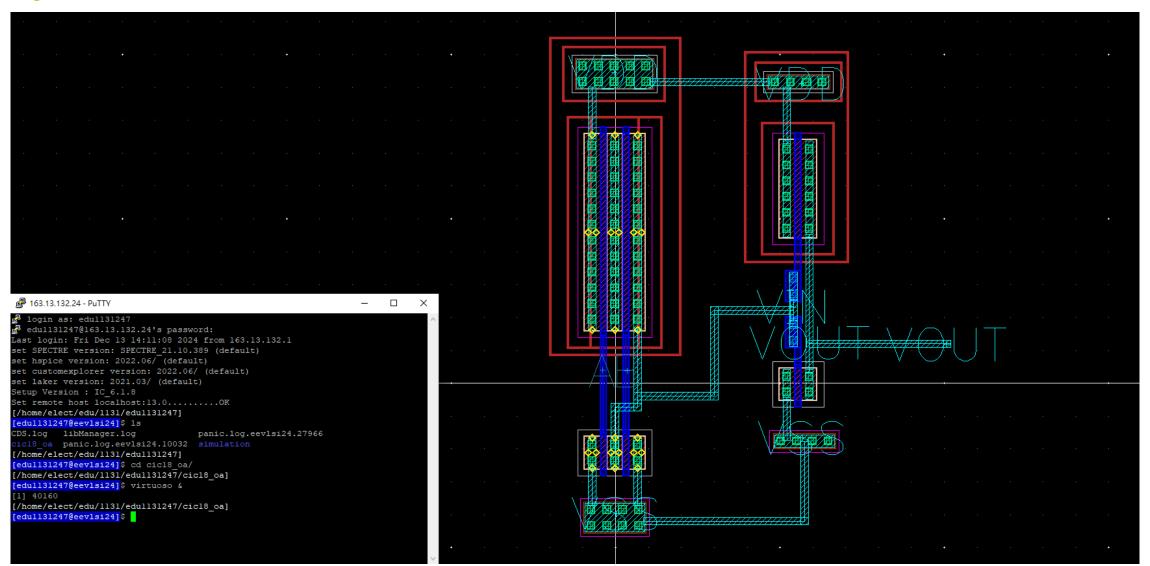






OR Layout

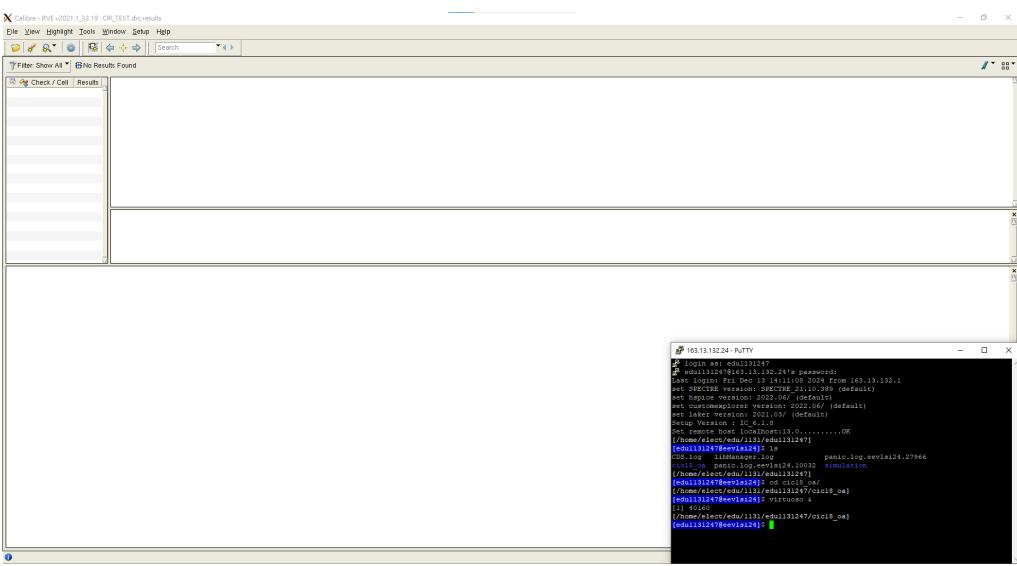






DRC

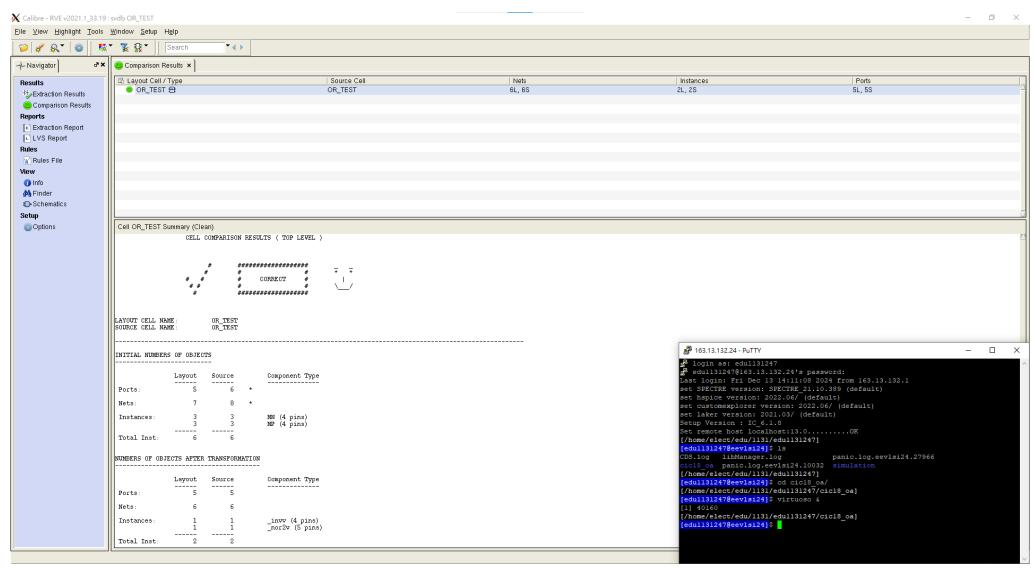






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XOR

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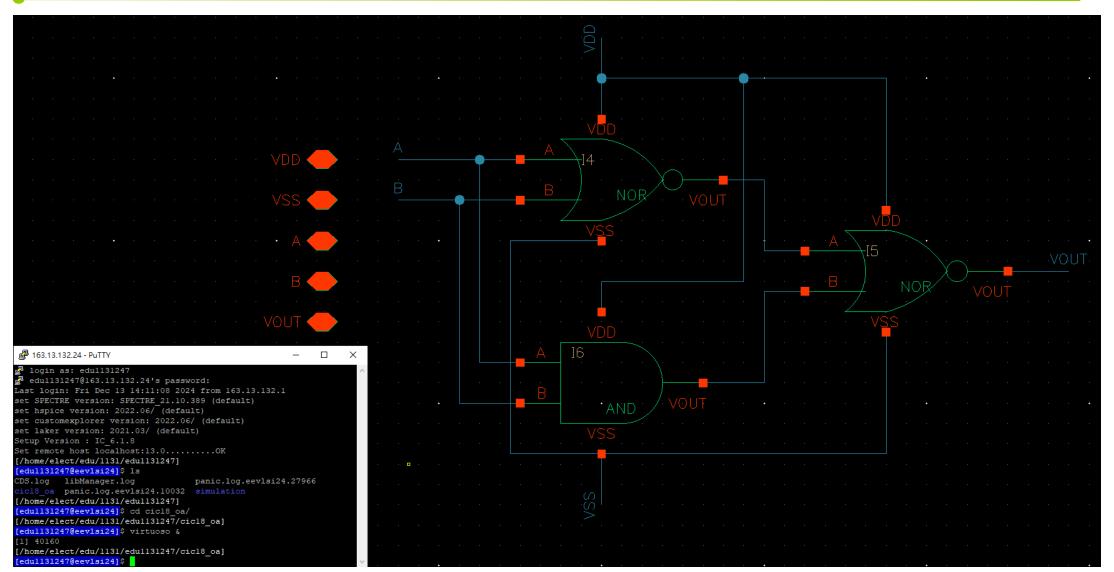
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XOR Schematic

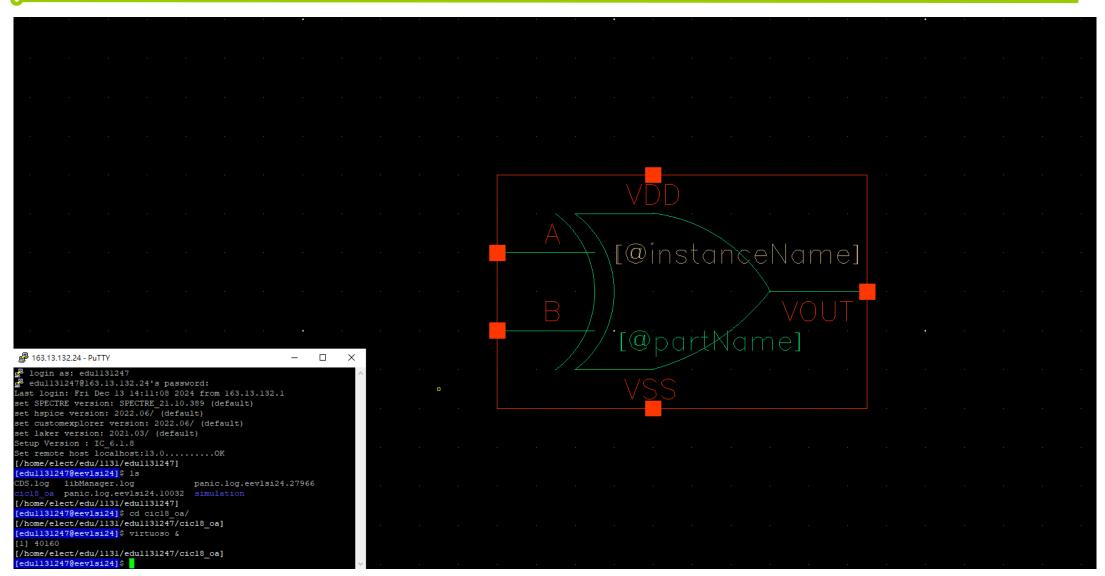






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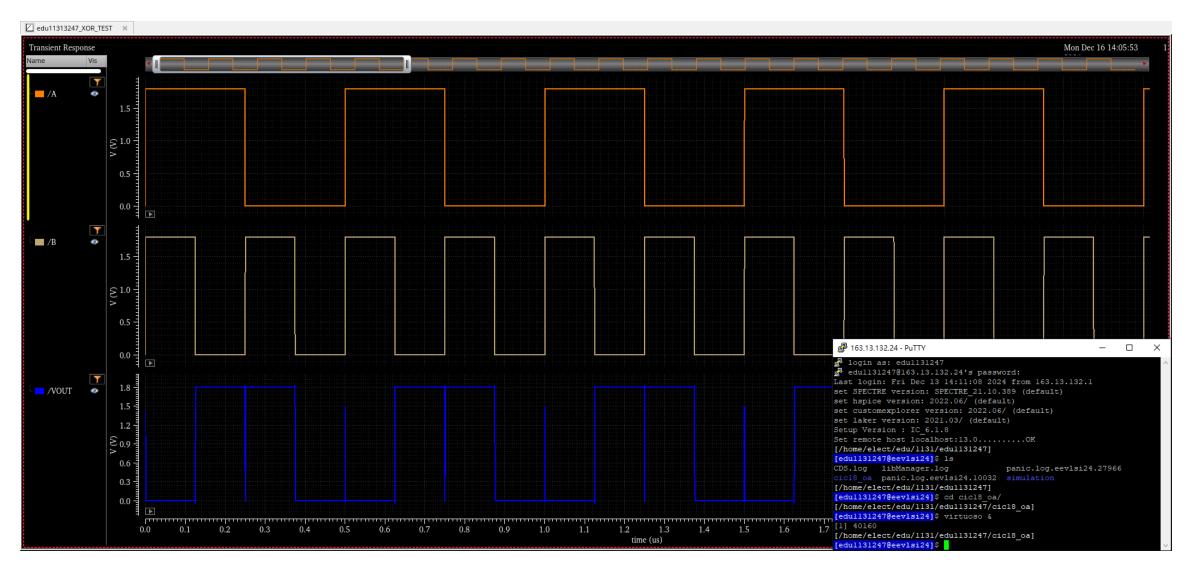






Pre-Sim

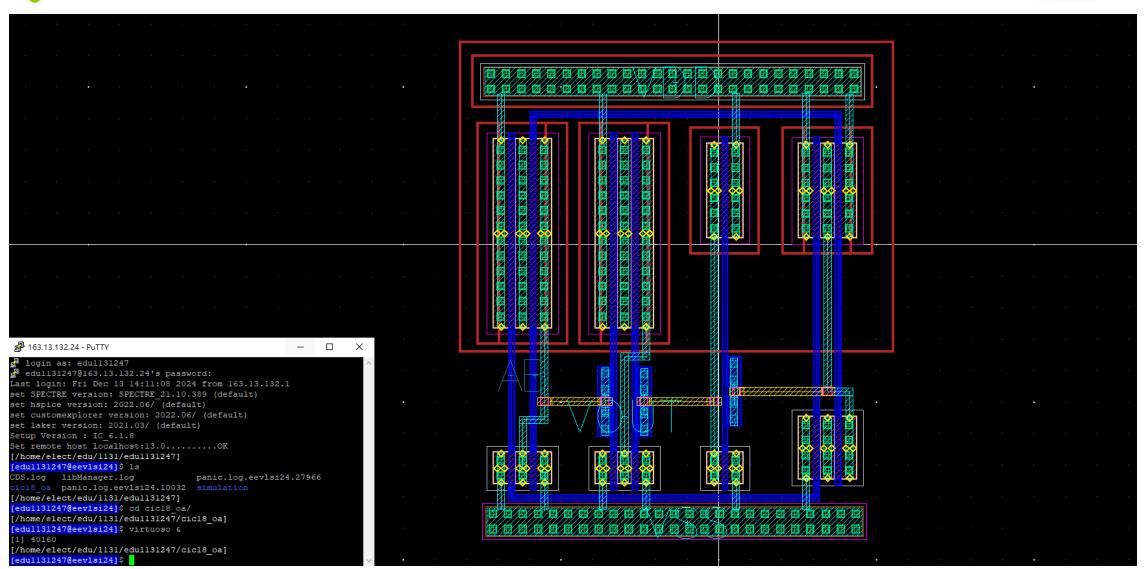






XOR Layout

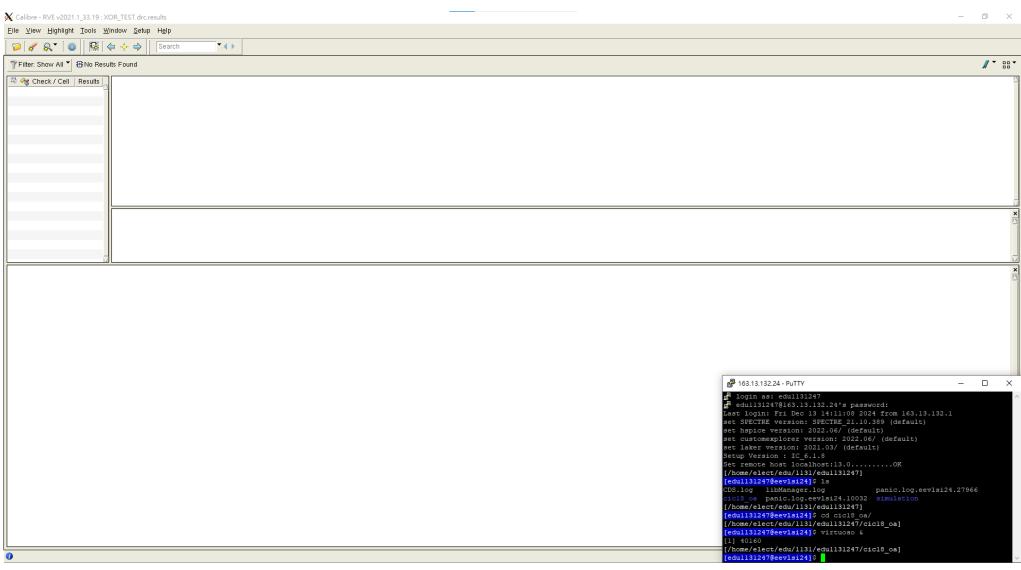






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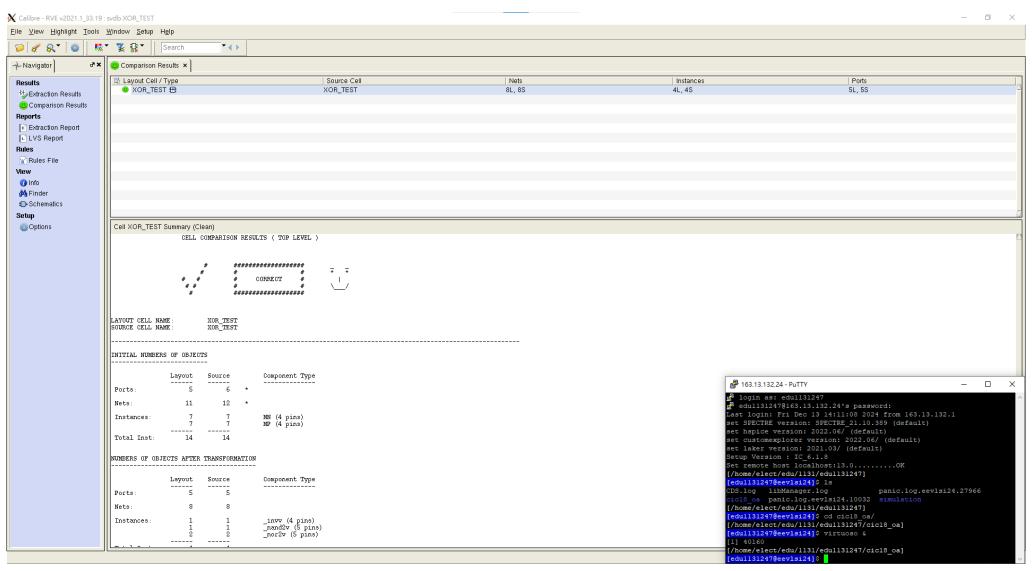






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Thanks for your attention !!