## Input **Convolution Coder** Randomizer Multiple outputs, with each A stream of bits output being the xor of a divided into frames, Also Known as whitener, few shift register entries. is a LFSR with a single tap diveded into PDUs. Each frame has a (output is input xor last Some bits are discarded to particular # of shift register, next bit achieve "rates" other than 1/X subchannels, frame id, shifted in is output) (where X is the number of output bits per 1 input bit) and more. Reed Solomon Encoder Galios Field multiplication **Constellation Mapper** Interleaver on GF(2^8) by a polynomial, results Swap chunks of the frame Convert a stream of bits shifted through a into an I,Q coordinate with other chunks of the series of GF(2^8) sized in one of the modulations frame. blocks. Remainder of operation appended to data. Pilot Subcarrier Insertion Output to analog **IFFT** wireless hardware Add I,Q = 1,0Inverse fast fourier components at Convert the digital I,Q to particular points transform. an analog equivilent. in the ODFM symbol (a series of I,Q coords)