
	<input checked="" type="checkbox"/> Preliminary Specification <input type="checkbox"/> Product Specification	
	Standard Product	Part Number: TM080JDHP95-00 Version: 0.7

SPECIFICATION

Tianma Part Number: TM080JDHP95-00
 Description: 8.0” HD Normally Black

	Department	Name	Signature
Tianma	Integrate Project Manager	Jon	
	Project Management	Bill_Hu	
Customer			

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Revision History

Version	Page	Revision Items	Name	Date
V0.1	All	First Release.	Ray Wen	2016-2-25
V0.2	P17	Update the Optical Characteristics	Ray Wen	2016-3-29
V 0.3	P26	Update the Appendix	Ray Wen	2016-4-27
V 0.4	P22	Update Drawing	Ray Wen	2016-4-28
V 0.5	P6	Update the Pin Assignment	Ray Wen	2016-7-19
V 0.6	P17	Update the white Chromaticity	Ray Wen	2016-8-03
V 0.7	P7	Update : 1、Scan direction Description 2、De-Rating Curve	Bill Hu	2016-10-20



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1. Precautions for Use of LCD Modules

1.1 Handling Precautions


- 1.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 1.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 1.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 1.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle the polarizer carefully.
- 1.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol
 Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
 - Water
 - Ketone
 - Aromatic solvents
- 1.1.6 Do not attempt to disassemble the LCD Module.
- 1.1.7 If the logic circuit power is off, do not apply the input signals.
- 1.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - 1.1.8.1 Be sure to ground the body when handling the LCD Modules.
 - 1.1.8.2 Tools required for assembly, such as soldering irons, must be properly grounded.
 - 1.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - 1.1.8.4 The LCD Module is covered with a film to protect the display surface. Be carefully and slowly when peeling off this protective film since static electricity may be generated.

1.2 Storage precautions

- 1.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 1.2.2 The LCD modules should be stored under the storage temperature range. the recommend condition is: Temperature : 0℃ ~ 40℃, Relatively humidity: ≤80%, and no more than 1 year..
- 1.2.3 The LCD modules should be stored in the room without acid, alkali and sulfur compound harmful gas, etc.

1.3 Transportation Precautions

- 1.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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2. Features

This is 8.0-inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. It is composed of a 8.0 inch TFT-LCD panel, LCD Driver IC with T-con integrated, FPC and a backlight unit.

This is 8.0-inch TFT-LCD Normally Black SFT technology module, which is designed for Automotive, and other high reliability electronic products required high performance flat panel displays. It is designed for Touch Panel by air-gap bonding, instead of optical bonding by OCR. Requirements on Environmental Protection of this 8.0-inch module are Following *RoHS*.

3. General Specifications


Feature		Spec
Display Spec.	Size	8.0 inch
	Resolution	1280(RGB) x 720
	Interface	One-port LVDS 24 bits VESA
	Color Depth	16.7 M
	Technology Type	a-Si
	Pixel Pitch (mm)	0.138 x 0.138
	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	Normally Black SFT technology
	Surface Treatment (LCD Upper Polarizer)	HC
	Gray Scale Inversion Direction	No gray inversion.
	Viewing Direction	Landscape Mode Optimized
	LCM (W x H x D) (mm)	192.8x116.9x6.4
	Polarizer Sunglass Compatible	Yes
Mechanical Characteristics	Active Area(mm)	176.64 x 99.36
	With /Without Touch Panel	Touch panel not included
	Weight (g)	Max. (220)g
	LED Configuration	3 parallels 7 serials

Table 3.1 General TFT Specifications

Note 1: Requirements on Environmental Protection: RoHS.

Note 2: The height dimension does not include the length of FPC.

Note 3: LCM weight tolerance: $\pm 10\%$.

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4. Input/output Terminals

4.1 CN1 pin assignment

Connector type: FH28D-30S-0.5SH (HIROSE)

No	Symbol	I/O	Description	Remark
1	NC	-	MUST be non-connection.	
2	VDD	P	Power supply 3.3V(Type)	
3	VDD	P	Power supply 3.3V(Type)	
4	GND	P	Power Ground	
5	RESET	I	Global reset signal	
6	STBYB	I	Standby mode control signal	
7	GND	P	Power Ground	
8	SDA	I/O	Serial Interface address and data input/output. if not use SPI , floating	
9	SCL	I	Serial Interface clock input , if not use SPI , floating	
10	CSB	I	Serial Interface chip enable signal CSB=0: Selected CSB=1: Not selected if not use SPI , floating	
11	GND	P	Power Ground	
12	TB	I	Vertical shift direction (gate output) selection	
13	RL	I	Horizontal shift direction (source output) selection	
14	GND	P	Power Ground	
15	LV0N	I	Negative LVDS Differential data input(0)	
16	LV0P	I	Positive LVDS Differential data input(0)	
17	GND	P	Power Ground	
18	LV1N	I	Negative LVDS Differential data input(1)	
19	LV1P	I	Positive LVDS Differential data input(1)	
20	GND	P	Power Ground	
21	LV2N	I	Negative LVDS Differential data input(2)	
22	LV2P	I	Positive LVDS Differential data input(2)	
23	GND	P	Power Ground	
24	CLKN	I	Negative LVDS Differential clock input	
25	CLKP	I	Positive LVDS Differential clock input	
26	GND	P	Power Ground	
27	LV3N	I	Negative LVDS Differential data input(3)	
28	LV3P	I	Positive LVDS Differential data input(3)	
29	GND	P	Power Ground	
30	NC	-	Module used,must be float	

Table 4.1.1 Pin assignment for TFT interface

Note1: All of GND pins should be connected to system ground.

Note2: I/O definition.

I---Input, O---Output, P--- Power/Ground, N--- No connection

Note3: VCOM is DC power supply

Note4:

Scan Control Input		Scanning Direction
TB	RL	
L	H	Bottom →Top, Left →Right
H	L	Top →Bottom, Right →Left
H	H	Top →Bottom, Left →Right
L	L	Bottom →Top, Right →Left

Table 4.1.2 Scan direction Description

The recommended resistance of pull high/low resistor in UPDN or SHLR pin is 4.7K ohm.

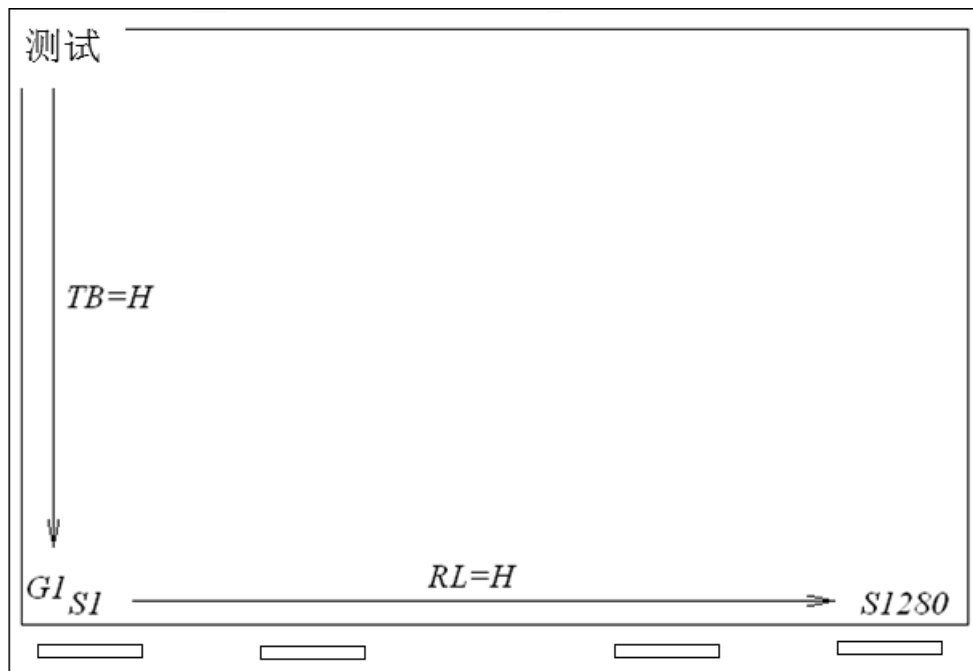



Figure 4.1.1 Scan direction Description

4.2 CN2 pin assignment (Backlight interface)

Connector type: FH28-10S-0.5SH Hirose

PIN NO.	Symbol
1	A1
2	A2
3	A3
4	NC
5	NTC+
6	NTC-
7	NC
8	C3
9	C2
10	C1

Table 4.2.1 Pin assignment for backlight interface

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5. Absolute Maximum Ratings

GND=0V, Ta = 25°C


Item	Symbol	Min	Max	Unit	Remark
Power supply voltage	VDD	-0.3	3.96	V	
Analog supply voltage	AVDDP	-0.3	6.5	V	Internal DCDC
Analog supply voltage	AVDDN	-6.5	0.3	V	
Gate on voltage	VGH	-0.3	VGL+40	V	
Gate off voltage	VGL	-25	+0.3	V	
Gate voltage range	VGH-VGL	12	40	V	
Digital I/O signal input	Vio	-0.3	VDD+0.3	V	
Back Light Forward Current	If	-	(150)	mA	Per chain
Operating Temperature	Top	-30	85	°C	Note
Storage Temperature	Tst	-40	85	°C	

Table 4.1.1 Absolute maximum rating

Note1: The temperature is the surface temperature of module

Note2: If the voltage exceeds its absolute maximum ratings, the LCM maybe damaged. In addition, if the LCM is operated with the absolute maximum ratings for a long time, its reliability drops.

Note3: Functional operation should be restricted under normal ambient temperature.

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6. Electrical Characteristics

6.1 DC Characteristics for Panel Driving


GND=0V

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power Supply Voltage	VDD	3.0	3.3	3.6	V	
Power consumption	Ivdd	-	(266.5)	(400)	mA	VDD=3.3V, white pattern, DCLK=63.7MHz , fv=60Hz Note 1
In-rush current	Ivdd-rush	-	-	(1)	A	VDD=3.3V Note 1
Control Signal	Vh	0.7*VDD	-	VDD	V	T=25℃
	VI	0	-	0.3*VDD	V	T=25℃
Allowable VDD ripple	VDD-ripple	-	-	100	mV	
Differential Input High Voltage(threshold)	Vth	-	-	0.1	V	Note2
Differential Input Low Voltage(threshold)	Vtl	-0.1	-	-	V	
Magnitude Differential Input Voltage	V _{id}	0.2	-	0.6	V	
Common Mode Voltage	V _{cm}	1	1.2	1.8- V _{id} /2	V	
Spread Spectrum Clocking Ratio	SSCR	50	-	200	KHz	

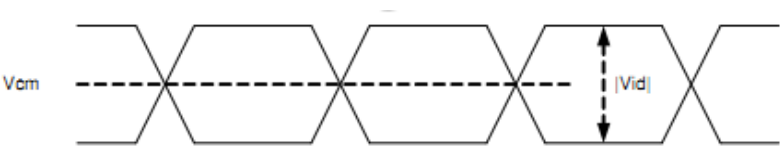
Table 6.1.1 LCD module electrical characteristics

Note1: For different LCM, the value may have a bit of difference.

Note2: Refers to the LVDS waveform below

	■ Preliminary Specification □ Product Specification		
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Single-ended:
CLKP
CLKN
LV[3:0]P
LV[3:0]N



Differential:
CLKP-CLKN
LV[3:0]P
LV[3:0]N

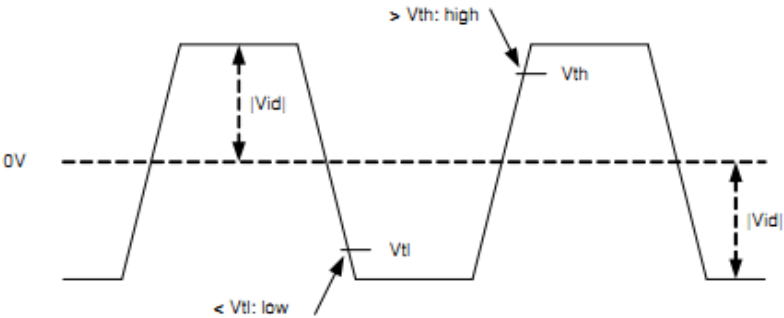


Figure 6.1.1 LVDS waveform


Parameter	Symbol	Min.	Spec. Typ.	Max.	Unit	Condition
Modulation Frequency	SSC _{MF}	-	-	200	Khz	LVDS clock frequency centered at 80MHz.
		-	-	150	Khz	LVDS clock frequency centered at 60MHz.
		-	-	100	Khz	LVDS clock frequency centered at 40MHz.
		-	-	50	Khz	LVDS clock frequency centered at 20MHz.
Modulation Rate	SSC _{MR}	-	-	±5	%	LVDS clock frequency + SSC _{MR} is in the range of 10~85MHz.

Table 6.1.2 SSC limitation of LVDS interface

6.2 DC Characteristics for Backlight Driving

Item	Symbol	Min	Typ	Max	Unit	Remark
Forward Current	I _{BL}	-	(95)	-	mA	Note1
Forward Voltage	V _{BL}	(18.9)	(21.7)	(23.8)	V	
LED Life Time	-	(30000)	-	-	Hrs	Note2
Backlight Power Consumption		0	(6.2)	(6.8)	W	

Table 6.2.1 LED backlight characteristics

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Note 1: I_{BL} is defined for one channel LED, There are total three LED channels in back light unit Under LCM operating, and the stable forward current should be inputted.Note 2: It is estimation result based on LED supplier data. Optical performance should be evaluated at $T_a=25^{\circ}\text{C}$ only. Operating life means brightness goes down to 50% of original brightness.

Note 3: it is suggested Customer to make sure the LCM module in the system is well heat dissipation. When operating at high temperature, keep panel surface temperature under 85°C or obey the de-rating curve as Figure 5.2.2.

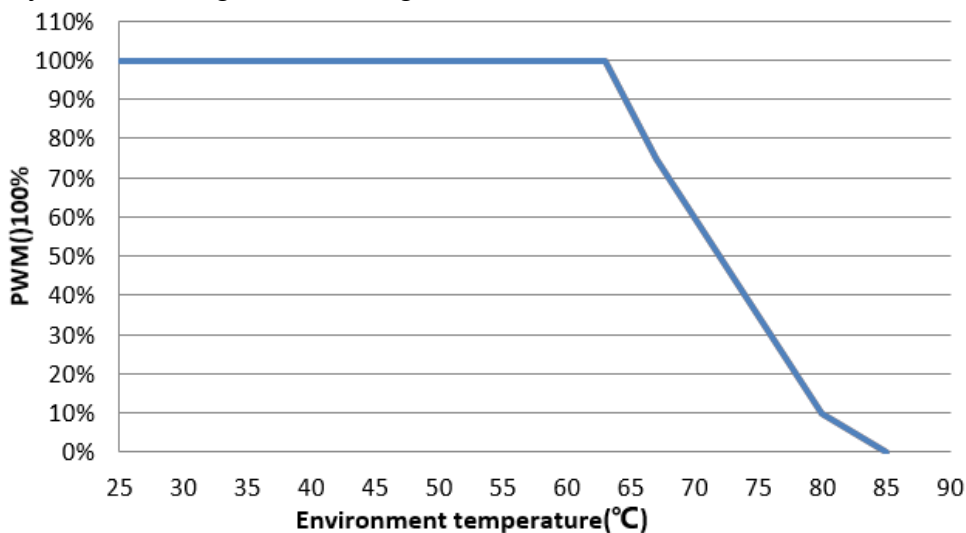


Figure 6.2.1 Backlight Current De-Rating Curve (Tentative)

6.3 Recommended Power ON/OFF Sequence

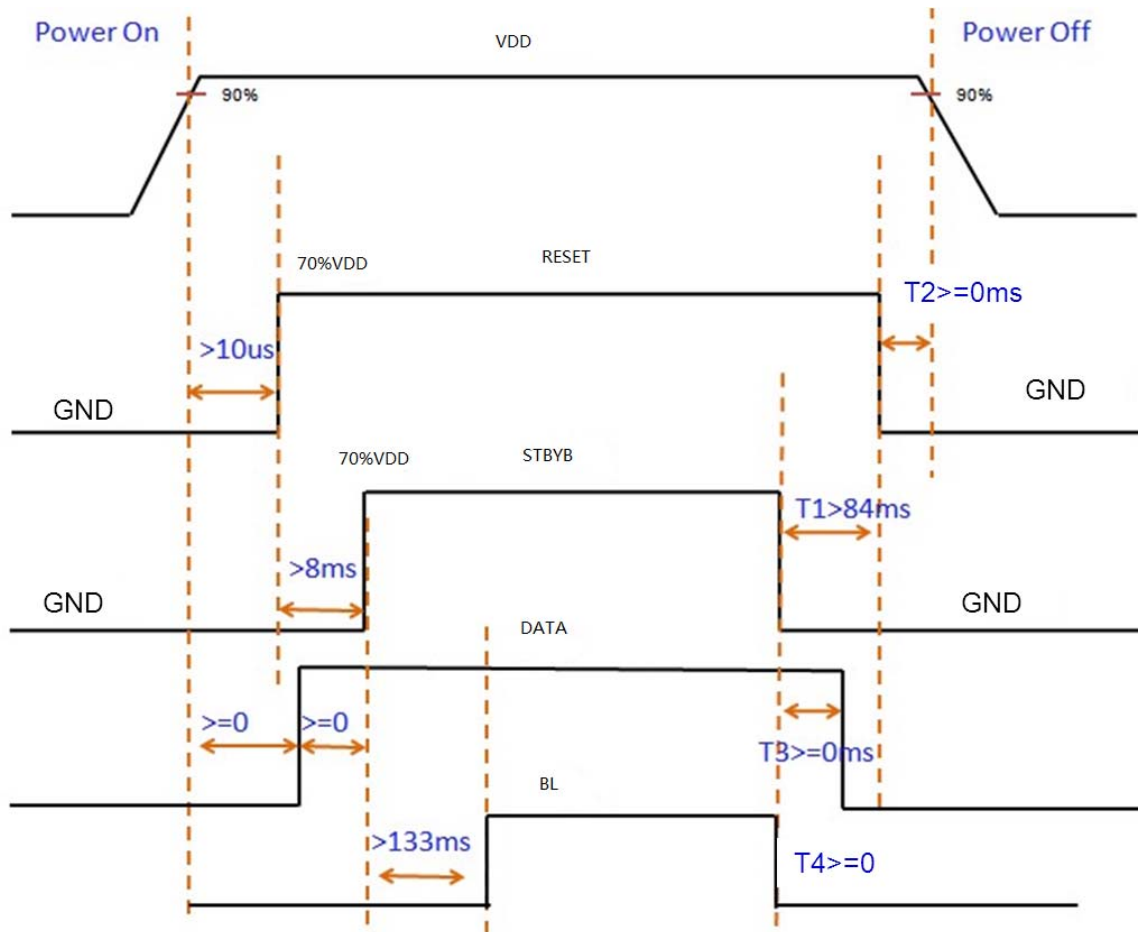



Figure 6.3.1 Power on/off Sequence

Note1: The low level of these signals and analog powers are GND level.

Note2: All of power and signals should be kept GND level before power on.
IF there are remaining voltages on them, LCD might become abnormal.

Note3: BL is the voltage applied to backlight, and it will stay low level before display stability; and it need to be turned off before STBYB off, refer to T4 in above figure.

Note4: This is preliminary SPEC, it may be updated according to panel actually display quality and actually electrical characteristics.

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7. Timing Characteristics

Frame rate=60Hz

7.1 Input timing

Parameter	Symbol	Unit	Min.	Typ.	Max.	Remarks
Clock Frequency	f_{dck}	MHz		63.7		1
H Total Time	T_{hp}	clocks	1336	1340	1472	
H Active Time	HA	clocks		1280		
H Blank	T_{hfp}	clocks	56	60	192	
V Total Time	T_{vp}	lines	730	792	864	
V Active Time	VA	lines		720		
V Blank	T_{vfp}	lines	10	72	144	
V Frequency	f_v	Hz	-	60	-	

Note1 : Need to modify the value of the relative registers.

Note2 : Above of all these information is just for reference, the final information should be based on the test result of module and be set for module in initial code.

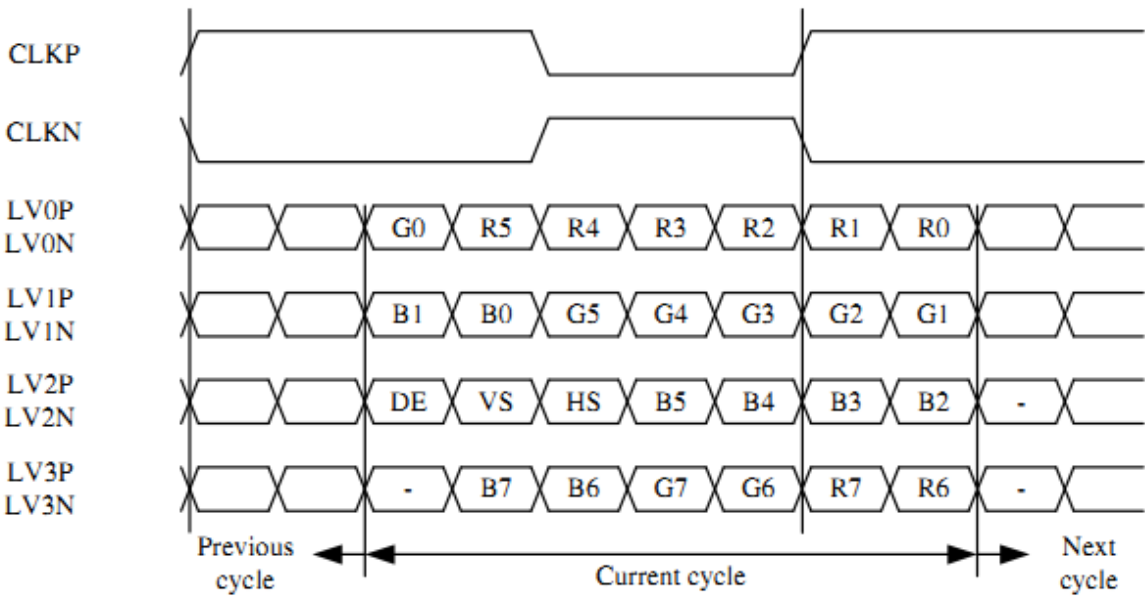


Figure 7.1.1. LVDS,8-bit,VESA format

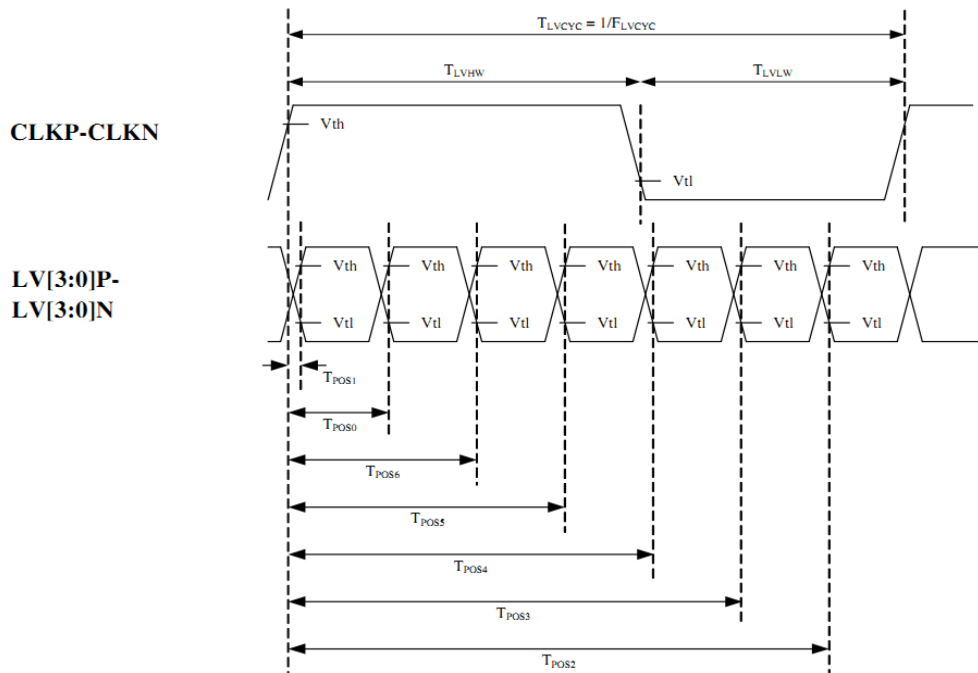
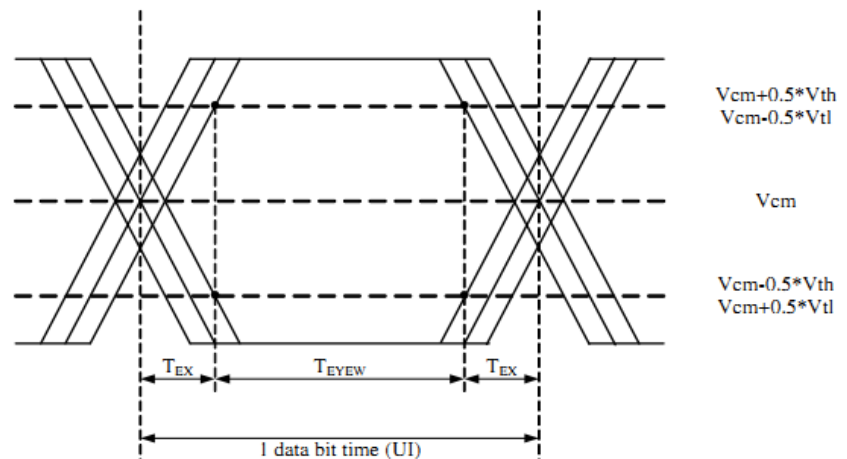
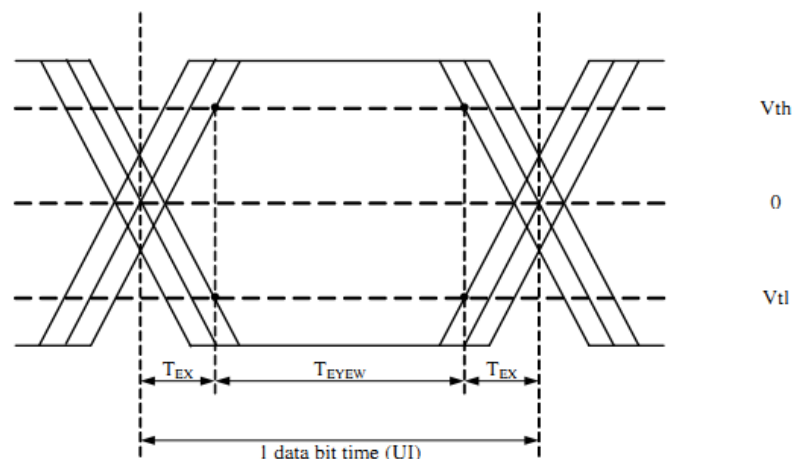



Figure 7.1.2. LVDS input timing

**Single-ended:
LV[3:0]P,
LV[3:0]N**



**Differential:
LV[3:0]P-LV[3:0]N**



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Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Clock frequency	FLVCYC	10	-	85	MHz
Clock period	TLVCYC	11.76	-	100	nsec
1 data bit time	UI	-	1/7	-	TLVCYC
Position 1	TPOS1	-0.2	0	0.2	UI
Position 0	TPOS0	0.8	1	1.2	UI
Position 6	TPOS6	1.8	2	2.2	UI
Position 5	TPOS5	2.8	3	3.2	UI
Position 4	TPOS4	3.8	4	4.2	UI
Position 3	TPOS3	4.8	5	5.2	UI
Position 2	TPOS2	5.8	6	6.2	UI
Input eye width	TEYEW	0.6	-	-	UI
Input eye border	TEX	-	-	0.2	UI
LVDS wake up time	TENLVDS	-	-	150	μs

Table 7.1.2. LVDS input timing parameters

7.2 SPI timing

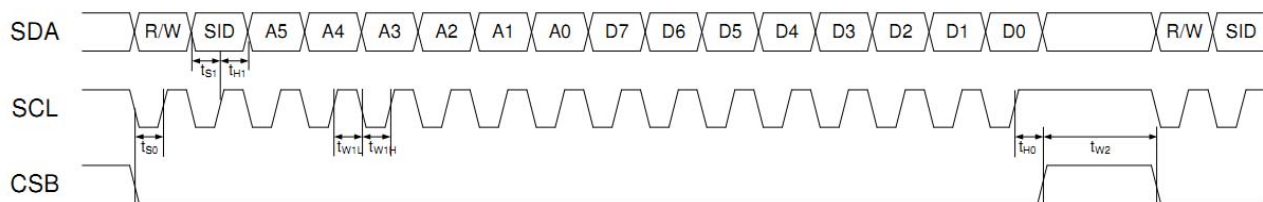



Figure 7.2.1 SPI signal timing

Parameter	Symbol	Conditions	Spec.			Unit
			Min.	Typ.	Max.	
SDA Setup Time	t _{S0}	CSB to SCL	60	-	-	ns
	t _{S1}	SDA to SCL	60	-	-	ns
SDA Hold Time	t _{H0}	CSB to SCL	60	-	-	ns
	t _{H1}	SDA to SCL	60	-	-	ns
Pulse Width	t _{W1L}	SCL pulse width	75	-	-	ns
	t _{W1H}	SCL pulse width	75	-	-	ns
	t _{W2}	CSB pulse width	1	-	-	μs
Clock duty	-	-	40	50	60	%

Figure 7.2.2 SPI signal parameter

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8. Optical Characteristics


Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		U	CR≥10	80	88	--	Degree	--
		D		80	88	--		
		L		80	88	--		
		R		80	88	--		
Contrast Ratio		CR	⊥,25℃	600:1	900:1	--		Note3 Note6
			⊥,85℃	50% value of 25℃				
Response Time		Ton+Toff	25℃	--	35	40	ms	Note4
			-20℃	--	250	400		
			-30℃	--	550	700		
Chromaticity	White	x	Backlight is on	(0.260)	(0.300)	(0.340)		Note5
		y		(0.300)	(0.330)	(0.380)		
	Red	x		(0.610)	(0.640)	(0.670)		
		y		(0.282)	(0.312)	(0.342)		
	Green	x		(0.276)	(0.306)	(0.336)		
		y		(0.601)	(0.631)	(0.661)		
	Blue	x		(0.128)	(0.158)	(0.188)		
		y		(0.044)	(0.074)	(0.104)		
Uniformity		White		75	80	--	%	Note1 Note6
NTSC			⊥	70	73	--	%	Note5
Luminance		L		600	800	--	cd/m ²	Note1 Note7
Flicker		dB	50%Gray pattern			(-20)		Center of Display.
Gamma		γ		1.6	2.2	2.8		VESA standard Perpendicular

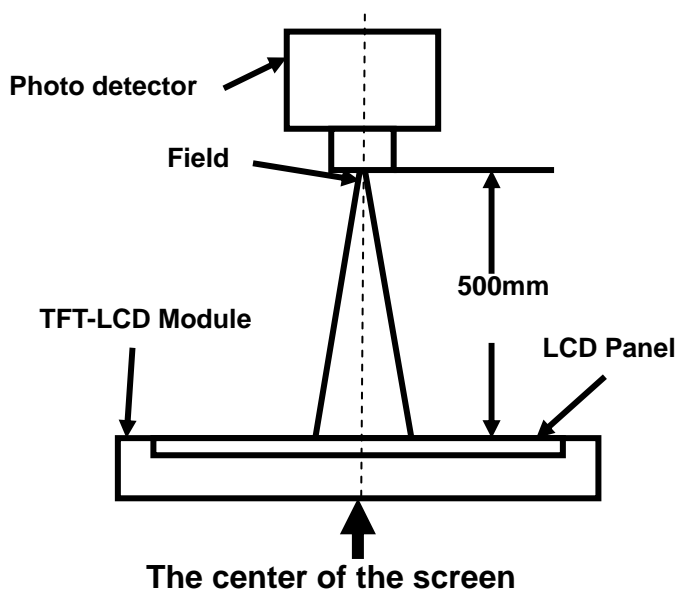
Test Conditions:

1. $I_F = 95\text{mA} \times 3\text{chains} = 285\text{mA}$, the panel surface temperature is 25℃.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

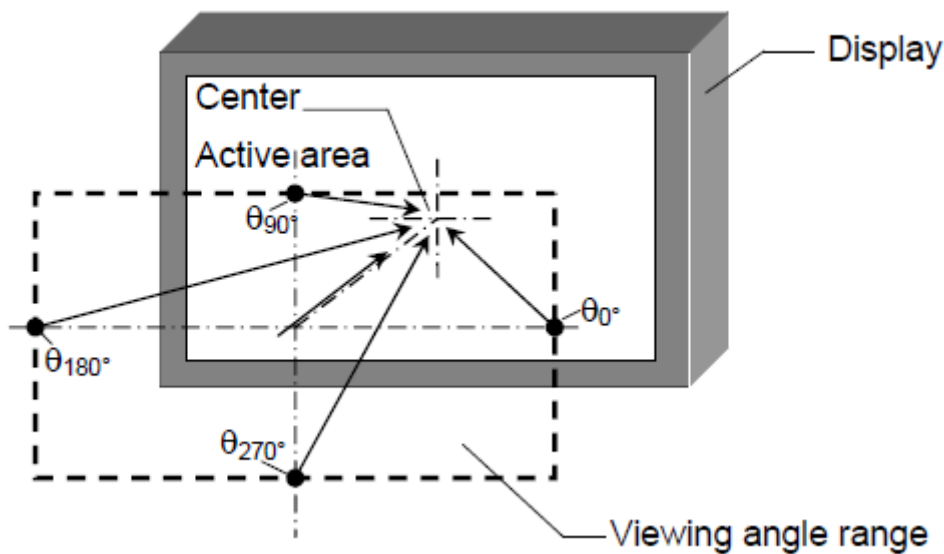
The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

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Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	DMS 803	3mm
Reflectivity	CM-3600	

Note 2: Definition of viewing angle range and measurement system



FPC at θ270°


Note 3: Definition of the contrast ratio

“White state “:The state is that the LCD should be driven by Vwhite.

“Black state”: The state is that the LCD should be driven by Vblack.

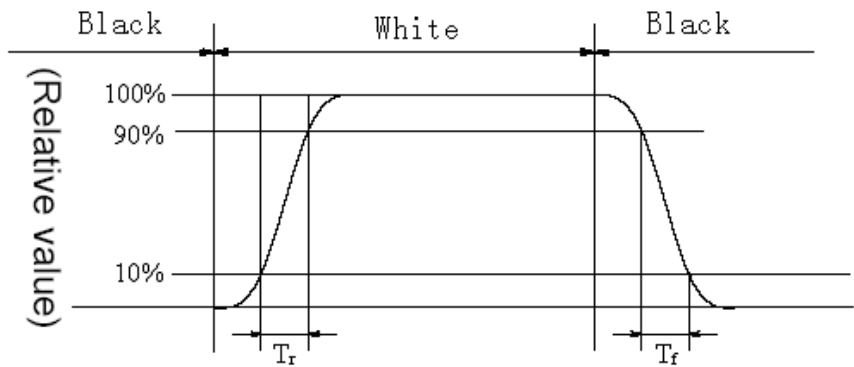
Vwhite: To be determined Vblack: To be determined.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

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Note 4: Definition for Response time
Ton + Toff

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 10% to 90%. And fall time (TOFF) is the time between photo detector output intensity changed from 90% to 10%. Refer to below.

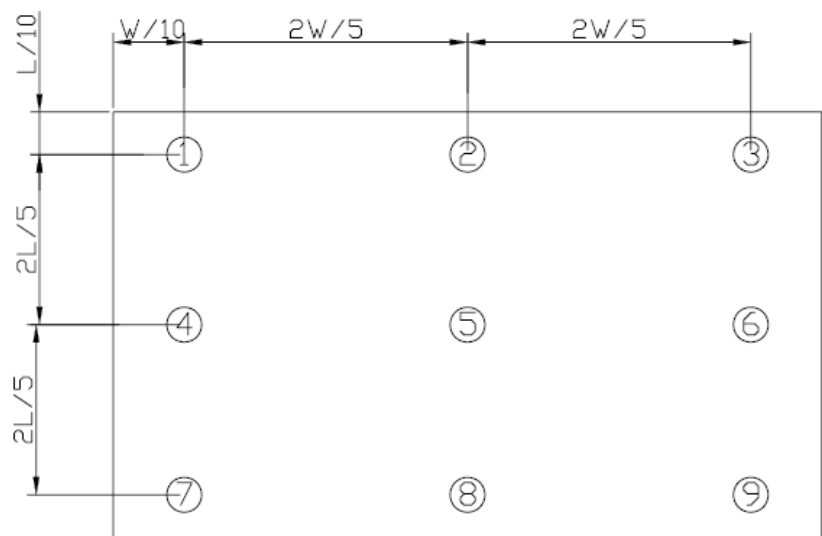


Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.


Note 6: Contrast ratio against temperature
CR defines the allowed contrast reduction at the defined temperature based on the CR at room temperature. For example: $CR = (CR@25^{\circ}C - CR@-30^{\circ}C) / CR@25^{\circ}C \times 100\%$

Note 7: Luminance homogeneity
The luminance uniformity is calculated by using following formula.

Luminance uniformity (Lu)=
$$\frac{\text{Minimum luminance from ① to ⑨}}{\text{Maximum luminance from ① to ⑨}}$$




Note 8: Definition of Luminance:
Measure the luminance of white state at center point.

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9. Reliability Test

9.1 Content of Reliability Test

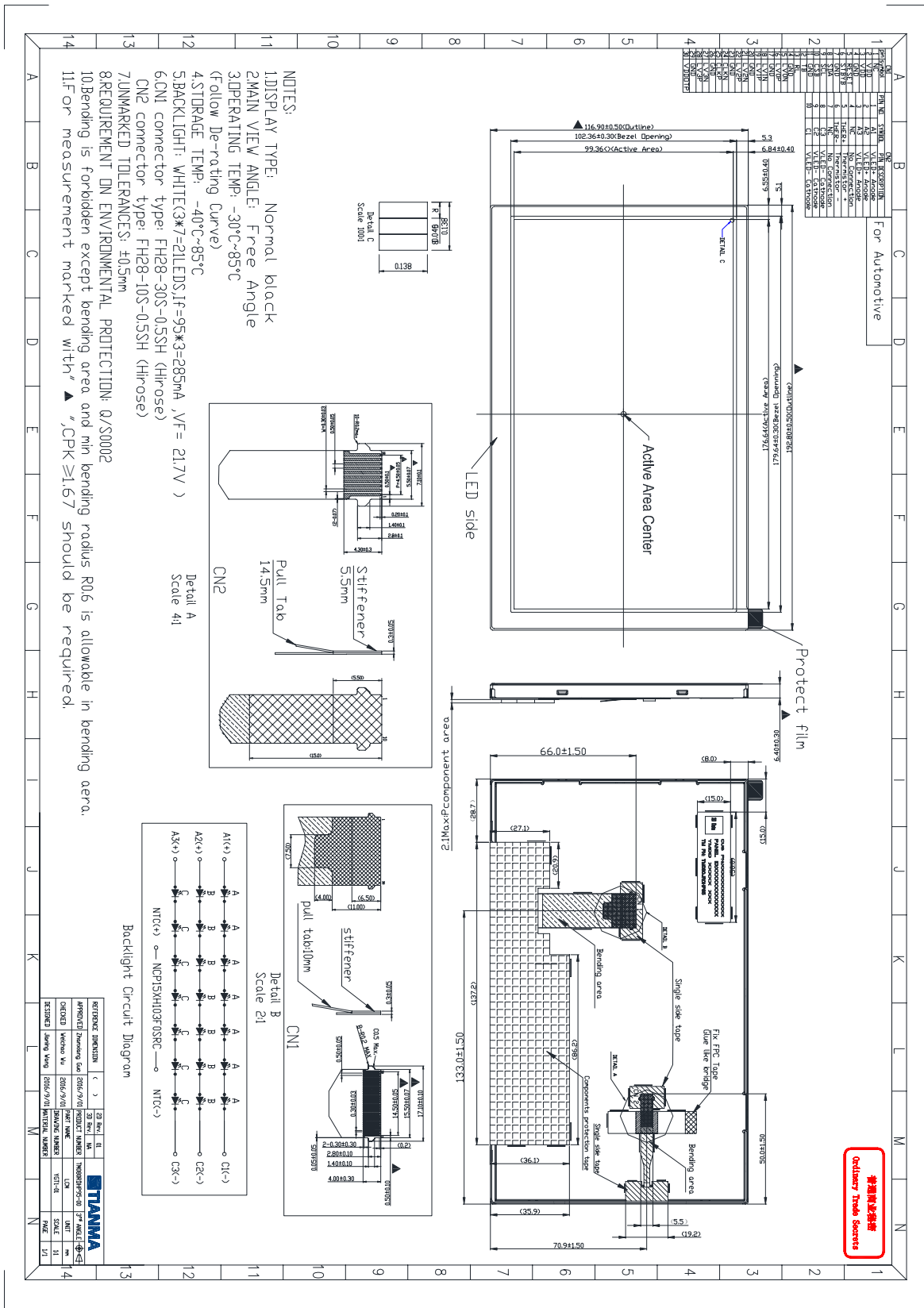
No	Test Item	Test condition	Criterion
1	High Temperature Storage	85℃ 240H RH≤45% Restore 2H at 25℃ non-operation	Note 1 IEC 60 068 - 2 - 2Bb
2	Low Temperature Storage	-40℃±3℃ 240H Restore 2H at 25℃ non-operation	Note 1 IEC 60 068 - 2 - 1Ab
3	High Temperature Operation	85℃±2℃ 240H RH≤45% Restore 2H at 25℃ operation	Note 1 IEC 60 068 - 2 - 2Bb
4	Low Temperature Operation	-30℃±3℃ 240H Restore 2H at 25℃ operation	Note 1 IEC 60 068 - 2 - 1Ab
5	High Temperature & Humidity Operation	60℃±2℃, 90±2%RH 240H operation	Note 1 IEC 60 068 - 2 - 3Ca
6	Thermal Shock	-40℃→ change→+85℃ 30min 30s 30min 100cycle non-operation	Note 1 IEC 60 068 - 2 - 14Nb
7	Vibration Test	Frequency: 8 - 33.3 Hz, Total amplitude: 1.3mm Frequency: 33.3 - 400 Hz, Acceleration: 29.4 m/s ² sweep time: 15 minutes 2 hours each for X and Z directions, 4 hours for Y direction (total 8 hours) Non-operation	Note 2 IEC 60 068 - 2 - 6Fc
8	Shock Test	60 x 9.8m/s ² , t=6ms, XYZ directions, Half sin curve, [non-operating],each directions 2 times	Note 2 IEC 60 068 - 2 - 27Ea
9	ESD	Air discharge: C=150pF±10%,R=330Ω±10%, 5 point/panel Air: +/-15KV, 5times	GB/T17626 (IEC61000) / ISO10605
		Contact discharge: C=150pF±10%,R=330Ω±10%, 5 point/panel Contact: +/-8KV,5times	GB/T17626 (IEC61000) / ISO10605


	<input checked="" type="checkbox"/> Preliminary Specification <input type="checkbox"/> Product Specification	
	Standard Product	Part Number: TM080JDHP95-00 Version: 0.7

Notes:

1. The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:
 - 1) Air bubble in the LCD
 - 2) Seal leak
 - 3) Non-display
 - 4) Missing segments
 - 5) Glass crack
2. Each test item applies for a test sample only once, The test sample can not be used again in any other test item.
3. For Damp Proof Test, Pure water (Resistance $> 10M\Omega$) should be used.
4. In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
5. In the test of High Temperature Operation and High Temperature & Humidity Operation. The operation temperature is the surface temperature of module
6. We will consult with our customers, if appearing problems during the reliability test.
7. LED forward current should follow the De-rating curve.

10. Mechanical Drawing



	<input checked="" type="checkbox"/> Preliminary Specification <input type="checkbox"/> Product Specification	
	Standard Product	Part Number: TM080JDHP95-00 Version: 0.7

11. Product Inspection Criteria

11.1 Inspection Conditions

11.1.1 Ambient conditions:

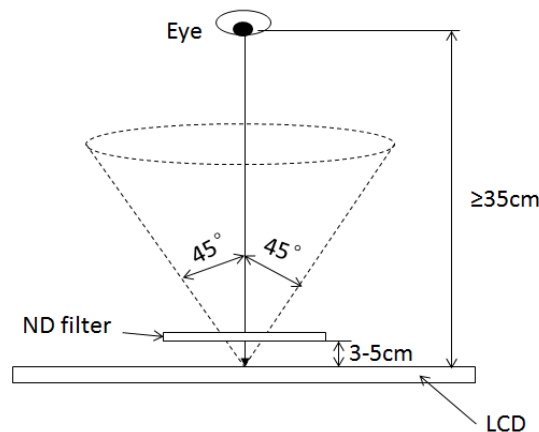
- Temperature: Room temperature $25\pm 5^{\circ}\text{C}$
- Humidity: $(60\pm 10)\% \text{RH}$
- Illumination (the surface of LCD)
 - backlight-on 100-300Lux
 - backlight-off 800-1200lux

11.1.2 Viewing distance

The distance between the LCD and the inspector's eyes shall be 35cm or more.

11.1.3 Viewing Angle

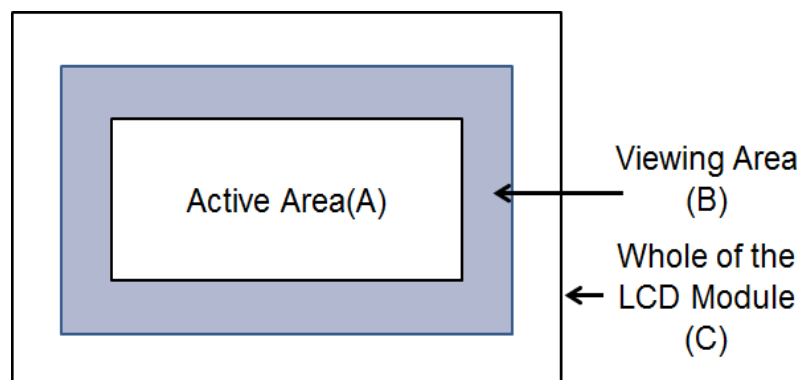
U/D: $45^{\circ}/45^{\circ}$, L/R: $45^{\circ}/45^{\circ}$



11.1.4 Light-on condition

The current of the Backlight should refer to the recommended typical value in this specification.


11.1.5 Definition of LCD zone (with front bezel)



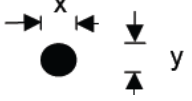
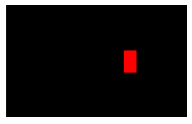

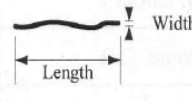
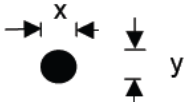

A-zone: The inside of the Active Area (as defined on the product drawing)

B-zone: The inside of the Viewing Area which is between A-zone and the metal frame (defined on the product drawing if no up metal frame)

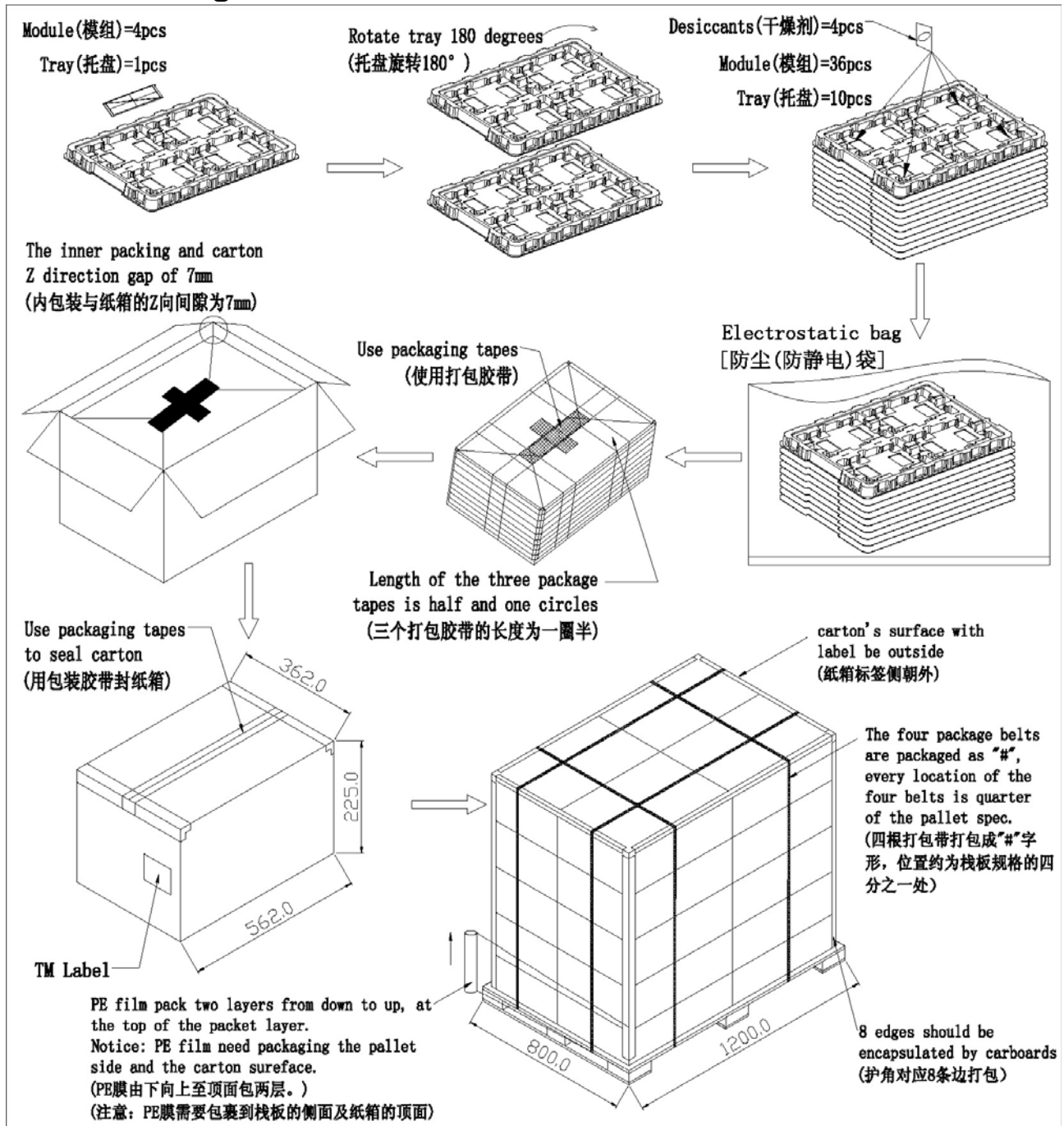
C-zone: Whole of the LCD Module except the zone A and B. (Including FPC& Metal Frame & backside of the LCD Module)


	<input checked="" type="checkbox"/> Preliminary Specification <input type="checkbox"/> Product Specification		
	Standard Product	Part Number: TM080JDHP95-00	Version: 0.7

11.2 Cosmetic Inspection Criteria

Inspection Item	Inspection Standards	Acceptable Qty.	Applied Zone	Inspection Mode	Note
Dark /Bright spots	$\phi \leq 0.2$	Ignore	A	Backlight-on	$\phi = (x+y) / 2$ 
	$0.2 < \phi \leq 0.4$ (the bright spots should not be visible through ND2%)	3			
	$0.40 < \phi$	None			
Bright pixel dot	1 sub-pixel, Visible through ND2%	None	A	Backlight-on	
Dark pixel dot	1 sub-pixel	3 (distance ≥ 5 mm)	A	Backlight-on	
Lints & Scratches	$W \leq 0.03$ and $L \leq 3.0$ $0.03 < W \leq 0.05$ and $L \leq 3.0$ $0.05 < W$ or $L > 3.0$	Ignore 2 None	A\B	Backlight-on Backlight-off	
Polarizer Dent/Bubble	$\phi \leq 0.2$ $0.2 < \phi \leq 0.4$ $0.40 < \phi$	Ignore 3 None	A\B	Backlight-on Backlight-off	$\phi = (x+y) / 2$ 
Mura	Visible through ND2% at full black pattern	None	A	Backlight-on	
Dirty/Dust	Those wiped out easily are acceptable		A\B	Backlight-off	\

12. Packing Instruction



	<input checked="" type="checkbox"/> Preliminary Specification <input type="checkbox"/> Product Specification	
	Standard Product	Part Number: TM080JDHP95-00 Version: 0.7

13. Appendix

TEMP (deg.C)	Resistance (kohm)
-40	195.652
-35	148.171
-30	113.347
-25	87.559
-20	68.237
-15	53.65
-10	42.506
-5	33.892
0	27.219
5	22.021
10	17.926
15	14.674
20	12.081
25	10
30	8.315
35	6.948
40	5.834
45	4.917
50	4.161
55	3.535
60	3.014
65	2.586
70	2.228
75	1.925
80	1.669
85	1.452
90	1.268
95	1.11
100	0.974
105	0.858
110	0.758
115	0.672
120	0.596
125	0.531