



Tutorial 2 – ARC EM9D AloT DK Project Environment Setup & Development Flow

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ARC EM9D AloT DK Project Development Flow

TensorFlow Model
Development

Convert

Firmware Development

Download img file Application On ARC EM9D AloT DK

Debug

Stage	TensorFlow Model Development	Firmware Development	Run / Update Application On ARC EM9D AloT DK
Tool	Anaconda Cygwin	Cygwin Metaware or ARC GNU VirtualBox (Ubuntu 20.04)	JTAG Himax-FT4222-GUI USB Cable
Language	Python 3	C language C++ language	

ARC EM9D AloT DK Project Development Flow

TensorFlow Model Development



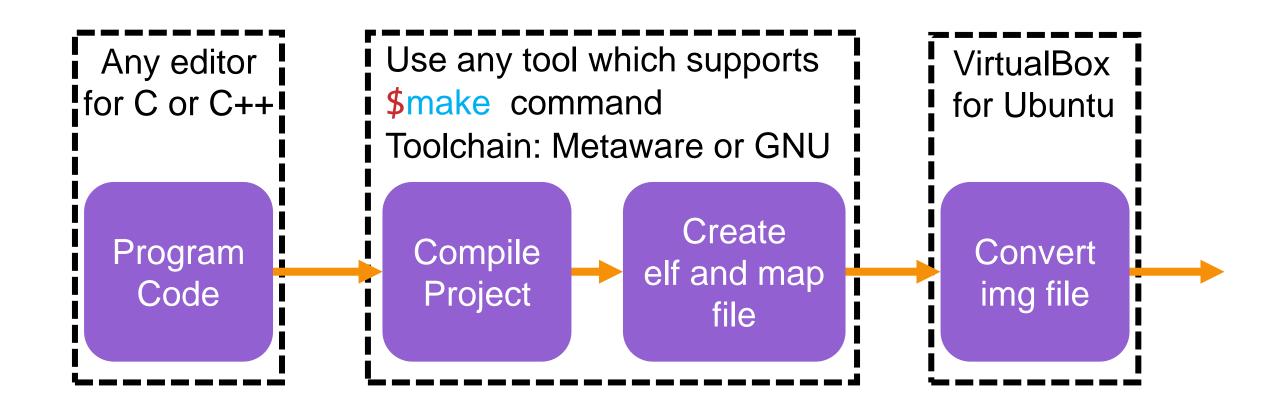
Download img file

Run / Update
Application
On ARC EM9D
AloT DK

Debug

Stage	TensorFlow Model Development	Firmware Development	Run / Update Application On ARC EM9D AloT DK
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Language	Python 3	C language C++ language	

Firmware Development









- 1. Open Cygwin64 Terminal
 - \$ cd c:
 - \$ cd Users/{username}/ (to your working file path)
 - \$ mkdir VM (Suggest create a new folder named "VM")
 - \$ cd VM

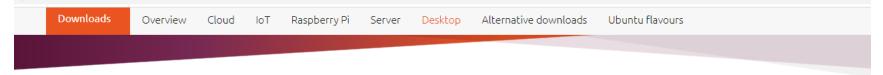
```
williet@wILLIET-7490 ~
$ cd c:
williet@wILLIET-7490 /cygdrive/c
$ cd Users/williet/
williet@wILLIET-7490 /cygdrive/c/Users/williet
$ mkdir VM
williet@wILLIET-7490 /cygdrive/c/Users/williet
$ cd VM
williet@wILLIET-7490 /cygdrive/c/Users/williet
$ cd VM
```

2. Download SDK and unzip to folder "C:\Users\{username}\VM" Please contact Synopsys Taiwan for the SDK

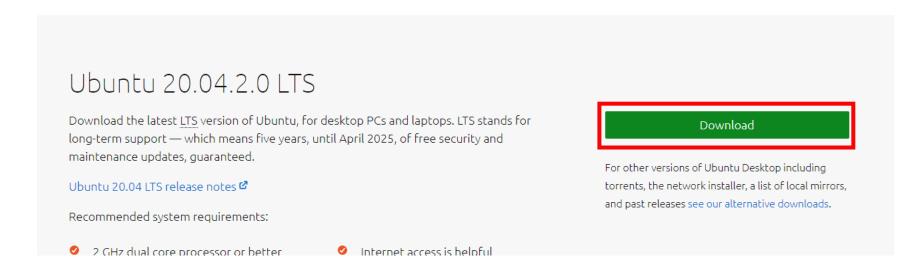
```
After these steps, your file structure will be like:
Synopsys_SDK_Vxx
---- Example_Project
   ---- Labx (Firmware project)
   ---- Labx (Python project for TensorFlow Ppoject)
   ---- LabPY (Python project for data convert)
---- others (Library, toolchain setting ...)
---- tools
   ---- image_gen_cstm (Convert elf and map file to image file)
   ---- HMX_FT4222H_GUI (Download image file to MCU)
```

3. Download Ubuntu iso file

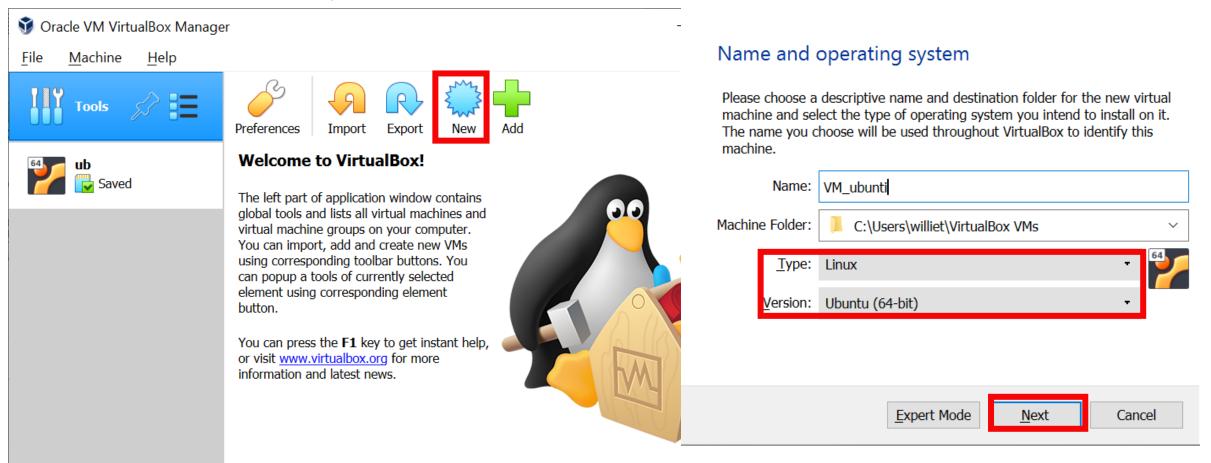
https://ubuntu.com/download/desktop



Download Ubuntu Desktop



- 4. Open VM Oracle VirualBox
- 5. New > Named your VM > select Linux, ubuntu (64-bit)



4 MB

6. User can select your memory size according to your needs Recommend more than 1024MB.

Select the amount of memory (RAM) in megabytes to be allocated to the virtual machine. The recommended memory size is **1024** MB.

16384 MB



7. Click "Create" and "Next"

Hard disk

If you wish you can add a virtual hard disk to the new machine. You can either create a new hard disk file or select one from the list or from another location using the folder icon.

If you need a more complex storage set-up you can skip this step and make the changes to the machine settings once the machine is created.

The recommended size of the hard disk is 10.00 GB.

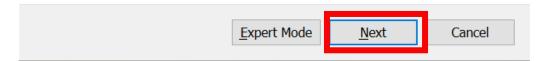
- Do not add a virtual hard disk
- Create a virtual hard disk now
- Use an existing virtual hard disk file



Hard disk file type

Please choose the type of file that you would like to use for the new virtual hard disk. If you do not need to use it with other virtualization software you can leave this setting unchanged.

- VDI (VirtualBox Disk Image)
- VHD (Virtual Hard Disk)
- VMDK (Virtual Machine Disk)



7. User can select your hard disk size according to your needs Recommend 20~40 GB

Storage on physical hard disk

Please choose whether the new virtual hard disk file should grow as it is used (dynamically allocated) or if it should be created at its maximum size (fixed size).

A **dynamically allocated** hard disk file will only use space on your physical hard disk as it fills up (up to a maximum **fixed size**), although it will not shrink again automatically when space on it is freed.

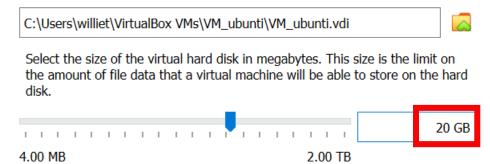
A **fixed size** hard disk file may take longer to create on some systems but is often faster to use.

Dynamically allocated

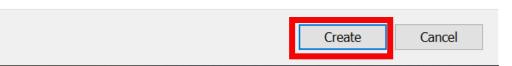
Fixed size

File location and size

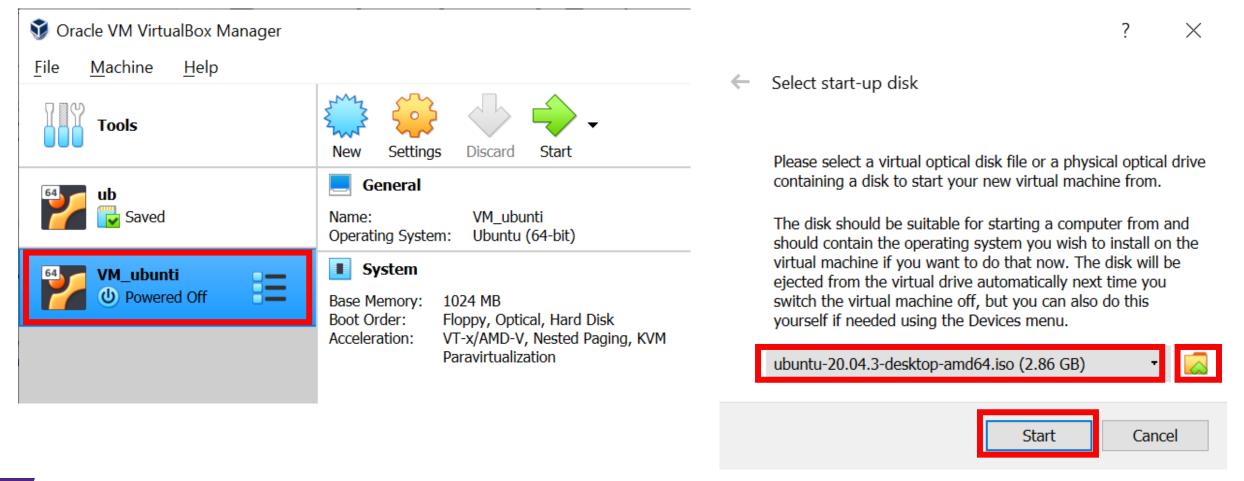
Please type the name of the new virtual hard disk file into the box below or click on the folder icon to select a different folder to create the file in.



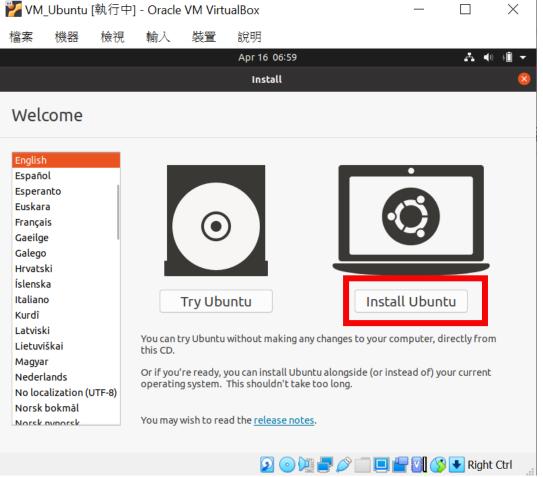




8. Start-up the VM and select the ISO file you download in step 3 (You need to set the user information in first time start-up in step 9)



Select install UbuntuUser can configure other setting accord your habit

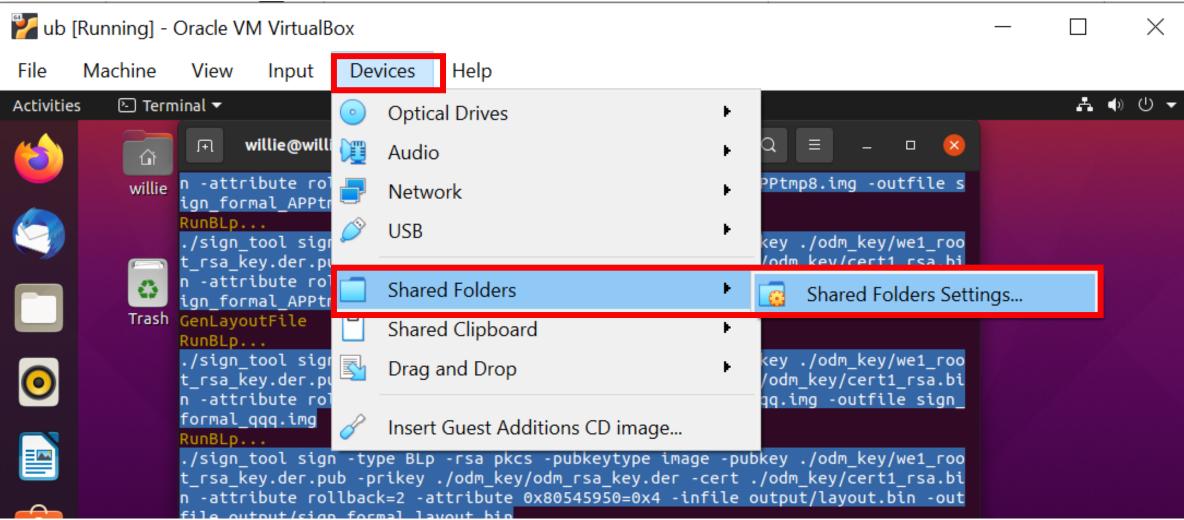


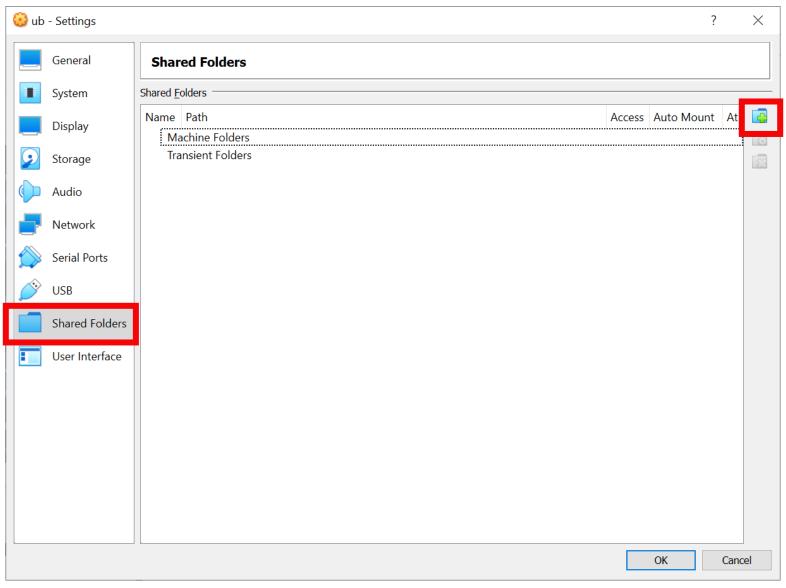
10. After install Ubuntu, open Ubuntu and set the shared folder

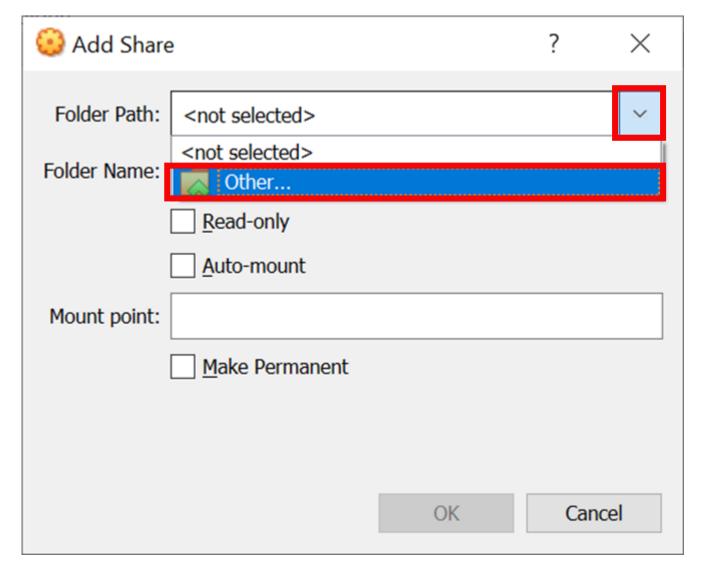
 User usually writes program and compiles in Windows but the image file for programming needs to convert in Ubuntu.
 Therefore, you need to set the shared folder that two development environments can edit and use the same files.

User can also write program and compile in Ubuntu.

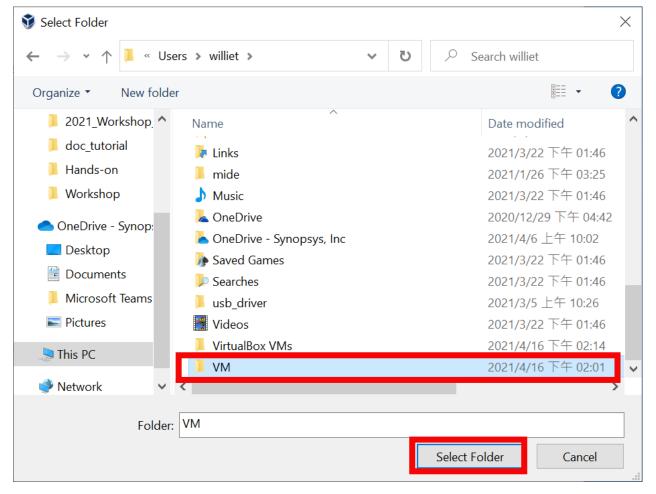
11. Set the folder to be shared

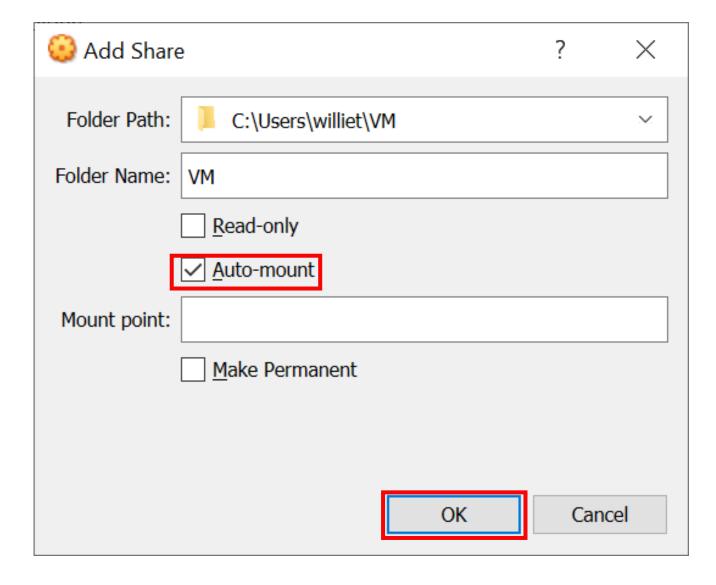


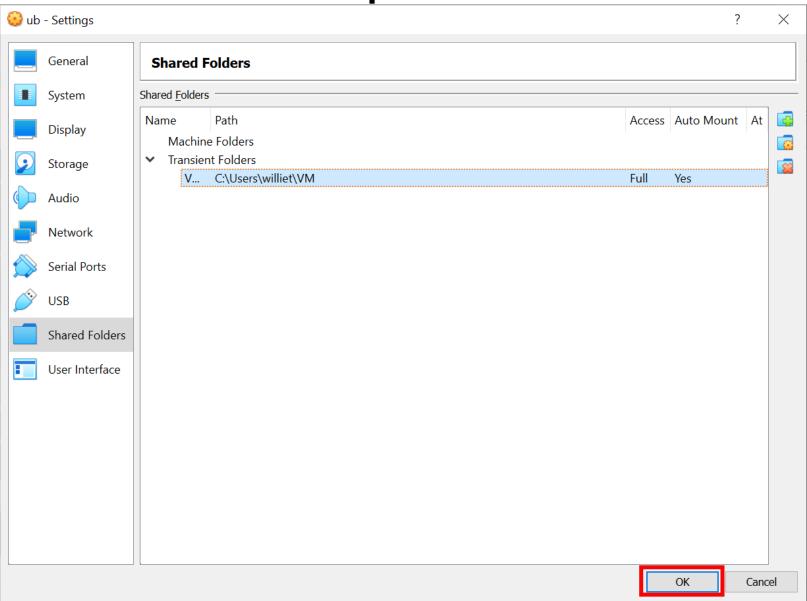




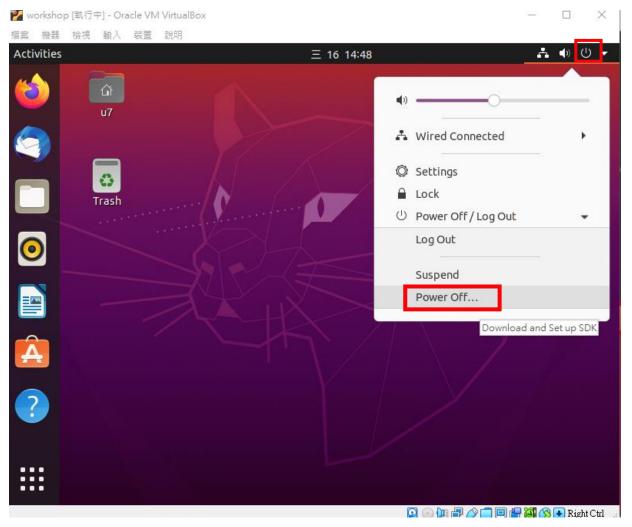
Path "C:\Users\{username}\VM"

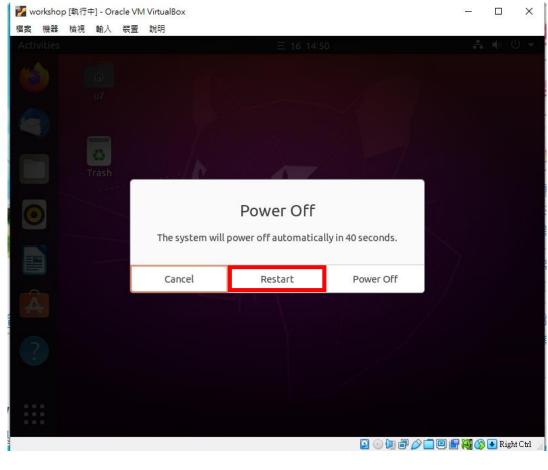




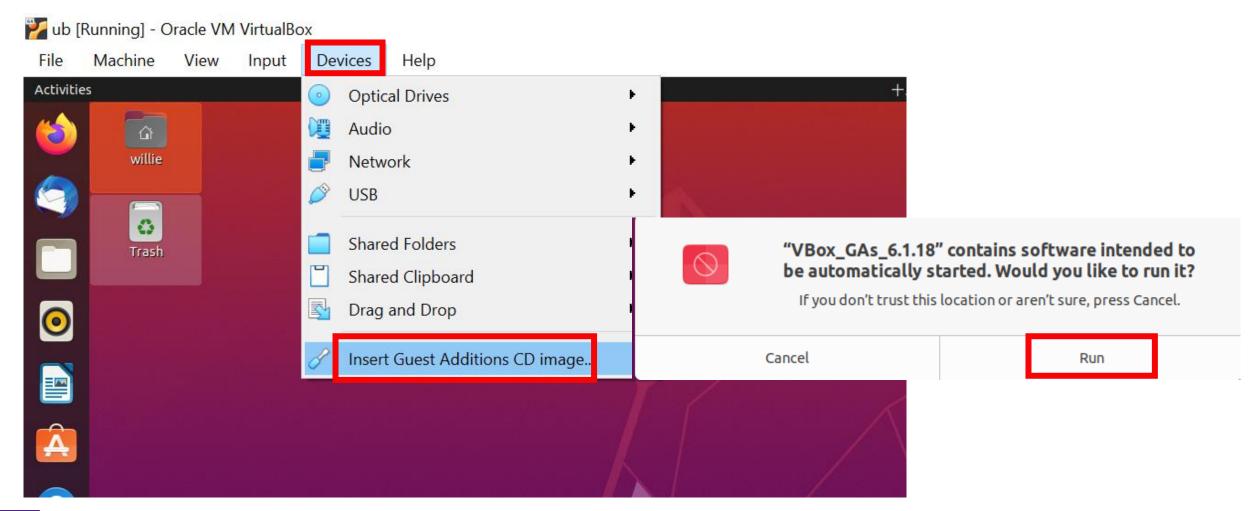


Restart

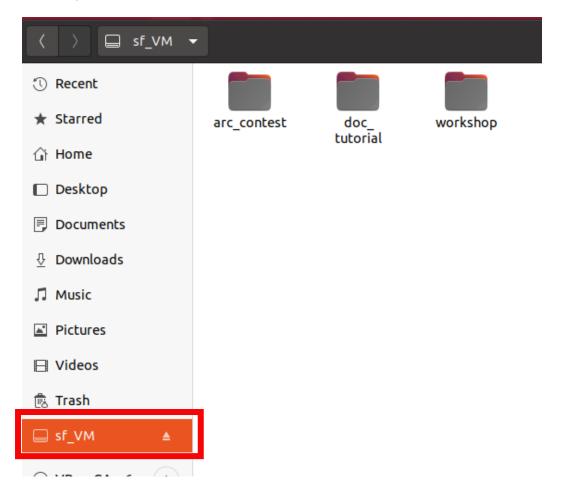




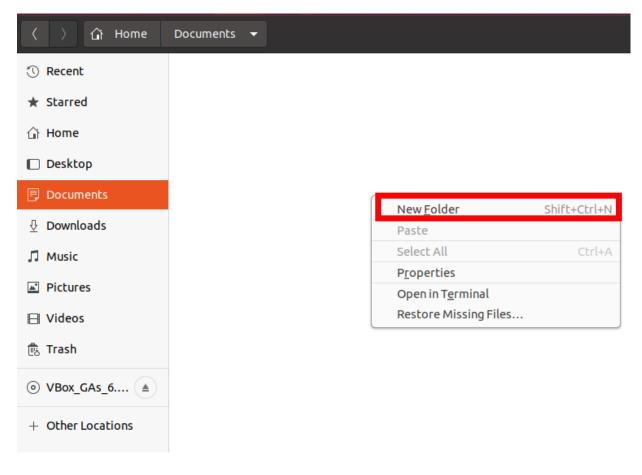
Run Guest Addition CD

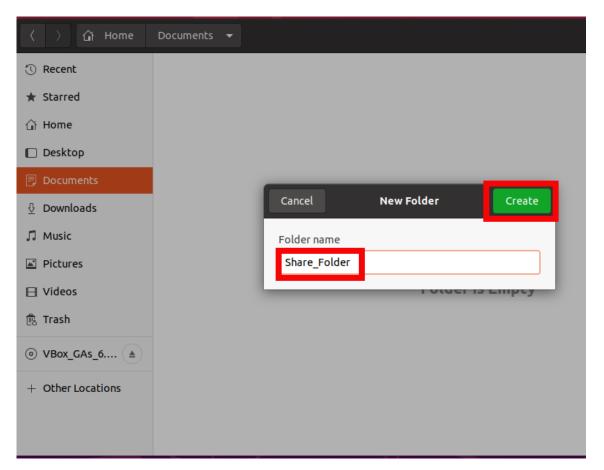


- After you mount, you can see files in Ubuntu "sf_VM" which are also in Windows "C:\Users\{username}\VM"
- If you mount well, go to step 13.
- If shared folder doesn't mount, please run step 12.



12.Create a new folder at "Home/Documents/Share_Folder"





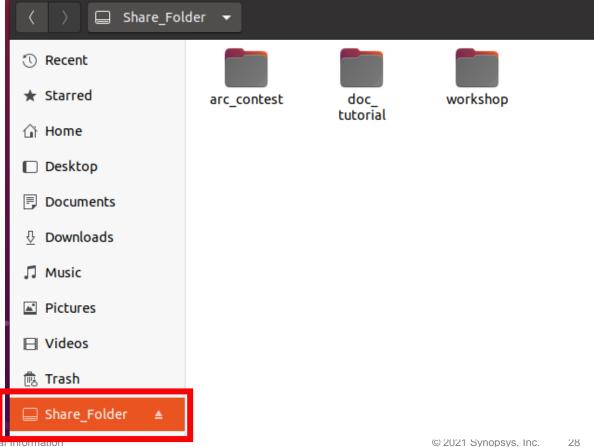
- Open Ubuntu terminal: Ctrl+Alt+t (or right click > Open in Terminal)
- \$ sudo mount -t vboxsf VM /home/{username}/Documents/Share_Folder
- \$ Key-in your user password

```
willie@willie-VirtualBox: ~/Documents/Share_Folder Q = _ □  
willie@willie-VirtualBox: ~/Documents/Share_Folder$ sudo mount -t vboxsf VM /home /willie/Documents/Share_Folder
```

After you mount, you can see files in Ubuntu

"home/{username}/Documents/Share_Folder" which are also in

Windows "C:\Users\{username}\VM"



- 13.Install "make" command
 - \$ sudo apt install make
 - \$ Key-in your user password

```
williet@williet-VirtualBox: /media/sf_VM/arc_contest/Syno...
rilliet@williet-VirtualBox:/media/sf_VM/arc_contest/Synopsys_SDK/Example_Projec
:/Lab4 person detection int8$ make flash
Command 'make' not found, but can be installed with:
sudo apt install make
                            # version 4.2.1-1.2, or
sudo apt install make-quile # version 4.2.1-1.2
villiet@williet-VirtualBox:/media/sf_VM/arc_contest/Synopsys_SDK/Example_Projec
villiet@williet-VirtualBox:/media/sf VM/arc contest/Synopsys SDK/Examwilliet@wi
villiet@williet-VirtualBox:/media/sf VM/arc contest/Synopsys SDK/Example Projec
/Lab4_person_detection_int8$ sudo apt install make
sudo] password for williet:
```

14. Download and setup Linux ARC GNU file (Version: 2019.09)

https://github.com/foss-for-synopsys-dwc-arc-processors/toolchain/releases/tag/arc-2019.09-release

Other supported devices are arc-hs, arc-600 and arc-700.

See section "3.2 Configuring the Ashling GDB Server" in the Ashling Opella-XD ARC User manual for more details.

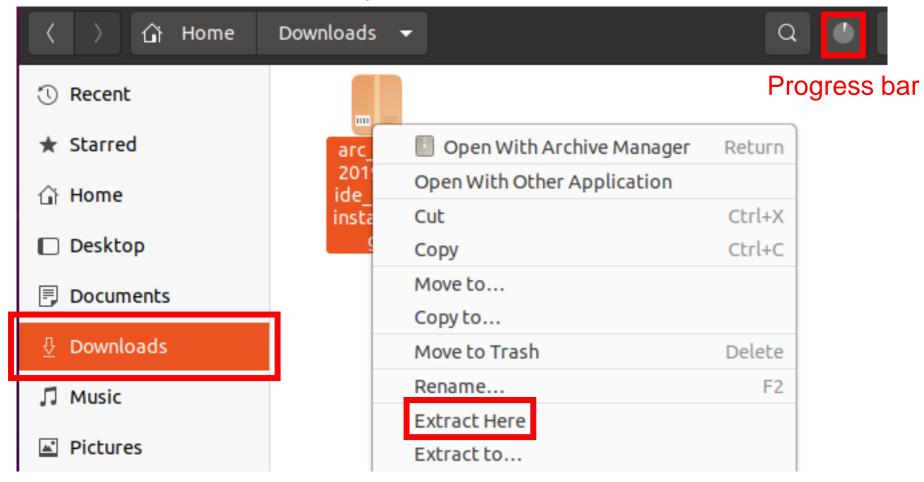
Newlib's libgloss doesn't support RF16 configuration of ARC cores when building for nSIM, see #231.

Please report any problems by filing an Issue in GitHub here.

Note: The toolchain is only supported for 64-bit versions of Linux hosts. 32-bit Linux hosts are not supported.

	Linux x86_64	Windows x86_64	Linux ARC HS	macOS x86_64
Baremetal	Little endian \ Big endian			Little endian \ Big endian
Linux/uClibc ARC700	Little endian \ Big endian			
Linux/uClibc ARC HS	Little endian \ Big endian		Little endian	
Linux/glibc ARC HS	Little endian \ Big endian			
IDE	Download	Download		Download

15.Go to "Downloads" and unzip install package (Right Click > Extract Here)



- 16. After unzip it, open the terminal. (Right Click > Open in Terminal)
 - \$ cd arc_gnu_2019.09_ide_linux_install/bin
 - \$ pwd

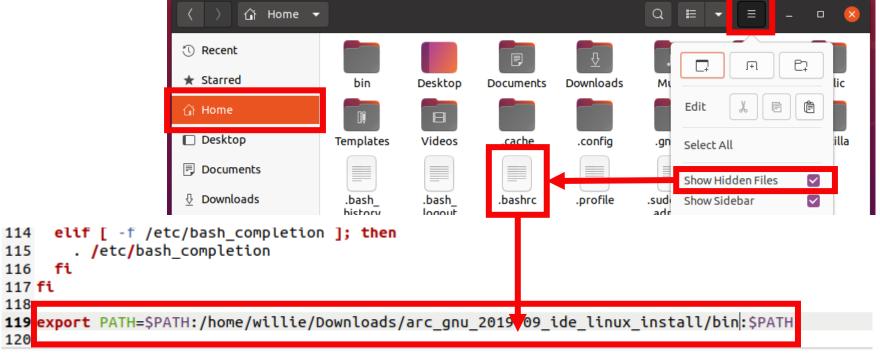
You will get the path and be used in next step.



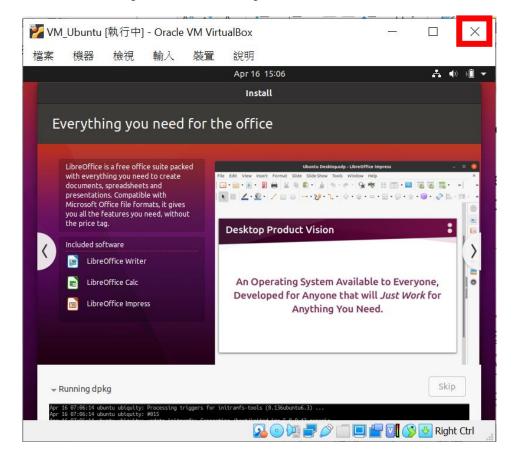
17. Edit "/home/.bashrc" for setting environment variable, add

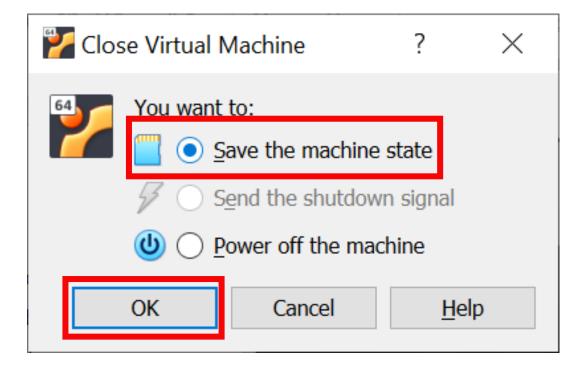
"export PATH=\$PATH:/home/{username}/Downloads/{ARC_GNU_ROOT}/bin:\$PATH" in the last line.

"/home/{username}/Downloads/{ARC_GNU_ROOT}/bin" this path must be the same as last step.



18. Recommend to close your VirtualBox with "Store Computer Stage" If not, you may need to remount shard folder every time.









Program Code & Make Project



Program Code

1. Go into folder

```
"Synopsys_SDK_Vxx/Example Project/Lab4_tflm_person_detect" you will see folder "src" and "inc" "src" folder: always keep your .c and .cpp file in here. "inc" folder: always keep your .h file in here. (c file: c language) (cpp file: c++ language)
```

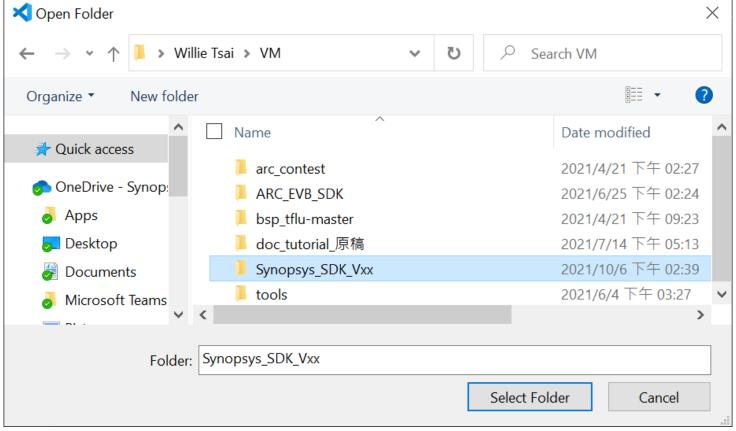
Make Project (By cygwin64)

- 2. Go to your project path in cygwin64 terminal
 - \$ cd c:
 - \$ cd Users/{username}/VM
 - \$ cd Synopsys_SDK_Vxx/Example Project/Lab4_tflm_person_detect
 - \$ make

Make Project (By Visual Studio Code)

3. You can also use Visual Studio Code to make project. Open Folder and Select Folder:

"...../Synopsys_SDK_Vxx"



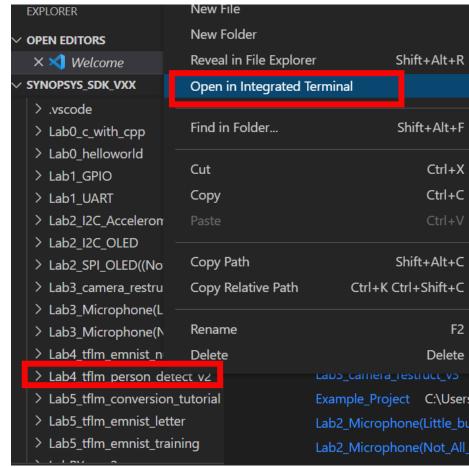
Make Project (By Visual Studio Code)

4. Navigate to

"Synopsys_SDK_Vxx/Example_Project/Lab4_tflm_person_detect"

in VS Code

Right Click "Lab4_tflm_person_detect" > "Open in Integrated Terminal"



Make Project (By Visual Studio Code)

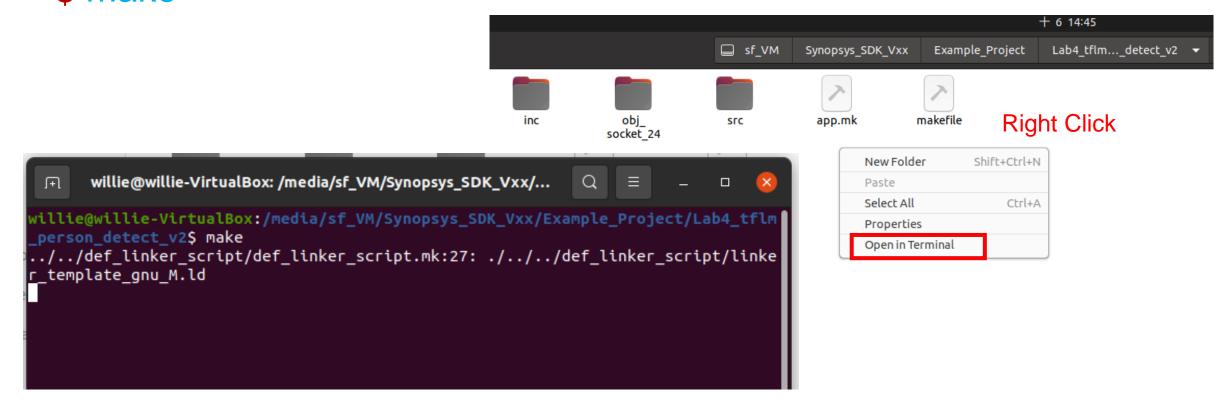
5. You will see terminal block

Type command:

\$ make

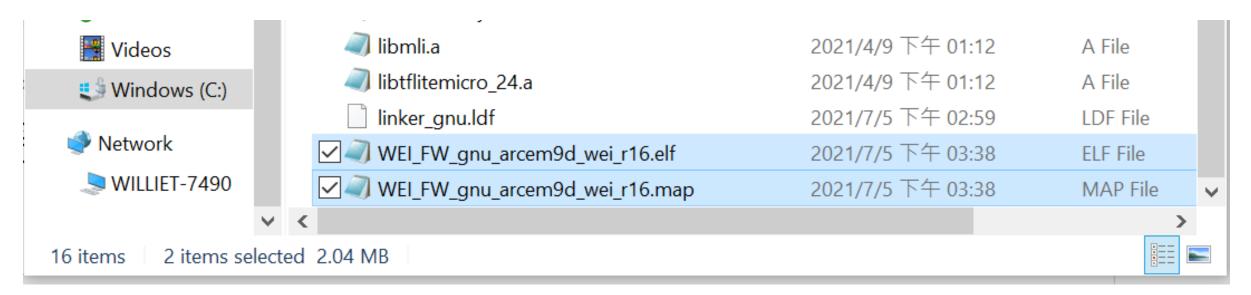
Make Project (By Ubuntu)

6. You can also use Ubuntu to make project Open terminal in VirtualBox Ubuntu, and go to the same path: "/Synopsys_SDK_Vxx/Example_Project/Lab4_tflm_person_detect" \$ make



Make Project

7. After you make project successfully, you will get ".elf" and ".map" file in "Synopsys_SDK_Vxx/Example_Project/Lab4_tflm_person_detect/obj_socket_24/gnu_arcem9d_wei_r16/"



WEI_FW_gnu_arcem9d_wei_r16: elf and map file are made by GNU toolchain WEI_FW_mw_arcem9d_wei_r16: elf and map file are made by Metaware toolchain

Make Project

There are some commands can be used,

- \$ make : compile and link your project with default toolchain.
- \$ make TOOLCHAIN=gnu: compile and link your project with GNU.
- \$ make TOOLCHAIN=mw: compile and link your project with Metaware.
- \$ make clean: remove all .o file of default toolchain of this project
- \$ make boardclean: remove all .o file of all toolchain of this project

You can add a command for changing toolchain (default toolchain is gnu, define in makefile) "TOOLCHAIN=mw": compile with MetaWare toolchain "TOOLCHAIN=gnu": compile with ARC GNU toolchain Please use \$ make boardclean after you change toolchain.

Make Project

\$ make TOOLCHAIN=gnu:

After compile successfully, create elf and map which are named bellow. "./obj_socket_24/gnu_arcem9d_wei_r16/WEI_FW_gnu_arcem9d_wei_r16.elf"

"./obj_socket_24/gnu_arcem9d_wei_r16/WEI_FW_gnu_arcem9d_wei_r16.map"

\$ make TOOLCHAIN=mw:

After compile successfully, create elf and map which are named bellow.

```
"./obj_socket_24/mw_arcem9d_wei_r16/WEI_FW_mw_arcem9d_wei_r16.elf"
"./obj_socket_24/mw_arcem9d_wei_r16/WEI_FW_mw_arcem9d_wei_r16.map"
```

After compile, elf and map files will be copied automatically to "tools/image_gen_cstm/input" and replace previous files automatically.



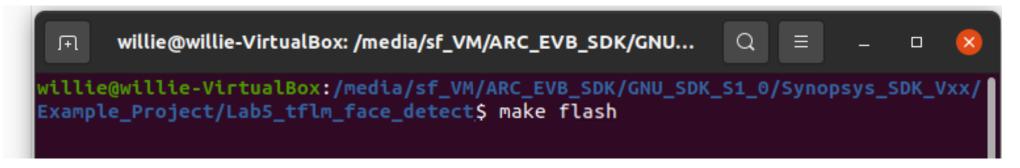


Make Flash File



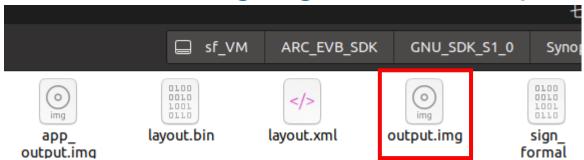
Make GNU Flash File (By image_gen_cstm_gnu)

Open terminal in VirtualBox Ubuntu, and go to the same path:
 "/Synopsys_SDK_Vxx/Example_Project/Lab4_tflm_person_detect"
 \$ make flash



2. Check your ".img" file

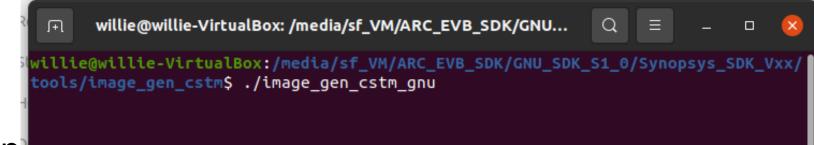
"...../Synopsys_SDK_Vxx/tools/image_gen_cstm/output/output.img"



Make GNU Flash File (By image_gen_cstm_gnu)

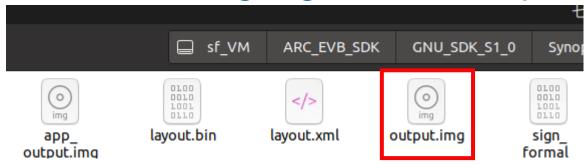
- 1. You can also use "image_gen_cstm_gnu" manually.

 Open terminal in VirtualBox Ubuntu, and go to the same path:
 - "...../Synopsys_SDK_Vxx/tools/image_gen_cstm"
 - \$./image_gen_cstm_gnu



2. Check your img file in

"...../Synopsys_SDK_Vxx/tools/image_gen_cstm/output/output.img"



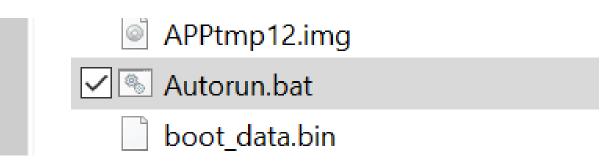
Make Metaware Flash File (By image_gen_cstm)

1. If your toolchain is Metaware, you should use "image_gen_cstm" to convert elf and map file to image file.

Use Windows and go to "Synopsys_SDK_Vxx/tools/image_gen_cstm/" Make sure the files bellow are in your "input" folder.

```
"WEI_FW_mw_arcem9d_wei_r16.elf"
"WEI_FW_mw_arcem9d_wei_r16.map"
(They should be copied automatically after you compiled)
```

2. Execute "Autorun.bat"



Make Metaware Flash File (By image_gen_cstm)

3. Type command "2"

You will get "output.img" in "output" folder after convert successful.

```
C:\WINDOWS\system32\cmd.exe
Please choice one
  Exit
  LQFP128 r16 HX6539-A04TLDG-0001K Debug BLp
  LQFP128 r16 HX6539-A04TLDG-1A11K Debug BLp
  LQFP128 r16 HX6539-A04TLDG-1111K Debug BLp
  QFN72 r16 HX6537-A09TDIG-1A11K Debug BLp
  QFN72 r16 HX6537-A09TDIG-1111K Debug BLp
  LQFP128 r16 HX6539-A04TLDG-1A11S Debug BLw
  LQFP128 r16 HX6539-A04TLDG-1111S Debug BLw
  QFN72 r16 HX6537-A09TDIG-1A11S Debug BLw
   QFN72 r16 HX6537-A09TDIG-1111S Debug BLw
    WLCSP38 r16 HX6540-A01TWA-1111S Debug BLw
    None, Create new list(DOTO NOT Finish yet)
Load setting txt Success - 11 lines.
EnterVar:2
```

```
signtool_odm.exe sign -type BLp -rsa pkcs
 rsa_key.der -cert odm_key\cert1_rsa.bin
ormal_qqq.img
signtool_odm.exe sign -type BLp -rsa pkcs
 rsa_key.der -cert odm_key\cert1_rsa.bin
 le output¥sign_formal_layout.bin
ReorderXML
GenWholeImage
      image size= 699 KB( Oxaee30 )
Generate Image Done
output¥app_output.img
output¥output.img
        2 file(s) copied.
output¥layout.bin
output¥sign_formal_layout.bin
        2 file(s) copied.
```







ARC EM9D AloT DK Project Development Flow

TensorFlow Model Development

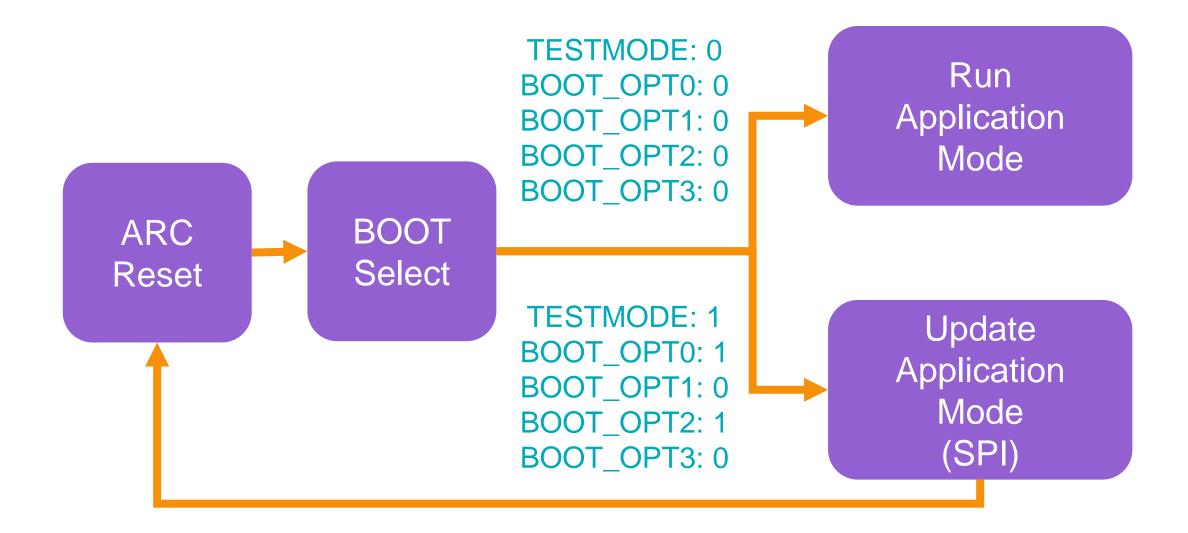
Firmware Development

Download img file

Run / Update Application On ARC EM9D AloT DK

Debug

Stage	TensorFlow Model Development	Firmware Development	Run / Update Application On ARC EM9D AloT DK		
Tool	Anaconda Cygwin	Cygwin Metaware or ARC GNU VirtualBox (Ubuntu 20.04)	JTAG Himax-FT4222-GUI USB Cable		
Language	Python 3	C language C++ language			



1. Always modify SW1 as bellow

2. Short J20 and J11 for update application mode

TESTMODE: 1

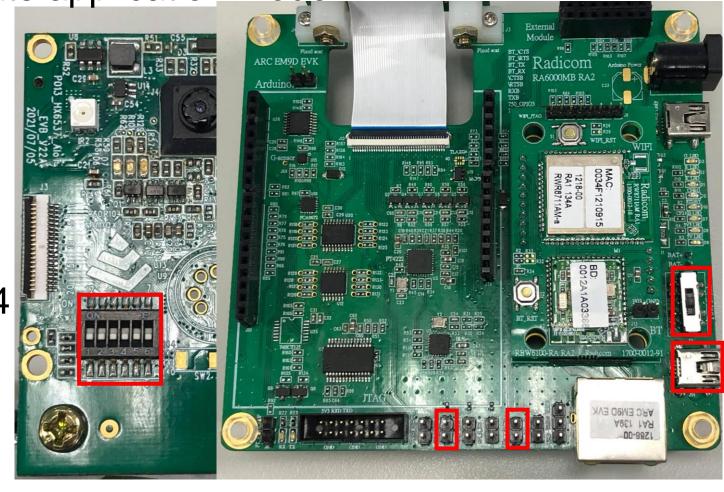
BOOT OPT0: 1

BOOT_OPT1: 0

BOOT OPT2: 1

BOOT OPT3:0

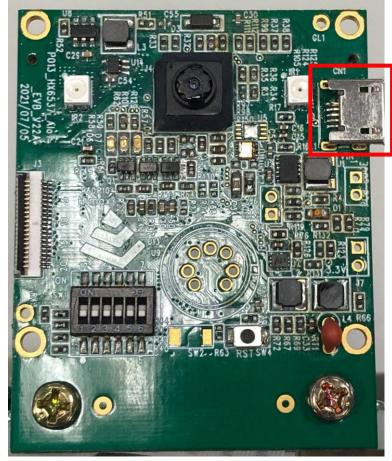
- 3. Turn power by S1
- 4. Connect USB Cable to J14



5. Connect USB cable to CN1 for CPU board

- This step is for some USB power source is lower than 5V.

- If only connect USB cable to extension board, CPU may have fault during program flash.

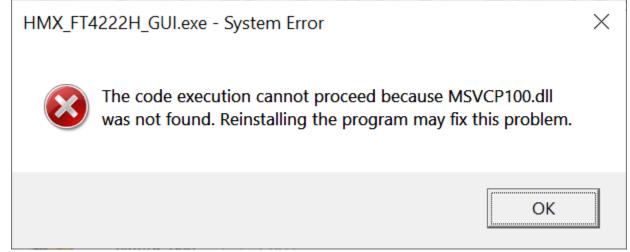


6. Execute "HMX_FT4222H_GUI.exe" in "Synopsys_SDK_Vxx/tools/HMX_FT4222H_GUI/"

If you have a warning message as left bellow, make sure the ARC EM9D AloT DK has been connected to PC, and USB device in PC is mounted.

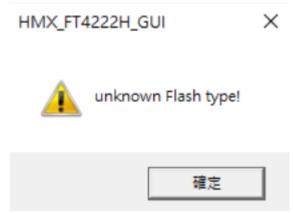
If USB device can't find correct driver, please refer to Appendix-2&3. If your have DLL error as right bellow, please refer to Appendix-4.

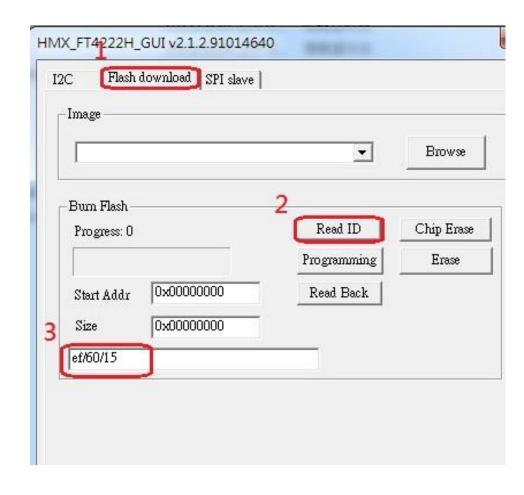




7. Select "Flash download" > "Read ID"
You will get "ef/60/15" if MCU is ready.
If the ID is incorrect or shows "unknow
Flash type!", make sure your bootsignals are right.

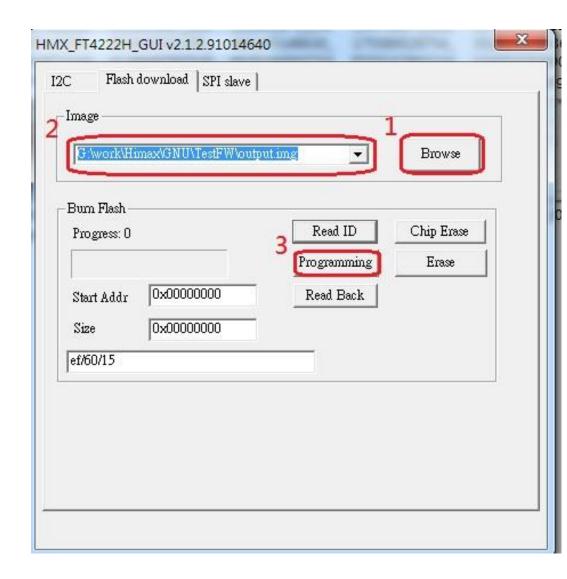
After that, you can power-up and press reset switch, then close and execute "HMX_FT4222H_GUI.exe" again.





- 8. Click "Browse" to select your image file and "Programming".
- 9. After programming finish, open J20 for running application.

```
TESTMODE: 0
BOOT_OPT0: 0
BOOT_OPT1: 0
BOOT_OPT2: 0
BOOT_OPT3: 0
```



1. Short J20 and J11 for update mode, open J20 for run mode

2. Press reset button SW4. MCU will start to run the application.

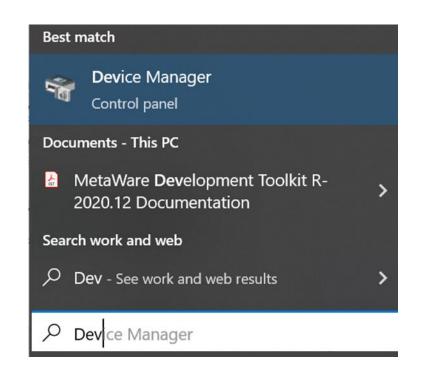
If MCU doesn't boot-up, make sure your boot-signals are set right,

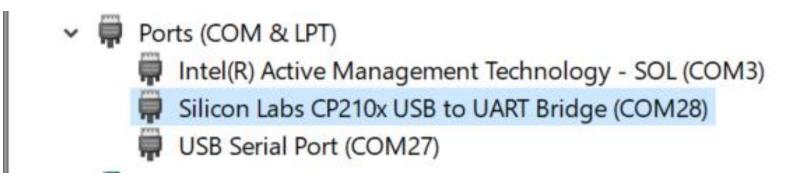
or you can try to power-up again.



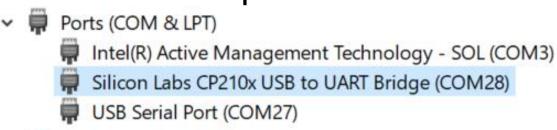


- You can also use USB VCP to receive package from ARC EM9D AloT DK.
- 4. Connect ARC EM9D AloT DK and PC by USB Cable





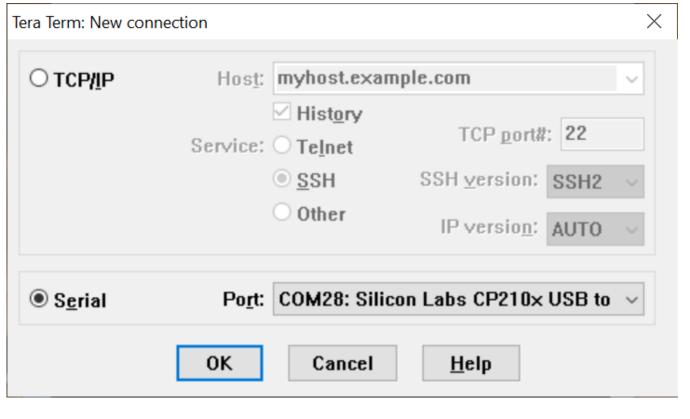
- 5. Check your ARC EM9D AloT DK USB port number We have 2 USB ports on ARC EM9D AloT DK. Uart0_CP2102: Silicon Labs CP210x USB to UART Bridge (COMx) Uart1_FT232: USB Serial Port (COMx) (If USB Serial Port is not shown here, please refer to Appendix-2 and Appendix-3)
- Device Manager> Ports(COM & LPT) > CP210x USB to UART (COMx)
 - x: This is your ARC EM9D AloT DK USB port number Please choose the USB serial port with CP2102 by default.

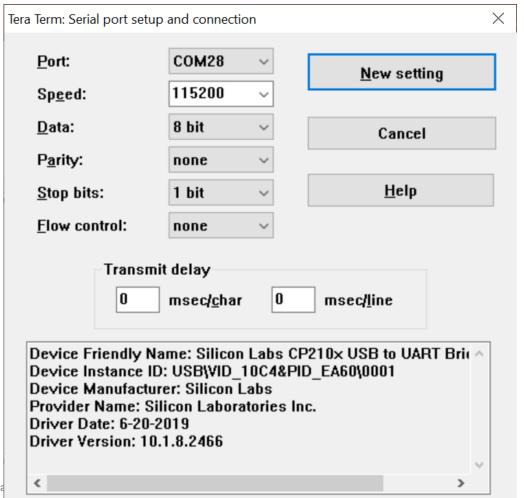


7. Open tera term and select "COMx: CP210x USB to UART (COMx)

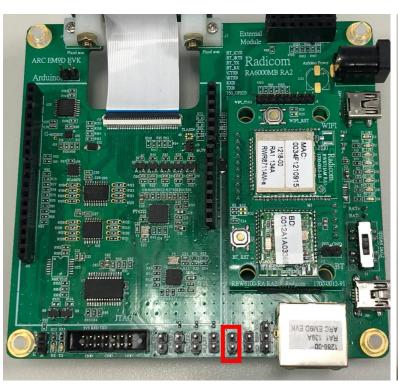
8. Tera term "Setup" > "Serial Port" > Change Baud to 115200

No need to change other settings.





- 9. Open J20 for run mode
- 10. Press reset button SW4. MCU will reset and run the application





```
Himax HEI Boot loader
енbARC Build Tine: Jan 42021, 13:44:14
Compiler Version: Metaµare, 4.2.1 Compatible Clang 8.0.1
Boot loader Version : 1.4.4 (Date:Jan 42021)
chip version : Ox8535a1
cpu speed : 4000000000 hz
spi speed : 50000000 hz
Hake up evt:4
 ...secure lib version = 352380df9a347b1187d2361bfcd4455178a1ebcb
1st APPLICATION addr[3]=21000 (наin-1966)
Bootloader Done !!!!!!
jump to app FH : 0x10000004
12 bytes lost due to alignment. To avoid this loss, please make su
HMO36O RevB,C,D Config
person score:-2 no person score 2
person score:-6 no person score 6
```





Appendix-2: Troubleshooting - FTDI VCP Driver

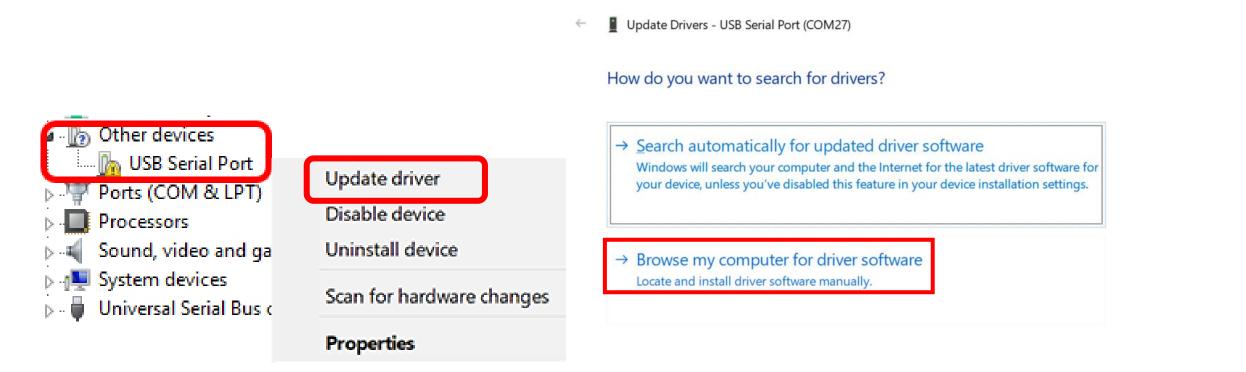


If the USB serial port is not shown in Ports (COM & LPT):

- Download VCP driver: https://ftdichip.com/drivers/vcp-drivers/
 Select Windows/X64 version
- 2. Unzip the downloaded file (CDM v2.XX.XX WHQL Certified)

				Proces	ssor Architecture	
Operating System	Release Date	X86 (32-Bit)	X64 (64-Bit)	PPC	ARM	MIPSII
Windows*	2017- 08-30	2.12.28	2.12.28	_	_	_
Linux	-	-	=	-	-	-

- 3. Click Device Manager > Other devices > USB Serial Port > Update driver
- 4. Choose "Browse my computer for driver software"



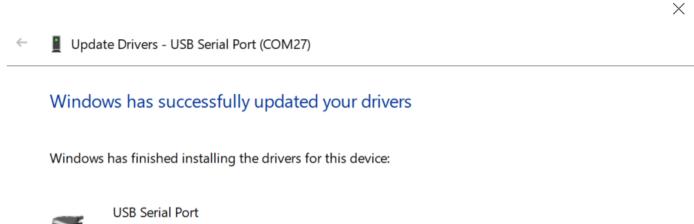
X

X Update Drivers - USB Serial Port (COM27) Browse for drivers on your computer 5 Choose the downloaded folder (CDM v2.XX.XX WHQL Certified) Search for drivers in this location: :\Users\williet\Downloads\CDM v2.12.36.4 WHQL Certified Browse.. nclude subfolders Select → Let me pick from a list of available drivers on my computer This list will show available drivers compatible with the device, and all drivers in the same category as the device.

Cancel

<u>N</u>ext

8. Finish









Appendix-3: Troubleshooting - CP2102 VCP Driver



If the USB serial port is not shown in Ports (COM & LPT):

1. Download VCP driver:

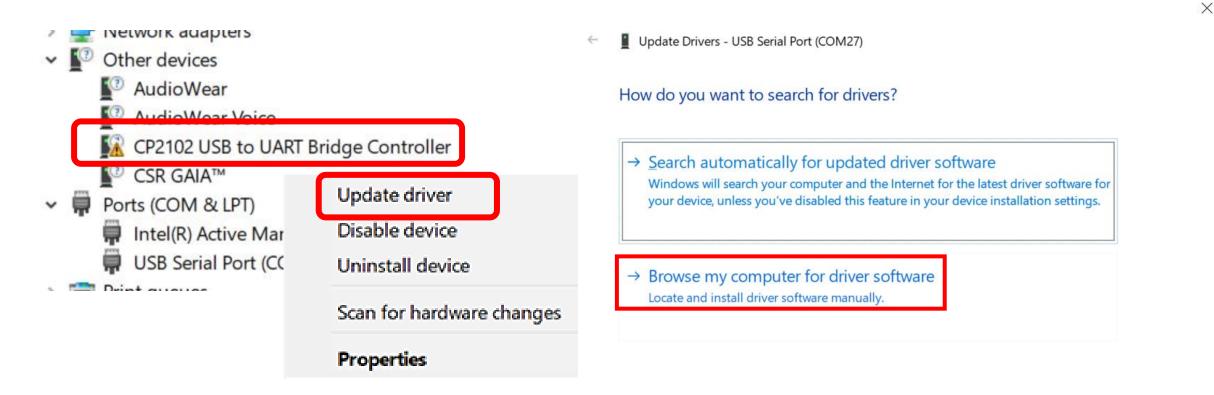
https://www.silabs.com/developers/usb-to-uart-bridge-vcp-drivers
Select CP210x VCP Windows version

2. Unzip the downloaded file (CP210x_VCP_Windows)
Software · 10

CP210x Universal Windows Driver	v11.1.0 3/22/2022
CP210x VCP Mac OSX Driver	v6.0.2 10/27/2021
CP210x Windows Drivers	v6.7.6 9/4/2020
CP210x Windows Drivers with Serial Enumerator	v6.7.6 9/4/2020
CP210x_5x_AppNote_Archive	9/4/2020

Show 5 more Software

- 3. Click Other devices > USB Serial Port > Update driver
- 4. Choose "Browse my computer for driver software"



Update Drivers - CP2102 USB to UART Bridge Controller

Browse for drivers on your computer 5. Choose the downloaded folder (CP210x_VCP_Windows)

Search for drivers in this location:

C:\Users\williet\Downloads\CP210x_VCP_Windows

Include subfolders

6. Select

→ Let me pick from a list of available drivers on my computer

This list will show available drivers compatible with the device, and all drivers in the same category as the device.

7.

<u>N</u>ext Cancel

8. Finish

← 📱

■ Update Drivers - Silicon Labs CP210x USB to UART Bridge (COM28)

The best drivers for your device are already installed

Windows has determined that the best driver for this device is already installed. There may be better drivers on Windows Update or on the device manufacturer's website.



Silicon Labs CP210x USB to UART Bridge

→ Search for updated drivers on Windows Update

<u>C</u>lose





Appendix-4: Troubleshooting – HMX_FT4222H_GUI.exe DLL File Missing



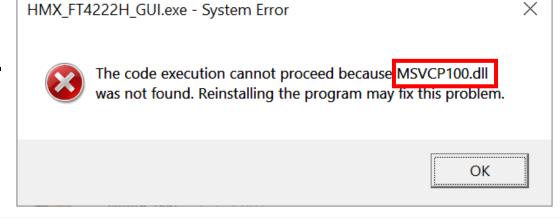
Troubleshooting – DLL File Missing

If the DLL file missing is ftd2xx.dll

 Download FTDI D2XX Drivers install it. <u>https://ftdichip.com/drivers/d2xx-drivers/</u>

Currently Supported D2XX Drivers:

Subscribe to Our Driver Updates



		Processor Architecture					
Operating System	Release Date	X86 (32-Bit)	X64 (64-Bit)	ARM	MIPS	SH4	Comments
Windows (Desktop)*	2021-07-15	2.12.36.4	2.12.36.4	2.12.36.4A****	_	-	WHQL Certified. Includes VCP and D2XX. Available as a setup executable Please see the Release Notes and Installation Guides.
Windows (Universal)****	2021-11-12	2.12.36.4U	2.12.36.4U	_	_	-	WHQL Certified. Includes VCP and D2XX.

Troubleshooting – DLL File Missing

If the DLL file missing is MSVCP100.dll or MSCR100.dll

- Download Microsoft Visual C++ 2010 and install it.
 <u>https://www.microsoft.com/en-us/download/details.aspx?id=26999</u>

 If the DLL file missing is MFC140.dll
- Download Microsoft Visual C++ 2015 and install it.
 https://www.microsoft.com/en-us/download/details.aspx?id=48145

 If missing other DLL files, please search on the internet

