Digital Integrated Circuits

#Home Work 8   **2024.06.05 (Due:2024.06.12, 15:30)**

1. Clock system is the most power hunger part of digital IC. Thus, dynamic clock gating is commonly used in digital IC to reduce power consumption
   (1) There are two methods, Gated-clock&Bypass-data and Bypass-clock&Gated data. Please describe the operation of these two methods and indicate the major difference of these two methods (Use the circuits shown at p62-p66 of the ADIC_LP text). (50%)
   (2) Describe the detail design concept and behavior of gating logic-2 to avoid glitch mentioned at p66 of the ADIC_LP text. (50%)