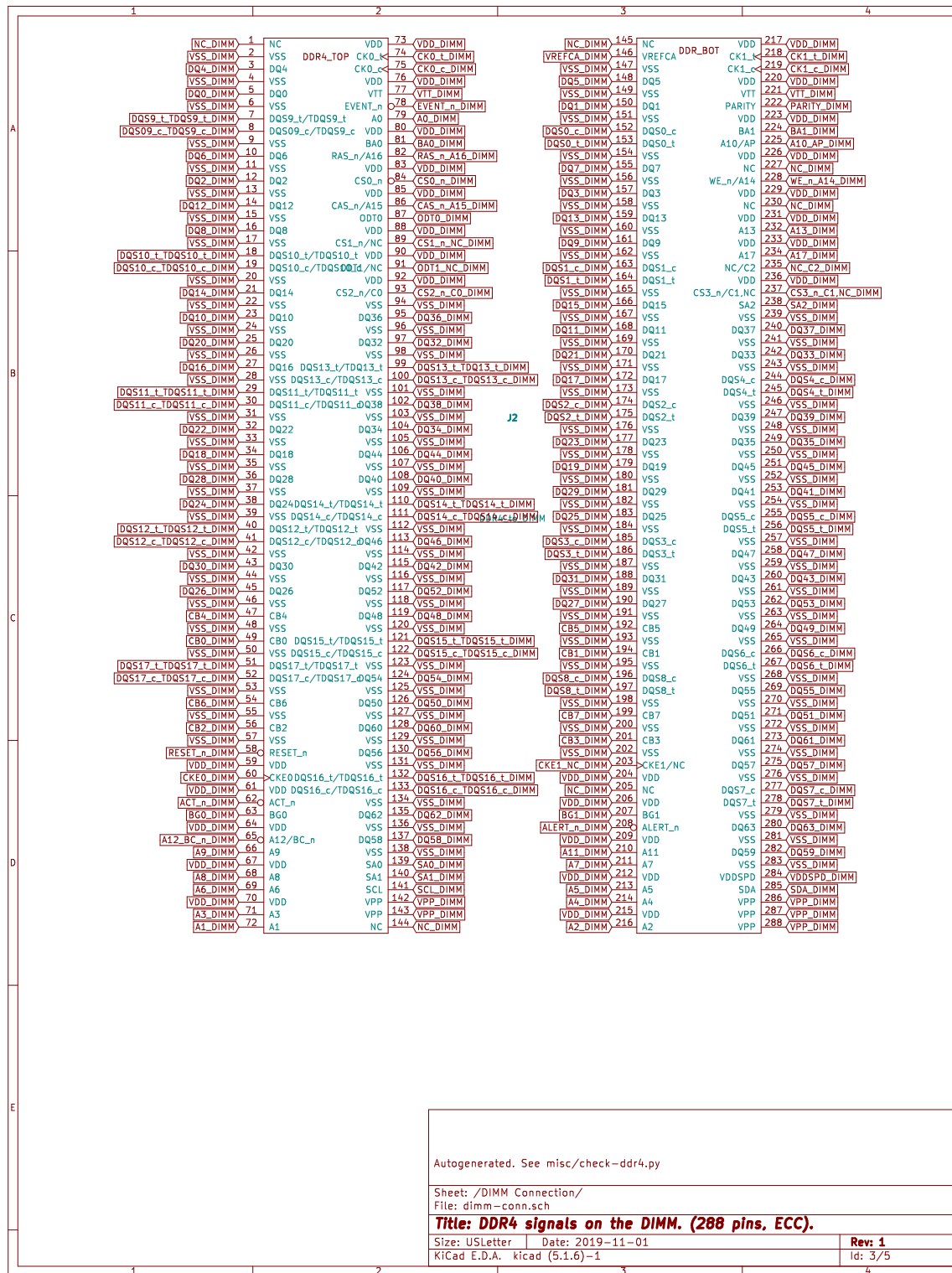


1	2	3	4	5							
A	<div>Sheet: Mother Board Connection</div> <div>File: mobo-conn.sch</div>		<div>Sheet: Interposer Control</div> <div>File: interposer.sch</div>		<div>Sheet: DIMM Connection</div> <div>File: dimm-conn.sch</div>						
B	<div>Sheet: One To One Connections</div> <div>File: one-to-one-conns.sch</div>										
C											
D	<div>The most interesting bits are in the "Interposer Control"</div> <div>Sheet: / File: fault-injector-3.sch</div> <div>Title: <b>Prototype for fault injector for DDR4 bus.</b></div> <table><tr><td>Size: USLetter</td><td>Date:</td><td>Rev: <b>1</b></td></tr><tr><td colspan="2">KiCad E.D.A. kicad (5.1.6)-1</td><td>Id: 1/5</td></tr></table>					Size: USLetter	Date:	Rev: <b>1</b>	KiCad E.D.A. kicad (5.1.6)-1		Id: 1/5
Size: USLetter	Date:	Rev: <b>1</b>									
KiCad E.D.A. kicad (5.1.6)-1		Id: 1/5									
1	2	3	4	5							

[illegible]



Autogenerated. See misc/check-ddr4.py

Sheet: /DIMM Connection/  
File: dimm-conn.sch

**Title: DDR4 signals on the DIMM. (288 pins, ECC).**

Size: USLetter Date: 2019-11-01  
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Rev: 1  
Id: 3/5

