

## Introduction

In the second lab on op-amps, we take a look at some new features since the last lab—some of which can be very useful for building new circuits, some of which have drawbacks or impose limits on normal operation of op-amps. We also take a look at the ways that positive feedback changes the behavior of a circuit, in comparison to the negative feedback we used in Lab 6.

## 1 Slew Rate

### 1.1 Experimental

Op-amps have a property called the *slew rate*, which defines how fast an amplifier can react to sudden changes in their input signal. This is significant because it essentially places a limit on how fast our input signal can cycle before the output signal looks significantly different than we expect.

By building a simple circuit around an op-amp, we're able to see this distortion very clearly. We're also able to measure the slew rate of the op-amp by inputting a square wave (the most abrupt signal changes we could possibly make) and measuring the change of the output signal with respect to time in regions where the input signal changes. This measurement of the slope of our output signal, and the literature value for the slew rate, come in units of  $V/\mu s$ .

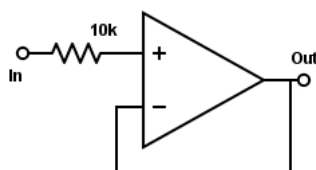


Figure 1: A simple op-amp circuit with negative feedback. The output of this signal demonstrates the distortion caused by the slew rate of the amplifier.

### 1.2 Results

Using an LM-741 op-amp, and applying a 3 V peak-to-peak square wave at 10 kHz, we recorded the output signal shown in figure 2.

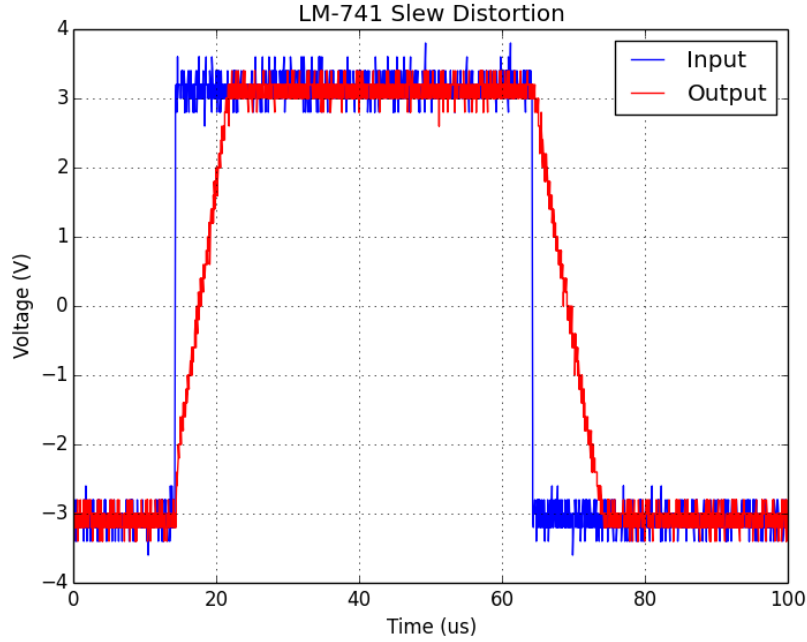


Figure 2: Slew distortion of circuit 1, using an LM-741 amplifier.

We find that the slew rate on the rising edge is about  $0.8 \text{ V}/\mu\text{s}$ , and on the falling side is about  $0.6 \text{ V}/\mu\text{s}$ . Both values are reasonably close to the literature value <sup>1</sup>  $0.5 \text{ V}/\mu\text{s}$ .

For the same device, we also applied a sine wave input for comparison. At  $10 \text{ kHz}$  we were unable to see any distortion for any input voltage—however, we began to see distortion at  $3 V_{pp}$  near  $50 \text{ kHz}$ . It seems reasonable that we had to increase input frequency by a factor in order to see distortion effects on a sine wave, considering the rate of change of a sine wave is much lower than that of a square wave.

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<sup>1</sup>Found in the datasheet at <http://www.ti.com/lit/ds/symlink/lm741.pdf>

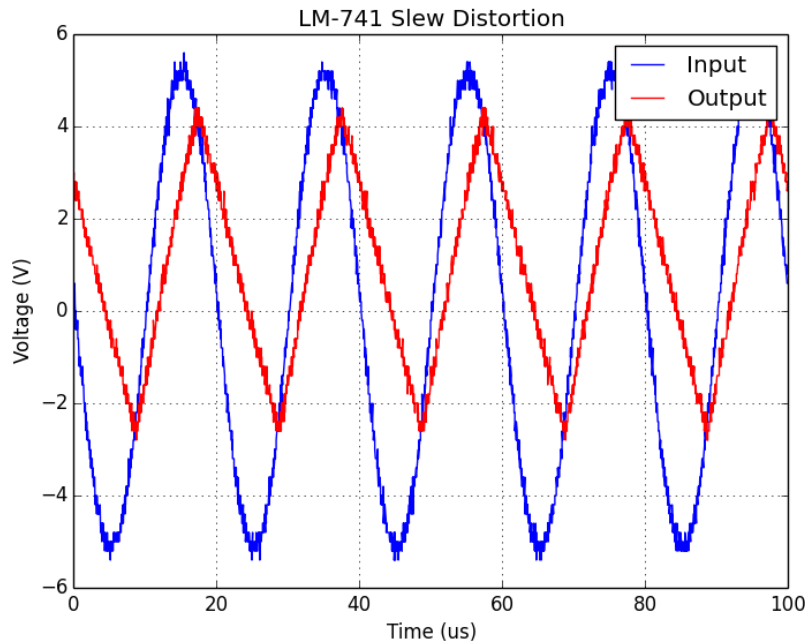


Figure 3: Slew distortion of circuit 1, using an LM-741 amplifier.

For the rising and falling edges of this output signal, we found the slew rate to be about  $0.6 \text{ V}/\mu\text{s}$ , closer to the literature value.

Using a TL-071 amplifier in the same circuit, we applied the same square wave input as above and found the slew rate to be  $12 \text{ V}/\mu\text{s}$ , compared to the  $13 \text{ V}/\mu\text{s}$  literature value <sup>2</sup>. For this amplifier, we had to increase the input frequency to around  $900 \text{ kHz}$  before we saw similar distortion on a sine wave—and found the slew rate there to be about  $8 \text{ V}/\mu\text{s}$ , which is given as the minimum slew rate in literature.

Because the rate of change of a sine wave is smaller than that of a square wave, it made sense for us to have increased the input frequency by a small factor for the first amplifier. Changing the frequency by an order of magnitude for the second amplifier isn't quite as reasonable—there is probably some other factor that is affecting the frequency-dependence of the slew rate, but I haven't been able to identify it.

## 2 Offset Voltage

### 2.1 Results

One of the features of op-amps is the *offset voltage*. This DC voltage determines the default offset between the two input voltages, and can be adjusted by applying a DC signal to two of the pins

<sup>2</sup>Found in the datasheet at <http://www.ti.com/lit/ds/symlink/tl072a.pdf>

on the amplifier.

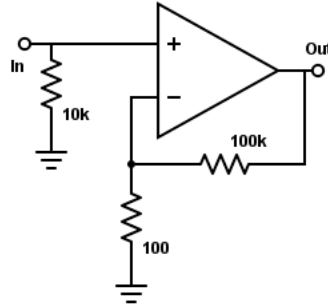


Figure 4: This non-inverting amplifier circuit has a gain of 1000. With the input grounded, we can measure the DC offset voltage of the op-amp.

Circuit 4, with the input grounded, can be used to measure the offset voltage of the amplifier. Because there is zero voltage at the non-inverting input, and some offset voltage between that and the inverting input, the circuit will output the offset voltage multiplied by the circuit gain.

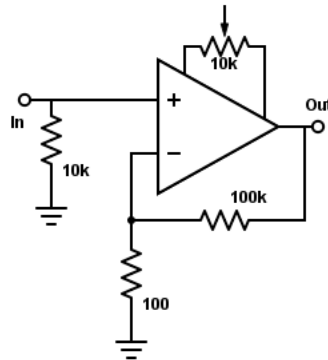


Figure 5: The addition to the circuit at pins 1 and 5 on the op-amp is very simple, and allows us to adjust the DC offset voltage easily.

By adding applying a DC signal to pins 1 and 5 of the op-amp through a potentiometer, we're able to adjust or "trim" the offset voltage.

## 2.2 Results

With the input of the circuit grounded, we were able to measure a DC signal around 800  $mV$ , which translates to an offset voltage of about 0.8  $mV$  considering the circuit gain of 1000. According to

the datasheet for the LM-741 amplifier, the offset voltage is typically  $1\text{ mV}$ .

The  $10\text{ k}\Omega$  potentiometer was not too sensitive for trimming this circuit, and we were able to adjust it so that the circuit output was very near zero with the input grounded.

## 3 Integrator

### 3.1 Experimental

As we did in earlier labs with passive elements, op-amps can also be used to build circuits that integrate or differentiate their input signals. These circuits have a number of applications already, and the use of an active element gives us extra functionality.

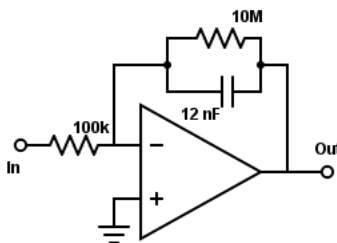


Figure 6: An integrator circuit that employs active and passive elements, including an op-amp.

In this part of the lab, we observe the resulting signal of different kinds of input, and use those to analyze the circuit's behavior as an integrator.

### 3.2 Results

Driving the circuit with a  $1\text{ V}$  peak-to-peak square wave at  $1\text{ kHz}$  resulted in a triangle wave output, with a much smaller amplitude (suggesting gain  $\beta < 1$ ) and a significant DC offset<sup>3</sup>. The type of output signal matches what we would expect from an integrator—regions of a triangle wave have constant rate of change, represented by the constant values of a square wave.

This integrating behavior was consistent for similar sine and triangle inputs as well.

Adjusting the DC offset of the input signal (via our function generator) also changed the DC offset of the output signal, with a noticeable ( $> 1$ ) gain, opposite the direction of the input offset change (positive offset of the input signal resulted in negative offset of the output signal).

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<sup>3</sup>Note: We removed the offset trimming circuitry from the previous circuit while building the circuit for this part of the lab, so the offset voltage of the amplifier could have played some part in the DC offset of this signal.

With the  $10\text{ M}\Omega$  resistor removed from the circuit, we noted a much larger gain in the circuit. Same as before, the DC offsets were inverse of each other; but, this time the DC gain appeared to be about 1.

This DC offset is the result of charge building up in the capacitor—it charges to a constant DC voltage, which offsets the AC signal passing through the  $10\text{ M}\Omega$  resistor. The DC gain that we noticed is likely a relationship between the impedances of the resistor and capacitor—and, naturally, the output offset moves opposite the input offset because we are driving the inverting input.

In the first circuit, the resistor has a much lower impedance than the capacitor, and prevents a large voltage from building up on the capacitor. Without the resistor, the voltage across the capacitor is directly related to the input signal and thus the DC gain appears to be about 1.