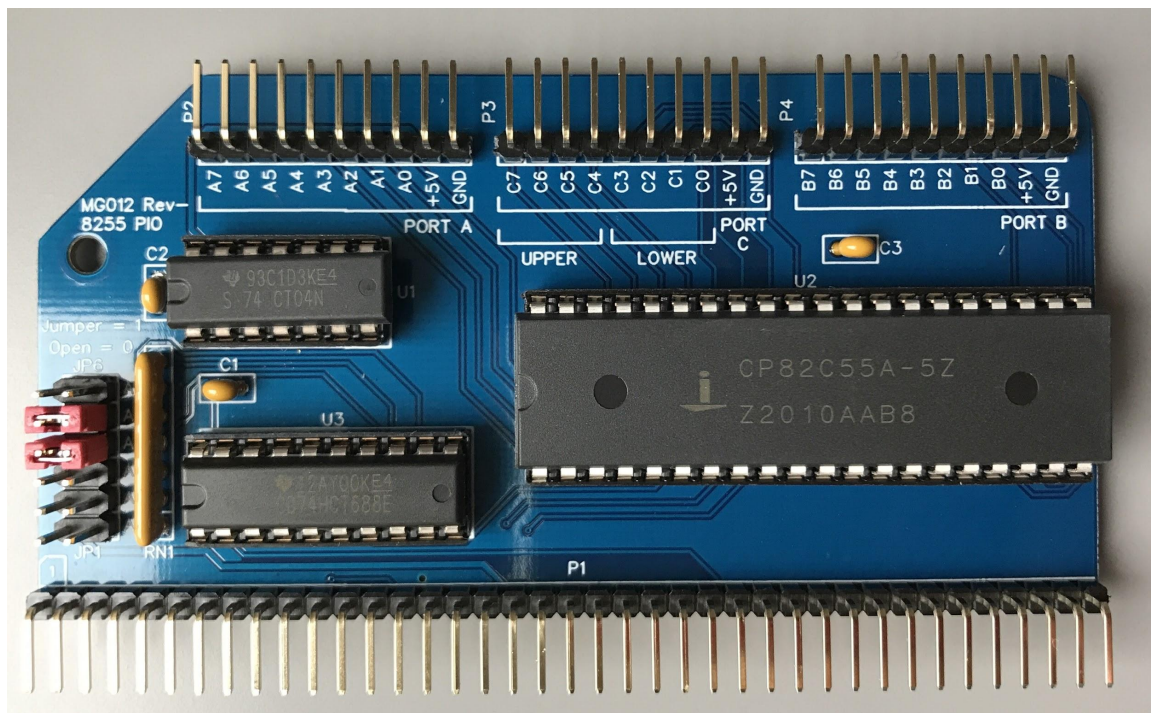


MG012 Programmable Input/Output



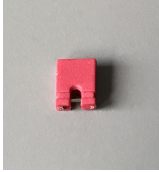
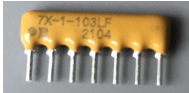
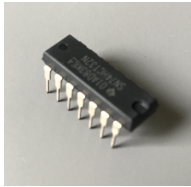

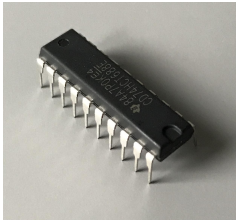
What is it?


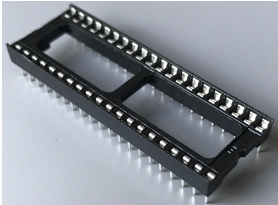
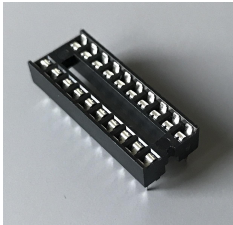
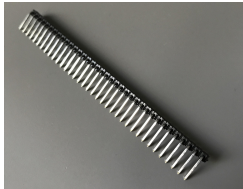
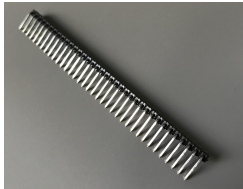
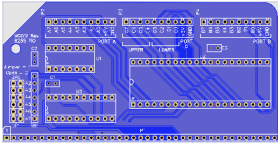
MG012 is a Programmable Input/Output (PIO) designed for RC2014. It uses the 82C55 Programmable Peripheral Interface to provide 24 I/O pins, which can be flexibly configured as input or output.

MG012 can be addressed using standard BASIC input and output commands, with one address being used for a control word, and three more for three groups (A, B and C) of eight I/O pins.



What's in the kit?

Name	Quantity	Description	Picture	Present?
C1-3	3	Capacitor, ceramic, 100 nF		,
JP1-6	1	Header, male, 2 x 6 pin, straight		,
JP1-6 Shunt	6	Jumper shunt		,
RN1	1	Fixed Network Resistor, 10 kohm		,
U1	1	74HCT04		,
U2	1	82C55A (option for buyer to supply)		,
U3	1	74HCT688		,

U1 socket	1	14-pin DIP socket		,
U2 Socket	1	40-pin DIP socket		
U3 socket	1	20-pin DIP socket		,
P1	1	Pin Header, Right Angle		,
P2-4	1	Pin Header, Right Angle (See "how do I build it")		,
PCB	1	MG012 PCB		,

How do I build it?

There's a good chance you will have some soldering experience, as you're likely to have built an RC2014 or equivalent to plug your MG012 into. If you haven't, I recommend searching for an online tutorial, there are some good ones on YouTube.

Recommended tools include:

- Soldering iron (ideally temperature controlled)
- Multicore solder
- Small snips to cut off leads
- Small pliers
- Desoldering pump and/or braid
- Anti-static wrist strap (or steer clear of materials that cause static and touch a grounded object every now and then).

The normal rule of thumb is to solder the lowest height components first, working up:

- P1. This is normally supplied with 40 pins, therefore one needs to be carefully cut off using a sharp knife. Then, solder one joint only, check the alignment, melt solder and correct alignment if required before soldering remaining joints. If supplied with 36 pins, fit at the Pin 1 end of the PCB
- P2-4. These are supplied as a single 40 pin assembly (the same as P1). Three 10-pin assemblies need to be cut from this using a sharp knife or snips
- C1-3. Orientation doesn't matter
- RN1. Note that the dot on one end should align with the marking on the PCB RN1 graphic
- Sockets for U1-3 (do not fit ICs yet). Similarly to P1, solder two opposite corners, check the socket is flat on the board before continuing. Make sure the notches at the end of the sockets match with the PCB graphics, to reduce the risk of installing the ICs the wrong way round
- JP1-6 (actually a single part)

If you have flux cleaner, clean all joints. Now inspect them carefully for issues (a magnifying glass of some sort can be very helpful, the camera on some phones works quite well).

The final step prior to plugging into the host system and testing is to fit the ICs into their sockets. Their legs will probably need a bit of gentle bending on a table or similar surface, to bring the two rows a little closer to each other. Pay attention to orientation (even after all this hard work, it's easy to get wrong).

The diagram illustrates a digital logic circuit using three integrated circuits: a 74HCT04 (U1.1), a 74HCT88 (U3), and a 74HCT55A (U2). The circuit is powered by a +5V supply and ground.

- U1.1 (74HCT04):** A hex inverter. Pin 14 is connected to +5V, and pin 7 is connected to GND. It is configured as a buffer, with its output (pin 2) connected to the input (pin 1) of the 74HCT88 (U3).
- U3 (74HCT88):** A 3-to-8 line decoder. Pin 16 (VCC) is connected to +5V, and pin 8 (GND) is connected to GND. The input pins A1, A2, and A3 are connected to the outputs of the 74HCT04 (pins 1, 2, and 3). The output pins Q0 through Q7 are connected to the inputs of the 74HCT55A (U2).
- U2 (74HCT55A):** A 3-to-8 line decoder. Pin 16 (VCC) is connected to +5V, and pin 8 (GND) is connected to GND. The input pins A1, A2, and A3 are connected to the outputs of the 74HCT88 (pins 1, 2, and 3). The output pins Q0 through Q7 are connected to the inputs of the 74HCT04 (pins 1, 2, and 3).

The circuit is tested using a logic analyzer, showing the output signals for the 74HCT04, 74HCT88, and 74HCT55A. The logic analyzer shows that the 74HCT04 is correctly inverting the output of the 74HCT88, and the 74HCT55A is correctly generating the output signals for the 74HCT88.

U1.1 inverts the reset signal (U2 needs an active high) and sets U2 to "all ports input" after a reset.

<https://www.renesas.com/us/en/document/dst/82c55a-datasheet>

The following section ("How do I use it") will give the basics of getting started in Mode 0.

How do I use it?

The first step is to set the address. If you are using the standard RC2014 serial I/O board, then setting A7 to zero (jumper removed) ensures the MG012 will not clash with it. If you have other I/O boards fitted, then you will need to take them into account also.

To use an example:

Jumper	A7	A6	A5	A4	A3	A2
Status	Open	Open	Open	Jumper	Jumper	Open
Value	0	0	0	16	8	0

Summing the values gives a decimal address of 24 (A0 and A1 both zero) to 27 (A0 and A1 both 1). These are used as follows:

- 24 - Port A
- 25 - Port B
- 26 - Port C
- 27 - Control Word

The basics of the Control Word in Mode 0 are:

Bit	7	6	5	4	3	2	1	0	Dec.
Port (1=IP, 0=OP)				A	C Upper		B	C Low	
Value (all ports IP)	1	0	0	1	1	0	1	1	155
Value (A=OP, B,C=IP)	1	0	0	0	1	0	1	1	139

Reading address 27 after a reset will get a result of 155 (all ports IP). Using the above it should be easy to set the different ports as input or output as

required. For example, writing 139 to address 27 will enable Port A as an output, after which the binary values of outputs to address 24 will appear at Port A pins. Similarly, reading addresses 25 and 26 will give the inputs that are being applied to Ports B and C respectively.

Example 1, Loopback Test:

A quick example follows (using the addresses of 24 to 27). It's a loopback test, and assumes A0 to A7 are connected to B0 to B7 respectively. It uses MG011 at address 30 to provide a random number between 0 and 255, but it could be rewritten to use the BASIC RND function, or to count up by one every time.

BASIC CODE

```
10 PRINT "A to B"
20 OUT 27,139

30 FOR A = 1 TO 1000
40 LET B = INP(30)

50 OUT 24,B
60 LET C = INP(25)
70 IF B<>C THEN PRINT "FAIL",B," ",C

80 NEXT A
90 PRINT "RUN COMPLETE"
```

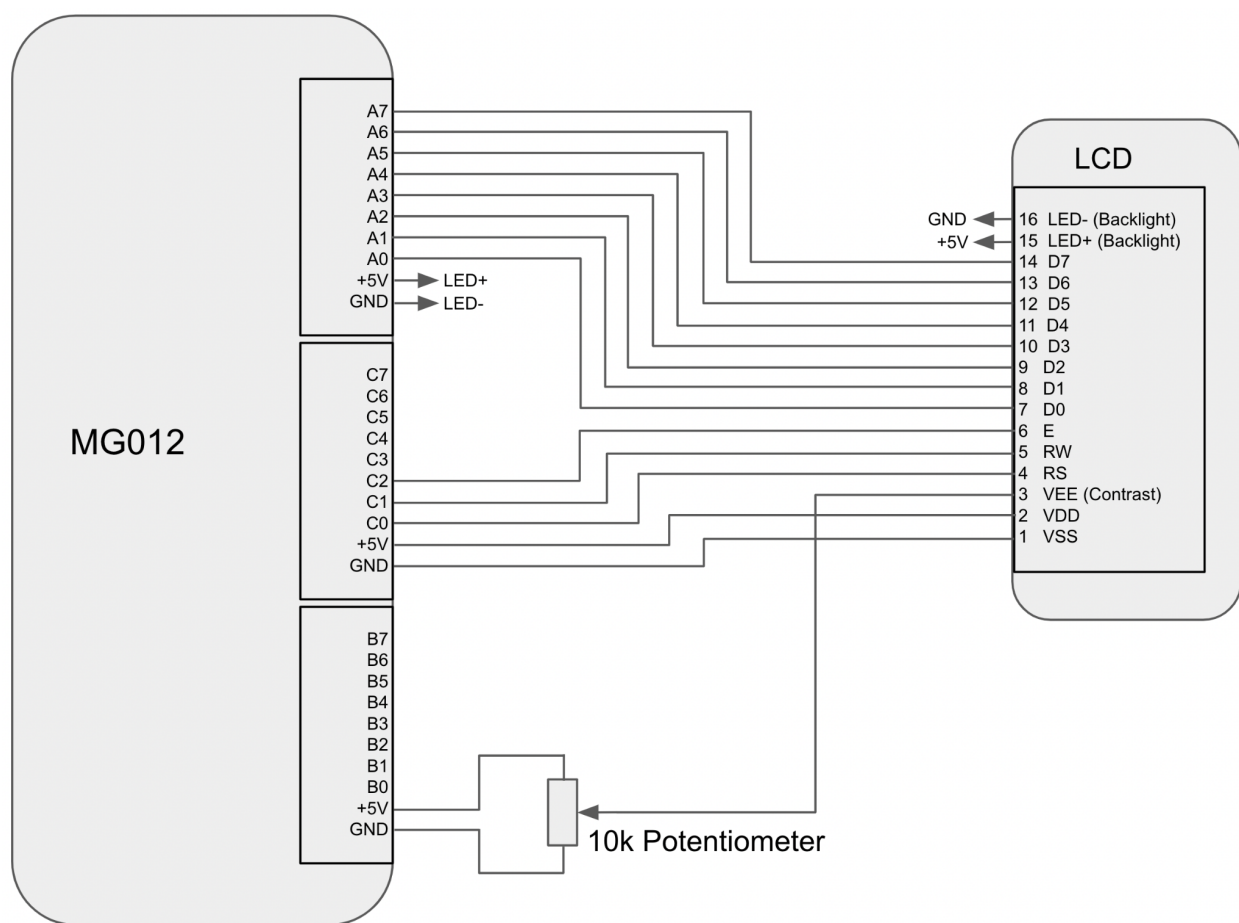
Notes

Set up Port A as OP, B,C as IP
Loop for 1000 numbers
Grab random number from MG011
Output number on Port A
Input number on Port B
Compare, and warn if different

Example 2, LCD Display:

This is a more “real world” example (the loopback test is a good example of input and output that doesn’t need additional hardware (beyond wires), but it’s not the most exciting).

The LCD display in question is a 16 x 2 digit 5V parallel interface LCD module. There are many slightly different variants of this out there, they feature configurable (8 or 4 bit) parallel buses (we will use 8 bit). The hookup uses A and some of B port, plus various 5V and ground connections:



Port C (OUT26, X) is connected as follows:

Port C Pin	Binary Value (X)	Connected to	Usage
C0	1	RS	Register select. 0 writes into instruction register, 1 into data register (for writing in text to be displayed)
C1	2	RW	Read/Write. We will only use 0 = write
C2	4	E	Enable input. A 1 to 0 transition on this pin writes (or reads) the contents of the data bus to or from either the instruction or data register

BASIC CODE

```
1000 REM Testprogram 8255 with LCD
1010 GOSUB 10000:REM Init LCD
```

```
2000 REM Main-Program
2010 TX$="82C55 I/O MG012"
2020 UX$="Type C to clear"
2030 GOSUB 10300
2040 INPUT "Type C to clear";C$
2050 IF C$="C" THEN GOTO 2080
2060 IF C$="c" THEN GOTO 2080
2070 GOTO 2040
2080 GOSUB 10200:REM clear LCD
2090 STOP
```

```
10000 REM Init LCD
10010 OUT 27,138 :REM 8255 all ports
Output
10020 OUT 24,0
10040 OUT 26,0
10050 FOR I=1 TO 4
10060 OUT 24,56 :REM 8Bit
```

Notes

Go to subroutine to initialise LCD

Text to be displayed in upper and lower LCD lines
Go to subroutine to write

Typing C goes to subroutine to clear LCD

Subroutine to initialise
Sets all 3 ports to output mode

Put 56 onto 8 bit bus

```
10070 OUT 26,4
10080 OUT 26,0
10090 NEXT I
```

*Toggles port C bit 4 ("E")
with bit 1 at zero (clocks 56
into instruction register to
set 8 bit mode)*

```
10100 OUT 24,6 :REM Increment
10110 OUT 26,4
10120 OUT 26,0
```

*Similarly, clocks 6 into
instruction register (cursor
will move to right)*

```
10130 OUT 24,12 :REM Cursor off, Display
on
10140 OUT 26,4
10150 OUT 26,0
```

*Similarly, clocks 12 for
display on, cursor not shown*

```
10160 GOSUB 10200:REM Clear display
10170 RETURN
```

Clear display and return

```
10200 REM Clear display
10210 OUT 24,1
10220 OUT 26,4
10230 OUT 26,0
10290 RETURN
```

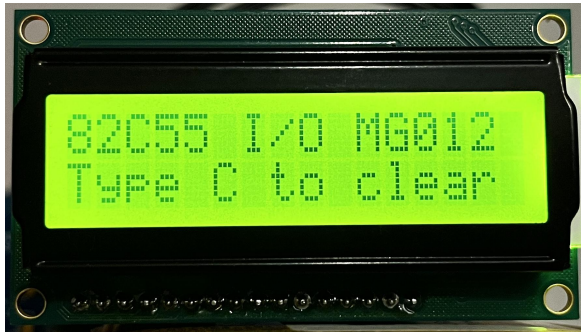
*Subroutine to clear display
Clocks 1 into instruction
register to clear display*

```
10300 REM Output string tx$ to LCD
10310 L=LEN(TX$)
10320 FOR I=1 TO L
10330 T=ASC(MID$(TX$,I,1))
10340 OUT 24,T
10350 OUT 26,5
10360 OUT 26,0
10370 NEXT
10400 REM Output string ux$ to LCD
10410 OUT 24,168
10415 OUT 26,4
10417 OUT 26,0
10420 L=LEN(UX$)
10430 FOR I=1 TO L
10440 T=ASC(MID$(UX$,I,1))
```

*Work out length of string
Loop through it
Let T = next character
Put T onto 8 bit bus
Clock T into data register*

*Same process all over again
for lower line*

```
10450 OUT 24,T
10460 OUT 26,5
10470 OUT 26,0
10480 NEXT
10490 RETURN
```



Timings and Compatibility

MG012 is supplied with either 5MHz or 8MHz 82C55 devices, depending on what's available (they are still being manufactured, price is essentially the same for either, but availability can be patchy). I have examined the timing diagrams and confirmed that either device should work equally well.

MG012 does not comply fully with 82C55's need for address/Chip select inputs to be stable 20ns after a write (by about 10ns). I have tested extensively and noticed no issues, and the majority of Z80/82C55 designs and implementations on the web have the same "feature", and are also apparently working fine.

More recently, I have tested MG012 (and three different 82C55s) for many hours with a Z80 based RC2014 running at 20MHz, running loopback tests. No failures at all have been logged. I have no means of testing against Z180 processors, but suspect results will be similar.

The only timing caution I would note is where users source their own 82C55 from Chinese or similar suppliers on eBay or elsewhere. The risk of these being fakes is high, sadly. If you decide to go this route, then I obviously can not guarantee the results.

Acknowledgements/Legal

MG012 has been designed for RC2014 with reference to the RC2014 Module Template. All pinouts used and the physical outline are in compliance with the RC2014 Module Template.

The BASIC code for Example 2 is based on code posted by Thomas Riesen in the RC2014-Z80 Google group.

RC2014 is a trademark of RFC2795 Ltd.

MG012 has been designed for hobbyist use only and is not to be used for safety or business critical applications.