Apex Instruction Set Architecture Simulator (`apex-sim`) Phase 1 Documentation

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1 Design

`apex-sim` is a simulator for the *Architecture Pipeline EXample* (APEX) Instruction Set Architecture (ISA). Figure 1 shows class interactions and data flow between each of the stages and support classes. `apex-sim` consists of the following components:

- `main.cpp` contains the driver program. The driver program provides file input for instructions, user interface operations, and maintaining persistent simulator state. This component is discussed in section 1.1.
- `code.cpp`, `data.cpp`, `registers.cpp`, `cpu.cpp`, and `stage.cpp` provide the objects modeling components of the pipeline. These are discussed in section 1.2.
- `simulate.cpp` provides the functions that allow the CPU to simulate working on each of its stages, inter-stage communication through advancement, stalls for basic inter-stage interlocks, and forwarding. These implementation details are described in section 2.

Finally, we opensource our work under the MIT license through a GitHub repository located at `https://github.com/colematt/apex-sim`. We discuss our team's work and matters related to our repository in 3.

1.1 Driver Program

1.2 Classes

1.2.1 Code

1.2.2 Data

1.2.3 Registers

1.2.4 CPU

1.2.5 Stages

2 Implementation

2.1 Work Phase

2.2 Advance Phase

2.3 Stalls

2.4 Forwarding

3 Production

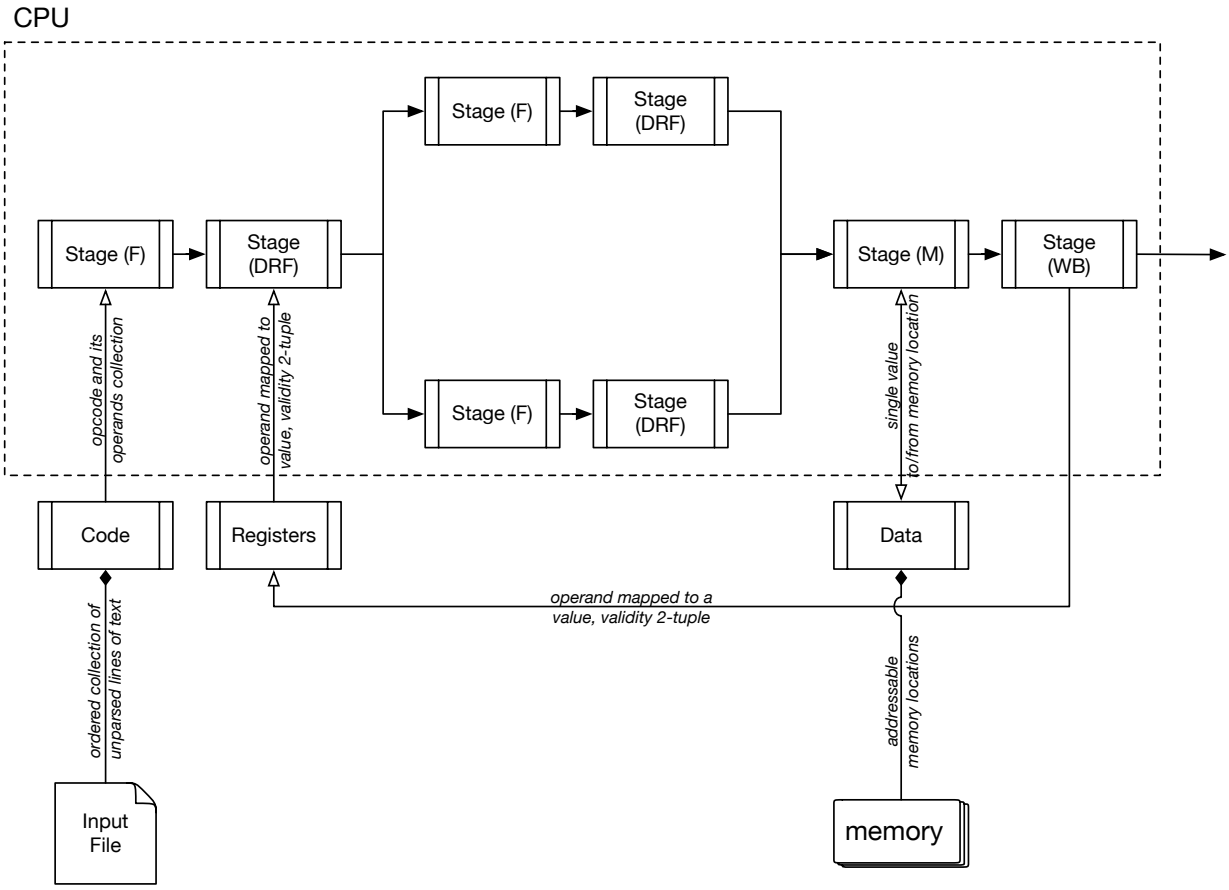

Figure 1: The APEX pipeline and class interactions.