

Report: Name of Module

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Introduction

```
[language=x86asm]
mov eax, 1
add eax, 1
sub eax, 1
mov ebx, 0
mov ecx, 0
mov edx, 0
int 0x80
```

The goal of the Concurrency Lab is to work with two, widely used, frameworks within two different programming languages. The first framework is Rust's *RTIC* *Real – TimeInterrupt – drivenConcurrency* and the second is using Python's Asyncio package under the CircuitPython framework. The goal of this lab is to understand the differences between the two frameworks and how they are used to handle concurrency. The lab can also be a demonstration of the differences between the two programming languages and how they handle memory management and concurrency.

Design

Conclusion