#### Differential Signals:

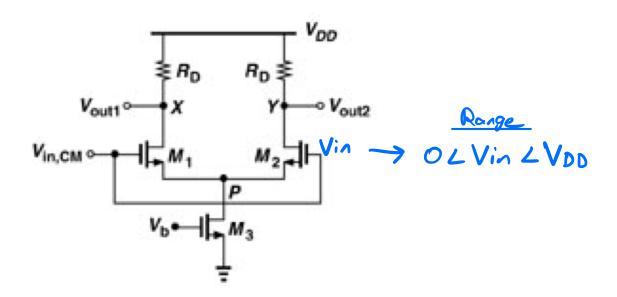
- Measured between two nodes as opposed to node to ground

$$\frac{V_{A} - V_{B}}{V_{A} + V_{B}} + \frac{V_{A} - V_{B}}{2} + \frac{V_{A} - V_{B}}{2} + \frac{V_{A} - V_{B}}{2}$$

- CM: Common Mode DC Level

- DM: Differential Mode Signal

## Common Mode Response:



if: Vin=0; VG=0, transistors are off. No current

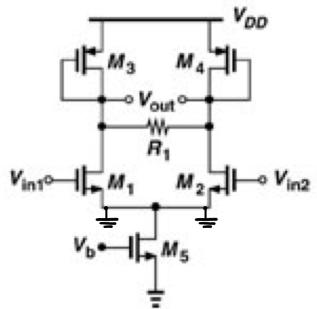
VGS, + (Vb-V+h3) -> minimum voltage needed to operate

# Half-Circuit Model:

Vo.ch = 
$$\frac{\sqrt{6d}}{2}$$
 $V_{\text{out1}} = \frac{\sqrt{6d}}{2}$ 
 $V_{\text{out2}} = \frac{\sqrt{6d$ 

$$V_{in_1} = \frac{V_{in_1} + V_{in_2}}{2} + \frac{V_{in_1} - V_{in_2}}{2} : V_{cM} = \frac{V_{id}}{2} = V_{oc} + Asin(\omega t)$$

$$Vin_2 = \frac{Vin_1 + Vin_2}{2} + \frac{Vin_1 - Vin_2}{2}$$
;  $Vcm = \frac{Vid}{2} = Voc - Asin(wt)$ 

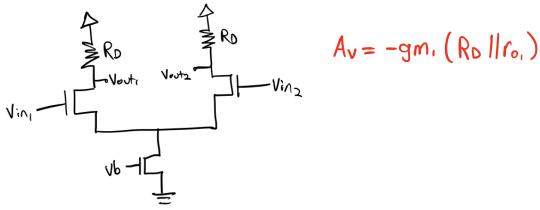


- Common Source amplifier with diode connected load in parallel with a resistive load

$$\frac{\lambda=0:}{Vod} = -9 \, \text{m.} \left( \frac{1}{9 \, \text{m}_3} \left| \left| \frac{R_1}{2} \right| \right)$$

$$\frac{\lambda=0:}{\sqrt{6d}} = -9 \, \text{m.} \left( \frac{1}{9m_3} \left\| \frac{R_1}{2} \right) \right) \qquad \frac{\sqrt{6d}}{\sqrt{6d}} = -9 \, \text{m.} \left( \frac{1}{9m_3} \left\| \frac{R_1}{6} \right) \right)$$

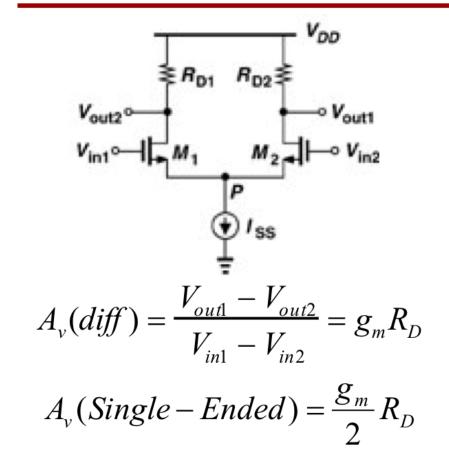
Sketch the small-signal gain of a differential pair as a function of its input common-mode level.



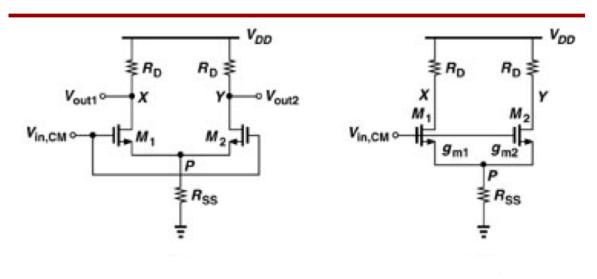
We have:

$$I_{D1} - I_{D2} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} \underbrace{(V_{in1} - V_{in2})}_{\text{voltage}} \sqrt{\frac{4I_{SS}}{\mu_n C_{ox} \frac{W}{L}} - (V_{in1} - V_{in2})^2}$$

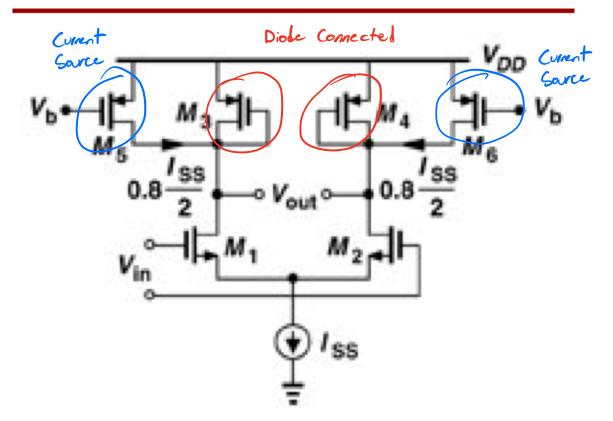
#### **Differential Gain**



#### Common-Mode Response



#### **MOS Loads**



### Gilbert Cell:

- Wanted to create a Variable gain amplifier (Differential)

