ECED3204 – Lab #4

STUDENT NAME(s): .

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# Pre-Lab Information

It is recommended that you read this entire lab ahead of time. Doing so will save you considerable time during the lab, as you will be required to write some simple C code during this lab!

# Overall Objective

This lab has several main objectives:

* Learn about using timers for event timing
* Learn about Pulse Width Modulation (PWM)
* Learn about Input Capture mode to measure pulse width

# Part #1: Timing Events

## Objective

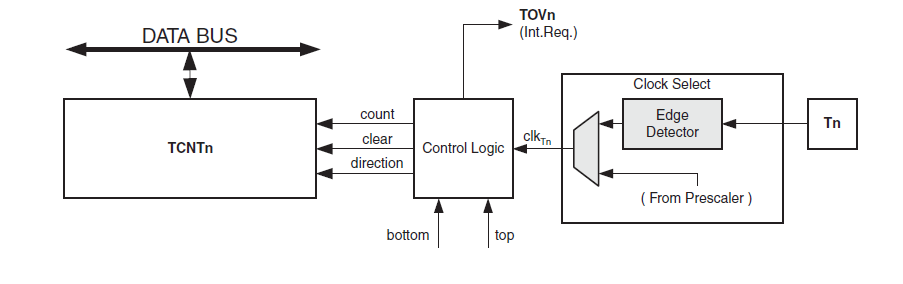
* Familiarize yourself with using a timer and interrupt.

## Required Materials

* Microprocessor Module with Programmer
* Breadboard
* USB Cable
* Power Supply
* Computer with Atmel Studio 6.2 and Programmer Utility installed
* Push Button

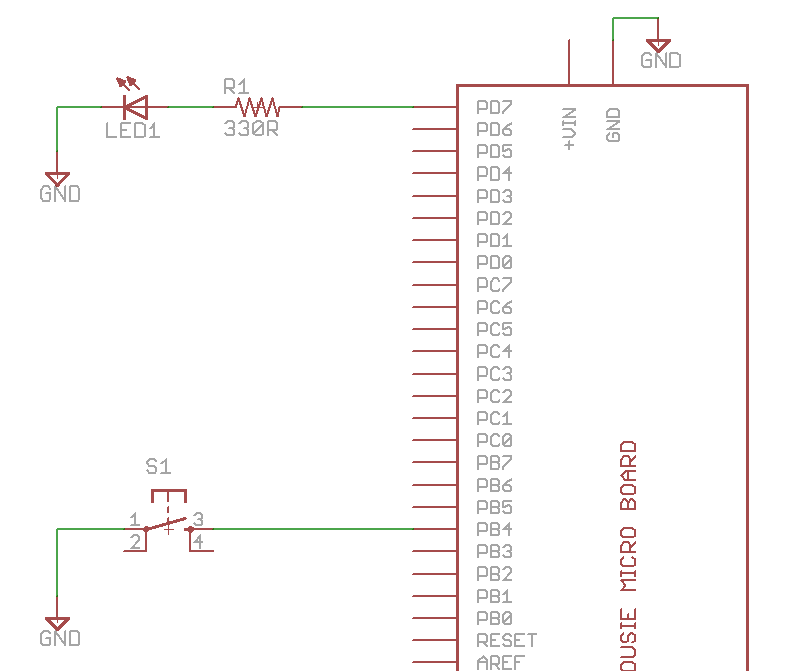
## Background

The timer system in the ATMega644P can be used for a variety of purposes. In this first part of the lab we will use the ‘overflow interrupt’ of the timer. The timer system has an 8-bit or 16-bit register (depending on the timer being used), which is incremented on a rate you can choose. See Chapter 11 of the course textbook for more information.

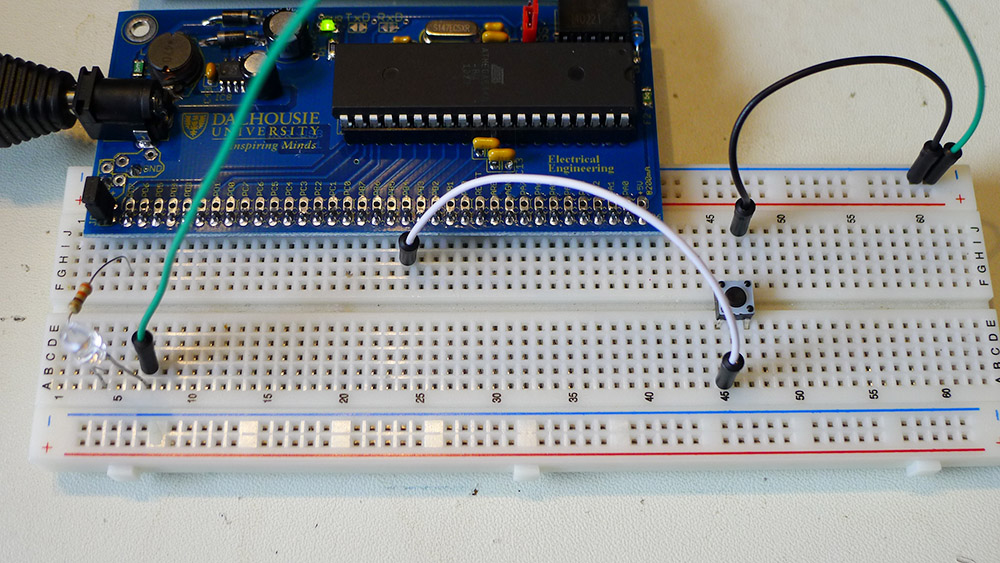


## Procedure

1. Build the same circuit from **Part #1** of **Lab #3**. For reference the schematic is shown below – we will only use the LED in Part 1, but will use the switch in Part 2.



Which might look as follows:



1. Start a new C/C++ project (see Lab #1 for details), copy the following template into it:

#include <avr/io.h>

#include <avr/interrupt.h>

volatile unsigned int tick;

#define LED\_ON() PORTD |= 1<<7

#define LED\_OFF() PORTD &= ~(1<<7)

int main(void)

{

DDRD |= 1<<7;

//Timer configuration

TCCR0A = ?

TCCR0B = ?

//Interrupt mask enables

TIMSK0 = ?

//Enable global interrupts

sei();

while(1);

}

ISR(SPECIFY\_NAME\_HERE)

{

//Code here

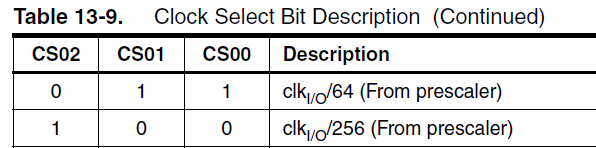
;

}

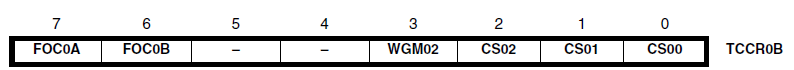
1. Before being able to compile this code, we need to finish a few things. The first is to determine the setup of TCCR0A and TCCR0B. We want the timer to overflow somewhere between 100-5000 times per second.

The clock source for the AVR is a 14745600Hz crystal (we learned this in Lab #1). If you used this to directly drive an 8-bit counter, the counter would overflow at the rate of 14745600 ÷ 28 = 14745600 ÷ 256 = 57600 times per second. That is too fast!

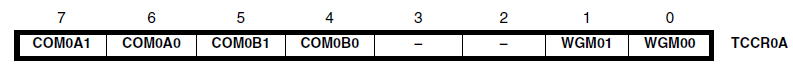
Finding Table 13-9, you can see some prescaler settings:



Using a prescaler of 64 would mean the timer now overflows 900 times per second. This tells us the setting for two bits in the TCCR0B register, as these CS00/CS01/CS02 bits are present in this register:



We will use the default (all-0) settings for TCCR0A:



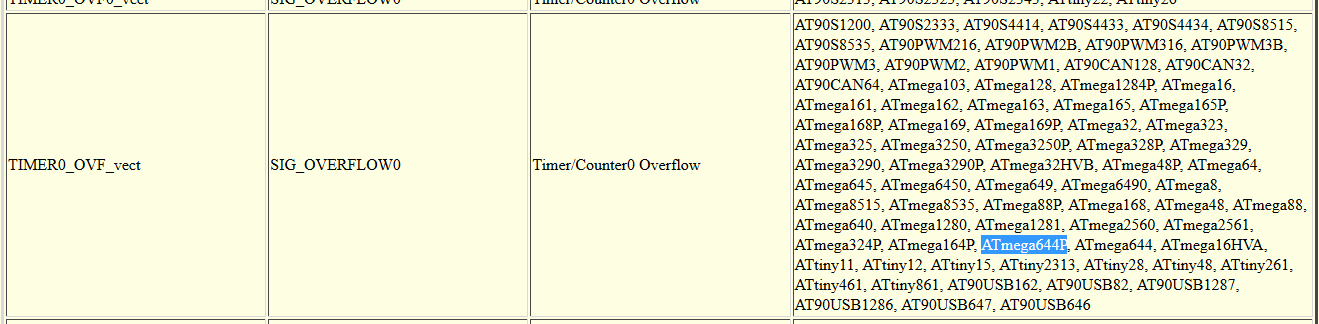
See the datasheet for a description of those bits. This means our setup area of the code looks like this:

//Timer configuration

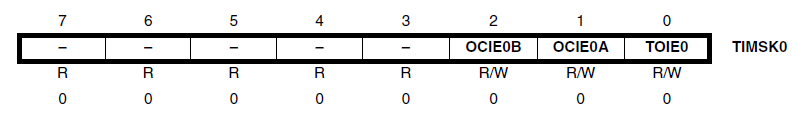
TCCR0A = 0;

TCCR0B = (1 << CS00) | (1 << CS01);

1. Next you need to fill in the SPECIFY\_NAME\_HERE section of the code, which is the interrupt vector. This routine will get called every time the interrupt occurs. You can find a list of all the names at <http://www.nongnu.org/avr-libc/user-manual/group__avr__interrupts.html> - you must be **VERY CAREFUL** to ensure you use a name that exists on the ATmega644P device! In this case look for the TIMER0\_OVF\_vect name, as shown in the following:



1. Finally, you need to enable the ‘overflow’ interrupt, which is done by setting bit 1 of the TIMSK0 register:



1. At this point, your code should look similar to this:

int main(void)

{

DDRD |= 1<<7;

//Timer configuration

TCCR0A = 0;

TCCR0B = (1 << CS00) | (1 << CS01);

//Interrupt mask enables

TIMSK0 |= 1 << TOIE0;

//Enable global interrupts

sei();

while(1);

}

ISR(TIMER0\_OVF\_vect)

{

//Code here

;

}

1. Finally, program the interrupt service routine (ISR) to toggle the LED. An example implementation is as follows, using the ‘tick’ variable to keep track of how many runs through the interrupt routine have occurred:

ISR(TIMER0\_OVF\_vect)

{

tick++;

if(tick == 1){

LED\_ON();

} else if (tick == 400){

LED\_OFF();

} else if (tick > 900){

tick = 0;

}

}

1. Adjust the delay to blink 5 seconds on, 5 seconds off.

# Part #2: PWM Output

## Objective

* Generate a PWM signal with a variable duty cycle.

## Required Materials

* Setup from Part #1

## Background

The timer system in the ATMega644P can be used for a variety of purposes. In this first part of the lab we will use the ‘overflow interrupt’ of the timer. The timer system has an 8-bit or 16-bit register (depending on the timer being used), which is incremented on a rate you can choose. See Chapter 11 of the course textbook for more information.

## Procedure

1. Build the same circuit from **Part #1** of this lab, or if you just completed Part #1 leave your circuit as-is.
2. Using Timer 2, generate a Pulse Width Modulation (PWM) signal of around 2-10 kHz. Output this on in PD7 (which is the OC2A pin). You will need the following references in the datasheet:
   * Section 15.11.1 – TCCR2A Register
   * Section 15.11.2 – TCCR2B Register

You should configure timer/counter 2 as the following:

* + PWM, Phase Correct (WGM2 = 0, WGM1 = 1, WGM0 = 0)
  + Clock divider = IOClock / 8
  + OC2A pin operating in non-inverting mode

The following shows the basic operating instructions:

#include <avr/io.h>

int main(void)

{

DDRD |= 1<<7;

//Set clock divider to be /8

TCCR2B = ??

//Set waveform generation mode

TCCR2A |= ??

//Set output on OC2A pin

TCCR2A |= ??

//Set PWM to half-way (50% duty cycle)

OCR2A = 127;

while(1);

}

1. The LED should be partially illuminated now. You can also check with an oscilloscope you are getting an appropriate signal.
2. Set the OCR2A register to various values in the range 0-255, and observe the effect on the output signal. Use an oscilloscope to measure how the duty cycle changes, and measure the frequency as well of the PWM output.
3. Add some code to slowly increase the value, and see what happens. For example here is a complete code listing:

#include <avr/io.h>

#include <util/delay.h>

int main(void)

{

DDRD |= 1<<7;

//Set clock divider to be /8

TCCR2B = 1<<CS21;

//Set waveform generation mode

TCCR2A |= 1<<WGM20;

//Set output on OC2A pin

TCCR2A |= 1<<COM2A1;

OCR2A = 0;

while(1){

OCR2A++;

\_delay\_ms(50);

}

}

# Part #3: Input Capture

## Objective

* Measure the time a button is pressed using the input capture

## Required Materials

* Setup from Part #1
* Push-Button wired into PORTD.6

## Background

The timer system in the ATMega644P can be used for a variety of purposes. This section will use the ‘Input Capture’ functionality

# Lab Questions

1. What causes problems when trying to read a switch, and how do we fix it?
2. The LED multiplexing could only illuminate certain LED patterns, as LEDs were linked together. How could we illuminate an arbitrary pattern (hint: our human eyes will blend together two quickly displayed patterns)?
3. Include code from each section in your report.