ECED3204 – Lab #6

STUDENT NAME(s): .

STUDENT NUMBER(s): B00 .

# Pre-Lab Information

It is recommended that you read this entire lab ahead of time. Doing so will save you considerable time during the lab, as you will be required to write some simple C code during this lab!

# Objective

* Use the SPI module in the AVR Mega microcontroller
* Interface to an EEPROM

# Required Materials

* Microprocessor Module with Programmer
* Breadboard
* USB Cable
* Power Supply
* Computer with Atmel Studio 6.2 and Programmer Utility installed
* 25LC080 EEPOM
* 1K Resistor

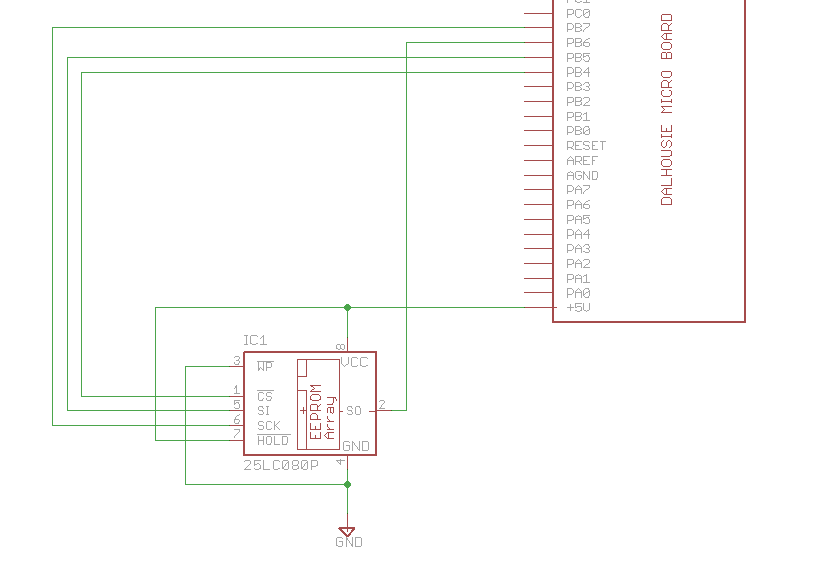
# Background

Serial Peripheral Interface (SPI) is used by many devices. This lab will have you dump the contents of a SPI-connected EEPROM device.

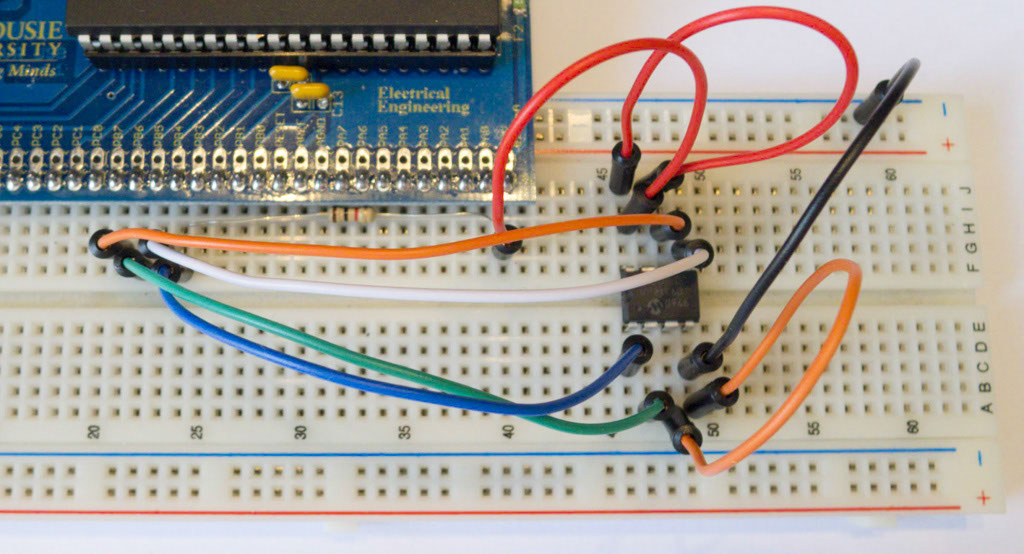
See the course textbook for information – the SPI chapter includes information on interfacing to these EEPROM devices. This lab assumes you have read that chapter!

# Procedure

1. Build the following circuit using the programmed 25LC080P device:



Which might look like this:



1. Start a new C/C++ project (see Lab #1 for details), write the following code into it:

#include <stdio.h>

#include <avr/io.h>

#include <avr/pgmspace.h>

static int uart\_putchar(char c, FILE \*stream);

static int uart\_getchar(FILE \*stream);

FILE mystdout = FDEV\_SETUP\_STREAM(uart\_putchar, NULL, \_FDEV\_SETUP\_WRITE);

FILE mystdin = FDEV\_SETUP\_STREAM(NULL, uart\_getchar, \_FDEV\_SETUP\_READ);

static int uart\_putchar(char c, FILE \*stream)

{

loop\_until\_bit\_is\_set(UCSR0A, UDRE0);

UDR0 = c;

return 0;

}

static int uart\_getchar(FILE \*stream)

{

loop\_until\_bit\_is\_set(UCSR0A, RXC0); /\* Wait until data exists. \*/

return UDR0;

}

void init\_uart(void)

{

UCSR0B = (1<<RXEN0) | (1<<TXEN0);

UBRR0 = 7;

stdout = &mystdout;

stdin = &mystdin;

}

#define CS\_HIGH() PORTB |= 1<<4

#define CS\_LOW() PORTB &= ~(1<<4)

#define INST\_WREN 0x06 /\* Write Enable \*/

#define INST\_WRDI 0x04 /\* Disable writes \*/

#define INST\_RDSR 0x05 /\* Read status/config register \*/

#define INST\_WRSR 0x01 /\* Write status/config register \*/

#define INST\_READ 0x03 /\* Read data from memory \*/

#define INST\_WRITE 0x02 /\* Write data to memory \*/

uint8\_t spi\_readwrite(uint8\_t data)

{

SPDR = data;

loop\_until\_bit\_is\_set(SPSR, SPIF);

return SPDR;

}

int main(void)

{

init\_uart();

printf\_P(PSTR("System Booted, built %s on %s\n"), \_\_TIME\_\_, \_\_DATE\_\_);

//Set CS as output, other lines set automatically

DDRB |= (1<<4) | (1<<5) | (1<<7);

CS\_HIGH();

//Slowest possible SPI clock (easiest with long wires)

SPCR = (1<<SPE) | (1<<MSTR) | (1<<SPR1) | (1<<SPR0);

uint8\_t data;

CS\_LOW();

spi\_readwrite(INST\_RDSR);

data = spi\_readwrite(0);

CS\_HIGH();

printf("Status register: %02x\n", data);

uint8\_t addr = 101;

CS\_LOW();

spi\_readwrite(INST\_READ);

spi\_readwrite(0);

spi\_readwrite(addr);

data = spi\_readwrite(0);

CS\_HIGH();

printf("Address %d: %02x\n", addr, data);

}

1. The EEPROM has been programmed with the following information:

Address 00: Secret Byte

Address 01: Secret Byte

…

Address 98: Secret Byte

Address 99: Secret Byte

Address 100: 0x00

Address 101: 0x01

Address 102: 0x02

Address 103: 0x03

Address 104: 0xDE

Address 105: 0xAD

Address 106: 0xBE

Address 107: 0xEF

Check your setup is working by verifying that address 100-107 have the expected values. You can use the printf() setup (see Lab #5 for getting this working) to dump these values.

1. Print the byte value corresponding to the last two digits of your banner number. For example if your student ID was B00123456, you would print the value stored at address **56**. This secret value will be used to verify your lab report.