



Dual P-CHANNEL ENHANCEMENT MODE MOSFET

Summary

V _{(BR)DSS}	R _{DS(on)} max	I _D max
	$260 \text{m}\Omega @V_{GS} = -4.5V$	
-20V	$500 \text{m}\Omega @V_{GS} = -2.5V$	-0.9 A
	1000mΩ @V _{GS} = -1.8V	

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Battery Disconnect Switch
- Load Switch for Power Management Functions

Features

- Low R_{DS(ON)} Minimizes Conduction Losses
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

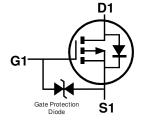
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



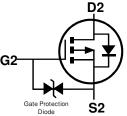




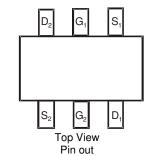
Top View



Q1 P-CHANNEAL



Q2 P-CHANNEAL



Ordering Information (Note 4)

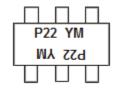
Part Number	Case	Packaging
DMP2200UDW-7	SOT363	3,000/Tape & Reel
DMP2200UDW-13	SOT363	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information

SOT363



P22 = Marking Code YM = Date Code Marking Y or Y= Year (ex: B = 2014) M = Month (ex: 9 = September)

Date Code Key

Year	2014	4	2015		2016	20	17	2018		2019	2	2020
Code	В		С		D	[Ξ	F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	-20	V	
Gate-Source Voltage		V_{GSS}	±8	V
Continuous Drain Current (Note 6)	T _A = +25 °C T _A = +85 °C	l _D	-0.9 -0.7	А

Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)		0	0.45	W
Total Power Dissipation (Note 6)		P _D	0.6	W
Thermal Resistance, Junction to Ambient (Note 5)		275		
Thermal Resistance, Junction to Ambient (Note 6) Steady State		$R_{\theta JA}$	208	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	72		
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	∞	

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.4	_	-1.2	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	R _{DS (ON)}	_	180 240 320	260 500 1,000	mΩ	$V_{GS} = -4.5V$, $I_D = -0.88A$ $V_{GS} = -2.5V$, $I_D = -0.71A$ $V_{GS} = -1.8V$, $I_D = -0.20A$
Diode Forward Voltage	V_{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V$, $I_{S} = -0.48A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	184		pF	V 40V V 0V
Output Capacitance	Coss	_	26.4		pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	18.5		pF	T = T.OWITIZ
Gate Resistance	R_{g}	_	221	_	Ω	$V_{DS} = V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge	Q_g		2.1	_	nC	V _{GS} = -4.5V, V _{DS} = -10V,
Gate-Source Charge	Q_{gs}	_	0.4	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_{D} = -1.7A$
Gate-Drain Charge	Q_{gd}		0.5	_	nC	ID = -1./A
Turn-On Delay Time	$t_{D(ON)}$	_	9.8	_	ns	
Turn-Off Delay Time	t _{D(OFF)}	_	24.4		ns	$V_{DD} = -10V, I_{D} = -1.5A,$
Turn-On Rise Time	t _r	_	88	_	ns	$V_{GS} = -4.5V$, $R_{GEN} = 1\Omega$
Turn-Off Fall Time	t _f		45	_	ns	

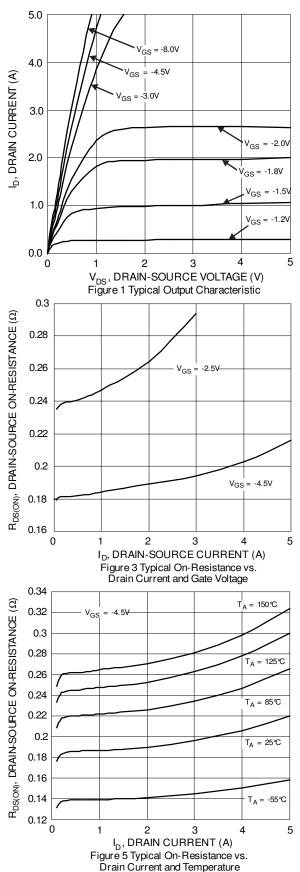
Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

- 8. Guaranteed by design. Not subject to product testing.







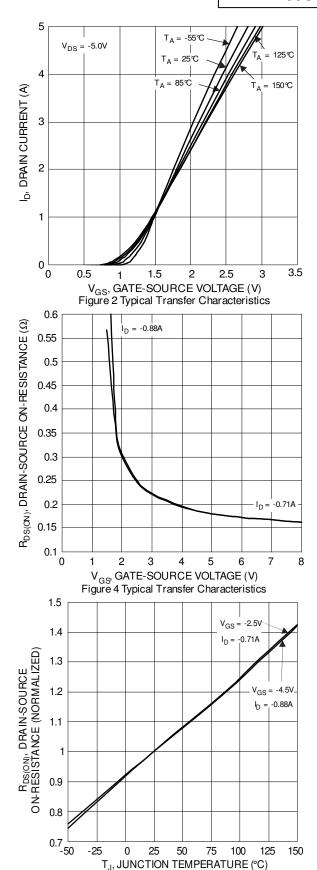
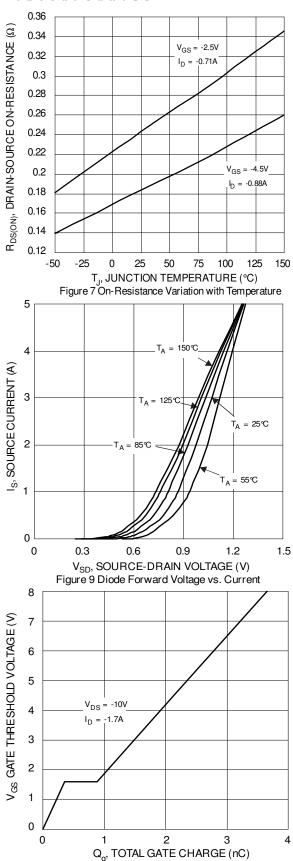
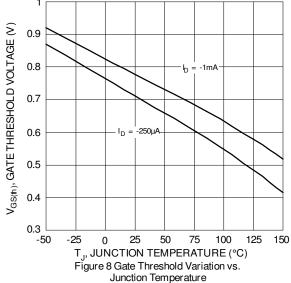


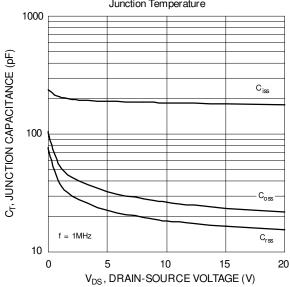
Figure 6 On-Resistance Variation with Temperature











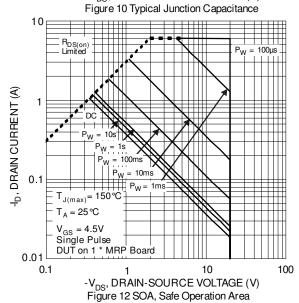
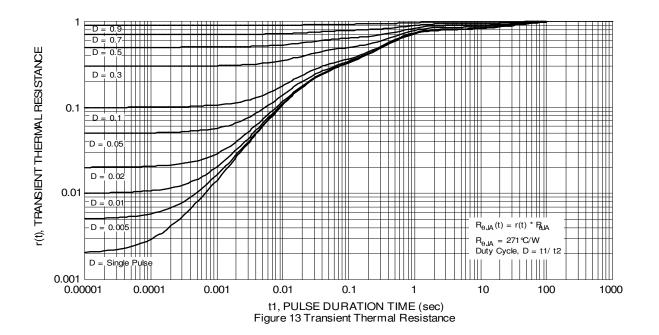


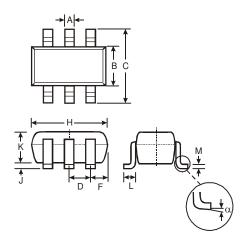
Figure 11 Gate Charge





Package Outline Dimensions

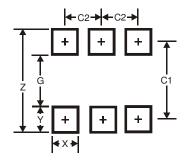
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT363							
Dim	Min	Max	Тур				
Α	0.10	0.30	0.25				
В	1.15	1.35	1.30				
O	2.00	2.20	2.10				
D		0.65 Ty	р				
F	0.40	0.45	0.425				
Η	1.80	2.20	2.15				
7	0	0.10	0.05				
K	0.90	1.00	1.00				
ш	0.25	0.40	0.30				
М	0.10	0.22	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
C1	1.9
C2	0.65



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