1. Description

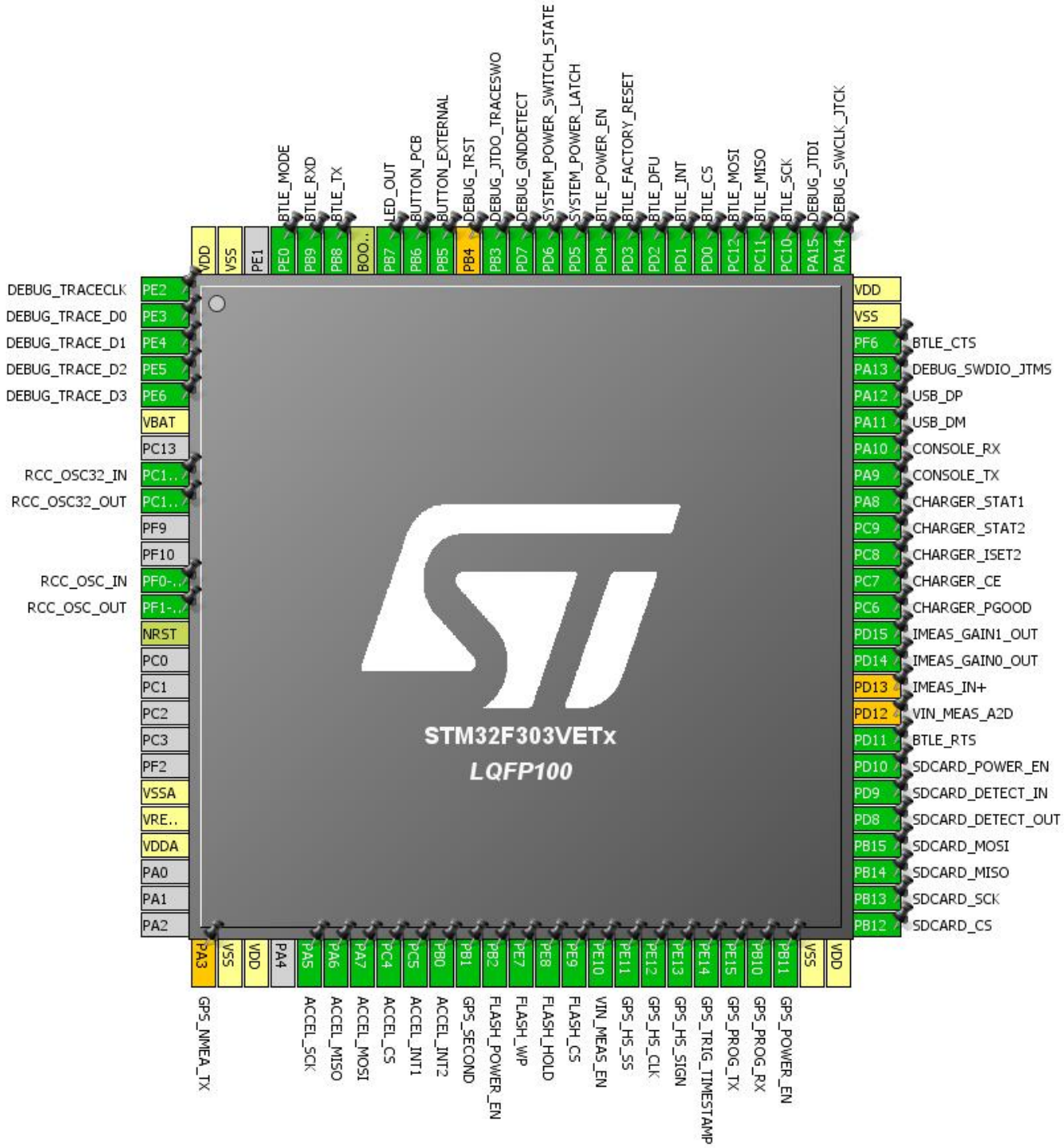
1.1. Project

Project Name	pin_configuration
Board Name	No information
Generated with:	STM32CubeMX 4.15.1
Date	06/24/2016

1.2. MCU

MCU Series	STM32F3
MCU Line	STM32F303
MCU name	STM32F303VETx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PE2	I/O	SYS_TRACECK	DEBUG_TRACECLK
2	PE3	I/O	SYS_TRACED0	DEBUG_TRACE_D0
3	PE4	I/O	SYS_TRACED1	DEBUG_TRACE_D1
4	PE5	I/O	SYS_TRACED2	DEBUG_TRACE_D2
5	PE6	I/O	SYS_TRACED3	DEBUG_TRACE_D3
6	VBAT	Power		
8	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
9	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
12	PF0-OSC_IN	I/O	RCC_OSC_IN	
13	PF1-OSC_OUT	I/O	RCC_OSC_OUT	
14	NRST	Reset		
20	VSSA	Power		
21	VREF+	Power		
22	VDDA	Power		
26	PA3 *	I/O	USART2_RX	GPS_NMEA_TX
27	VSS	Power		
28	VDD	Power		
30	PA5	I/O	SPI1_SCK	ACCEL_SCK
31	PA6	I/O	SPI1_MISO	ACCEL_MISO
32	PA7	I/O	SPI1_MOSI	ACCEL_MOSI
33	PC4 **	I/O	GPIO_Output	ACCEL_CS
34	PC5	I/O	GPIO_EXTI5	ACCEL_INT1
35	PB0	I/O	GPIO_EXTI0	ACCEL_INT2
36	PB1	I/O	TIM3_CH4	GPS_SECOND
37	PB2 **	I/O	GPIO_Output	FLASH_POWER_EN
38	PE7 **	I/O	GPIO_Output	FLASH_WP
39	PE8 **	I/O	GPIO_Output	FLASH_HOLD
40	PE9 **	I/O	GPIO_Output	FLASH_CS
41	PE10 **	I/O	GPIO_Output	VIN_MEAS_EN
42	PE11	I/O	SPI4_NSS	GPS_HS_SS
43	PE12	I/O	SPI4_SCK	GPS_HS_CLK
44	PE13	I/O	SPI4_MISO	GPS_HS_SIGN
45	PE14 **	I/O	GPIO_Output	GPS_TRIG_TIMESTAMP
46	PE15	I/O	USART3_RX	GPS_PROG_TX
47	PB10	I/O	USART3_TX	GPS_PROG_RX
48	PB11 **	I/O	GPIO_Output	GPS_POWER_EN

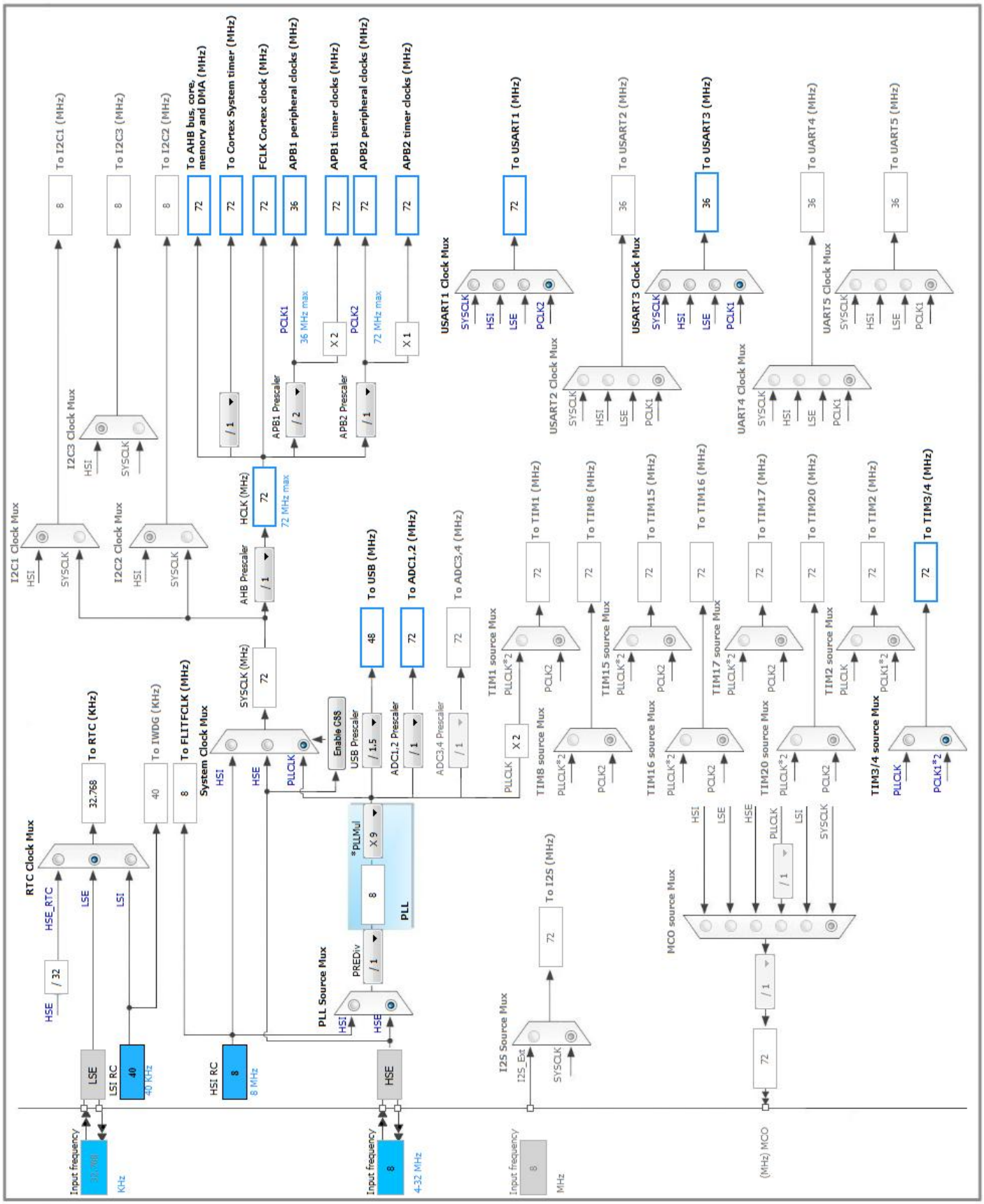
Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
49	VSS	Power		
50	VDD	Power		
51	PB12 **	I/O	GPIO_Output	SDCARD_CS
52	PB13	I/O	SPI2_SCK	SDCARD_SCK
53	PB14	I/O	SPI2_MISO	SDCARD_MISO
54	PB15	I/O	SPI2_MOSI	SDCARD_MOSI
55	PD8 **	I/O	GPIO_Output	SDCARD_DETECT_OUT
56	PD9	I/O	GPIO_EXTI9	SDCARD_DETECT_IN
57	PD10 **	I/O	GPIO_Output	SDCARD_POWER_EN
58	PD11	I/O	USART3_CTS	BTLE_RTS
59	PD12 *	I/O	ADC3_IN9	VIN_MEAS_A2D
60	PD13 *	I/O	ADC3_IN10	IMEAS_IN+
61	PD14 **	I/O	GPIO_Output	IMEAS_GAIN0_OUT
62	PD15 **	I/O	GPIO_Output	IMEAS_GAIN1_OUT
63	PC6 **	I/O	GPIO_Input	CHARGER_PGOOD
64	PC7 **	I/O	GPIO_Output	CHARGER_CE
65	PC8 **	I/O	GPIO_Output	CHARGER_ISET2
66	PC9 **	I/O	GPIO_Input	CHARGER_STAT2
67	PA8 **	I/O	GPIO_Input	CHARGER_STAT1
68	PA9	I/O	USART1_TX	CONSOLE_TX
69	PA10	I/O	USART1_RX	CONSOLE_RX
70	PA11	I/O	USB_DM	
71	PA12	I/O	USB_DP	
72	PA13	I/O	SYS_JTMS-SWDIO	DEBUG_SWDIO_JTMS
73	PF6	I/O	USART3_RTS	BTLE_CTS
74	VSS	Power		
75	VDD	Power		
76	PA14	I/O	SYS_JTCK-SWCLK	DEBUG_SWCLK_JTCK
77	PA15	I/O	SYS_JTDI	DEBUG_JTDI
78	PC10	I/O	SPI3_SCK	BTLE_SCK
79	PC11	I/O	SPI3_MISO	BTLE_MISO
80	PC12	I/O	SPI3_MOSI	BTLE_MOSI
81	PD0 **	I/O	GPIO_Output	BTLE_CS
82	PD1	I/O	GPIO_EXTI1	BTLE_INT
83	PD2 **	I/O	GPIO_Output	BTLE_DFU
84	PD3 **	I/O	GPIO_Output	BTLE_FACTORY_RESET
85	PD4 **	I/O	GPIO_Output	BTLE_POWER_EN
86	PD5 **	I/O	GPIO_Output	SYSTEM_POWER_LATCH

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
87	PD6 **	I/O	GPIO_Output	SYSTEM_POWER_SWITCH_STATE
88	PD7 **	I/O	GPIO_Input	DEBUG_GNDDETECT
89	PB3	I/O	SYS_JTDO-TRACESWO	DEBUG_JTDO_TRACESWO
90	PB4 *	I/O	SYS_NJTRST	DEBUG_TRST
91	PB5 **	I/O	GPIO_Input	BUTTON_EXTERNAL
92	PB6	I/O	GPIO_EXTI6	BUTTON_PCB
93	PB7 **	I/O	GPIO_Output	LED_OUT
94	BOOT0	Boot		
95	PB8 **	I/O	GPIO_Input	BTLE_TX
96	PB9 **	I/O	GPIO_Output	BTLE_RXD
97	PE0 **	I/O	GPIO_Output	BTLE_MODE
99	VSS	Power		
100	VDD	Power		

** The pin is affected with an I/O function

* The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. ADC1

mode: Temperature Sensor Channel

mode: Vrefint Channel

mode: Vbat Channel

5.1.1. Parameter Settings:

ADCs_Common_Settings:

Mode	Independent mode

ADC_Settings:

Clock Prescaler	ADC Asynchronous clock mode
Resolution	ADC 12-bit resolution
Data Alignment	Right alignment
Scan Conversion Mode	Disabled
Continuous Conversion Mode	Disabled
Discontinuous Conversion Mode	Disabled
DMA Continuous Requests	Disabled
End Of Conversion Selection	End of single conversion
Overrun behaviour	Overrun data overwritten
Low Power Auto Wait	Disabled

ADC_Regular_ConversionMode:

Enable Regular Conversions	Enable
Number Of Conversion	1
External Trigger Conversion Edge	None
Rank	1
Channel	Channel Temperature Sensor
Sampling Time	1.5 Cycles
Offset Number	No offset
Offset	0

ADC_Injected_ConversionMode:

Enable Injected Conversions	Enable
Number Of Conversions	0

Analog Watchdog 1:

Enable Analog WatchDog1 Mode	false

Analog Watchdog 2:

Enable Analog WatchDog2 Mode	false

Analog Watchdog 3:

Enable Analog WatchDog3 Mode	false

5.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

5.2.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
HSE Startup Timout Value (ms)	100
LSE Startup Timout Value (ms)	5000

5.3. RTC

Alarm A: Internal Alarm A

WakeUp: Internal WakeUp

5.3.1. Parameter Settings:

General:

Hour Format	Hourformat 24
Asynchronous Predivider value	127
Synchronous Predivider value	255

Calendar Time:

Data Format	BCD data format
Hours	0
Minutes	0
Seconds	0
Day Light Saving: value of hour adjustment	Daylightsaving None
Store Operation	Storeoperation Reset

Calendar Date:

Week Day	Monday
Month	January
Date	1
Year	0

Alarm A:

Hours	0
Minutes	0
Seconds	0
Sub Seconds	0
Alarm Mask	Alarm Mask None
Alarm Sub Second Mask	All Alarm SS fields are masked.
Alarm Date Week Day Sel	Date
Alarm Date	1

Wake UP:

Wake Up Clock	RTCCLK / 16
Wake Up Counter	0

5.4. SPI1

Mode: Full-Duplex Master

5.4.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	2
Baud Rate	**36.0 MBits/s ***
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSSP Mode	Enabled
NSS Signal Type	Software

5.5. SPI2

Mode: Full-Duplex Master

5.5.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	2
Baud Rate	**18.0 MBits/s ***
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSSP Mode	Enabled
NSS Signal Type	Software

5.6. SPI3

Mode: Full-Duplex Master

5.6.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	2
Baud Rate	**18.0 MBits/s ***
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSSP Mode	Enabled
NSS Signal Type	Software

5.7. SPI4

Mode: Half-Duplex Slave

Hardware NSS Signal: Hardware NSS Input Signal

5.7.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Input Hardware

5.8. SYS

Debug: JTAG with Trace Synchro(4 bits)

Timebase Source: SysTick

5.9. TIM3

Clock Source : Internal Clock

Channel4: Input Capture direct mode

5.9.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	0
Internal Clock Division (CKD)	No Division

Trigger Output (TRGO) Parameters:

Master/Slave Mode	Disable (no sync between this TIM (Master) and its Slaves

Trigger Event Selection TRGO	Reset (UG bit from TIMx_EGR)

Input Capture Channel 4:

Polarity Selection	Rising Edge
IC Selection	Direct
Prescaler Division Ratio	No division
Input Filter (4 bits value)	0

5.10. USART1

Mode: Asynchronous

5.10.1. Parameter Settings:

Basic Parameters:

Baud Rate	38400
Word Length	7 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

5.11. USART3

Mode: Asynchronous

Hardware Flow Control (RS232): CTS/RTS

5.11.1. Parameter Settings:

Basic Parameters:

Baud Rate	38400
Word Length	7 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

5.12. USB

mode: Device (FS)

5.12.1. Parameter Settings:

Basic Parameters:

Speed	Full Speed 12MBit/s
Endpoint 0 Max Packet size	8 Bytes
Physical interface	Internal Phy

Power Parameters:

Low Power	Disabled
Link Power Management	Disabled

5.13. FATFS

mode: User-defined

5.13.1. Set Defines:

Version:

FATFS version	R0.11

Function Parameters:

FS_TINY (Tiny mode)	Disabled
FS_READONLY (Read-only mode)	Disabled
FS_MINIMIZE (Minimization level)	Disabled
USE_STRFUNC (String functions)	Enabled with LF -> CRLF conversion
USE_FIND (Find functions)	Disabled
USE_MKFS (Make filesystem function)	Enabled
USE_FORWARD (Forward function)	Disabled
USE_LABEL (Volume label functions)	Disabled
USE_FASTSEEK (Fast seek function)	Enabled

Locale and Namespace Parameters:

CODE_PAGE (Code page on target)	Latin 1 (Windows)
USE_LFN (Use Long Filename)	Disabled
MAX_LFN (Max Long Filename)	255
LFN_UNICODE (Enable Unicode)	ANSI/OEM
STRF_ENCODE (Character encoding)	UTF-8
FS_RPATH (Relative Path)	Disabled

Physical Drive Parameters:

VOLUMES (Logical drives)	1
MAX_SS (Maximum Sector Size)	512
MIN_SS (Minimum Sector Size)	512
MULTI_PARTITION (Volume partitions feature)	Disabled
USE_TRIM (Erase feature)	Disabled
FS_NOFSINFO (Force full FAT scan)	0

System Parameters:

FS_NORTC (Timestamp feature)	Dynamic timestamp
NORTC_YEAR (Year for timestamp)	2015
NORTC_MON (Month for timestamp)	6
NORTC_MDAY (Day for timestamp)	4
WORD_ACCESS (Platform dependent access option)	Byte access
FS_REENTRANT (Re-Entrancy)	Disabled
FS_TIMEOUT (Timeout ticks)	1000
SYNC_t (O/S sync object)	osSemaphoreId
FS_LOCK (Number of files opened simultaneously)	2

*** User modified value**

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14-OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15-OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	
	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PF1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull up pull down	**High ***	ACCEL_SCK
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull up pull down	**High ***	ACCEL_MISO
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull up pull down	**High ***	ACCEL_MOSI
SPI2	PB13	SPI2_SCK	Alternate Function Push Pull	No pull up pull down	**High ***	SDCARD_SCK
	PB14	SPI2_MISO	Alternate Function Push Pull	No pull up pull down	**High ***	SDCARD_MISO
	PB15	SPI2_MOSI	Alternate Function Push Pull	No pull up pull down	**High ***	SDCARD_MOSI
SPI3	PC10	SPI3_SCK	Alternate Function Push Pull	No pull up pull down	**High ***	BTLE_SCK
	PC11	SPI3_MISO	Alternate Function Push Pull	No pull up pull down	**High ***	BTLE_MISO
	PC12	SPI3_MOSI	Alternate Function Push Pull	No pull up pull down	**High ***	BTLE_MOSI
SPI4	PE11	SPI4_NSS	Alternate Function Push Pull	No pull up pull down	**High ***	GPS_HS_SS
	PE12	SPI4_SCK	Alternate Function Push Pull	No pull up pull down	**High ***	GPS_HS_CLK
	PE13	SPI4_MISO	Alternate Function Push Pull	No pull up pull down	**High ***	GPS_HS_SIGN
SYS	PE2	SYS_TRACECK	n/a	n/a	n/a	DEBUG_TRACECLK
	PE3	SYS_TRACED0	n/a	n/a	n/a	DEBUG_TRACE_D0
	PE4	SYS_TRACED1	n/a	n/a	n/a	DEBUG_TRACE_D1
	PE5	SYS_TRACED2	n/a	n/a	n/a	DEBUG_TRACE_D2
	PE6	SYS_TRACED3	n/a	n/a	n/a	DEBUG_TRACE_D3
	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	DEBUG_SWDIO_JTMS
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	DEBUG_SWCLK_JTCK
	PA15	SYS_JTDI	n/a	n/a	n/a	DEBUG_JTDI
	PB3	SYS_JTDO-TRACESWO	n/a	n/a	n/a	DEBUG_JTDO_TRACESWO
TIM3	PB1	TIM3_CH4	Alternate Function Push Pull	No pull up pull down	Low	GPS_SECOND

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
USART1	PA9	USART1_TX	Alternate Function Push Pull	Pull up	**High ***	CONSOLE_TX
	PA10	USART1_RX	Alternate Function Push Pull	Pull up	**High ***	CONSOLE_RX
USART3	PE15	USART3_RX	Alternate Function Push Pull	Pull up	**High ***	GPS_PROG_TX
	PB10	USART3_TX	Alternate Function Push Pull	Pull up	**High ***	GPS_PROG_RX
	PD11	USART3_CTS	Alternate Function Push Pull	No pull up pull down	**High ***	BTLE_RTS
	PF6	USART3_RTS	Alternate Function Push Pull	No pull up pull down	**High ***	BTLE_CTS
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
Single Mapped Signals	PA3	USART2_RX	Alternate Function Push Pull	No pull up pull down	**High ***	GPS_NMEA_TX
	PD12	ADC3_IN9	Analog mode	No pull up pull down	**n/a**	VIN_MEAS_A2D
	PD13	ADC3_IN10	Analog mode	No pull up pull down	**n/a**	IMEAS_IN+
	PB4	SYS_NJTRST	n/a	n/a	**n/a**	DEBUG_TRST
GPIO	PC4	GPIO_Output	Output Push Pull	No pull up pull down	Low	ACCEL_CS
	PC5	GPIO_EXTI5	External Interrupt Mode with Rising edge trigger detection	No pull up pull down	n/a	ACCEL_INT1
	PB0	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	No pull up pull down	n/a	ACCEL_INT2
	PB2	GPIO_Output	Output Push Pull	No pull up pull down	Low	FLASH_POWER_EN
	PE7	GPIO_Output	Output Push Pull	No pull up pull down	Low	FLASH_WP
	PE8	GPIO_Output	Output Push Pull	No pull up pull down	Low	FLASH_HOLD
	PE9	GPIO_Output	Output Push Pull	No pull up pull down	Low	FLASH_CS
	PE10	GPIO_Output	Output Push Pull	No pull up pull down	Low	VIN_MEAS_EN
	PE14	GPIO_Output	Output Push Pull	No pull up pull down	Low	GPS_TRIG_TIMESTAMP
	PB11	GPIO_Output	Output Push Pull	No pull up pull down	Low	GPS_POWER_EN
	PB12	GPIO_Output	Output Push Pull	No pull up pull down	Low	SDCARD_CS
	PD8	GPIO_Output	Output Push Pull	No pull up pull down	Low	SDCARD_DETECT_OUT
	PD9	GPIO_EXTI9	External Interrupt Mode with Rising edge trigger detection	No pull up pull down	n/a	SDCARD_DETECT_IN
	PD10	GPIO_Output	Output Push Pull	No pull up pull down	Low	SDCARD_POWER_EN
	PD14	GPIO_Output	Output Push Pull	No pull up pull down	Low	IMEAS_GAIN0_OUT
	PD15	GPIO_Output	Output Push Pull	No pull up pull down	Low	IMEAS_GAIN1_OUT
	PC6	GPIO_Input	Input mode	No pull up pull down	n/a	CHARGER_PGOOD
	PC7	GPIO_Output	Output Push Pull	No pull up pull down	Low	CHARGER_CE
	PC8	GPIO_Output	Output Push Pull	No pull up pull down	Low	CHARGER_ISET2
	PC9	GPIO_Input	Input mode	No pull up pull down	n/a	CHARGER_STAT2
	PA8	GPIO_Input	Input mode	No pull up pull down	n/a	CHARGER_STAT1
	PD0	GPIO_Output	Output Push Pull	No pull up pull down	Low	BTLE_CS
	PD1	GPIO_EXTI1	External Interrupt Mode with Rising edge trigger detection	No pull up pull down	n/a	BTLE_INT

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PD2	GPIO_Output	Output Push Pull	No pull up pull down	Low	BTLE_DFU
	PD3	GPIO_Output	Output Push Pull	No pull up pull down	Low	BTLE_FACTORY_RESET
	PD4	GPIO_Output	Output Push Pull	No pull up pull down	Low	BTLE_POWER_EN
	PD5	GPIO_Output	Output Push Pull	No pull up pull down	Low	SYSTEM_POWER_LATCH
	PD6	GPIO_Output	Output Push Pull	No pull up pull down	Low	SYSTEM_POWER_SWITCH_STATE
	PD7	GPIO_Input	Input mode	No pull up pull down	n/a	DEBUG_GNDDETECT
	PB5	GPIO_Input	Input mode	No pull up pull down	n/a	BUTTON_EXTERNAL
	PB6	GPIO_EXTI6	External Interrupt Mode with Rising edge trigger detection	No pull up pull down	n/a	BUTTON_PCB
	PB7	GPIO_Output	Output Push Pull	No pull up pull down	Low	LED_OUT
	PB8	GPIO_Input	Input mode	No pull up pull down	n/a	BTLE_TX
	PB9	GPIO_Output	Output Push Pull	No pull up pull down	Low	BTLE_RXD
	PE0	GPIO_Output	Output Push Pull	No pull up pull down	Low	BTLE_MODE

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line 16	unused		
RTC wake-up interrupt through EXTI line 20	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
EXTI line0 interrupt	unused		
EXTI line1 interrupt	unused		
ADC1 and ADC2 interrupts	unused		
USB high priority or CAN_TX interrupts	unused		
USB low priority or CAN_RX0 interrupts	unused		
EXTI line[9:5] interrupts	unused		
TIM3 global interrupt	unused		
SPI1 global interrupt	unused		
SPI2 global interrupt	unused		
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	unused		
USART3 global interrupt / USART3 wake-up interrupt through EXTI line 28	unused		
RTC alarms A and B interrupt through EXTI line 17	unused		
SPI3 global interrupt	unused		
USB high priority interrupt remap	unused		
USB low priority interrupt remap	unused		
Floating point unit interrupt	unused		
SPI4 global interrupt	unused		

*** User modified value**

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F3
Line	STM32F303
MCU	STM32F303VETx
Datasheet	026415_Rev4

7.2. Parameter Selection

Temperature	25
Vdd	3.6