

Lecture 2

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1 Integer

1.1 Addition

unsigned addition implements modular arithmetic

- $\text{Uadd}(u, v) = (u + v) \% 2^w$

Tadd and Uadd has identical bit-level operation

```
int a, b;
int Usum, Tsum;
Usum = (int) ((unsigned) a + (unsigned) b);
Tsum = a + b;
```

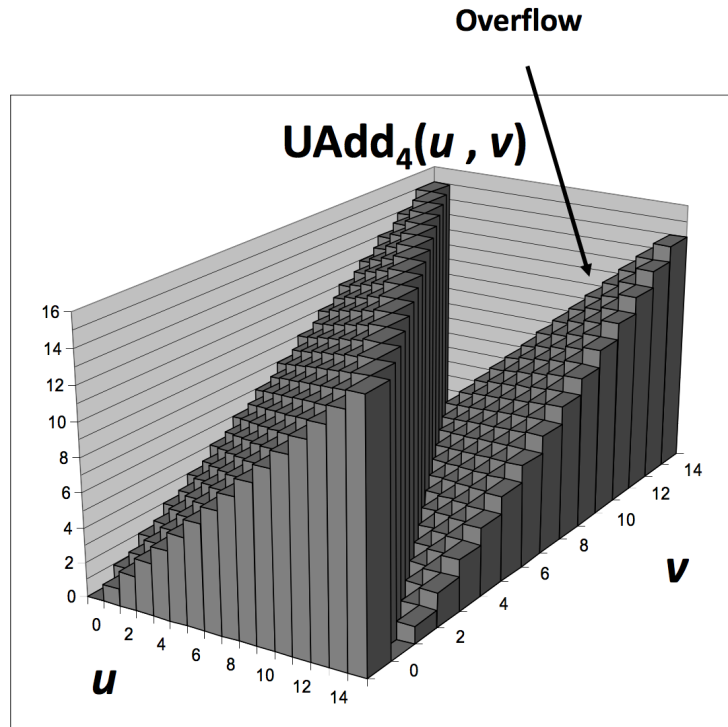


Figure 1: unsigned addition overflow

1.2 Multiplication

ignores higher order w bits

unsigned multiplication implements modular arithmetic

- $\text{Umult}(u, v) = (u * v) \% 2^w$

1.2.1 power of 2 multiply with shift

$(u \ll 5) - (u \ll 3) == u * 24$

- most machines shift and add faster than multiply
 - use this method for static code, i.e. compile-time constants

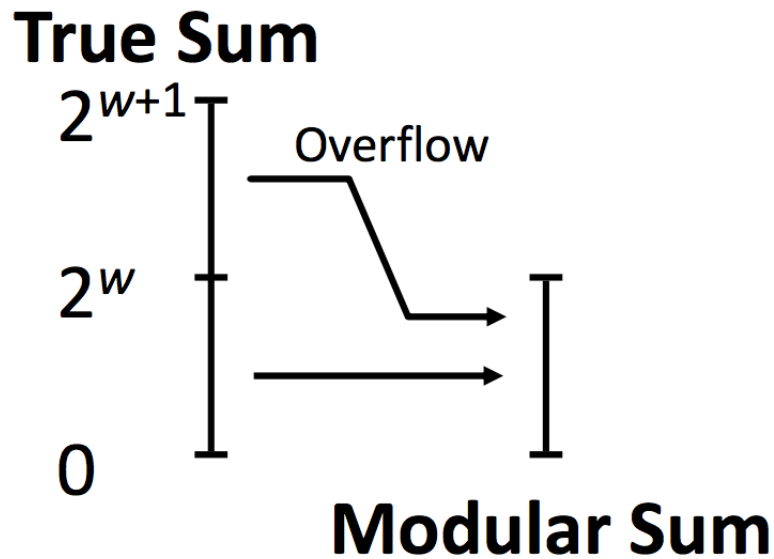


Figure 2: unsigned addition overflow

1.2.2 power of 2 unsigned division with shift

- use logical shift: pad with 0

$$u \gg k == \text{floor}(u / 2^k)$$

operation	decimal	hex	bin
x	15213U	3B 6D	00111011 01101101
x » 1	7606U	1D B6	00011101 10110110
y	240U	F0	11110000
y » 2	60U	3C	00111100

2 Byte-Oriented Memory Organization

word size

- nominal size of integer value and address

2.1 Address specify byte location

- Address of first byte in word

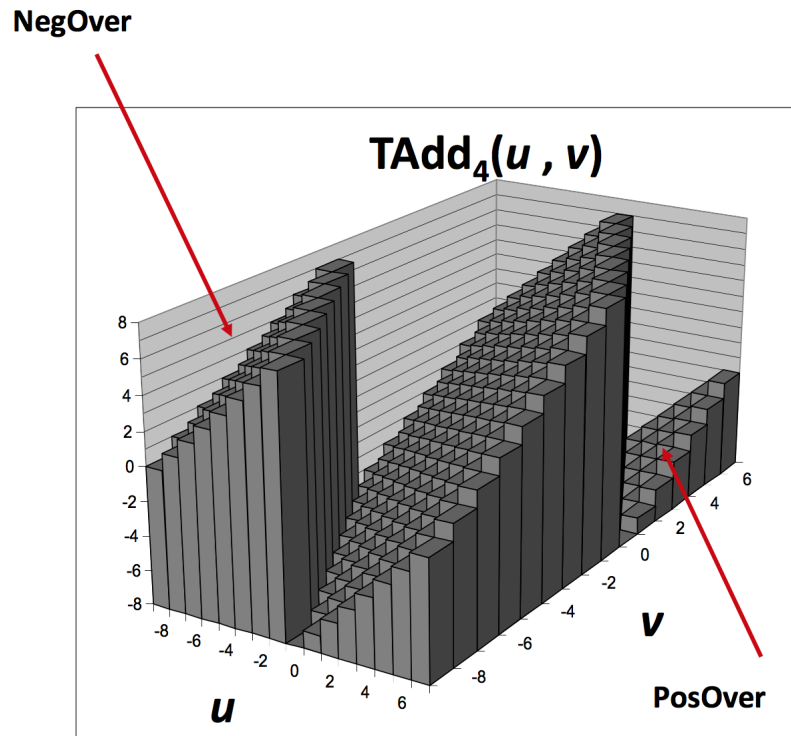


Figure 3: signed addition overflow

- Address of successive words differ depending on word size

2.2 Byte Ordering

Endianness

- Big Endian: least significant bit has highest address
- Little Endian: least significant bit has lowest address

address	0x100	0x101	0x102	0x103
big endian	01	23	45	67
little endian	67	45	23	01

```
printf("%p", address);
printf("%x", hexadecimal);
```

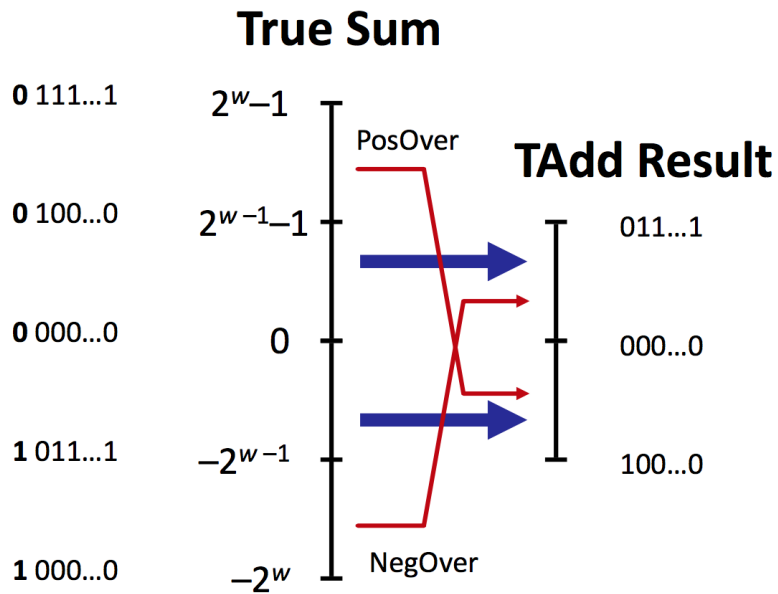


Figure 4: unsigned addition overflow

2.2.1 String

- array of characters
- byte ordering not an issue
- high addresses, latter elements(character)

2.2.2 example

```
int ix;
unsigned ux;
```

Table 1: Integer C puzzles

condition	statement	result	note
<code>ix & 7 == 7</code>	<code>(ix << 30) < 0</code>	true	last 3 bits are 111
<code>ux >= 0</code>		true	
<code>ix * ix >= 0</code>		false	
<code>ix >= 0</code>	<code>-ix <= 0</code>	true	
<code>ix <= 0</code>	<code>-ix >= 0</code>	false	<code>abs(TMIN) > abs(TMAX)</code>
<code>(ix -ix) >> 31 == -1</code>		true	
<code>ux >> 3 == ux / 8</code>		true	
<code>ix >> 3 == ix / 8</code>		false	signed division truncates towards 0
<code>x & (x - 1) != 0</code>		false	<code>x == 0</code>