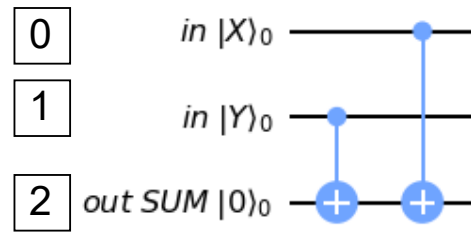
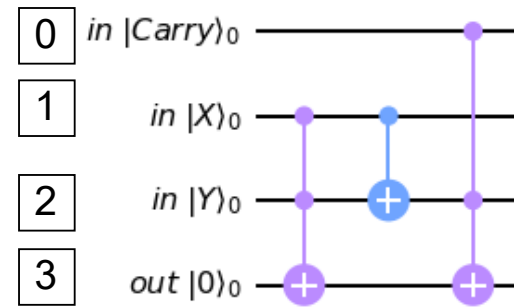


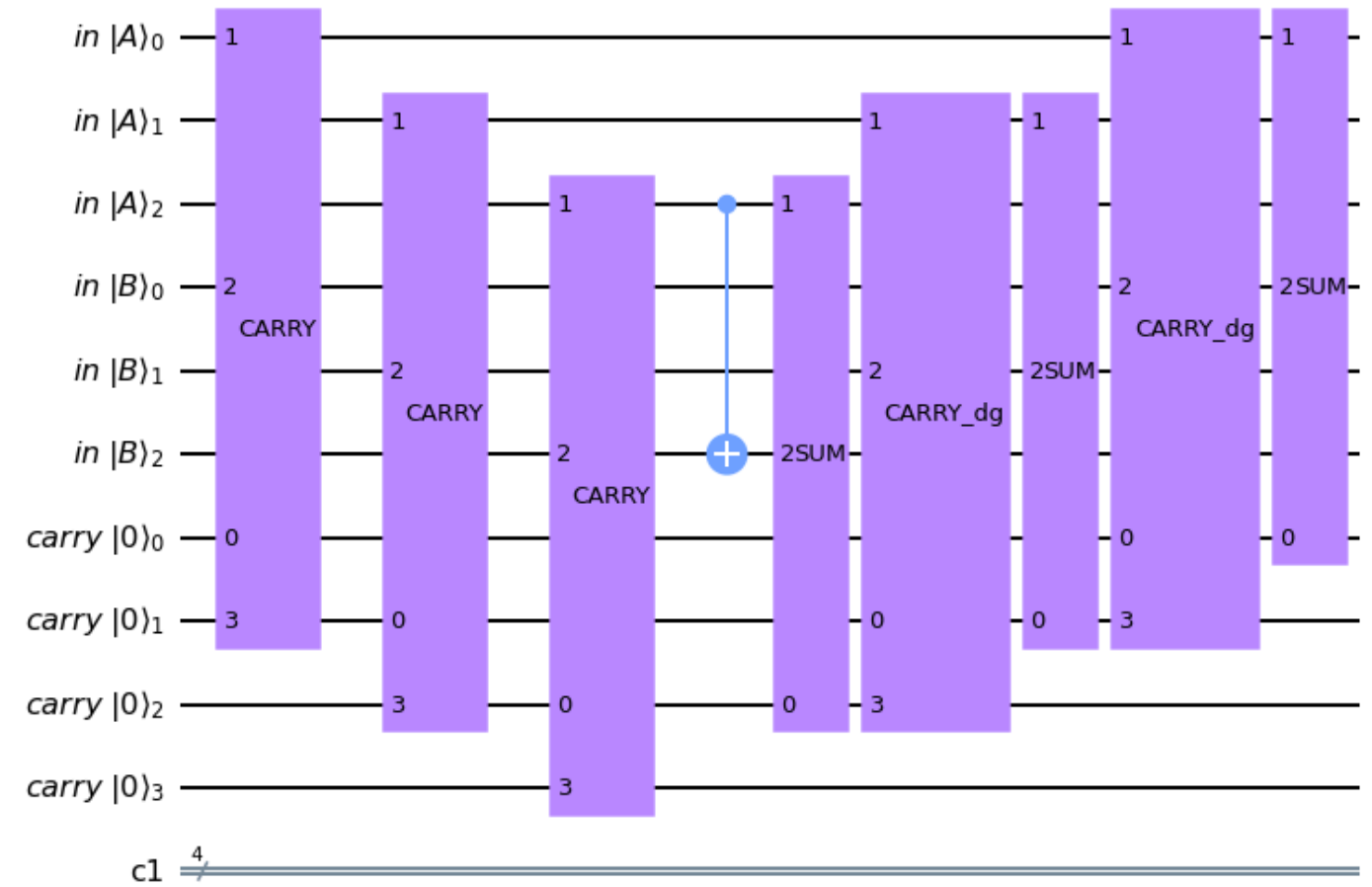
## SUM GATE



## CARRY GATE



## FULL CIRCUIT



- Qubits going to gates in FULL CIRCUIT correspond by number to in-gate operations
- CARRY\_dg is CARRY, but with the gates applied in reverse-order
- Bit-Count is VARIABLE in our program, so these gates must be applied through loops.

Source: [https://github.com/JAllsop/Quantum-Full-Adder/blob/master/ELEN4022\\_Lab\\_2\\_2021.ipynb](https://github.com/JAllsop/Quantum-Full-Adder/blob/master/ELEN4022_Lab_2_2021.ipynb)

