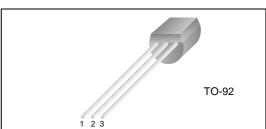


September 2006

KSC1008 **NPN Epitacial Silicon Transistor**

Features

- · Low frequency amplifier medium speed switching.
- High Collector-Base Voltage : V_{CBO}=80V.
- Collector Current : I_C=700mA
- Collector Power Dissipation : P_C=800mW
- Suffix "-C" means Center Collector (1.Emitter 2.Collector 3.Base)
- Non suffix "-C" means Side Collector (1.Emitter 2.Base 3.Collector)
- Complement to KSA708



KSC1008 : 1. Emitter 2. Base 3. Collector KSC1008C: 1. Emitter 2. Collector 3. Base

Absolute Maximum Ratings * Ta = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	80	V
V _{CEO}	Collector-Emitter Voltage	60	V
V_{EBO}	Emitter-Base Voltage	8	V
I _C	Collector current	700	mA
P _C	Collector Power Dissipation	800	mW
T _J	Junction Temperature	+150	°C
T _{stg}	Storage Temperature	-55 ~ +150	°C

Electrical Characteristics * $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =100μA, I _E =0	80			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =10mA, I _B =0	60			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E =10μA, I _C =0	8			V
I _{CBO}	Collector Cut-off Current	V _{CB} =60V, I _E =0			0.1	μΑ
I _{EBO}	Emitter Cut-off Current	V_{EB} =5V, I_C =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =2V, I _C =50mA	40		400	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =500mA, I _B =50mA		0.2	0.4	V
V _{BE} (sat) Base-Emitter Saturation Voltage		I _C =500mA, I _B =50mA		0.86	1.1	V
f_{T}	Current Gain Bandwidth Product	V _{CE} =10V, I _C =50mA	30	50		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, I _E =0, f=1MHz		8		pF

^{*} DC Item are tested by Pulse Test: Pulse Width≤300us, Duty Cycle≤2%

her Classification

FE				
Classification	R	0	Υ	G
h _{FE}	40 ~ 80	70 ~ 140	120 ~ 240	200 ~ 400

^{* 1.} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Package Marking and Ordering Information

Device(note)	Device Marking	Package	Packing Method	Qty(pcs)	Pin Definitions
KSC1008COBU	C1008OC	TO-92	BULK		1.Emitter 2.Collector 3.Base
KSC1008COTA	C1008OC	TO-92	TAPE & AMMO	2,000	1.Emitter 2.Collector 3.Base
KSC1008CYBU	C1008YC	TO-92	BULK		1.Emitter 2.Collector 3.Base
KSC1008CYTA	C1008YC	TO-92	TAPE & AMMO	2,000	1.Emitter 2.Collector 3.Base
KSC1008GBU	C1008G	TO-92	BULK		1.Emitter 2.Base 3.Collector
KSC1008GTA	C1008G	TO-92	TAPE & AMMO	2,000	1.Emitter 2.Base 3.Collector
KSC1008OBU	C1008O	TO-92	BULK		1.Emitter 2.Base 3.Collector
KSC1008OTA	C1008O	TO-92	TAPE & AMMO	2,000	1.Emitter 2.Base 3.Collector
KSC1008RBU	C1008R	TO-92	BULK		1.Emitter 2.Base 3.Collector
KSC1008RTA	C1008R	TO-92	TAPE & AMMO	2,000	1.Emitter 2.Base 3.Collector
KSC1008YBU	C1008Y	TO-92	BULK		1.Emitter 2.Base 3.Collector
KSC1008YTA	C1008Y	TO-92	TAPE & AMMO	2,000	1.Emitter 2.Base 3.Collector
KSC1008YTF	C1008Y	TO-92	TAPE & REEL	2,000	1.Emitter 2.Base 3.Collector

Note: Affix "-C-" - center collector pin.

Affix "-R-, -O-, -Y-, -G-" - h_{HE} classification

Suffix "-BU" - Bulk packing, straight lead form.(see package dimensions)

Suffix "-TF" - Tape& Reel packing, 0.200 In-Line Spacing lead form. (see package dimensions)

SUffix "-TA" - Tape& AMMO packing, 0.200 In-Line Spacing lead form. (see package dimensions)

Typical Characteristics

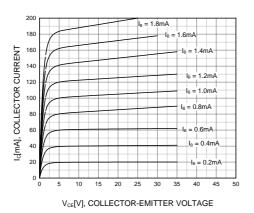


Figure 1. Static Characteristic

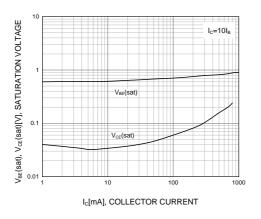


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

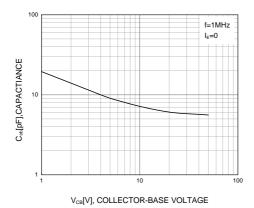


Figure 5. Collector Output Capacitance

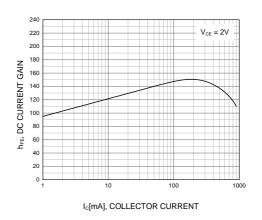


Figure 2. DC current Gain

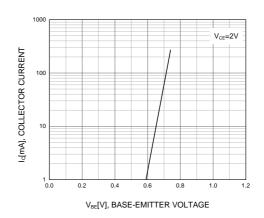
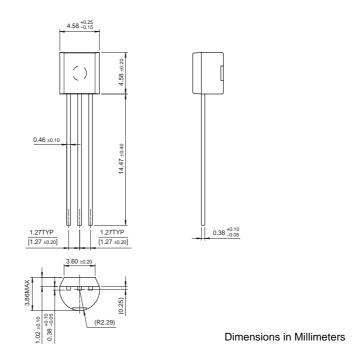


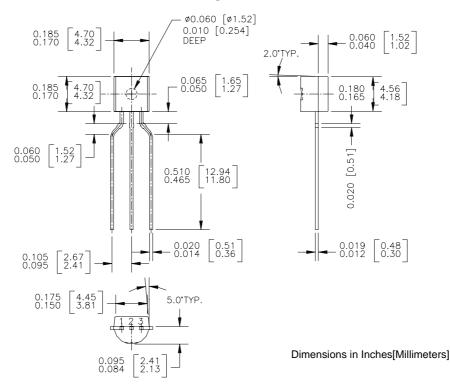
Figure 4. Base-Emitter On Voltage

Package Dimensions

TO-92 Straight Lead Form



TO-92 0.200 In-Line Spacing Lead Form



UltraFET® UniFET™ VCX™ Wire™

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PRODUCT STATUS DEFINITIONS

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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