Silicon NPN Power Transistors

2SD1555

DESCRIPTION

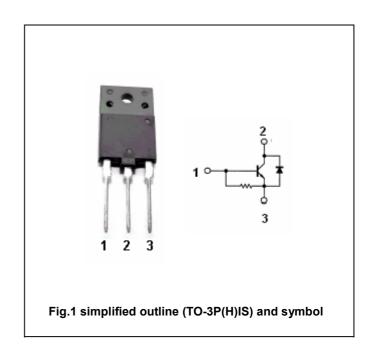
- ·With TO-3P(H)IS package
- ·Built-in damper diode
- ·High voltage ,high speed
- ·Low collector saturation voltage

APPLICATIONS

·For color TV horizontal output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings (Ta=25□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V_{CEO}	Collector-emitter voltage	Open base	600	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
Ic	Collector current		5	Α
I _B	Base current		2.5	Α
Pc	Collector power dissipation	T _C =25□	50	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

Tj=25□ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =0.2A , I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4A ;I _B =0.8A		3.0	5.0	٧
V_{BEsat}	Base-emitter saturation voltage	I _C =4A; I _B =0.8A			1.5	٧
I _{CBO}	Collector cut-off current	V _{CB} =500V; I _E =0			10	μA
h _{FE}	DC current gain	I _C =1A ; V _{CE} =5V	8			
f _T	Transition frequency	I _C =0.1A; V _{CE} =10V		3		MHz
Сов	Collector output capacitance	I _E =0 ; V _{CB} =10V;f=1MHz		165		pF
V _F	Diode forward voltage	I _F =5A			2.0	٧
t _f	Fall time	I _{CP} =4A ;I _{B1(end)} =0.8A		0.5	1.0	μs

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PACKAGE OUTLINE

