

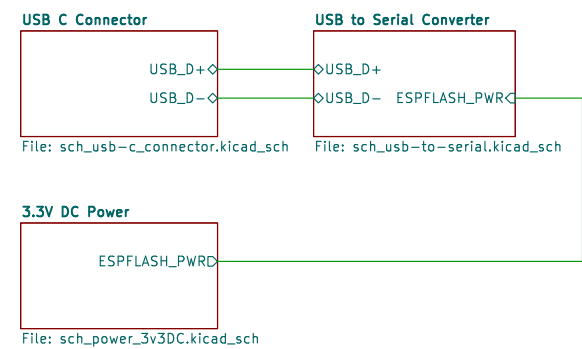
# YAOEF (Yet Another Open ESP Flasher)

Issued 2025-03-20

Status: PROTOTYPE

Rev 1.1

## PROJECT ARCHITECTURE



## PROJECT DESCRIPTION

A design for a simple "dongle" for flashing ESP32 based devices that have the ESPFlash header implemented (per Jon Ozer, SuperHouse. Ref <http://superhouse.tv/espflash>).

Why build USB capabilities into every ESP32 design when a simple header will suffice? Saves on parts count, additional footprint, etc. The dongle supports auto-programming logic. A power switch allows the dongle to be used for serial debugging when under circuit power (i.e., turn the switch off to disable USB power to ESPFlash header).

## PROJECT NOTES

N/a

## DESIGN NOTES KEY

DESIGN NOTE:  
Example text for  
informational design notes.

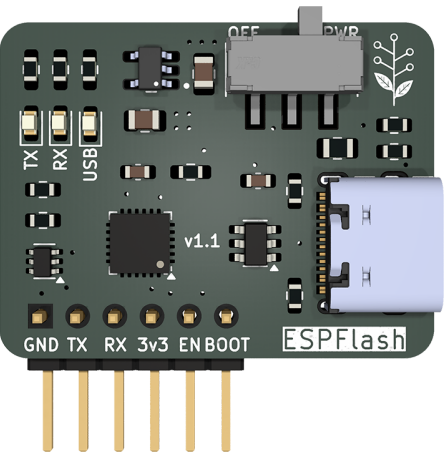
DESIGN NOTE:  
Example text for cautionary  
design notes.


DESIGN NOTE:  
Example text for critical  
design notes.

LAYOUT NOTE:  
Example text for critical  
layout guidelines.

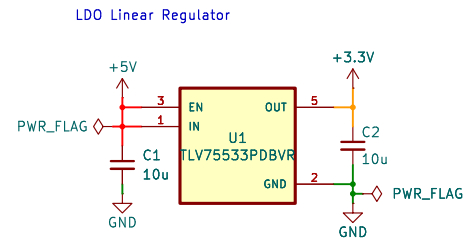
DRAFT – Very early stage of schematic, ignore details.  
PRELIM – Close to final schematic.  
PROTOTYPE – Untested in its built form.  
TESTED – A board with this schematic has been built and tested.

## TOP VIEW



Title: YAOEF (Yet Another Open ESP Flasher)			 <b>Wattle Labs</b>
Sheet: /			
File: ESPFlash.kicad_sch			
Rev: 1.1	Date: 2025-03-20	Id: 1/5	KiCad E.D.A. 9.0.0

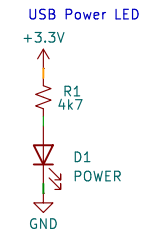
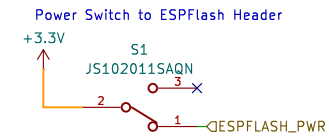
### [3] 3.3v DC Power Supply



#### DESIGN NOTE:

V<sub>IN</sub>: 1.45 to 5.5V

V<sub>OUT</sub>: fixed 3.3V, max 500mA



Title: 3.3v DC Power Supply

Sheet: /3.3V DC Power/

File: sch\_power\_3v3DC.kicad\_sch

Rev: 1.1

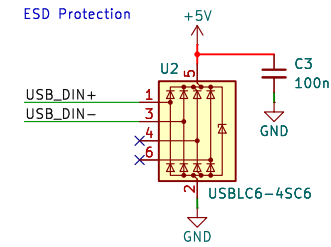
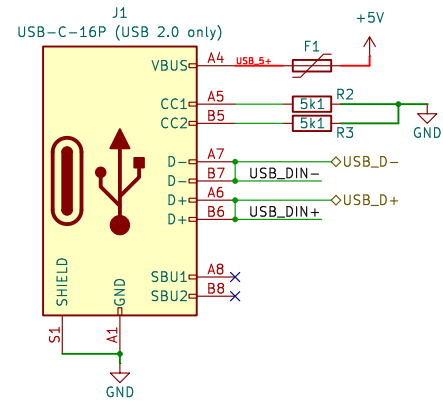
Date: 2025-03-20

Id: 3/5

KiCad E.D.A. 9.0.0



## [4] USB C Connector & ESD Protection



Title: USB C Connector & ESD Protection

Sheet: /USB C Connector/

File: sch\_usb-c\_connector.kicad\_sch

Rev: 1.1

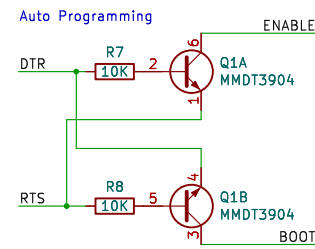
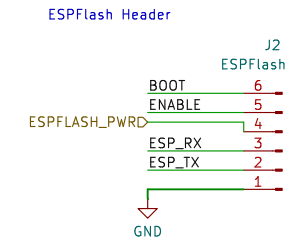
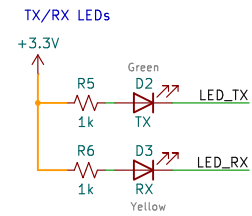
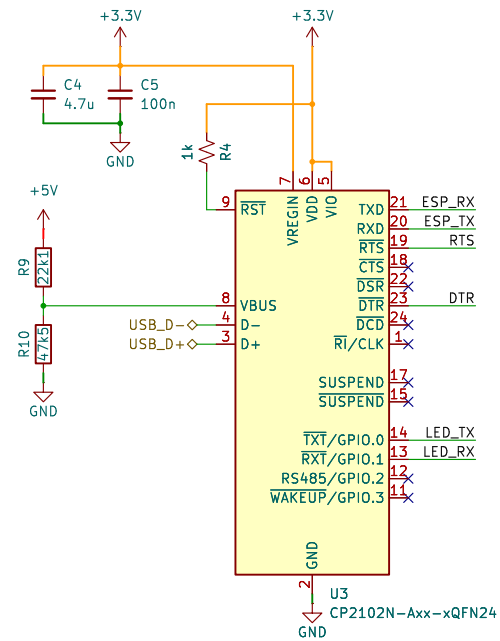
Date: 2025-03-20

Id: 4/5

KiCad E.D.A. 9.0.0



## [5] USB to Serial Converter



## [99] Revision History

14-Mar-2025 – Rev 1.0  
Status: Prototype


Initial version.

20-Mar-2025 – Rev 1.1  
Status: Prototype

\* Swapped LDO regulator for a SOT-23 device (previous was large SOT223).  
This opens up room on the board for a power switch.

\* Added surface mount power switch to allow 3.3V to be disabled on ESPFlash  
header. Allows in circuit serial monitoring/flashing when circuit is under normal  
power.

xx-xxx-20xx – Rev 0.0  
Status: ???

Title: Revision History			 Wattle Labs
Sheet: /Revision History/ File: project history.kicad_sch			
Rev: 1.1	Date: 2025-03-20	Id: 99/5	