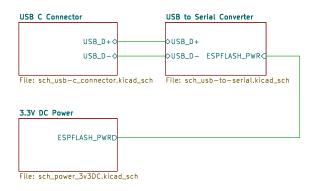
Status: PROTOTYPE Rev 1.1

PROJECT ARCHITECTURE



PROJECT DESCRIPTION

A design for a simple "dongle" for flashing ESP32 based devices that have the ESPFlash header implemented (per Jon Oxer, SuperHouse. Ref http://superhouse.tv/espflash).

Why build USB capabilities into every ESP32 design when a simple header will suffice? Saves on parts count, additional footprint, etc. The dongle supports auto-programming logic. A power switch allows the dongle to be used for serial debugging when under circuit power (I.e., turn the switch off to disable USB power to ESPFlash header).

PROJECT NOTES

N/a

DESIGN NOTES KEY

DESIGN NOTE:

Example text for informational design notes.

DESIGN NOTE:

Example text for cautionary design notes.

DESIGN NOTE:

Example text for critical design notes.

LAYOUT NOTE:

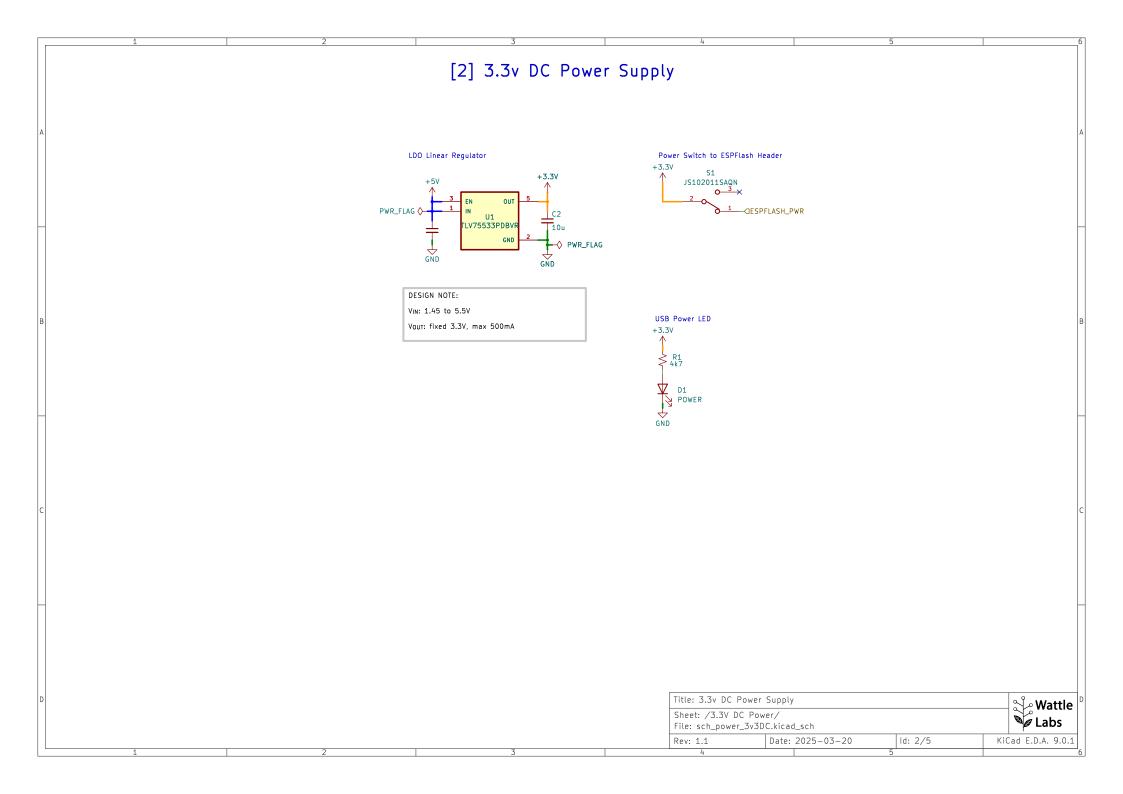
Example text for critical layout guidelines.

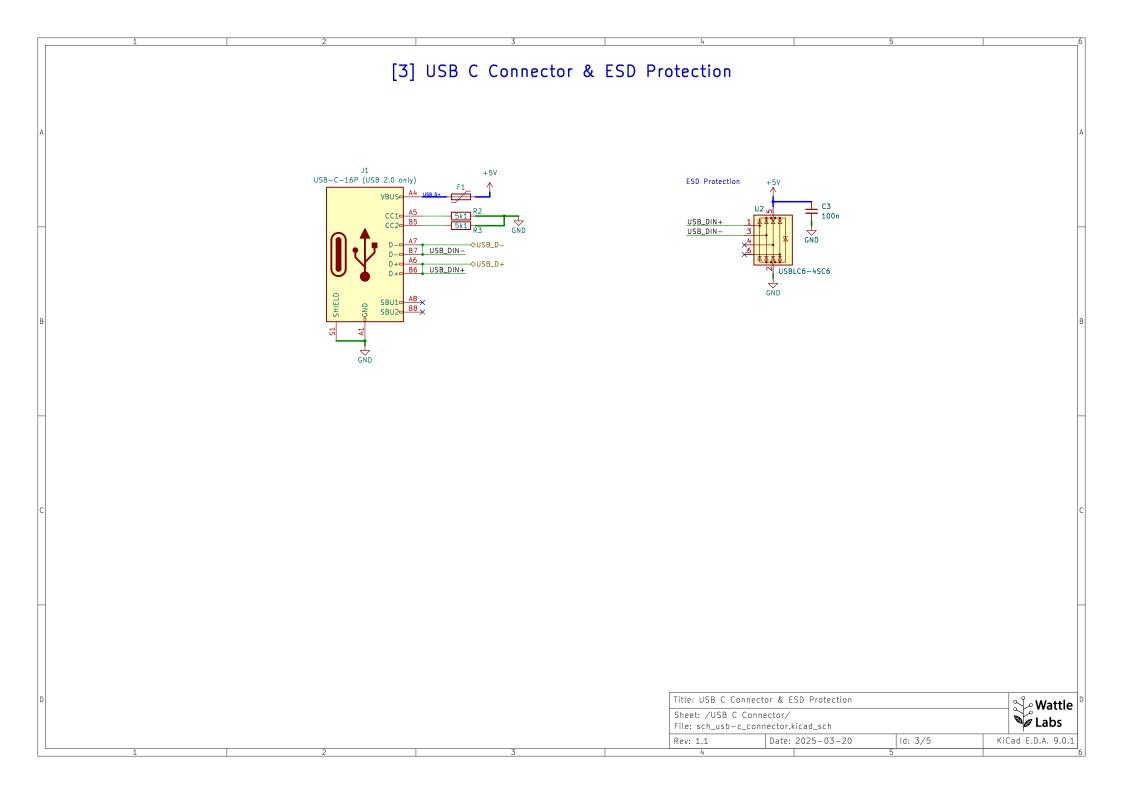
DRAFT — Very early stage of schematic, ignore details.
PRELIM — Close to final schematic.
PROTOTYPE — Untested in its built form.
TESTED — A board with this schematic has been built and tested.

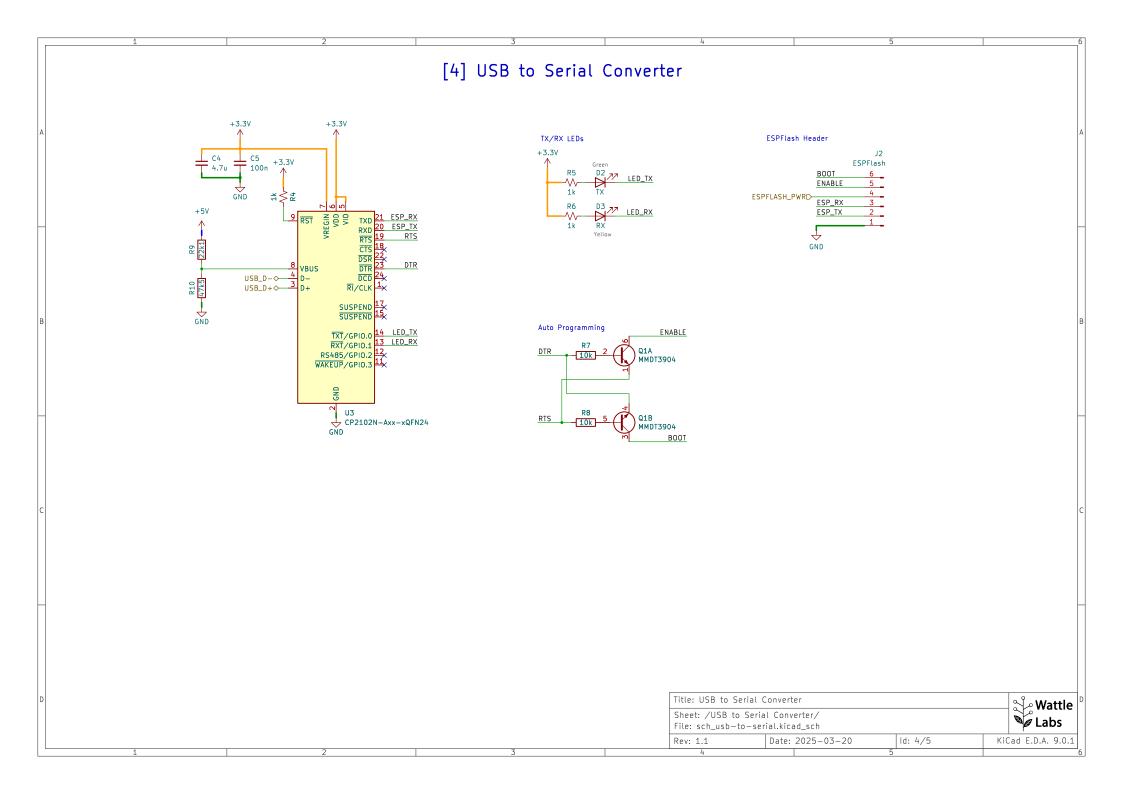
TOP VIEW



Title: YAOEF (Yet Another Open ESP Flasher)				∛ Wattle
Sheet: / File: ESPFlash.kicad_sch			Labs	
Rev: 1.1	Date: 2025-03-20	ld: 1/5	Kid	Cad E.D.A. 9.0.1







[5] Revision History 14-Mar-2025 - Rev 1.0 20-Mar-2025 - Rev 1.1 xx-xxx-20xx - Rev 0.0Status: ??? Status: Prototype Status: Prototype * Swapped LDO regulator for a SOT-23 device (previous was large SOT223). This opens up room on the board for a power switch. Initial version. * Added surface mount power switch to allow 3.3V to be disabled on ESPFlash header. Allows in circuit serial monitoring/flashing when circuit is under normal * Added tear drops to vias and critical through—holes. * Updated BOM entries to include DigiKey cut-tape options (and noted own stocked items). Title: Revision History Sheet: /Revision History/ File: project history.kicad_sch Rev: 1.1 Date: 2025-03-20 ld: 5/5 KiCad E.D.A. 9.0.1