



This PCB revision uses a stripboard (hence predefined tracks).

Note some DIP pins from ATmega socket were removed

Revision

- Rev. 1: Compact PCB
- Rev. 1b: Optimised for daughter cards
- Rev. 1c: Optimised for buttons placement

See  
[https://github.com/comododragon/seikosha80/blob/main/schematics\\_layouts/rev1/kicadproj/output.pdf](https://github.com/comododragon/seikosha80/blob/main/schematics_layouts/rev1/kicadproj/output.pdf) for discrete schematic

Sheet:  
File: kicadproj.kicad\_pcb

**Title: Seikosha80 Westminster Chimer**

Size: A4	Date:	Rev: 1c
KiCad E.D.A. 8.0.1		Id: 1/1