

This PCB revision (hence predefined tracks).

Note some DIP pins from ATmega socket were removed

Rev. 1: Compact PCB Rev. 1b: Optimised for daughter cards Rev. 1c: Optimised for buttons placement

https://github.com/comododragon/seikosha80/blob/main/schematics_layouts/rev1/kicadproj/output.pdf for discrete schematic

Sheet: File: kicadproj.kicad_pcb

Title: Seikosha80 Westminster Chimer

Size: A4 Date: Rev: 1b KiCad E.D.A. 8.0.1 ld: 1/1