**Module Design Document**

**For**

**BmwHaptcFb**

**May 18, 2018**

**Prepared For:**

**Software Engineering**

**Nexteer Automotive,**

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**Saginaw, MI, USAChange History**

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| **Description** | **Author** | **Version** | **Date** |
| Initial version | Krzysztof Byrski | 1 | 18-May-2018 |

**Table of Contents**

[1 Introduction 4](#_Toc514415834)

[1.1 Purpose 4](#_Toc514415835)

[1.2 Scope 4](#_Toc514415836)

[2 BmwHaptcFb & High-Level Description 5](#_Toc514415837)

[3 Design details of software module 6](#_Toc514415838)

[3.1 Graphical representation of BmwHaptcFb 6](#_Toc514415839)

[3.2 Data Flow Diagram 6](#_Toc514415840)

[3.2.1 Component level DFD 6](#_Toc514415841)

[3.2.2 Function level DFD 6](#_Toc514415842)

[4 Constant Data Dictionary 7](#_Toc514415843)

[4.1 Program (fixed) Constants 7](#_Toc514415844)

[4.1.1 Embedded Constants 7](#_Toc514415845)

[5 Software Component Implementation 8](#_Toc514415846)

[5.1 Sub-Module Functions 8](#_Toc514415847)

[5.1.1 Init: BmwHaptcFbInit1 8](#_Toc514415848)

[5.1.2 Per: BmwHaptcFbPer1 8](#_Toc514415849)

[5.2 Server Runables 8](#_Toc514415850)

[5.3 Interrupt Functions 8](#_Toc514415851)

[5.4 Module Internal (Local) Functions 9](#_Toc514415852)

[5.4.1 Local Function CalcBmwHaptcFbPatNr 9](#_Toc514415853)

[5.4.2 Local Function CalcBmwHaptcFbIntenNr 9](#_Toc514415854)

[5.4.3 Local Function CalcHwOscnEna 10](#_Toc514415855)

[5.4.4 Local Function CalcAmpAndFrq 10](#_Toc514415856)

[5.5 GLOBAL Function/Macro Definitions 11](#_Toc514415857)

[6 Known Limitations with Design 12](#_Toc514415858)

[7 UNIT TEST CONSIDERATION 13](#_Toc514415859)

[Appendix A Abbreviations and Acronyms 14](#_Toc514415860)

[Appendix B Glossary 15](#_Toc514415861)

[Appendix C References 16](#_Toc514415862)

# Introduction

## Purpose

Module Design Document for CF020A\_BmwHaptcFb\_Impl.

## Scope

The following definitions are used throughout this document:

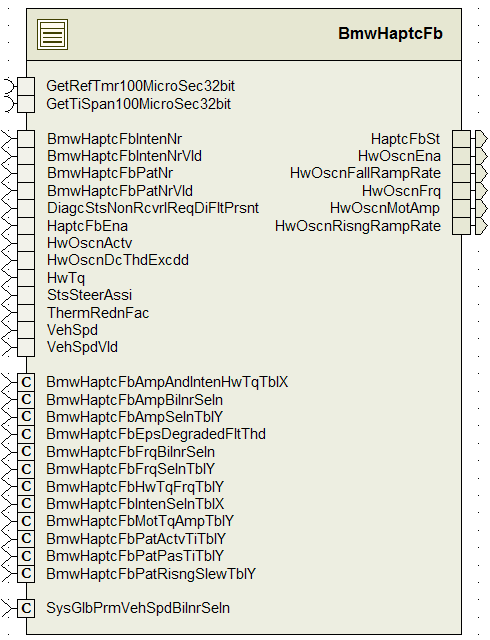
* **Shall**: indicates a mandatory requirement without exception in compliance.
* **Should**: indicates a mandatory requirement; exceptions allowed only with documented justification.
* **May**: indicates an optional action.

# BmwHaptcFb & High-Level Description

This function will alert the driver through handwheel haptic feedback if a lane departure is detected. This function accepts pulse pattern, amplitude, and frequency inputs from the customer and then provides an enable/disable signal.

# Design details of software module

## Graphical representation of BmwHaptcFb



## Data Flow Diagram

### Component level DFD

Refer FDD

### Function level DFD

Refer FDD

# Constant Data Dictionary

## Program (fixed) Constants

### Embedded Constants

#### Local Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| - |  |  |  |

\*Refer FDD for local constants

# Software Component Implementation

## Sub-Module Functions

The sub-module functions are grouped based on similar functionality that needs to be executed in a given “State” of the system (refer States and Modes). For a given module, the MDD will identify the type and number of sub-modules required. The sub-module types are described below.

### Init: Init1

#### Design Rationale

Refer FDD

#### Module Outputs

Refer FDD

### Per: Per1

#### Design Rationale

Refer FDD

#### Store Module Inputs to Local copies

Refer FDD

#### (Processing of function)………

Refer FDD

#### Store Local copy of outputs into Module Outputs

Refer FDD

## Server Runables

None

## Interrupt Functions

None

## Module Internal (Local) Functions

### Local Function CalcBmwHaptcFbPatNr

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcBmwHaptcFbPatNr | Type | Min | Max |
| **Arguments Passed** | BmwHaptcFbPatNr\_Cnt\_T\_enum | enum | 0 | 15 |
|  | BmwHaptcFbPatNrVld\_Cnt\_T\_logl | boolean | FALSE | TRUE |
|  | HwOscnActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | BmwHaptcFbPatNr\_Cnt\_T\_enum | enum | 0 | 15 |
|  | BmwHaptcFbPatNrLgc\_Cnt\_T\_logl | boolean | FALSE | TRUE |

#### Design Rationale

Implementation of "CalcBmwHaptcFbPatNr" and "BmwHaptcFbPatNrLgc" Simulink block

#### Processing

Refer FDD

### Local Function CalcBmwHaptcFbIntenNr

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcBmwHaptcFbIntenNr | Type | Min | Max |
| **Arguments Passed** | BmwHaptcFbIntenNr\_Cnt\_T\_enum | enum | 0 | 15 |
|  | BmwHaptcFbIntenNrVld\_Cnt\_T\_logl | boolean | FALSE | TRUE |
|  | HwOscnActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | BmwHaptcFbIntenNr\_Cnt\_T\_enum | enum | 0 | 15 |
|  | BmwHaptcFbIntenNrLgc\_Cnt\_T\_logl | boolean | FALSE | TRUE |

#### Design Rationale

Implementation of "CalcBmwHaptcFbIntenNr" and "BmwHaptcFbIntenNrLgc" Simulink block

#### Processing

Refer FDD

### Local Function CalcHwOscnEna

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcHwOscnEna | Type | Min | Max |
| **Arguments Passed** | PatChgReq\_Cnt\_T\_logl | float32 | FALSE | TRUE |
|  | ActvTi\_MilliSec\_T\_f32 | float32 | 0 | 60 |
|  | PasTi\_MilliSec\_T\_f32 | float32 | 0 | 3 |
| **Return Value** | HwOscnEna\_Cnt\_T\_logl | boolean | FALSE | TRUE |

#### Design Rationale

Implementation of "CalcHwOscnEna" Simulink block

#### Processing

Refer FDD

### Local Function CalcAmpAndFrq

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcAmpAndFrq | Type | Min | Max |
| **Arguments Passed** | VehSpd\_Kph\_T\_f32 | float32 | 0 | 511 |
|  | HwTq\_HwNwtMtr\_T\_f32 | float32 | -10 | 10 |
| **Return Value** | HwOscnMotAmp\_MotNwtMtr\_T\_f32 | float32 | 0 | 10 |
|  | HwOscnFrq\_Hz\_T\_f32 | float32 | 10 | 50 |

#### Design Rationale

Implementation of "CalcAmpAndFrq" Simulink block

#### Processing

Refer FDD

## GLOBAL Function/Macro Definitions

None

# Known Limitations with Design

None

# UNIT TEST CONSIDERATION

None

1. Abbreviations and Acronyms

| **Abbreviation or Acronym** | **Description** |
| --- | --- |
| FDD | Functional Design Document. (See references) |

1. Glossary

**Note**: Terms and definitions from the source “Nexteer Automotive” take precedence over all other definitions of the same term. Terms and definitions from the source “Nexteer Automotive” are formulated from multiple sources, including the following:

* ISO 9000
* ISO/IEC 12207
* ISO/IEC 15504
* Automotive SPICE® Process Reference Model (PRM)
* Automotive SPICE® Process Assessment Model (PAM)
* ISO/IEC 15288
* ISO 26262
* IEEE Standards
* SWEBOK
* PMBOK
* Existing Nexteer Automotive documentation

| **Term** | **Definition** | **Source** |
| --- | --- | --- |
| MDD | Module Design Document |  |
| DFD | Data Flow Diagram |  |

1. References

| **Ref. #** | **Title** | **Version** |
| --- | --- | --- |
| 1 | AUTOSAR Specification of Memory Mapping (Link:[AUTOSAR\_SWS\_MemoryMapping.pdf](https://www.autosar.org/fileadmin/files/standards/classic/4-0/software-architecture/implementation-integration/standard/AUTOSAR_SWS_MemoryMapping.pdf)) | v1.4.0 R4.0 Rev 3 |
| 2 | MDD Guideline EA4 | 1.02 |
| 3 | EA4 Software Naming Conventions | 1.01 |
| 4 | Software Design and Coding Standards | 2.01 |
| 5 | CF020A\_BmwHaptcFb\_Design | See Synergy Sub Project Version |