**Module Design Document**

**For**

**BmwTqOvrlCdngAndDrvgDynFac**

**April 26, 2018**

**Prepared For:**

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| --- | --- | --- | --- |
| **Description** | **Author** | **Version** | **Date** |
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**Table of Contents**

[1 Introduction 5](#_Toc512505866)

[1.1 Purpose 5](#_Toc512505867)

[1.2 Scope 5](#_Toc512505868)

[2 BmwTqOvrlCdngAndDrvgDynFac & High-Level Description 6](#_Toc512505869)

[3 Design details of software module 7](#_Toc512505870)

[3.1 Graphical representation of BmwTqOvrlCdngAndDrvgDynFac 7](#_Toc512505871)

[3.2 Data Flow Diagram 7](#_Toc512505872)

[3.2.1 Component level DFD 8](#_Toc512505873)

[3.2.2 Function level DFD 8](#_Toc512505874)

[4 Constant Data Dictionary 9](#_Toc512505875)

[4.1 Program (fixed) Constants 9](#_Toc512505876)

[4.1.1 Embedded Constants 9](#_Toc512505877)

[5 Software Component Implementation 10](#_Toc512505878)

[5.1 Sub-Module Functions 10](#_Toc512505879)

[5.1.1 Init: BmwTqOvrlCdngAndDrvgDynFacInit1 10](#_Toc512505880)

[5.1.2 Per: BmwTqOvrlCdngAndDrvgDynFacPer1 10](#_Toc512505881)

[5.2 Server Runables 10](#_Toc512505882)

[5.3 Interrupt Functions 10](#_Toc512505883)

[5.4 Module Internal (Local) Functions 11](#_Toc512505884)

[5.4.1 CalcCdndTqOvrl 11](#_Toc512505885)

[5.4.2 CalcnDampgCmdSca 11](#_Toc512505886)

[5.4.3 CalcnEffortCmdSca 11](#_Toc512505887)

[5.4.4 CalcnLimdCdndTqOvrl 11](#_Toc512505888)

[5.4.5 CalcnRtnCmdSca 12](#_Toc512505889)

[5.4.6 StTranDetn 12](#_Toc512505890)

[5.4.7 TqOvrlCdng 12](#_Toc512505891)

[5.5 GLOBAL Function/Macro Definitions 12](#_Toc512505892)

[6 Known Limitations with Design 14](#_Toc512505893)

[7 UNIT TEST CONSIDERATION 15](#_Toc512505894)

[Appendix A Abbreviations and Acronyms 16](#_Toc512505895)

[Appendix B Glossary 17](#_Toc512505896)

[Appendix C References 18](#_Toc512505897)

# Introduction

## Purpose

Module Design Document for CF040A\_BmwTqOvrlCdngAndDrvgDynFac\_Impl.

## Scope

The following definitions are used throughout this document:

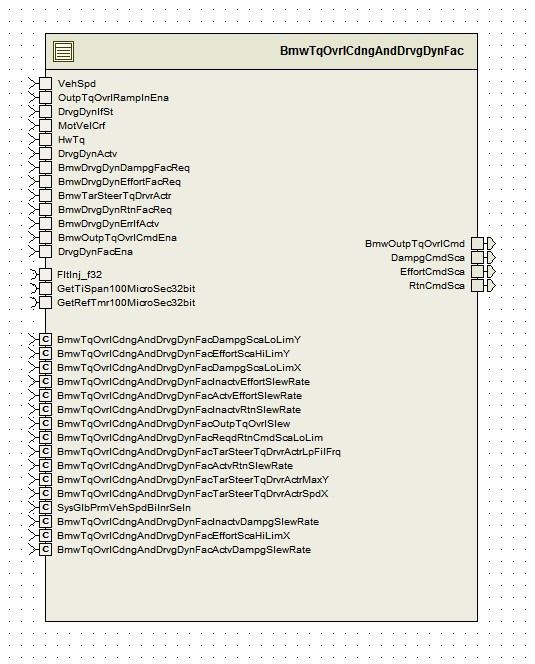
* **Shall**: indicates a mandatory requirement without exception in compliance.
* **Should**: indicates a mandatory requirement; exceptions allowed only with documented justification.
* **May**: indicates an optional action.

# BmwTqOvrlCdngAndDrvgDynFac & High-Level Description

The BMW Torque Overlay Conditioning And Driving Dynamic Factor function handles the filtering and ramping of the BMW Output Torque Overlay Command functionality and conditioning of the BMW Driving Dynamic Factors for Effort/Assist, Return and Damping.

# Design details of software module

## Graphical representation of BmwTqOvrlCdngAndDrvgDynFac



## Data Flow Diagram

Refer FDD

### Component level DFD

Refer FDD

### Function level DFD

Refer FDD

# Constant Data Dictionary

## Program (fixed) Constants

### Embedded Constants

#### Local Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| - |  |  |  |

Refer FDD for local constants.

# Software Component Implementation

## Sub-Module Functions

The sub-module functions are grouped based on similar functionality that needs to be executed in a given “State” of the system (refer States and Modes). For a given module, the MDD will identify the type and number of sub-modules required. The sub-module types are described below.

### Init: BmwTqOvrlCdngAndDrvgDynFacInit1

#### Design Rationale

Refer FDD

#### Module Outputs

Refer FDD

### Per: BmwTqOvrlCdngAndDrvgDynFacPer1

#### Design Rationale

Refer FDD

#### Store Module Inputs to Local copies

Refer FDD

#### (Processing of function)………

Refer FDD

#### Store Local copy of outputs into Module Outputs

Refer FDD

## Server Runables

None

## Interrupt Functions

None

## Module Internal (Local) Functions

### CalcCdndTqOvrl

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcCdndTqOvrl | Type | Min | Max |
| **Arguments Passed** | FildBmwTarSteerTqDrvrActr\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |
|  | BmwTarSteerTqDrvrActr\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |
|  | BmwDrvgDynErrIfActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | CdndTqOvrl\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |

#### 

### CalcnDampgCmdSca

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcnDampgCmdSca | Type | Min | Max |
| **Arguments Passed** | DrvgDynActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
|  | ReqdDampgCmdSca\_Uls\_T\_f32 | float32 | 0 | 1 |
|  | DrvgDynActvTrig\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | - | - | - | - |

### CalcnEffortCmdSca

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcnEffortCmdSca | Type | Min | Max |
| **Arguments Passed** | DrvgDynActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
|  | ReqdEffortCmdSca\_Uls\_T\_f32 | float32 | 1 | 2 |
|  | DrvgDynActvTrig\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | - | - | - | - |

### CalcnLimdCdndTqOvrl

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcnLimdCdndTqOvrl | Type | Min | Max |
| **Arguments Passed** | CdndTqOvrl\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |
|  | VehSpd\_Kph\_T\_u9p7 | uint16 | 0 | 68408 |
| **Return Value** | - | - | - | - |

### CalcnRtnCmdSca

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcnRtnCmdSca | Type | Min | Max |
| **Arguments Passed** | DrvgDynActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
|  | ReqdRtnCmdSca\_Uls\_T\_f32 | float32 | 0 | 1 |
|  | DrvgDynActvTrig\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | - | - | - | - |

### StTranDetn

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | StTranDetn | Type | Min | Max |
| **Arguments Passed** | DrvgDynIfSt\_Cnt\_T\_u08 | uint8 | 32 | 255 |
|  | BmwTarSteerTqDrvrActr\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |
|  | FildBmwTarSteerTqDrvrActr\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |
| **Return Value** | OutpRstTrig\_Cnt\_T\_logl | boolean | FALSE | TRUE |

### TqOvrlCdng

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | TqOvrlCdng | Type | Min | Max |
| **Arguments Passed** | DrvgDynIfSt\_Cnt\_T\_u08 | uint8 | 32 | 255 |
|  | BmwTarSteerTqDrvrActr\_MotNwtMtr\_T\_f32 | float32 | -8.8 | 8.8 |
|  | VehSpd\_Kph\_T\_u9p7 | uint16 | 0 | 68408 |
|  | BmwDrvgDynErrIfActv\_Cnt\_T\_logl | boolean | FALSE | TRUE |
| **Return Value** | OutpRstTrig\_Cnt\_T\_logl | boolean | FALSE | TRUE |

#### Design Rationale

Refer FDD

#### Processing

Refer FDD

## GLOBAL Function/Macro Definitions

None

# Known Limitations with Design

None

# UNIT TEST CONSIDERATION

None

1. Abbreviations and Acronyms

| **Abbreviation or Acronym** | **Description** |
| --- | --- |
| FDD | Functional Design Document. (See references) |

1. Glossary

**Note**: Terms and definitions from the source “Nexteer Automotive” take precedence over all other definitions of the same term. Terms and definitions from the source “Nexteer Automotive” are formulated from multiple sources, including the following:

* ISO 9000
* ISO/IEC 12207
* ISO/IEC 15504
* Automotive SPICE® Process Reference Model (PRM)
* Automotive SPICE® Process Assessment Model (PAM)
* ISO/IEC 15288
* ISO 26262
* IEEE Standards
* SWEBOK
* PMBOK
* Existing Nexteer Automotive documentation

| **Term** | **Definition** | **Source** |
| --- | --- | --- |
| MDD | Module Design Document |  |
| DFD | Data Flow Diagram |  |

1. References

| **Ref. #** | **Title** | **Version** |
| --- | --- | --- |
| 1 | AUTOSAR Specification of Memory Mapping (Link:[AUTOSAR\_SWS\_MemoryMapping.pdf](https://www.autosar.org/fileadmin/files/standards/classic/4-0/software-architecture/implementation-integration/standard/AUTOSAR_SWS_MemoryMapping.pdf)) | v1.4.0 R4.0 Rev 3 |
| 2 | MDD Guideline EA4 | 1.02 |
| 3 | EA4 Software Naming Conventions | 1.01 |
| 4 | Software Design and Coding Standards | 2.01 |
| 5 | CF011A\_BmwTqOvrlCdngAndDrvgDynFac\_Design | See Synergy Sub Project Version |