**Module Design Document**

**For**

**SerlComTrcvIf**

**February 21, 2018**

**Prepared For:**

**Software Engineering**

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|  |  |  |  |
| --- | --- | --- | --- |
| **Description** | **Author** | **Version** | **Date** |
| Initial version (based on Design 1.1.0) | Krzysztof Byrski | 1 | 21-Feb-2018 |

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# Introduction

## Purpose

Module Design Document for SerlComTrcvIf.

## Scope

The following definitions are used throughout this document:

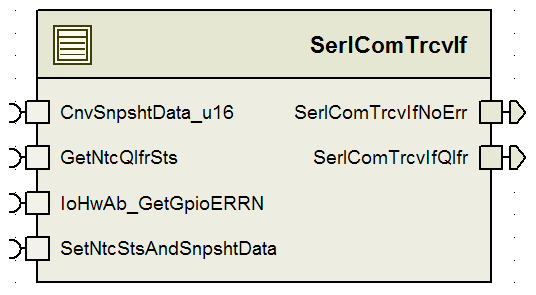
* **Shall**: indicates a mandatory requirement without exception in compliance.
* **Should**: indicates a mandatory requirement; exceptions allowed only with documented justification.
* **May**: indicates an optional action.

# SerlComTrcvIf & High-Level Description

The Serial Communication Transceiver Interface function monitors error status of FlexRay transceiver, reads the Status Registers via SPI and sets appropriate NTCs.

# Design details of software module

## Graphical representation of SerlComTrcvIf



## Data Flow Diagram

Refer FDD

### Component level DFD

Refer FDD

### Function level DFD

Refer FDD

# Constant Data Dictionary

## Program (fixed) Constants

### Embedded Constants

#### Local Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| SERLCOMTRCVIFNTCMASKBIT5\_CNT\_U08 | 1 | Cnt | 32 |
| SERLCOMTRCVIFNTCMASKBIT7\_CNT\_U08 | 1 | Cnt | 128 |
| SERLCOMTRCVIFRESDBITS\_CNT\_U16 | 1 | Cnt | 20480 |
| SERLCOMTRCVIFSPIERRCNTRMAX\_CNT\_U08 | 1 | Cnt | 1 |
| STMONRERRPIN\_CNT\_U08 | 1 | Cnt | 1 |
| STREADANDDECOD\_CNT\_U08 | 1 | Cnt | 2 |
| SERLCOMTRCVIFMASKBITS11TO14\_CNT\_U16 | 1 | Cnt | 0x7800 |
| SERLCOMTRCVIFMASKBITS5TO10\_CNT\_U16 | 1 | Cnt | 0x7E0 |

# Software Component Implementation

## Sub-Module Functions

The sub-module functions are grouped based on similar functionality that needs to be executed in a given “State” of the system (refer States and Modes). For a given module, the MDD will identify the type and number of sub-modules required. The sub-module types are described below.

### Init: SerlComTrcvIfInit1

#### Design Rationale

Refer FDD

#### Module Outputs

None

### Per: SerlComTrcvIfPer1

#### Design Rationale

Refer FDD

#### Store Module Inputs to Local copies

None

#### (Processing of function)………

Refer FDD

#### Store Local copy of outputs into Module Outputs

Refer FDD

## Server Runables

None

## Interrupt Functions

None

## Module Internal (Local) Functions

### Local Function MonitorERRN

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | MonitorERRN | Type | Min | Max |
| **Arguments Passed** | None |  |  |  |
| **Return Value** | None |  |  |  |

#### Design Rationale

Implementation of Simulink block "MonitorERRN".

#### Processing

Refer Simulink block "MonitorERRN".

### Local Function ReadAndAnalyze

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | ReadAndAnalyze | Type | Min | Max |
| **Arguments Passed** | None |  |  |  |
| **Return Value** | None |  |  |  |

#### Design Rationale

Implementation of Simulink block "ReadAndAnalyze".

#### Processing

Refer Simulink block "ReadAndAnalyze".

### Local Function AnalyzeRegister

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | AnalyzeRegister | Type | Min | Max |
| **Arguments Passed** | RegIn\_Cnt\_T\_u16 | uint16 | 0 | 65535 |
| **Return Value** | NtcStsInfo\_Cnt\_T\_u08 | uint8 | 0 | 128 |

#### Design Rationale

Implementation of Simulink block "AnalyzeRegister".

#### Processing

Refer Simulink block "AnalyzeRegister".

### Local Function ParityErrorCheck

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | ParityErrorCheck | Type | Min | Max |
| **Arguments Passed** | RegIn\_Cnt\_T\_u16 | uint16 | 0 | 65535 |
| **Return Value** | Parity\_Cnt\_T\_logl | boolean | FALSE | TRUE |

#### Design Rationale

Implementation of Simulink block "ParityErrorCheck".

#### Processing

Refer Simulink block "ParityErrorCheck".

## GLOBAL Function/Macro Definitions

None

# Known Limitations with Design

None

# UNIT TEST CONSIDERATION

None

1. Abbreviations and Acronyms

| **Abbreviation or Acronym** | **Description** |
| --- | --- |
| FDD | Functional Design Document. (See references) |

1. Glossary

**Note**: Terms and definitions from the source “Nexteer Automotive” take precedence over all other definitions of the same term. Terms and definitions from the source “Nexteer Automotive” are formulated from multiple sources, including the following:

* ISO 9000
* ISO/IEC 12207
* ISO/IEC 15504
* Automotive SPICE® Process Reference Model (PRM)
* Automotive SPICE® Process Assessment Model (PAM)
* ISO/IEC 15288
* ISO 26262
* IEEE Standards
* SWEBOK
* PMBOK
* Existing Nexteer Automotive documentation

| **Term** | **Definition** | **Source** |
| --- | --- | --- |
| MDD | Module Design Document |  |
| DFD | Data Flow Diagram |  |

1. References

| **Ref. #** | **Title** | **Version** |
| --- | --- | --- |
| 1 | AUTOSAR Specification of Memory Mapping (Link:[AUTOSAR\_SWS\_MemoryMapping.pdf](https://www.autosar.org/fileadmin/files/standards/classic/4-0/software-architecture/implementation-integration/standard/AUTOSAR_SWS_MemoryMapping.pdf)) | v1.4.0 R4.0 Rev 3 |
| 2 | MDD Guideline EA4 | 1.02 |
| 3 | EA4 Software Naming Conventions | 1.01 |
| 4 | Software Design and Coding Standards | 2.01 |
| 5 | ES340A\_SerlComTrcvIf\_Design | See Synergy Sub Project Version |