**Module Design Document**

**For**

**HysCmp**

**Jan 04, 2017**

**Prepared For:**

**Software Engineering**

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**Saginaw, MI, USAChange History**

|  |  |  |  |
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# Introduction

## Purpose

## Scope

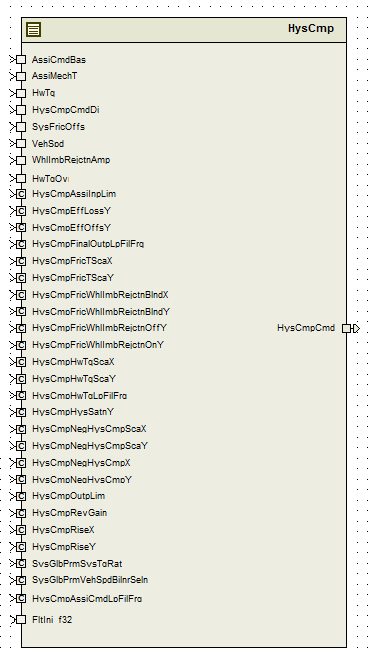
# HysCmp & High-Level Description

*Refer FDD*

# Design details of software module

*Refer FDD*

## Graphical representation of HysCmp



## Data Flow Diagram

Refer FDD

### Component level DFD

Refer FDD

### Function level DFD

Refer FDD

# Constant Data Dictionary

## Program (fixed) Constants

### Embedded Constants

#### Local Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| Refer .m file |  |  |  |

# Software Component Implementation

Refer FDD

## Sub-Module Functions

## Init: HysCmpInit1

Refer FDD

## Per: HysCmpPer1

*Refer FDD*

## Server Runables

None

## Interrupt Functions

None

## Module Internal (Local) Functions

## Local Function #1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | MoreCmp | Type | Min | Max |
| **Arguments Passed** | TqChg\_HwNwtMtr\_T\_f32 | Float32 | 0 | 20 |
|  | \*RiseXPtr\_HwNwtMtr\_T\_f32 | Float32 | 0 | 1 |
|  | \*RiseXFac\_HwNwtMtr\_T\_f32 | Float32 | 0 | 1 |
| **Return Value** | RiseY\_Uls\_T\_f32 | Float32 | 0 | 1 |

## Design Rationale

None

## Processing

Refer ‘MoreCmp’ block in Simulink model

## Local Function #1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | LessCmp | Type | Min | Max |
| **Arguments Passed** | TqChg\_HwNwtMtr\_T\_f32 | Float32 | 0 | 20 |
|  | \*RiseYPtr\_Uls\_T\_f32 | Float32 | 0 | 1 |
|  | \*RiseXFac\_HwNwtMtr\_T\_f32 | Float32 | 0 | 1 |
| **Return Value** | RiseY\_Uls\_T\_f32 | Float32 | 0 | 1 |

## Design Rationale

None

## Processing

Refer ‘LessCmp’ block in Simulink model

## Local Function #1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | CalcAvlCmp | Type | Min | Max |
| **Arguments Passed** | HwTqFildVal\_HwNwtMtr\_T\_f32 | Float32 | -10 | 10 |
|  | AssiCmdFildVal\_HwNwtMtr\_T\_f32 | Float32 | -352 | 352 |
| **Return Value** | HysCmpAvl\_HwNwtMtr\_T\_f32 | Float32 | -8.8 | 8.8 |

## Design Rationale

None

## Processing

Refer ‘CalcAvlCmp’ block in Simulink model

## GLOBAL Function/Macro Definitions

None

# Known Limitations with Design

None

# UNIT TEST CONSIDERATION

None

Abbreviations and Acronyms

| **Abbreviation or Acronym** | **Description** |
| --- | --- |
|  |  |
|  |  |

Glossary

**Note**: Terms and definitions from the source “Nexteer Automotive” take precedence over all other definitions of the same term. Terms and definitions from the source “Nexteer Automotive” are formulated from multiple sources, including the following:

* ISO 9000
* ISO/IEC 12207
* ISO/IEC 15504
* Automotive SPICE® Process Reference Model (PRM)
* Automotive SPICE® Process Assessment Model (PAM)
* ISO/IEC 15288
* ISO 26262
* IEEE Standards
* SWEBOK
* PMBOK
* Existing Nexteer Automotive documentation

| **Term** | **Definition** | **Source** |
| --- | --- | --- |
| MDD | Module Design Document |  |
| DFD | Data Flow Diagram |  |

References

| **Ref. #** | **Title** | **Version** |
| --- | --- | --- |
| 1 | AUTOSAR Specification of Memory Mapping (Link:[AUTOSAR\_SWS\_MemoryMapping.pdf](http://www.autosar.org/download/R4.0/AUTOSAR_SWS_MemoryMapping.pdf)) | v1.3.0 R4.0 Rev 2 |
| 2 | MDD Guideline | Process 04.02.00 |
| 3 | [Software Naming Conventions.doc](http://misagweb01.nexteer.com/eRoomReq/Files/erooms8/NextGeneration/0_fc55f/Software%20Naming%20Conventions%2003x(In%20Work).doc) | Process 04.02.00 |
| 4 | [Software Design and Coding Standards.doc](http://eroom1.nexteer.com/eRoomReq/Files/erooms8/NextGeneration/0_1a67a9/Software%20Design%20and%20Coding%20Standards.doc) | Process 04.02.00 |
| 5 | FDD – SF012A\_HysCmp\_Design | See Synergy SubProject version |