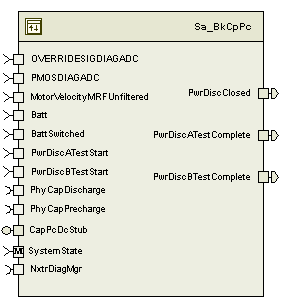
# Module –

# High-Level Description

This module handles precharging of the bulk capacitor during initialization. It is part of a larger initialization sequence, along with motor driver diagnostics and temporal monitor.

# Figures

## Component Diagram



# Variable Data Dictionary

For details on module input / output variable, refer to the Data Dictionary for the application. Input / output variable names are listed here for reference.

|  |  |  |
| --- | --- | --- |
| Module Inputs | Module Outputs | |
| OVERRIDESIGDIAGADC\_Volt\_f32 | | PwrDiscClosed\_Cnt\_lgc |
| PMOSDIAGADC\_Volt\_f32 | | PwrDiscATestComplete\_Cnt\_lgc |
| MotorVelocityMRFUnfiltered\_MtrRadpS\_f32 | | PwrDiscBTestComplete\_Cnt\_lgc |
| Batt\_Volt\_f32 | |  |
| BattSwitched\_Volt\_f32 | |  |
| PwrDiscATestStart\_Cnt\_lgc | |  |
| PwrDiscBTestStart\_Cnt\_lgc | |  |

## Module Internal Variables

This section identifies the name, range and resolutions for module specific data created by this module. If there are no range restrictions on the variable, the term “FULL” is placed into the table for legal range.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Variable Name | Resolution | Legal Range  (min) | Legal Range  (max) | Software Segment |
| FirstRunComplete\_Cnt\_M\_lgc | n/a | FALSE | TRUE | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| PowerRelayInitFltFailed\_Cnt\_M\_lgc | n/a | FALSE | TRUE | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| PwrDiscATestComplete\_Cnt\_M\_lgc | n/a | FALSE | TRUE | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| PwrDiscBTestComplete\_Cnt\_M\_lgc | n/a | FALSE | TRUE | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| PwrDiscClosed\_Cnt\_M\_lgc | n/a | FALSE | TRUE | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| BulkCapPrechargeState\_Cnt\_M\_enum | 1 | 0 | 7 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| RunTimeFaultAcc\_Cnt\_M\_u16 | 1 | FULL | FULL | BKCPPC\_START\_SEC\_VAR\_CLEARED\_16 |
| VerifyDiscOpenDiagTimer\_mS\_M\_u32 | 1 | FULL | FULL | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| WaitForSqrWaveDiagTimer\_mS\_M\_u32 | 1 | FULL | FULL | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| PrechargeDiagTimer\_mS\_M\_u32 | 1 | FULL | FULL | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| PostCloseDiagTimer\_mS\_M\_u32 | 1 | FULL | FULL | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| VerifyCloseDiagTimer\_mS\_M\_u32 | 1 | FULL | FULL | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| VdischMax\_Volts\_M\_f32 | Single Precision Float | 0 | 21 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| VdischMin\_Volts\_M\_f32 | Single Precision Float | 0 | 19 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| VbattStart\_Volts\_M\_f32 | Single Precision Float | 0 | 30 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| VswitchStart\_Volts\_M\_f32 | Single Precision Float | 0 | 20 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| MotionDetected\_Cnt\_D\_lgc | n/a | FALSE | TRUE | BKCPPC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| DeltaV\_Volts\_D\_f32 | Single Precision Float | -20 | 30 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |
| VswitchCorrected\_Volts\_D\_f32 | Single Precision Float | 0 | 120 | BKCPPC\_START\_SEC\_VAR\_CLEARED\_32 |

### User defined typedef definition/declaration

This section documents any user types uniquely used for the module.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Typedef Name | Element Name | User Defined Type | Legal Range  (min) | Legal Range  (max) |
| BulkCapPrechargeSequenceType | BULKCAP\_WAITFORSTARTA = 0  BULKCAP\_VERIFYDISCOPEN = 1  BULKCAP\_WAITFORSQRWAVE = 2  BULKCAP\_PRECHARGE = 3  BULKCAP\_WAITFORSTARTB = 4  BULKCAP\_POSTCLOSE = 5  BULKCAP\_VERIFYCLOSE = 6  BULKCAP\_RUNTIMEDIAG = 7 | uint8 | 0 | 7 |

# Constant Data Dictionary

## Calibration Constants

This section lists the calibrations used by the module. For details on calibration constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| k\_MtrMotionThresh\_MtrRadpS\_f32 |
| k\_MaxSwitchedVolt\_Volts\_f32 |
| k\_PwrDiscOpenThresh\_Volts\_f32 |
| k\_PMOSDIAGOpenThresh\_Volts\_f32 |
| k\_OVERRIDESIGDIAGOpenThresh\_Volts\_f32 |
| k\_VerifyPwrDiscOpenThresh\_mS\_u16 |
| k\_WaitForSqrWaveThresh\_mS\_u16 |
| k\_PwrDiscCloseThresh\_Volts\_f32 |
| k\_PrechargeThresh\_mS\_u16 |
| k\_PMOSVError\_Volts\_f32 |
| k\_PMOSTError\_mS\_u16 |
| k\_MaxDischEst\_Uls\_f32 |
| k\_MinDischEst\_Uls\_f32 |
| k\_VswitchDeltaThresh\_Volts\_f32 |
| k\_VerifyPwrDiscCloseThresh\_mS\_u16 |
| k\_ChargeMinDelta\_Volts\_f32 |
| k\_VbattSwitchThreshNonExt\_Volt\_f32 |
| k\_VbattSwitchThreshExNorm\_Volt\_f32 |
| k\_ChargeMinDeltaNonOp\_Volt\_f32 |
| k\_ChargeMinDeltaExtOp\_Volt\_f32 |
| k\_ChargeMinDeltaNormlOp\_Volt\_f32 |
| k\_ChargePumpDiag\_Cnt\_str |

## Program(fixed) Constants

### Embedded Constants

All embedded constants whose values are provided in Eng units will be evaluated to the equivalent counts by using the FPM\_InitFixedPoint\_m() macro within the #define statement.

#### Local

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| D\_VDISCHMAXFACTOR\_ULS\_F32 | Single Precision Float | Unitless | 1.05 |
| D\_VDISCHMINFACTOR\_ULS\_F32 | Single Precision Float | Unitless | 0.95 |
| D\_PWRDISCCONFIGB\_CNT\_U08 | 1 | Count | 2 |

#### Global

This section lists the global constants used by the module. For details on global constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| STD\_LOW |
| STD\_HIGH |
| D\_ZERO\_CNT\_U16 |
| RTE\_E\_OK (0) - see Data Dictionary |
| D\_PWRDISCCONFIGURATION\_CNT\_U08 - see Data Dictionary |

### Module specific Lookup Tables Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Value | Software Segment |
| None |  |  |  |

# Functions/Macros used by the Sub-Modules

## Library Functions / Macros

The library and functions / Macros that are called by the various sub modules are identified below,

1. Abs\_f32\_m
2. Min\_m
3. DiagPStep\_m
4. DiagNStep\_m
5. DiagFailed\_m

## Data Hiding Functions

1. Rte\_Call\_SystemTime\_GetSystemTime\_mS\_u32
2. Rte\_Call\_SystemTime\_DtrmnElapsedTime\_mS\_u16
3. Rte\_Call\_NxtrDiagMgr\_SetNTCStatus
4. Rte\_Call\_Vbatt\_Batt\_V\_f32
5. Rte\_Call\_Vswitch\_BattSwitched\_V\_f32
6. Rte\_Call\_PhyCapPrecharge\_OP\_SET
7. Rte\_Call\_PhyCapDischarge\_OP\_SET

## Global Functions/Macros Defined by this Module

None

# Software Module Implementation

## Runtime Environment (RTE) Initial Values

This section lists the initial values of data written by this module but controlled by the RTE. After RTE initialization, the data in this table will contain these values.

|  |  |
| --- | --- |
| Data | Value |
| Rte\_InitValue\_MotorVelocityMRFUnfiltered\_MtrRadpS\_f32 | 0 |
| Rte\_InitValue\_OVERRIDESIGDIAGADC\_Volt\_f32 | 0 |
| Rte\_InitValue\_PMOSDIAGADC\_Volt\_f32 | 0 |
| Rte\_InitValue\_PwrDiscATestComplete\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscATestStart\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscBTestComplete\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscBTestStart\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscClosed\_Cnt\_lgc | FALSE |

## Initialization Functions

None

## Periodic Functions

### Per: \_Per1

#### Design Rationale

None

#### Program Flow Start

Rte\_Call\_BkCpPc\_Per1\_CP0\_CheckpointReached()

#### Store Module Inputs to Local copies

MotorVelocityMRFUnfiltered\_MtrRadpS\_T\_f32 = Rte\_IRead\_BkCpPc\_Per1\_MotorVelocityMRFUnfiltered\_MtrRadpS\_f32()

OVERRIDESIGDIAGADC\_Volt\_T\_f32 = Rte\_IRead\_BkCpPc\_Per1\_OVERRIDESIGDIAGADC\_Volt\_f32()

PMOSDIAGADC\_Volt\_T\_f32 = Rte\_IRead\_BkCpPc\_Per1\_PMOSDIAGADC\_Volt\_f32()

PwrDiscATestStart\_Cnt\_T\_lgc = Rte\_IRead\_BkCpPc\_Per1\_PwrDiscATestStart\_Cnt\_lgc()

PwrDiscBTestStart\_Cnt\_T\_lgc = Rte\_IRead\_BkCpPc\_Per1\_PwrDiscBTestStart\_Cnt\_lgc()

Vbatt\_Volts\_T\_f32 = Rte\_IRead\_BkCpPc\_Per1\_Batt\_Volt\_f32()

Vswitch\_Volts\_T\_f32 = Rte\_IRead\_BkCpPc\_Per1\_BattSwitched\_Volt\_f32()

#### Motor Motion Check, Calculate Delta Voltage



#### Determine State



#### State – Wait for Start A



#### State – Verify Disconnect Open



#### State – Wait for Square Wave



#### State – Bulk Capacitor Precharge



#### State – Wait for Start B



#### State – Post Close Power Disconnect



#### State – Verify Power Disconnect Closed



#### State – Run Time Diagnostics

fbu



#### Store Local copy of outputs into Module Outputs

Rte\_IWrite\_BkCpPc\_Per1\_PwrDiscATestComplete\_Cnt\_lgc(PwrDiscATestComplete\_Cnt\_M\_lgc)

Rte\_IWrite\_BkCpPc\_Per1\_PwrDiscBTestComplete\_Cnt\_lgc(PwrDiscBTestComplete\_Cnt\_M\_lgc)

Rte\_IWrite\_BkCpPc\_Per1\_PwrDiscClosed\_Cnt\_lgc(PwrDiscClosed\_Cnt\_M\_lgc)

#### Program Flow End

Rte\_Call\_BkCpPc\_Per1\_CP1\_CheckpointReached()

## Fault Recovery Functions

None

## Shutdown Functions

None

## Interrupt Functions

None

## Serial Communication Functions

None

## Server Functions

### OP\_SET: CapPcDcStub

#### Design Rationale

The digital output ports PhyCapDischarge\_OP\_SET and PhyCapPrecharge\_OP\_SET that are applicable only to Configuration B require a client/server port in Davinci Developer. However, for programs that only supports Configuration A, where these physical capacitor pins are NOT available, the Developer tool flags an error during integration of the component, since the client server ports are not connected. This server function stub will be connected to both the Client/Server ports, PhyCapDischarge\_OP\_SET and PhyCapPrecharge\_OP\_SET, to avoid the configuration error flaged by the Davinci Developer tool during integration.

#### Program Flow Start

N/A

#### Store Module Inputs to Local copies

None

#### Capacitor Discharge/Precharge server stub – for Configuration A

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| CapPcDcStub\_OP\_SET |  | Type | Min | Max | UTP Tol. |
| **Arguments Passed** | signal | Uint8  (IoHwAb\_BoolType is a **uint8**) | 0 | 1 | 0 |
| **Return Value** | RTE\_E\_OK | Uint8 | 0 | 0 | 0 |



#### Store Local copy of outputs into Module Outputs

None

#### Program Flow End

N/A

## Transition Functions

### Trns: \_Trns1

#### Design Rationale

None

#### Program Flow Start

N/A

#### Store Module Inputs to Local copies

None

#### Set Outputs to Safe Conditions



#### Store Local copy of outputs into Module Outputs

None

#### Program Flow End

N/A

### Trns: \_Trns2

#### Design Rationale

None

#### Program Flow Start

N/A

#### Store Module Inputs to Local copies

None

#### Initialize Outputs



#### Store Local copy of outputs into Module Outputs

None

#### Program Flow End

N/A

# Execution Requirements

## Execution Rates for sub-modules called by the Scheduler

This table serves as reference for the Scheduler design

|  |  |  |
| --- | --- | --- |
| Function Name | Calling Frequency | System State(s) in which the function is called |
| BkCpPc\_Per1 | 2 ms | WARMINIT, OPERATE |
| BkCpPc\_Trns1 | On Event | On Entering DISABLE |
| BkCpPc\_Trns2 | On Event | On Entering WARMINIT |
| CapPcDcStub\_OP\_SET | N/A – stub only | N/A – stub only |

## Execution Requirements for Serial Communication Functions

|  |  |
| --- | --- |
| Function Name | Sub-Module called by (Serial Comm Function Name) |
| None |  |

# Memory Map Definition Requirements

## Sub Modules (Functions)

This table identifies the software segments for functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
| BkCpPc\_Per1 | RTE\_START\_SEC\_SA\_BKCPPC\_APPL\_CODE |
| BkCpPc\_Trns1 | RTE\_START\_SEC\_SA\_BKCPPC\_APPL\_CODE |
| BkCpPc\_Trns2 | RTE\_START\_SEC\_SA\_BKCPPC\_APPL\_CODE |
| CapPcDcStub\_OP\_SET | RTE\_START\_SEC\_SA\_BKCPPC\_APPL\_CODE |

## Local Functions

This table identifies the software segments for local functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
| None |  |

# Known Issues / Limitations With Design

1. INLINE functions defined in GlobalMacro.h are not unit tested.

# Revision Control Log

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Item #** | **Rev #** | **Change Description** | **Date** | **Author Initials** |
| 1 | 1.0 | Initial Version (FDD 11B v001) | 13-Sep-12 | OT |
| 2 | 2.0 | UTP Updates | 20-Sep-12 | OT |
| 3 | 3.0 | Added Trns2 function to initialize startup sequence | 27-Sep-12 | OT |
| 4 | 4.0 | Anomaly 3912 – write outputs in all branches | 24-Oct-12 | OT |
| 5 | 5.0 | Added checkpoint statements | 21-Nov-12 | OT |
| 6 | 6.0 | Updated to version 3 FDD 11B | 28-Feb-13 | Selva |
| 7 | 7.0 | Set RunTimeFaultAcc\_Cnt\_M\_u16 to zero in tans 2 | 1-Mar-13 | Selva |
| 8 | 8.0 | Anomaly 5092 – add power disconnect configurable parameter | 29-May-13 | BDO |
| 9 | 9.0 | Anomaly 5122 – updates to address integration issues | 04-June-13 | BDO |
| 10 | 10.0 | Set PwrDiscATestComplete in BULKCAP\_PRECHARGE state for Configuration A | 05-June-13 | BDO |
| 11 | 11.0 | Updated to add clarification for unit testing. | 21-June-13 | BDO |