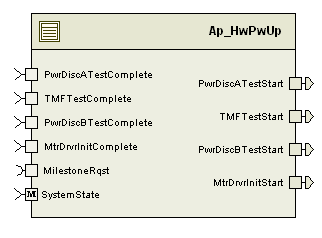
# Module – Hardware Power Up Sequence

# High-Level Description

This module controls the startup initialization sequence for several modules that would otherwise conflict with one another. It uses a series of boolean inputs and outputs to control these modules.

# Figures

## Component Diagram



# Variable Data Dictionary

For details on module input / output variable, refer to the Data Dictionary for the application. Input / output variable names are listed here for reference.

|  |  |  |
| --- | --- | --- |
| Module Inputs | Module Outputs | |
| PwrDiscATestComplete\_Cnt\_lgc | | PwrDiscATestStart\_Cnt\_lgc |
| TMFTestComplete\_Cnt\_lgc | | TMFTestStart\_Cnt\_lgc |
| PwrDiscBTestComplete\_Cnt\_lgc | | PwrDiscBTestStart\_Cnt\_lgc |
| MtrDrvrInitComplete\_Cnt\_lgc | | MtrDrvrInitStart\_Cnt\_lgc |

## Module Internal Variables

This section identifies the name, range and resolutions for module specific data created by this module. If there are no range restrictions on the variable, the term “FULL” is placed into the table for legal range.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Variable Name | Resolution | Legal Range  (min) | Legal Range  (max) | Software Segment |
| PowerUpState\_Cnt\_M\_enum | 1 | 0 | 6 | HWPWUP\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| PwrDiscATestStart\_Cnt\_M\_lgc | boolean | FALSE | TRUE | HWPWUP\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TMFTestStart\_Cnt\_M\_lgc | boolean | FALSE | TRUE | HWPWUP\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| PwrDiscBTestStart\_Cnt\_M\_lgc | boolean | FALSE | TRUE | HWPWUP\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| MtrDrvrInitStart\_Cnt\_M\_lgc | boolean | FALSE | TRUE | HWPWUP\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |

### User defined typedef definition/declaration

This section documents any user types uniquely used for the module.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Typedef Name | Element Name | User Defined Type | Legal Range  (min) | Legal Range  (max) |
| PowerUpSequenceType | PWRUP\_PWRDISCSTEPA = 0  PWRUP\_TMFINIT = 1  PWRUP\_PWRDISCSTEPB = 2  PWRUP\_MTRDRIVERINIT = 3  PWRUP\_WARMINITCOMPLETE = 4  PWRUP\_RUN = 5  PWRUP\_DISABLE = 6 | uint8 | 0 | 6 |

# Constant Data Dictionary

## Calibration Constants

This section lists the calibrations used by the module. For details on calibration constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| none |

## Program(fixed) Constants

### Embedded Constants

All embedded constants whose values are provided in Eng units will be evaluated to the equivalent counts by using the FPM\_InitFixedPoint\_m() macro within the #define statement.

#### Local

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| D\_PWRDISCSTEPAMASK\_CNT\_U16 | 1 | Counts | 0x0001 |
| D\_PWRDISCSTEPBMASK\_CNT\_U16 | 1 | Counts | 0x0004 |
| D\_PGMSPECMASK\_CNT\_U16 | 1 | Counts | configurable |

#### Global

This section lists the global constants used by the module. For details on global constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| None |

### Module specific Lookup Tables Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Value | Software Segment |
| None |  |  |  |

# Functions/Macros used by the Sub-Modules

## Library Functions / Macros

The library and functions / Macros that are called by the various sub modules are identified below,

1. None

## Data Hiding Functions

1. Rte\_Call\_MilestoneRqst\_WarmInitMilestoneComplete
2. Rte\_Call\_MilestoneRqst\_WarmInitMilestoneNotComplete

## Global Functions/Macros Defined by this Module

None

# Software Module Implementation

## Runtime Environment (RTE) Initial Values

This section lists the initial values of data written by this module but controlled by the RTE. After RTE initialization, the data in this table will contain these values.

|  |  |
| --- | --- |
| Data | Value |
| Rte\_InitValue\_MtrDrvrInitComplete\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_MtrDrvrInitStart\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscATestComplete\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscATestStart\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscBTestComplete\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_PwrDiscBTestStart\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_TMFTestComplete\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_TMFTestStart\_Cnt\_lgc | FALSE |

## Initialization Functions

None

## Periodic Functions

### Per: \_Per1

#### Design Rationale

None

#### Program Flow Start

Rte\_Call\_HwPwUp\_Per1\_CP0\_CheckpointReached()

#### Store Module Inputs to Local copies

PwrDiscATestComplete\_Cnt\_T\_lgc = Rte\_IRead\_HwPwUp\_Per1\_PwrDiscATestComplete\_Cnt\_lgc()

TMFTestComplete\_Cnt\_T\_lgc = Rte\_IRead\_HwPwUp\_Per1\_TMFTestComplete\_Cnt\_lgc()

PwrDiscBTestComplete\_Cnt\_T\_lgc = Rte\_IRead\_HwPwUp\_Per1\_PwrDiscBTestComplete\_Cnt\_lgc()

MtrDrvrInitComplete\_Cnt\_T\_lgc = Rte\_IRead\_HwPwUp\_Per1\_MtrDrvrInitComplete\_Cnt\_lgc()

#### Process State Machine



#### Store Local copy of outputs into Module Outputs

Rte\_IWrite\_HwPwUp\_Per1\_PwrDiscATestStart\_Cnt\_lgc(PwrDiscATestStart\_Cnt\_M\_lgc)

Rte\_IWrite\_HwPwUp\_Per1\_TMFTestStart\_Cnt\_lgc(TMFTestStart\_Cnt\_M\_lgc)

Rte\_IWrite\_HwPwUp\_Per1\_PwrDiscBTestStart\_Cnt\_lgc(PwrDiscBTestStart\_Cnt\_M\_lgc)

Rte\_IWrite\_HwPwUp\_Per1\_MtrDrvrInitStart\_Cnt\_lgc(MtrDrvrInitStart\_Cnt\_M\_lgc)

#### Program Flow End

Rte\_Call\_HwPwUp\_Per1\_CP1\_CheckpointReached()

## Fault Recovery Functions

None

## Shutdown Functions

None

## Interrupt Functions

None

## Serial Communication Functions

None

## Transition Functions

### Trns: \_Trns1

#### Design Rationale

None

#### Program Flow Start

N/A

#### Store Module Inputs to Local copies

None

#### Reset State Machine and Outputs



#### Store Local copy of outputs into Module Outputs

None

#### Program Flow End

N/A

### Trns: \_Trns2

#### Design Rationale

None

#### Program Flow Start

N/A

#### Store Module Inputs to Local copies

None

#### Set Power Up State



#### Store Local copy of outputs into Module Outputs

None

#### Program Flow End

N/A

# Execution Requirements

## Execution Rates for sub-modules called by the Scheduler

This table serves as reference for the Scheduler design

|  |  |  |
| --- | --- | --- |
| Function Name | Calling Frequency | System State(s) in which the function is called |
| HwPwUp\_Per1 | 2 ms | ALL |
| HwPwUp\_Trns1 | On Event | On Entering WARMINIT, On Leaving DISABLE |
| HwPwUp\_Trns2 | On Event | On Entering DISABLE |

## Execution Requirements for Serial Communication Functions

|  |  |
| --- | --- |
| Function Name | Sub-Module called by (Serial Comm Function Name) |
| None |  |

# Memory Map Definition Requirements

## Sub Modules (Functions)

This table identifies the software segments for functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
| HwPwUp\_Per1 | RTE\_START\_SEC\_AP\_HWPWUP\_APPL\_CODE |
| HwPwUp\_Trns1 | RTE\_START\_SEC\_AP\_HWPWUP\_APPL\_CODE |
| HwPwUp\_Trns2 | RTE\_START\_SEC\_AP\_HWPWUP\_APPL\_CODE |

## Local Functions

This table identifies the software segments for local functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
| None |  |

# Known Issues / Limitations With Design

1. None

# Revision Control Log

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Item #** | **Rev #** | **Change Description** | **Date** | **Author Initials** |
| 1 | 1.0 | Initial Version (FDD 13B v001) | 11-Sep-12 | OT |
| 2 | 2.0 | UTP Updates | 17-Sep-12 | OT |
| 3 | 3.0 | Added checkpoints and memmap software segment is updated for static variables | 29-Sep-12 | Selva |
| 4 | 4.0 | Anomaly 3912 – fixed writing outputs in all branches | 24-Oct-12 | OT |
| 5 | 5.0 | Changed k\_PgmSpecMask\_Cnt\_u16 to D\_PGMSPECMASK\_CNT\_U16 (configurable) | 08-Nov-12 | JJW |
| 6 | 6.0 | Updated to FDD V003 | 14-Mar-13 | SP |