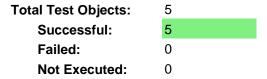
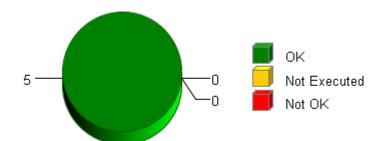


### **Summary**

# **Overall Test Object Results (including Coverage)**



**Date:** 2016-06-02 **Time:** 10:15:58+0530



### **Selected Project Items**

Test Object "UnitTest/IoHwAbstractionUsr/IoHwAb CaptureADC"

Test Object "UnitTest/IoHwAbstractionUsr/IoHwAb Init"

Test Object "UnitTest/IoHwAbstractionUsr/IoHwAb\_ReadAdc"

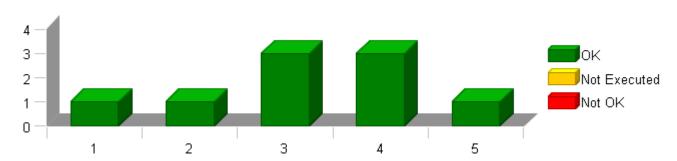
Test Object "UnitTest/IoHwAbstractionUsr/IoHwAb SlowADCGroupValidity"

Test Object "UnitTest/IoHwAbstractionUsr/IoHwAb StartAdc"

### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

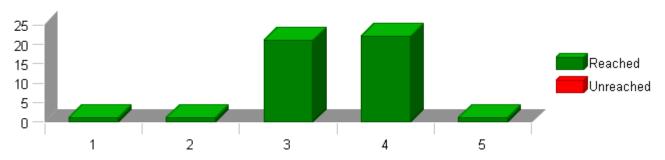
### Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

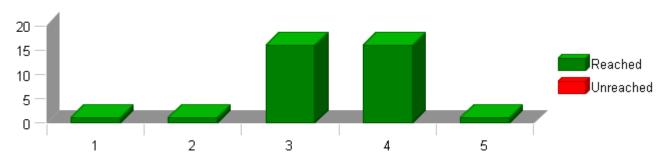


### Statement (C0) Coverage: Total Statements for Each Test Object



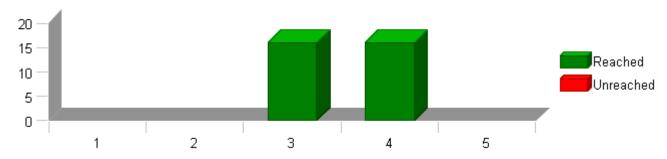
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

### **Decision Coverage: Total Decision Outcomes for Each Test Object**

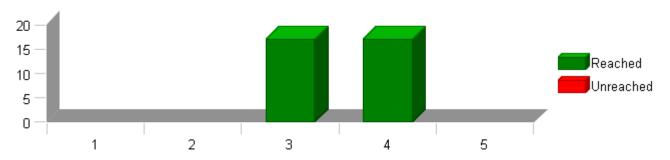


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



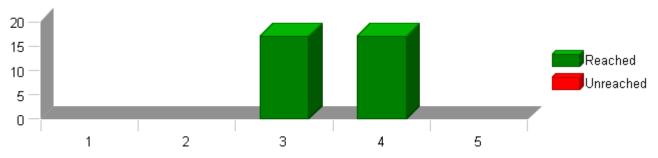
### MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### **TEST OVERVIEW REPORT**

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Project IoHwAbstractionUsr\_FIASA

# **Test Object List**

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases Res	sult
	IoHwAbstractionUsr_FIASA	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	•
	UnitTest	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	•
	IoHwAbstractionUsr	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	•
1	IoHwAb CaptureADC	100 %	100 %	-	-	-	1 of 1 passed	•
2	<u>loHwAb Init</u>	100 %	100 %	-	-	-	1 of 1 passed	•
3	<u>IoHwAb ReadAdc</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
4	IoHwAb SlowADCGroupValidity	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
5	<u>IoHwAb StartAdc</u>	100 %	100 %	-	-	-	1 of 1 passed	•

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Project IoHwAbstractionUsr\_FIASA

ModuleIoHwAbstractionUsrTest ObjectIoHwAb\_CaptureADC

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA			
Configuration File	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml			
Target Environment	TI TMS 570 PLS UDE (Default)			
Kind of Test	Unit Test			
Linker Options				
Source File(s)				
File	\$(PROJECTROOT)\loHwAbstractionUsr\src\loHwAbstractionUsr.c			
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\lot\utp\contract -I\$(PROJECTROOT)\lot\utp\contract -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include			

Name	Text
/lodule 'loHwAbstractionUsr'	Name of Tester:Sarika Natu Code File(s) Under Test:IoHwAbstractionUsr.c Code File(s) Version:5 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version: 4 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.32 Total FLASH Used (Bytes):1020 Total RAM Used (Bytes):32 Special Test Requirements:NA Test Date:6/2/2016 Comments:"Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested.  Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 3:Output Variables Batt_Volt_M_f32 and Batt_Volt_f32 are going out of range.  Note 4: In function ""IoHwAb_SlowADCGroupValidity"", extra code is added in VBA, to exit the while loop and to replicate the usercode functionality of TESSY."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	False

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IoHwAb\_CaptureADC



Attributes					
Name	Value				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg					
Workspace File D:\Synergy_Work_Area\IoHwAbstractionUsr_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP					



### Test Case 1: Boundary Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS 1.1 15 Cycles TS 1.2 15 Cycles TS 1.3 15 Cycles TS 1.4 15 Cycles TS 1.5 15 Cycles TS 1.6 15 Cycles TS 1.7 15 Cycles

Description Vector Description:

TS1.1All Min TS1.2All Max

TS1.3DMAData\_G\_str.FastADC\_Cnt\_u16[12]=Min TS1.4DMAData\_G\_str.FastADC\_Cnt\_u16[12]=Max TS1.5DMAData\_G\_str.FastADC\_Cnt\_u16[12]=Pos TS1.6CDD\_CDDDataAccessBfr\_Cnt\_G\_u16=Min TS1.7CDD\_CDDDataAccessBfr\_Cnt\_G\_u16=Max

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CDD_CDDDataAccessBfr_Cnt_G_u16	0		
CDD_SysCVSwitchADC_Cnt_G_u16[0]	0		
CDD_SysCVSwitchADC_Cnt_G_u16[1]	0		
DMAData_G_str.FastADC_Cnt_u16[0]	0		
DMAData_G_str.FastADC_Cnt_u16[1]	0		
DMAData_G_str.FastADC_Cnt_u16[2]	0		
DMAData_G_str.FastADC_Cnt_u16[3]	0		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	0	0	~
CDD_SysCVSwitchADC_Cnt_G_u16[1]	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			~
Name	Input Value		
CDD_CDDDataAccessBfr_Cnt_G_u16	1		
CDD_SysCVSwitchADC_Cnt_G_u16[0]	65535		
CDD_SysCVSwitchADC_Cnt_G_u16[1]	65535		
DMAData_G_str.FastADC_Cnt_u16[0]	65535		
DMAData_G_str.FastADC_Cnt_u16[1]	65535		
DMAData_G_str.FastADC_Cnt_u16[2]	65535		
DMAData_G_str.FastADC_Cnt_u16[3]	65535		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	65535	65535	•
CDD SysCVSwitchADC Cnt G u16[1]	65535	65535	<b>₩</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	_

Test Step 1.3 (Repeat Count = 1)			
Name	Input Value		
CDD_CDDDataAccessBfr_Cnt_G_u16	0		
CDD_SysCVSwitchADC_Cnt_G_u16[0]	1235		
CDD_SysCVSwitchADC_Cnt_G_u16[1]	5825		
DMAData_G_str.FastADC_Cnt_u16[0]	0		
DMAData_G_str.FastADC_Cnt_u16[1]	0		
DMAData_G_str.FastADC_Cnt_u16[2]	0		
DMAData_G_str.FastADC_Cnt_u16[3]	0		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	0	0	•
CDD_SysCVSwitchADC_Cnt_G_u16[1]	5825	5825	•





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CDD_CDDDataAccessBfr_Cnt_G_u16	1		
CDD_SysCVSwitchADC_Cnt_G_u16[0]	3526		
CDD_SysCVSwitchADC_Cnt_G_u16[1]	6582		
DMAData_G_str.FastADC_Cnt_u16[0]	65535		
DMAData_G_str.FastADC_Cnt_u16[1]	65535		
DMAData_G_str.FastADC_Cnt_u16[2]	65535		
DMAData_G_str.FastADC_Cnt_u16[3]	65535		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	3526	3526	~
CDD_SysCVSwitchADC_Cnt_G_u16[1]	65535	65535	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
CDD_CDDDataAccessBfr_Cnt_G_u16	0		
CDD_SysCVSwitchADC_Cnt_G_u16[0]	8795		
CDD_SysCVSwitchADC_Cnt_G_u16[1]	12542		
DMAData_G_str.FastADC_Cnt_u16[0]	1		
DMAData_G_str.FastADC_Cnt_u16[1]	2		
DMAData_G_str.FastADC_Cnt_u16[2]	3		
DMAData_G_str.FastADC_Cnt_u16[3]	4		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	4	4	~
CDD_SysCVSwitchADC_Cnt_G_u16[1]	12542	12542	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CDD_CDDDataAccessBfr_Cnt_G_u16	0		
CDD_SysCVSwitchADC_Cnt_G_u16[0]	15822		
CDD_SysCVSwitchADC_Cnt_G_u16[1]	25685		
DMAData_G_str.FastADC_Cnt_u16[0]	43550		
DMAData_G_str.FastADC_Cnt_u16[1]	3171		
DMAData_G_str.FastADC_Cnt_u16[2]	39909		
DMAData_G_str.FastADC_Cnt_u16[3]	27403		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	27403	27403	~
CDD_SysCVSwitchADC_Cnt_G_u16[1]	25685	25685	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.7 (Repeat Count = 1)	
Name	Input Value
CDD_CDDDataAccessBfr_Cnt_G_u16	1
CDD_SysCVSwitchADC_Cnt_G_u16[0]	24862
CDD_SysCVSwitchADC_Cnt_G_u16[1]	45855

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IoHwAb\_CaptureADC

Name	Input Value		
DMAData_G_str.FastADC_Cnt_u16[0]	17739		
DMAData_G_str.FastADC_Cnt_u16[1]	16937		
DMAData_G_str.FastADC_Cnt_u16[2]	33551		
DMAData_G_str.FastADC_Cnt_u16[3]	43823		
Name	Actual Value	Expected Value	Result
CDD_SysCVSwitchADC_Cnt_G_u16[0]	24862	24862	~
CDD_SysCVSwitchADC_Cnt_G_u16[1]	43823	43823	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

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Project IoHwAbstractionUsr\_FIASA

ModuleIoHwAbstractionUsrTest ObjectIoHwAb\_StartAdc

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA	
Configuration File	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml	
Target Environment	TI TMS 570 PLS UDE (Default)	
Kind of Test	Unit Test	
Linker Options		
Source File(s)		
File	\$(PROJECTROOT)\loHwAbstractionUsr\src\loHwAbstractionUsr.c	
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\lot\utp\contract -I\$(PROJECTROOT)\lot\utp\contract -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include	

Name	Text
/lodule 'loHwAbstractionUsr'	Name of Tester:Sarika Natu Code File(s) Under Test:IoHwAbstractionUsr.c Code File(s) Version:5 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version: 4 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.32 Total FLASH Used (Bytes):1020 Total RAM Used (Bytes):32 Special Test Requirements:NA Test Date:6/2/2016 Comments:"Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested.  Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 3:Output Variables Batt_Volt_M_f32 and Batt_Volt_f32 are going out of range.  Note 4: In function ""IoHwAb_SlowADCGroupValidity"", extra code is added in VBA, to exit the while loop and to replicate the usercode functionality of TESSY."

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4	
Time Unit	cycles	
Timer Enabled	False	

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IoHwAb\_StartAdc

Attributes		
Name	Value	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\IoHwAbstractionUsr_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



### Test Case 1: Boundary Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS 1.1 11Cycles

Description Vector Description:

TS1.1 Test case to check output

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
adcREG1_temp target_adcREG1_temp			
Name	Actual Value	Expected Value	Result
CDD_ePWM4CMPB_Cnt_G_u16	100	100	~
target_adcREG1_temp.GxINTFLG[0]	0	0	~
target_adcREG1_temp.GxINTFLG[1]	0	0	~
target_adcREG1_temp.GxINTFLG[2]	8	8	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	•

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 Project
 IoHwAbstractionUsr\_FIASA

 Module
 IoHwAbstractionUsr

 Test Object
 IoHwAb\_Init

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

# **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA
Configuration File	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\loHwAbstractionUsr\src\loHwAbstractionUsr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\lot\underline\utp\contract -I\$(PROJECTROOT)\lot\underline\utp\contract -I\$(ProgramFiles)\underline\utp\contract -I\underline\utp\contract -I\underline\underline\utp\contract -I\underline\utp\contract -I\underline\utp

Module 'IoHwAbstractionUsr'	
	Name of Tester:Sarika Natu Code File(s) Under Test:IoHwAbstractionUsr.c Code File(s) Version:5 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version: 4 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.32 Total FLASH Used (Bytes):50 Total CALS Used (Bytes):32 Special Test Requirements:NA Test Date:6/2/2016 Comments:"Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested.
	Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.
	Note 3:Output Variables Batt_Volt_M_f32 and Batt_Volt_f32 are going out of range.
	Note 4: In function ""loHwAb_SlowADCGroupValidity"", extra code is added in VBA, to exit the while loop and to replicate the usercode functionality of TESSY.

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4	
Time Unit	cycles	
Timer Enabled	False	

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Attributes		
Name	Value	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\IoHwAbstractionUsr_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	0		
Batt_Volt_M_f32	0		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
SysCVSwitch_Volt_M_f32	0		
Temperature_Volt_M_f32	0		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0	0	~
Batt_Volt_M_f32	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	2 0	0	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0	0	<b>~</b>
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Temperature. The property of $	4 0	0	~
SysCVSwitch_Volt_M_f32	0	0	~
Temperature_Volt_M_f32	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
BattSwitched_Volt_M_f32	31
Batt_Volt_M_f32	31
DMAData_G_str.SlowADC_Cnt_u16[0]	65535
DMAData_G_str.SlowADC_Cnt_u16[1]	65535
DMAData_G_str.SlowADC_Cnt_u16[2]	65535
DMAData_G_str.SlowADC_Cnt_u16[3]	65535

SysCVSwitch\_Volt\_M\_f32

Temperature\_Volt\_M\_f32

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IoHwAb\_Init Input Value SysCVSwitch\_Volt\_M\_f32 31 Temperature\_Volt\_M\_f32 5 k\_TempSensorOffset\_Volt\_f32 0 k\_TempSensorScale\_Uls\_f32 k\_VbattScale\_Uls\_f32 6.74712992 k\_VswitchScale\_Uls\_f32 3.36966991 **Actual Value Expected Value** Result Name BattSwitched\_Volt\_M\_f32 16.848341 16.8483505 Batt\_Volt\_M\_f32 33.7356339 33.7356491 Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Batt\_Volt\_f32 33.7356491 33.7356339 Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched 16.8483505 16.848341 Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_SysCVSwitch\_16.8483505 16.848341 5

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

16.8483505

5

16.848341

5

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	16.9843006		
Batt_Volt_M_f32	25.1035995		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
SysCVSwitch_Volt_M_f32	30.8057003		
Temperature_Volt_M_f32	4.07730007		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0	0	~
Batt_Volt_M_f32	0	0	•
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0	0	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RattSwitched\_IoHwAbstractionUsr\_IoHwAbsTractIoHwA$	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0	0	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Temperature \textit{Assign} and \textit{Assign} are \textit{Assign} and \textit{Assign} are \textit{Assign} and \textit{Assign} are \textit{Assign}$	0	0	~
SysCVSwitch_Volt_M_f32	0	0	~
Temperature_Volt_M_f32	0	0	~

Test Step Call Trace					<b>✓</b>
A	ctual Function	Count	Expected Function	Count	Result
1*	none*	0	*** No Call Expected ***	0	~

Name	Input Value		
BattSwitched_Volt_M_f32	12.3696003		
Batt_Volt_M_f32	1.60839999		
DMAData_G_str.SlowADC_Cnt_u16[0]	65535		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535		
SysCVSwitch_Volt_M_f32	29.8577003		
Temperature_Volt_M_f32	3.36720991		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	16.8483505	16.848341	•
Batt_Volt_M_f32	33.7356491	33.7356339	•
Rte Task Init 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb Init.Rte Batt Volt f32	33.7356491	33.7356339	•

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Name	Actual Value	Expected Value	Result
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched_	16.8483505	16.848341	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	16.8483505	16.848341	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	5	5	~
SysCVSwitch_Volt_M_f32	16.8483505	16.848341	~
Temperature_Volt_M_f32	5	5	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	<b>✓</b>

Test Step 1.5 (Repeat Count = 1)			V
Name	Input Value		
BattSwitched_Volt_M_f32	28.5587997		
Batt_Volt_M_f32	16.5764999		
DMAData_G_str.SlowADC_Cnt_u16[0]	1		
DMAData_G_str.SlowADC_Cnt_u16[1]	2		
DMAData_G_str.SlowADC_Cnt_u16[2]	3		
DMAData_G_str.SlowADC_Cnt_u16[3]	4		
SysCVSwitch_Volt_M_f32	8.27009964		
Temperature_Volt_M_f32	4.4580102		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.016457485	0.0164574757	~
Batt_Volt_M_f32	0.0164765082	0.0164764989	•
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f3	0.0164765082	0.0164764989	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched	0.016457485	0.0164574757	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitc	h 0.016457485	0.0164574757	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Temperature	0.00122100126	0.00122100103	~
SysCVSwitch_Volt_M_f32	0.016457485	0.0164574757	~
Temperature_Volt_M_f32	0.00122100126	0.00122100103	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	0		
Batt_Volt_M_f32	24.2248993		
DMAData_G_str.SlowADC_Cnt_u16[0]	15		
DMAData_G_str.SlowADC_Cnt_u16[1]	16		
DMAData_G_str.SlowADC_Cnt_u16[2]	17		
DMAData_G_str.SlowADC_Cnt_u16[3]	18		
SysCVSwitch_Volt_M_f32	26.8668003		
Temperature_Volt_M_f32	1.09467995		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.0740586817	0.0740586445	~
Batt_Volt_M_f32	0.131812066	0.131811991	•
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.131812066	0.131811991	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched_	0.0740586817	0.0740586445	•
Rte Task Init 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb Init.Rte SysCVSwitch	0.0740586817	0.0740586445	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	0.0183150191	0.0183150172	~
SysCVSwitch_Volt_M_f32	0.0740586817	0.0740586445	~
Temperature Volt M f32	0.0183150191	0.0183150172	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~



Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	31		
Batt_Volt_M_f32	25.6214008		
DMAData_G_str.SlowADC_Cnt_u16[0]	29		
DMAData_G_str.SlowADC_Cnt_u16[1]	30		
DMAData_G_str.SlowADC_Cnt_u16[2]	31		
DMAData_G_str.SlowADC_Cnt_u16[3]	32		
SysCVSwitch_Volt_M_f32	13.0983		
Temperature_Volt_M_f32	1.12269998		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_UIs_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.13165988	0.131659806	~
Batt_Volt_M_f32	0.24714762	0.247147486	•
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.24714762	0.247147486	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbsTractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbsTractionUsr\_IoHwAb\_I$	0.13165988	0.131659806	<b>✓</b>
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.13165988	0.131659806	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Temperature And the Management of Management of$	0.035409037	0.0354090333	•
SysCVSwitch_Volt_M_f32	0.13165988	0.131659806	~
Temperature_Volt_M_f32	0.035409037	0.0354090333	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.8 (Repeat Count = 1)			~
Name	Input Value		
BattSwitched_Volt_M_f32	15.2539997		
Batt_Volt_M_f32	10.4066		
DMAData_G_str.SlowADC_Cnt_u16[0]	43		
DMAData_G_str.SlowADC_Cnt_u16[1]	44		
DMAData_G_str.SlowADC_Cnt_u16[2]	45		
DMAData_G_str.SlowADC_Cnt_u16[3]	46		
SysCVSwitch_Volt_M_f32	25.7688007		
Temperature_Volt_M_f32	4.36112022		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.189261064	0.189260975	~
Batt_Volt_M_f32	0.362483174	0.362482995	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.362483174	0.362482995	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbsTractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbsTractionUsr\_IoHwAb\_I$	0.189261064	0.189260975	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.189261064	0.189260975	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Temperature \textit{Assign} and \textit{Assign} are \textit{Assign} and \textit{Assign} are \textit{Assign} and \textit{Assign} are \textit{Assign}$	0.0525030531	0.0525030531	~
SysCVSwitch_Volt_M_f32	0.189261064	0.189260975	~
Temperature_Volt_M_f32	0.0525030531	0.0525030531	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	_

Test Step 1.9 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	11.9289999
Batt_Volt_M_f32	0
DMAData_G_str.SlowADC_Cnt_u16[0]	57
DMAData_G_str.SlowADC_Cnt_u16[1]	58
DMAData_G_str.SlowADC_Cnt_u16[2]	59
DMAData_G_str.SlowADC_Cnt_u16[3]	60

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Name	Input Value		
SysCVSwitch_Volt_M_f32	13.0983		
Temperature_Volt_M_f32	2.77731991		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.246862277	0.246862143	~
Batt_Volt_M_f32	0.477818757	0.477818489	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.477818757	0.477818489	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched_	0.246862277	0.246862143	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.246862277	0.246862143	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	0.0695970729	0.0695970729	~
SysCVSwitch_Volt_M_f32	0.246862277	0.246862143	~
Temperature_Volt_M_f32	0.0695970729	0.0695970729	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.10 (Repeat Count = 1)			•
Name	Input Value		
BattSwitched_Volt_M_f32	7.42719984		
Batt_Volt_M_f32	31		
DMAData_G_str.SlowADC_Cnt_u16[0]	71		
DMAData_G_str.SlowADC_Cnt_u16[1]	72		
DMAData_G_str.SlowADC_Cnt_u16[2]	73		
DMAData_G_str.SlowADC_Cnt_u16[3]	74		
SysCVSwitch_Volt_M_f32	8.27009964		
Temperature_Volt_M_f32	3.6875		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.304463476	0.304463297	~
Batt_Volt_M_f32	0.593154311	0.593153954	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.593154311	0.593153954	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched_	0.304463476	0.304463297	✓
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.304463476	0.304463297	·
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	0.086691089	0.086691089	~
SysCVSwitch_Volt_M_f32	0.304463476	0.304463297	<b>✓</b>
Temperature Volt M f32	0.086691089	0.086691089	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Name	Input Value		
BattSwitched_Volt_M_f32	5.83570004		
Batt_Volt_M_f32	5.25400019		
DMAData_G_str.SlowADC_Cnt_u16[0]	85		
DMAData_G_str.SlowADC_Cnt_u16[1]	86		
DMAData_G_str.SlowADC_Cnt_u16[2]	87		
DMAData_G_str.SlowADC_Cnt_u16[3]	88		
SysCVSwitch_Volt_M_f32	5.58349991		
Temperature_Volt_M_f32	0.567834973		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.36206466	0.362064481	~
Batt_Volt_M_f32	0.708489835	0.708489478	~
Rte Task Init 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb Init.Rte Batt Volt f32	0.708489835	0.708489478	<b>✓</b>

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Name	Actual Value	Expected Value	Result
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAbs$	0.36206466	0.362064481	•
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.36206466	0.362064481	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Temperature/	0.103785105	0.103785105	~
SysCVSwitch_Volt_M_f32	0.36206466	0.362064481	~
Temperature_Volt_M_f32	0.103785105	0.103785105	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	13.7133999		
Batt_Volt_M_f32	29.9330006		
DMAData_G_str.SlowADC_Cnt_u16[0]	99		
DMAData_G_str.SlowADC_Cnt_u16[1]	100		
DMAData_G_str.SlowADC_Cnt_u16[2]	101		
DMAData_G_str.SlowADC_Cnt_u16[3]	102		
SysCVSwitch_Volt_M_f32	0		
Temperature_Volt_M_f32	2.75803995		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.419665873	0.419665635	~
Batt_Volt_M_f32	0.823825419	0.823824942	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.823825419	0.823824942	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched_	0.419665873	0.419665635	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.419665873	0.419665635	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	0.120879129	0.120879121	~
SysCVSwitch_Volt_M_f32	0.419665873	0.419665635	~
Temperature Volt M f32	0.120879129	0.120879121	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Step 1.13 (Repeat Count = 1)			
Name	Input Value		
BattSwitched_Volt_M_f32	26.3334999		
Batt_Volt_M_f32	21.7457008		
DMAData_G_str.SlowADC_Cnt_u16[0]	113		
DMAData_G_str.SlowADC_Cnt_u16[1]	114		
DMAData_G_str.SlowADC_Cnt_u16[2]	115		
DMAData_G_str.SlowADC_Cnt_u16[3]	116		
SysCVSwitch_Volt_M_f32	31		
Temperature_Volt_M_f32	4.72224998		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.477267057	0.477266818	~
Batt_Volt_M_f32	0.939161003	0.939160466	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	0.939161003	0.939160466	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_Init\_Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_Init.Rte\_RB.Rte\_IoHwAb\_IoHwA$	0.477267057	0.477266818	~
Rte Task Init 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb Init.Rte SysCVSwitch	0.477267057	0.477266818	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Temperature \textit{Assign} and \textit{Assign} are \textit{Assign} and \textit{Assign} are \textit{Assign} and \textit{Assign} are \textit{Assign}$	0.137973145	0.137973145	~
SysCVSwitch_Volt_M_f32	0.477267057	0.477266818	~
Temperature_Volt_M_f32	0.137973145	0.137973145	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	



Test Step 1.14 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	13.026		
Batt_Volt_M_f32	14.3886003		
DMAData_G_str.SlowADC_Cnt_u16[0]	127		
DMAData_G_str.SlowADC_Cnt_u16[1]	128		
DMAData_G_str.SlowADC_Cnt_u16[2]	129		
DMAData_G_str.SlowADC_Cnt_u16[3]	130		
SysCVSwitch_Volt_M_f32	17.2539997		
Temperature_Volt_M_f32	4.18861008		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_UIs_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.53486824	0.534868002	~
Batt_Volt_M_f32	1.05449653	1.05449593	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	1.05449653	1.05449593	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbsTractionUsr\_IoHwAb\_Init.Rte\_BattSwitched\_IoHwAbsTractionUsr\_IoHwAb\_IoHwAbsTractionUsr\_IoHwAbsTractionUs$	0.53486824	0.534868002	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.53486824	0.534868002	~
$Rte\_Task\_Init\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_Init.Rte\_Temperature And the Management of Management of$	0.155067161	0.155067161	~
SysCVSwitch_Volt_M_f32	0.53486824	0.534868002	~
Temperature_Volt_M_f32	0.155067161	0.155067161	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Step 1.15 (Repeat Count = 1)			<b>V</b>
Name	Input Value		
BattSwitched Volt M f32	11.3526001		
Batt_Volt_M_f32	9.02960014		
DMAData_G_str.SlowADC_Cnt_u16[0]	141		
DMAData_G_str.SlowADC_Cnt_u16[1]	142		
DMAData_G_str.SlowADC_Cnt_u16[2]	143		
DMAData_G_str.SlowADC_Cnt_u16[3]	144		
SysCVSwitch_Volt_M_f32	22.1615009		
Temperature_Volt_M_f32	0		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.592469454	0.592469156	~
Batt_Volt_M_f32	1.16983211	1.1698314	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	1.16983211	1.1698314	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched	0.592469454	0.592469156	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.592469454	0.592469156	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Temperature.	0.172161177	0.172161177	~
SysCVSwitch_Volt_M_f32	0.592469454	0.592469156	~
Temperature_Volt_M_f32	0.172161177	0.172161177	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	_	

Test Step 1.16 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
BattSwitched_Volt_M_f32	17.9727993	
Batt_Volt_M_f32	17.9594002	
DMAData_G_str.SlowADC_Cnt_u16[0]	155	
DMAData_G_str.SlowADC_Cnt_u16[1]	156	
DMAData_G_str.SlowADC_Cnt_u16[2]	157	
DMAData_G_str.SlowADC_Cnt_u16[3]	158	

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Name	Input Value		
SysCVSwitch_Volt_M_f32	3.53239989		
Temperature_Volt_M_f32	5		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.650070667	0.65007031	~
Batt_Volt_M_f32	1.28516757	1.28516698	•
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	1.28516757	1.28516698	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched_	0.650070667	0.65007031	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.650070667	0.65007031	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	0.189255193	0.189255193	~
SysCVSwitch_Volt_M_f32	0.650070667	0.65007031	~
Temperature_Volt_M_f32	0.189255193	0.189255193	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Stan 4 47 (Penast Count = 4)			-0
Test Step 1.17 (Repeat Count = 1)			
Name	Input Value		
BattSwitched_Volt_M_f32	11.8743		
Batt_Volt_M_f32	10.9239998		
DMAData_G_str.SlowADC_Cnt_u16[0]	169		
DMAData_G_str.SlowADC_Cnt_u16[1]	170		
DMAData_G_str.SlowADC_Cnt_u16[2]	171		
DMAData_G_str.SlowADC_Cnt_u16[3]	172		
SysCVSwitch_Volt_M_f32	3.51160002		
Temperature_Volt_M_f32	2.25399995		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712992		
k_VswitchScale_Uls_f32	3.36966991		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.707671821	0.707671463	~
Batt_Volt_M_f32	1.40050316	1.40050244	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_Batt_Volt_f32	1.40050316	1.40050244	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_BattSwitched	0.707671821	0.707671463	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_SysCVSwitch	0.707671821	0.707671463	~
Rte_Task_Init_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_Init.Rte_TemperatureA	0.206349209	0.206349209	<b>~</b>
SysCVSwitch_Volt_M_f32	0.707671821	0.707671463	~
Temperature Volt M f32	0.206349209	0.206349209	<b>V</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

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Project IoHwAbstractionUsr\_FIASA

Module IoHwAbstractionUsr

Test Object IoHwAb\_SlowADCGroupValidity

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### Statistics

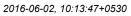
Total Testcases	3
Successful	3
Failed	0
Not Executed	0

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA
Configuration File	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\loHwAbstractionUsr\src\loHwAbstractionUsr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\IoHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\IoHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\IoHwAbstractionUsr\include -I\$(PROJECTROOT)\Include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'loHwAbstractionUsr'	Name of Tester:Sarika Natu Code File(s) Under Test:IoHwAbstractionUsr.c Code File(s) Version:5 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version: 4 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.32 Total FLASH Used (Bytes):50 Total FLASH Used (Bytes):50 Total RAM Used (Bytes):50 Total Test Requirements:NA Test Date:6/2/2016 Comments:"Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested.  Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 3:Output Variables Batt_Volt_M_f32 and Batt_Volt_f32 are going out of range.  Note 4: In function ""IoHwAb_SlowADCGroupValidity"", extra code is added in VBA, to exit the while loop and to replicate the usercode functionality of TESSY."

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd			





Attributes					
Name	Value				
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tp1</pre>				
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>				
Time Unit	cycles				
Timer Enabled	False				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\IoHwAbstractionUsr_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				



#### **Test Case 1: Metrics Test**

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS 1.1 3194.00 Cycles TS 1.2 3916.00 Cycles

#### Description Vector Description:

TS1.1"Shortest Path:

(ICMAData\_G\_str.SlowADC\_Cnt\_u16[i] == 0xFFFFU)==>FALSE && (ElapsedTime\_uS\_T\_u32 < k\_ReadAdcWaitTimeout\_uS\_u32))
(WaitedTime\_uS\_T\_u32 > ReadAdcMaxWaitedTime\_uS\_D\_u32)==> FALSE
(DMAData\_G\_str.SlowADC\_Cnt\_u16[i] == 0xFFFFU)==> FALSE
(SlowADCFItAc\_Cnt\_M\_u16> k\_SlowADCValidDiag\_Cnt\_str.Nstep)==> FALSE"

Test Step 1.1 (Repeat Count = 1)			✓	
Name	Input Value			
DMAData_G_str.SlowADC_Cnt_u16[0]	0			
DMAData_G_str.SlowADC_Cnt_u16[1]	0	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0	0		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	u32_CurrentTime		
SlowADCFltAcc_Cnt_M_u16	0			
k_ReadAdcWaitTimeout_uS_u32	0	0		
k_SlowADCValidDiag_Cnt_str.Threshold	0	0		
k_SlowADCValidDiag_Cnt_str.PStep	0	0		
k_SlowADCValidDiag_Cnt_str.NStep	0			
target_GetSystemTime_uS_u32_CurrentTime	0			
Name	Actual Value	Expected Value	Result	
IoHwAb_SlowADCGroupValidity()	1	1	✓	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	0	0 0		
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	0	0 0		
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	0	0 0		
SlowADCFitAcc_Cnt_M_u16	0	0	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>~</b>

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
DMAData_G_str.SlowADC_Cnt_u16[0]	65535			
DMAData_G_str.SlowADC_Cnt_u16[1]	65535			
DMAData_G_str.SlowADC_Cnt_u16[2]	65535			
DMAData_G_str.SlowADC_Cnt_u16[3]	65535			
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	2_CurrentTime		
SlowADCFltAcc_Cnt_M_u16	10			
k_ReadAdcWaitTimeout_uS_u32	24585			
k_SlowADCValidDiag_Cnt_str.Threshold	255	255		
k_SlowADCValidDiag_Cnt_str.PStep	10			
k_SlowADCValidDiag_Cnt_str.NStep	253			
target_GetSystemTime_uS_u32_CurrentTime	3489753329			
Name	Actual Value	Expected Value	Result	
IoHwAb_SlowADCGroupValidity()	0	0	~	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	0	0	~	
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	0	0	~	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>	
SlowADCFltAcc_Cnt_M_u16	50	50	~	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	5	DtrmnElapsedTime_uS_u32	5	•	





### **Test Case 2: Boundary Test**

#### Specification

```
Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS 2.1 3316.00 Cycles
TS 2.2 5267.00 Cycles
TS 2.2 5267.00 Cycles
TS 2.3 3091.00 Cycles
TS 2.4 3158.00 Cycles
TS 2.5 3106.00 Cycles
TS 2.6 3052.00 Cycles
TS 2.7 3868.00 Cycles
TS 2.7 3868.00 Cycles
TS 2.7 3074.00 Cycles
TS 2.10 3074.00 Cycles
TS 2.10 3074.00 Cycles
TS 2.11 3079.00 Cycles
TS 2.13 3097.00 Cycles
TS 2.13 3097.00 Cycles
TS 2.14 5207.00 Cycles
TS 2.15 3084.00 Cycles
TS 2.16 3071.00 Cycles
TS 2.17 3058.00 Cycles
TS 2.18 3054.00 Cycles
TS 2.18 3054.00 Cycles
TS 2.29 3060.00 Cycles
TS 2.20 3060.00 Cycles
TS 2.21 5662.00 Cycles
TS 2.22 3060.00 Cycles
TS 2.23 3045.00 Cycles
TS 2.24 3045.00 Cycles
TS 2.24 3045.00 Cycles
TS 2.25 5142.00 Cycles
TS 2.26 3047.00 Cycles
```

#### Description

#### Vector Description:

```
TS2.1All Min
TS2.2All Max
TS2.3SlowADCFltAcc_Cnt_M_u16==> Min
TS2.4SlowADCFltAcc_Cnt_M_u16==> Max
TS2.3SlowADCFltAcc_Cnt_M_u16==> Max
TS2.5SlowADCFltAcc_Cnt_M_u16==> Mid
TS2.6DMAData_G_str.SlowADC_Cnt_u16[4]==> Min
TS2.7DMAData_G_str.SlowADC_Cnt_u16[4]==> Min
TS2.7DMAData_G_str.SlowADC_Cnt_u16[4]==> Mid
TS2.9k_ReadAdcWaitTimeout_uS_u32==> Min
TS2.10k_ReadAdcWaitTimeout_uS_u32==> Min
TS2.10k_ReadAdcWaitTimeout_uS_u32==> Mid/ Default
TS2.11k_ReadAdcWaitTimeout_uS_u32==> Mid/ Default
TS2.12k_SlowADCValidDiag_Cnt_str.Nstep==> Min
TS2.13k_SlowADCValidDiag_Cnt_str.Nstep==> Mid/ Default
TS2.14k_SlowADCValidDiag_Cnt_str.Pstep==> Mid/ Default
TS2.15k_SlowADCValidDiag_Cnt_str.Pstep==> Mid/ Default
TS2.16k_SlowADCValidDiag_Cnt_str.Pstep==> Mid/ Default
TS2.18k_SlowADCValidDiag_Cnt_str.Threshold==> Min
TS2.19k_SlowADCValidDiag_Cnt_str.Threshold==> Min
TS2.19k_SlowADCValidDiag_Cnt_str.Threshold==> Mid/ Default
TS2.18k_SlowADCValidDiag_Cnt_str.Threshold==> Mid/ Default
TS2.21Re_Call_SystemTime_GetSystemTime_uS_u32==> Min
TS2.22Rte_Call_SystemTime_DtrmnElapsedTime_uS_u32==> Min
TS2.24Rte_Call_SystemTime_DtrmnElapsedTime_uS_u32==> Mia
```

TS2.26Rte\_Call\_SystemTime\_DtrmnElapsedTime\_uS\_u32==> Mid

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTi	me	
SlowADCFltAcc_Cnt_M_u16	0		
k_ReadAdcWaitTimeout_uS_u32	0		
k_SlowADCValidDiag_Cnt_str.Threshold	0		
k_SlowADCValidDiag_Cnt_str.PStep	0		
k_SlowADCValidDiag_Cnt_str.NStep	0		
target_GetSystemTime_uS_u32_CurrentTime	0		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	0	0	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	0	0	<b>~</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	0	0	~
SlowADCFltAcc_Cnt_M_u16	0	0	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>



Test Step 2.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	65535		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_	u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	65535		
k_ReadAdcWaitTimeout_uS_u32	65535		
k_SlowADCValidDiag_Cnt_str.Threshold	65535		
k_SlowADCValidDiag_Cnt_str.PStep	65535		
k_SlowADCValidDiag_Cnt_str.NStep	65535		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	✓
SlowADCFltAcc_Cnt_M_u16	65535	65535	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10 ReportNTCStatus	1	NxtrDiagMgr10 ReportNTCStatus	1	<b>~</b>

Test Step 2.3 (Repeat Count = 1)			~
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	16069		
DMAData_G_str.SlowADC_Cnt_u16[1]	44055		
DMAData_G_str.SlowADC_Cnt_u16[2]	16069		
DMAData_G_str.SlowADC_Cnt_u16[3]	4405		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	J32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	0		
k_ReadAdcWaitTimeout_uS_u32	400		
k_SlowADCValidDiag_Cnt_str.Threshold	119		
k_SlowADCValidDiag_Cnt_str.PStep	84		
k_SlowADCValidDiag_Cnt_str.NStep	166		
target_GetSystemTime_uS_u32_CurrentTime	3826103446		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	✓
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	•
SlowADCFItAcc Cnt M u16	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>

Test Step 2.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DMAData_G_str.SlowADC_Cnt_u16[0]	60192	
DMAData_G_str.SlowADC_Cnt_u16[1]	62200	
DMAData_G_str.SlowADC_Cnt_u16[2]	60192	
DMAData_G_str.SlowADC_Cnt_u16[3]	6220	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	65535	

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Name	Input Value		
k ReadAdcWaitTimeout uS u32	39582		
k_SlowADCValidDiag_Cnt_str.Threshold	159		
k_SlowADCValidDiag_Cnt_str.PStep	50		
k_SlowADCValidDiag_Cnt_str.NStep	73		
target_GetSystemTime_uS_u32_CurrentTime	2195757816		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>~</b>
SlowADCFltAcc_Cnt_M_u16	65243	65243	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•	

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	1421		
DMAData_G_str.SlowADC_Cnt_u16[1]	23191		
DMAData_G_str.SlowADC_Cnt_u16[2]	1421		
DMAData_G_str.SlowADC_Cnt_u16[3]	2319		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	43580		
k_ReadAdcWaitTimeout_uS_u32	18562		
k_SlowADCValidDiag_Cnt_str.Threshold	149		
k_SlowADCValidDiag_Cnt_str.PStep	237		
k_SlowADCValidDiag_Cnt_str.NStep	146		
target_GetSystemTime_uS_u32_CurrentTime	20507433		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
SlowADCFltAcc Cnt M u16	42996	42996	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	43925		
k_ReadAdcWaitTimeout_uS_u32	43696		
k_SlowADCValidDiag_Cnt_str.Threshold	102		
k_SlowADCValidDiag_Cnt_str.PStep	185		
k_SlowADCValidDiag_Cnt_str.NStep	134		
target_GetSystemTime_uS_u32_CurrentTime	3634607182		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>~</b>
SlowADCFltAcc_Cnt_M_u16	43389	43389	~



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnFlansedTime_uS_u32	4	DtrmnFlansedTime_uS_u32	4	-	

Test Step 2.7 (Repeat Count = 1)			<b>√</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	65535		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	2_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	10		
k_ReadAdcWaitTimeout_uS_u32	24585		
k_SlowADCValidDiag_Cnt_str.Threshold	255		
k_SlowADCValidDiag_Cnt_str.PStep	10		
k_SlowADCValidDiag_Cnt_str.NStep	253		
target_GetSystemTime_uS_u32_CurrentTime	3489753329		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	50	50	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime uS u32	5	DtrmnElapsedTime uS u32	5	<b>✓</b>	

Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	5792		
DMAData_G_str.SlowADC_Cnt_u16[1]	16485		
DMAData_G_str.SlowADC_Cnt_u16[2]	5792		
DMAData_G_str.SlowADC_Cnt_u16[3]	1648		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	2_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	1286		
k_ReadAdcWaitTimeout_uS_u32	26770		
k_SlowADCValidDiag_Cnt_str.Threshold	17		
k_SlowADCValidDiag_Cnt_str.PStep	217		
k_SlowADCValidDiag_Cnt_str.NStep	178		
target_GetSystemTime_uS_u32_CurrentTime	861546824		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
SlowADCFltAcc_Cnt_M_u16	574	574	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•	

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
DMAData_G_str.SlowADC_Cnt_u16[0]	36024
DMAData_G_str.SlowADC_Cnt_u16[1]	14757
DMAData_G_str.SlowADC_Cnt_u16[2]	36024
DMAData_G_str.SlowADC_Cnt_u16[3]	1475
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
SlowADCFltAcc_Cnt_M_u16	60482

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Name	Input Value		
k_ReadAdcWaitTimeout_uS_u32	0		
k_SlowADCValidDiag_Cnt_str.Threshold	181		
k_SlowADCValidDiag_Cnt_str.PStep	85		
k_SlowADCValidDiag_Cnt_str.NStep	217		
target_GetSystemTime_uS_u32_CurrentTime	4139727429		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	59614	59614	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	56229		
DMAData_G_str.SlowADC_Cnt_u16[1]	42178		
DMAData_G_str.SlowADC_Cnt_u16[2]	56229		
DMAData_G_str.SlowADC_Cnt_u16[3]	4217		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_	u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	49261		
k_ReadAdcWaitTimeout_uS_u32	65535		
k_SlowADCValidDiag_Cnt_str.Threshold	234		
k_SlowADCValidDiag_Cnt_str.PStep	22		
k_SlowADCValidDiag_Cnt_str.NStep	219		
target_GetSystemTime_uS_u32_CurrentTime	2517312924		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
SlowADCFltAcc_Cnt_M_u16	48385	48385	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>	

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	15456		
DMAData_G_str.SlowADC_Cnt_u16[1]	15450		
DMAData_G_str.SlowADC_Cnt_u16[2]	15456		
DMAData_G_str.SlowADC_Cnt_u16[3]	1545		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	16572		
k_ReadAdcWaitTimeout_uS_u32	400		
k_SlowADCValidDiag_Cnt_str.Threshold	146		
k_SlowADCValidDiag_Cnt_str.PStep	146		
k_SlowADCValidDiag_Cnt_str.NStep	112		
target_GetSystemTime_uS_u32_CurrentTime	3086448313		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
SlowADCFltAcc_Cnt_M_u16	16124	16124	✓



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>	

Test Step 2.12 (Repeat Count = 1)			
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	10049		
DMAData_G_str.SlowADC_Cnt_u16[1]	10324		
DMAData_G_str.SlowADC_Cnt_u16[2]	10049		
DMAData_G_str.SlowADC_Cnt_u16[3]	1032		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	2_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	38463		
k_ReadAdcWaitTimeout_uS_u32	56454		
k_SlowADCValidDiag_Cnt_str.Threshold	236		
k_SlowADCValidDiag_Cnt_str.PStep	186		
k_SlowADCValidDiag_Cnt_str.NStep	0		
target_GetSystemTime_uS_u32_CurrentTime	2046175492		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	•
SlowADCFItAcc Cnt M u16	38463	38463	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime uS u32	4	DtrmnElapsedTime uS u32	4	<b>✓</b>	

Test Step 2.13 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	25607		
DMAData_G_str.SlowADC_Cnt_u16[1]	50852		
DMAData_G_str.SlowADC_Cnt_u16[2]	25607		
DMAData_G_str.SlowADC_Cnt_u16[3]	5085		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	2_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	35912		
k_ReadAdcWaitTimeout_uS_u32	61733		
k_SlowADCValidDiag_Cnt_str.Threshold	197		
k_SlowADCValidDiag_Cnt_str.PStep	230		
k_SlowADCValidDiag_Cnt_str.NStep	65535		
target_GetSystemTime_uS_u32_CurrentTime	57471248		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•	

Test Step 2.14 (Repeat Count = 1)		
Name	Input Value	
DMAData_G_str.SlowADC_Cnt_u16[0]	65535	
DMAData_G_str.SlowADC_Cnt_u16[1]	65535	
DMAData_G_str.SlowADC_Cnt_u16[2]	65535	
DMAData_G_str.SlowADC_Cnt_u16[3]	65535	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	60285	

NxtrDiagMgr10\_ReportNTCStatus(Status\_Cnt\_T\_enum)

SlowADCFltAcc\_Cnt\_M\_u16

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Name	Input Value		
k_ReadAdcWaitTimeout_uS_u32	48478		
k_SlowADCValidDiag_Cnt_str.Threshold	247		
k_SlowADCValidDiag_Cnt_str.PStep	53		
k_SlowADCValidDiag_Cnt_str.NStep	1		
target_GetSystemTime_uS_u32_CurrentTime	3285270500		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	✓

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Fest Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~

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Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	54838		
DMAData_G_str.SlowADC_Cnt_u16[1]	35067		
DMAData_G_str.SlowADC_Cnt_u16[2]	54838		
DMAData_G_str.SlowADC_Cnt_u16[3]	3506		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	18022		
k_ReadAdcWaitTimeout_uS_u32	52013		
k_SlowADCValidDiag_Cnt_str.Threshold	30		
k_SlowADCValidDiag_Cnt_str.PStep	0		
k_SlowADCValidDiag_Cnt_str.NStep	100		
target_GetSystemTime_uS_u32_CurrentTime	2133832437		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	17622	17622	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~	

Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	6295		
DMAData_G_str.SlowADC_Cnt_u16[1]	30745		
DMAData_G_str.SlowADC_Cnt_u16[2]	6295		
DMAData_G_str.SlowADC_Cnt_u16[3]	3074		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	59599		
k_ReadAdcWaitTimeout_uS_u32	41113		
k_SlowADCValidDiag_Cnt_str.Threshold	121		
k_SlowADCValidDiag_Cnt_str.PStep	65535		
k_SlowADCValidDiag_Cnt_str.NStep	122		
target_GetSystemTime_uS_u32_CurrentTime	2176681352		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	_

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Name	Actual Value	Expected Value	Result
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
SlowADCFitAcc_Cnt_M_u16	59111	59111	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~	

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	39654		
DMAData_G_str.SlowADC_Cnt_u16[1]	57044		
DMAData_G_str.SlowADC_Cnt_u16[2]	39654		
DMAData_G_str.SlowADC_Cnt_u16[3]	5704		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	15637		
k_ReadAdcWaitTimeout_uS_u32	53295		
k_SlowADCValidDiag_Cnt_str.Threshold	80		
k_SlowADCValidDiag_Cnt_str.PStep	1		
k_SlowADCValidDiag_Cnt_str.NStep	172		
target_GetSystemTime_uS_u32_CurrentTime	3507376676		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	✓
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	14949	14949	✓

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~		
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>		

Name	Input Value			
DMAData_G_str.SlowADC_Cnt_u16[0]	60811			
DMAData_G_str.SlowADC_Cnt_u16[1]	48781			
DMAData_G_str.SlowADC_Cnt_u16[2]	60811			
DMAData_G_str.SlowADC_Cnt_u16[3]	4878			
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32	2_CurrentTime		
SlowADCFltAcc_Cnt_M_u16	7995			
k_ReadAdcWaitTimeout_uS_u32	38002	38002		
k_SlowADCValidDiag_Cnt_str.Threshold	0			
k_SlowADCValidDiag_Cnt_str.PStep	186			
k_SlowADCValidDiag_Cnt_str.NStep	128			
target_GetSystemTime_uS_u32_CurrentTime	2530847871			
Name	Actual Value	Expected Value	Result	
IoHwAb_SlowADCGroupValidity()	1	1	•	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•	
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	•	
SlowADCFItAcc Cnt M u16	7483	7483	•	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•	

Test Step 2.19 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DMAData_G_str.SlowADC_Cnt_u16[0]	54872
DMAData_G_str.SlowADC_Cnt_u16[1]	56344
DMAData_G_str.SlowADC_Cnt_u16[2]	54872

SlowADCFltAcc\_Cnt\_M\_u16

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IoHwAb_SlowADCGroupValidity			Razorcat
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[3]	5634		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	59561		
k_ReadAdcWaitTimeout_uS_u32	2840		
k_SlowADCValidDiag_Cnt_str.Threshold	65535		
k_SlowADCValidDiag_Cnt_str.PStep	68		
k_SlowADCValidDiag_Cnt_str.NStep	218		
target_GetSystemTime_uS_u32_CurrentTime	2171724739		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~	

58689

58689

Test Step 2.20 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	58029		
DMAData_G_str.SlowADC_Cnt_u16[1]	49186		
DMAData_G_str.SlowADC_Cnt_u16[2]	58029		
DMAData_G_str.SlowADC_Cnt_u16[3]	4918		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	39345		
k_ReadAdcWaitTimeout_uS_u32	60120		
k_SlowADCValidDiag_Cnt_str.Threshold	1		
k_SlowADCValidDiag_Cnt_str.PStep	121		
k_SlowADCValidDiag_Cnt_str.NStep	73		
target_GetSystemTime_uS_u32_CurrentTime	17904585		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
SlowADCFltAcc_Cnt_M_u16	39053	39053	<b>✓</b>

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~		
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~		

Name	Input Value		
DMAData G str.SlowADC Cnt u16[0]	65535		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3:	2_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	2309		
k_ReadAdcWaitTimeout_uS_u32	46206		
k_SlowADCValidDiag_Cnt_str.Threshold	150		
k_SlowADCValidDiag_Cnt_str.PStep	221		
k_SlowADCValidDiag_Cnt_str.NStep	69		
target_GetSystemTime_uS_u32_CurrentTime	0		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	•
SlowADCFitAcc Cnt M u16	150	150	•



Test Step Call Trace	Fest Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	2	DtrmnElapsedTime_uS_u32	2	<b>✓</b>	
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	<b>~</b>	
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	<b>✓</b>	
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	<b>~</b>	
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	<b>✓</b>	
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	-	
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	<b>✓</b>	
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	<b>~</b>	

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	57340		
DMAData_G_str.SlowADC_Cnt_u16[1]	24410		
DMAData_G_str.SlowADC_Cnt_u16[2]	57340		
DMAData_G_str.SlowADC_Cnt_u16[3]	2441		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	2_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	31759		
k_ReadAdcWaitTimeout_uS_u32	26338		
k_SlowADCValidDiag_Cnt_str.Threshold	192		
k_SlowADCValidDiag_Cnt_str.PStep	205		
k_SlowADCValidDiag_Cnt_str.NStep	126		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
SlowADCFltAcc Cnt M u16	31255	31255	<b>✓</b>

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>~</b>

Test Step 2.23 (Repeat Count = 1)			~
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	23593		
DMAData_G_str.SlowADC_Cnt_u16[1]	19206		
DMAData_G_str.SlowADC_Cnt_u16[2]	23593		
DMAData_G_str.SlowADC_Cnt_u16[3]	1920		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u3	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	46568		
k_ReadAdcWaitTimeout_uS_u32	24183		
k_SlowADCValidDiag_Cnt_str.Threshold	232		
k_SlowADCValidDiag_Cnt_str.PStep	250		
k_SlowADCValidDiag_Cnt_str.NStep	145		
target_GetSystemTime_uS_u32_CurrentTime	1298311081		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>~</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
SlowADCFItAcc Cnt M u16	45988	45988	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>



Test Step 2.24 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_	u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	43925		
k_ReadAdcWaitTimeout_uS_u32	43696		
k_SlowADCValidDiag_Cnt_str.Threshold	102	102	
k_SlowADCValidDiag_Cnt_str.PStep	185	185	
k_SlowADCValidDiag_Cnt_str.NStep	134	134	
target_GetSystemTime_uS_u32_CurrentTime	3634607182		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
SlowADCFltAcc_Cnt_M_u16	43389	43389	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	65535		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	54914		
k_ReadAdcWaitTimeout_uS_u32	24585		
k_SlowADCValidDiag_Cnt_str.Threshold	39	39	
k_SlowADCValidDiag_Cnt_str.PStep	107		
k_SlowADCValidDiag_Cnt_str.NStep	253	253	
target_GetSystemTime_uS_u32_CurrentTime	3489753329		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
SlowADCFltAcc Cnt M u16	39	39	<b>✓</b>

Test Step Call Trace	Test Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	-
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10 ReportNTCStatus	1	NxtrDiagMgr10 ReportNTCStatus	1	<b>✓</b>

Test Step 2.26 (Repeat Count = 1)		
Name	Input Value	
DMAData_G_str.SlowADC_Cnt_u16[0]	5792	
DMAData_G_str.SlowADC_Cnt_u16[1]	16485	
DMAData_G_str.SlowADC_Cnt_u16[2]	5792	
DMAData_G_str.SlowADC_Cnt_u16[3]	1648	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	1286	
k_ReadAdcWaitTimeout_uS_u32	26770	

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IoHwAb\_SlowADCGroupValidity

**Test Case 3: Path Test** 

Name	Input Value		
k_SlowADCValidDiag_Cnt_str.Threshold	17		
k_SlowADCValidDiag_Cnt_str.PStep	217		
k_SlowADCValidDiag_Cnt_str.NStep	178		
target_GetSystemTime_uS_u32_CurrentTime	861546824		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	574	574	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>	

Specification
Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 3.1 3226.00 Cycles
TS 3.2 5231.00 Cycles
TS 3.3 3229.00 Cycles
TS 3.4 3984.00 Cycles

TS3.1"((DMAData G str.SlowADC\_Cnt\_u16[i] == 0xFFFFU)==>FALSE && (ElapsedTime\_uS\_T\_u32 < k\_ReadAdcWaitTimeout\_uS\_u32))
(WaitedTime\_uS\_T\_u32 > ReadAdcMaxWaitedTime\_uS\_D\_u32)==> FALSE
(DMAData G\_str.SlowADC\_Cnt\_u16[i] == 0xFFFFU)==> FALSE"
TS3.2"((DMAData\_G\_str.SlowADC\_Cnt\_u16[i] == 0xFFFFU)==>TRUE && (ElapsedTime\_uS\_T\_u32 < k\_ReadAdcWaitTimeout\_uS\_u32)==> FALSE)
(DMAData G\_str.SlowADC\_Cnt\_u16[i] == 0xFFFFU)==> TRUE
(SlowADCFItAcc\_Cnt\_M\_u16 < (k\_SlowADCValidDiag\_Cnt\_str.Threshold - k\_SlowADCValidDiag\_Cnt\_str.PStep)) ==> FALSE
(SlowADCFItAcc\_Cnt\_M\_u16 > k\_SlowADCValidDiag\_Cnt\_str.Nstep)==> TRUE
TS3.4"(SlowADCFItAcc\_Cnt\_M\_u16 < k\_SlowADCValidDiag\_Cnt\_str.Threshold) - k\_SlowADCValidDiag\_Cnt\_str.PStep)) ==> TRUE
(SlowADCFItAcc\_Cnt\_M\_u16 > k\_SlowADCValidDiag\_Cnt\_str.Threshold) - k\_SlowADCValidDiag\_Cnt\_str.Th

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	0		
k_ReadAdcWaitTimeout_uS_u32	0		
k_SlowADCValidDiag_Cnt_str.Threshold	0		
k_SlowADCValidDiag_Cnt_str.PStep	0		
k_SlowADCValidDiag_Cnt_str.NStep	0		
target_GetSystemTime_uS_u32_CurrentTime	0		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	0	0	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	0	0	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	0	0	•
SlowADCFltAcc Cnt M u16	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime uS u32	4	DtrmnElapsedTime uS u32	4	<b>✓</b>	

Test Step 3.2 (Repeat Count = 1)	✓
Name	Input Value
DMAData_G_str.SlowADC_Cnt_u16[0]	65535

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IoHwAb\_SlowADCGroupValidity

Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	65535		
k_ReadAdcWaitTimeout_uS_u32	65535		
k_SlowADCValidDiag_Cnt_str.Threshold	65535		
k_SlowADCValidDiag_Cnt_str.PStep	65535		
k_SlowADCValidDiag_Cnt_str.NStep	65535		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
SlowADCFltAcc_Cnt_M_u16	65535	65535	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	•
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[0]	60192		
DMAData_G_str.SlowADC_Cnt_u16[1]	62200		
DMAData_G_str.SlowADC_Cnt_u16[2]	60192		
DMAData_G_str.SlowADC_Cnt_u16[3]	6220		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u	u32_CurrentTime	
SlowADCFltAcc_Cnt_M_u16	65535		
k_ReadAdcWaitTimeout_uS_u32	39582		
k_SlowADCValidDiag_Cnt_str.Threshold	159		
k_SlowADCValidDiag_Cnt_str.PStep	50		
k_SlowADCValidDiag_Cnt_str.NStep	73		
target_GetSystemTime_uS_u32_CurrentTime	2195757816		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	1	1	✓
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SlowADCFltAcc_Cnt_M_u16	65243	65243	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•	

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
DMAData_G_str.SlowADC_Cnt_u16[0]	65535
DMAData_G_str.SlowADC_Cnt_u16[1]	65535
DMAData_G_str.SlowADC_Cnt_u16[2]	65535
DMAData_G_str.SlowADC_Cnt_u16[3]	65535
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
SlowADCFltAcc_Cnt_M_u16	10
k_ReadAdcWaitTimeout_uS_u32	24585
k_SlowADCValidDiag_Cnt_str.Threshold	255
k_SlowADCValidDiag_Cnt_str.PStep	10
k_SlowADCValidDiag_Cnt_str.NStep	253

IoHwAb\_SlowADCGroupValidity

SlowADCFltAcc\_Cnt\_M\_u16

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Name	Input Value		
target_GetSystemTime_uS_u32_CurrentTime	3489753329		
Name	Actual Value	Expected Value	Result
IoHwAb_SlowADCGroupValidity()	0	0	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	✓
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10 ReportNTCStatus(Status Cnt T enum)	1	*none*	✓

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	5	DtrmnElapsedTime_uS_u32	5	~

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IoHwAb\_ReadAdc

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Project IoHwAbstractionUsr\_FIASA

ModuleIoHwAbstractionUsrTest ObjectIoHwAb\_ReadAdc

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\loHwAbstractionUsr_FIASA	
Configuration File	D:\Synergy_Work_Area\IoHwAbstractionUsr_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml	
Target Environment	TI TMS 570 PLS UDE (Default)	
Kind of Test	Unit Test	
Linker Options		
Source File(s)		
File	\$(PROJECTROOT)\loHwAbstractionUsr\src\loHwAbstractionUsr.c	
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\loHwAbstractionUsr\utp\contract -I\$(PROJECTROOT)\loHwAbstractionUsr\include -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include	

Name	Text
Module 'IoHwAbstractionUsr'	Unit Test Information*  Name of Tester:Sarika Natu Code File(s) Under Test:IoHwAbstractionUsr.c Code File(s) Version:5  Module Design Document:NA Module Design Document Version:NA Data Dictionary Version: 4 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.32 Total FLASH Used (Bytes):50 Total FLASH Used (Bytes):50 Total RAM Used (Bytes):50 Total Test Date:6/2/2016 Comments:"Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested.  Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In function ""IoHwAb_SlowADCGroupValidity"", extra code is added in VBA, to exit the while loop and to replicate the usercode functionality of TESSY.  """

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd			

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Attributes	
Name	Value
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	False
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\IoHwAbstractionUsr_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### **Test Case 1: Metrics Test**

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS 1.1 1749.00 Cycles TS 1.2 6603.00 Cycles

Description

TS1.1"Shortest Execution Path: ( DataValid\_Cnt\_T\_lgc == TRUE )

TS1.1"Shortest Execution Path: ( Datavanu\_Cnt\_1\_igc -- INOL ) = false"
TS1.2"Longest Execution Path: ( DataValid\_Cnt\_T\_lgc == TRUE )==> TRUE ( (Vref\_Volt\_T\_f32 > k\_VrefMax\_Volt\_f32) || (Vref\_Volt\_T\_f32 < k\_VrefMin\_Volt\_f32) )==> TRUE (VrefFltAcc\_Cnt\_M\_u16 < (k\_VrefDiag\_Cnt\_str.Threshold - k\_VrefDiag\_Cnt\_str.Pstep))==> FALSE (Vref\_Volt\_T\_f32 > k\_VrefMax\_Volt\_f32) ==> FALSE ( Vref\_Volt\_T\_f32 > k\_VrefMax\_Volt\_f32) ==> FALSE

Name	Input Value		
	31		
BattSwitched_Volt_M_f32	31		
Batt_Volt_M_f32	65535		
DMAData_G_str.SlowADC_Cnt_u16[0]			
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData_G_str.SlowADC_Cnt_u16[3]	65535	00 FI IT	
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32	_Current i ime	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value			
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC			
SlowADCFitAcc_Cnt_M_u16	65535		
SysCVSwitch_Volt_M_f32	31		
Temperature_Volt_M_f32	5		
VrefFltAcc_Cnt_M_u16	65535		
k_ReadAdcWaitTimeout_uS_u32	65535		
k_SlowADCValidDiag_Cnt_str.Threshold	255		
k_SlowADCValidDiag_Cnt_str.PStep	255		
k_SlowADCValidDiag_Cnt_str.NStep	255		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	255		
k_VrefDiag_Cnt_str.PStep	255		
k_VrefDiag_Cnt_str.NStep	255		
k_VrefMax_Volt_f32	6		
k_VrefMin_Volt_f32	6		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4294967295		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Resul
BattSwitched Volt M f32	31	31	
Batt Volt M f32	31	31	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	32	
NxtrDiagMgr10 ReportNTCStatus(Status Cnt T enum)	1	1	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	31	31	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.valu	'	31	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC		31	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tem		5	
SlowADCFItAcc Cnt M u16	255	255	
SysCVSwitch_Volt_M_f32	31	31	
	0	31	· · · · · · · · · · · · · · · · · · ·
Temperature Volt M f32	5	5	



Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	-
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	•
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	23.6083183		
Batt Volt M f32	4.23414135		
DMAData G str.SlowADC Cnt u16[0]	43		
DMAData G str.SlowADC Cnt u16[1]	44		
DMAData G str.SlowADC Cnt u16[2]	45		
DMAData G str.SlowADC Cnt u16[3]	46		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	dTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTin		
Rte Task 2ms 10.Rte TB.Rte I CDDInterface SysCVSwitchADC Cnt u16.value			
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\			
SlowADCFItAcc_Cnt_M_u16	65535		
SysCVSwitch Volt M f32	24.7070389		
Temperature Volt M f32	3.26238513		
VrefFltAcc Cnt M u16	530		
k ReadAdcWaitTimeout uS u32	39582		
k SlowADCValidDiag Cnt str.Threshold	159		
k SlowADCValidDiag Cnt str.PStep	50		
k_SlowADCValidDiag_Cnt_str.NStep	73		
k TempSensorOffset Volt f32	0		
k TempSensorScale Uls f32	1		
k_VbattScale_Uls_f32	6.74712658		
k VrefDiag Cnt str.Threshold	31		
k VrefDiag Cnt str.PStep	163		
k VrefDiag Cnt str.NStep	60		
k_VrefMax_Volt_f32	2.25536227		
k VrefMin Volt f32	2.34912372		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
	2089692451		
target_DtrmnElapsedTime_uS_u32_ElapsedTime			
target_GetSystemTime_uS_u32_CurrentTime	2195757816	I=	
Name	Actual Value	Expected Value	Resul
BattSwitched_Volt_M_f32	0.189260975	0.189260975	•
Batt_Volt_M_f32	0.362482995	0.362482995	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0.362482995	0.362482995	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value		0.189260975	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\		0.102859229	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporary		0.0525030531	•
SlowADCFltAcc_Cnt_M_u16	65243	65243	•
SysCVSwitch_Volt_M_f32	0.102859229	0.102859229	•
Temperature_Volt_M_f32	0.0525030531	0.0525030531	•
VrefFltAcc_Cnt_M_u16	31	31	•
NTC	0x46	0x46	•
Param	0x02	0x02	•
Status	0x01	0x01	•

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	-
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~



#### **Test Case 2: Boundary Test**

#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS 2.1 6643.00 Cycles
TS 2.2 1771.00 Cycles
TS 2.3 6595.00 Cycles
TS 2.4 1743.00 Cycles
TS 2.5 6567.00 Cycles
TS 2.6 6592.00 Cycles
TS 2.7 6592.00 Cycles
TS 2.8 6592.00 Cycles
TS 2.9 6592.00 Cycles
TS 2.10 6592.00 Cycles
TS 2.11 6592.00 Cycles
TS 2.12 6592.00 Cycles
TS 2.13 6592.00 Cycles
TS 2.14 6592.00 Cycles
TS 2.13 6592.00 Cycles
TS 2.14 6592.00 Cycles
TS 2.13 6592.00 Cycles TS 2.13 6592.00 Cycles TS 2.15 1197.00 Cycles TS 2.16 6592.00 Cycles TS 2.17 6592.00 Cycles TS 2.18 6592.00 Cycles TS 2.18 6592.00 Cycles
TS 2.20 6575.00 Cycles
TS 2.21 6559.00 Cycles
TS 2.21 6559.00 Cycles
TS 2.22 6592.00 Cycles
TS 2.23 6592.00 Cycles
TS 2.24 6592.00 Cycles
TS 2.25 6592.00 Cycles
TS 2.26 6577.00 Cycles
TS 2.26 6577.00 Cycles
TS 2.27 6592.00 Cycles
TS 2.28 6592.00 Cycles
TS 2.29 1206.00 Cycles
TS 2.29 1206.00 Cycles
TS 2.30 1139.00 Cycles
TS 2.31 6567.00 Cycles TS 2.31 6567.00 Cycles TS 2.32 6592.00 Cycles TS 2.33 1162.00 Cycles TS 2.34 6592.00 Cycles TS 2.34 6592.00 Cycles
TS 2.35 6592.00 Cycles
TS 2.36 6603.00 Cycles
TS 2.37 6599.00 Cycles
TS 2.38 6599.00 Cycles
TS 2.39 6592.00 Cycles
TS 2.40 6603.00 Cycles

#### Description

#### Vector Description:

TS2.1All Min

TS2.3DMAData\_G\_str.SlowADC\_Cnt\_u16[4]=Min TS2.4DMAData\_G\_str.SlowADC\_Cnt\_u16[4]=Max TS2.5DMAData\_G\_str.SlowADC\_Cnt\_u16[4]=Pos

TS2.6SysCVSwitchADC\_Cnt\_u16=Min TS2.7SysCVSwitchADC\_Cnt\_u16=Max TS2.8SysCVSwitchADC\_Cnt\_u16=Pos

TS2.9BattSwitched\_Volt\_M\_f32=Min TS2.10BattSwitched\_Volt\_M\_f32=Max TS2.11BattSwitched\_Volt\_M\_f32=Pos

TS2.12Batt\_Volt\_M\_f32=Min TS2.13Batt\_Volt\_M\_f32=Max TS2.14Batt\_Volt\_M\_f32=Pos

TS2.15SysCVSwitch\_Volt\_M\_f32=Min

TS2.15SysCVSwitch\_Volt\_M\_f32=Min TS2.16SysCVSwitch\_Volt\_M\_f32=Max TS2.17SysCVSwitch\_Volt\_M\_f32=Min TS2.18Temperature\_Volt\_M\_f32=Min TS2.19Temperature\_Volt\_M\_f32=Max TS2.20Temperature\_Volt\_M\_f32=Max TS2.21K\_VrefMax\_Volt\_f32=Min TS2.22k\_VrefMax\_Volt\_f32=Max TS2.23k\_VrefMax\_Volt\_f32=Pos TS2.24k\_VrefMax\_Volt\_f32=Pos TS2.24k\_VrefMax\_Volt\_f32=Default TS2.25k\_VrefMin\_Volt\_f32=Min TS2.26k\_VrefMin\_Volt\_f32=Max TS2.27k\_VrefMin\_Volt\_f32=Pos TS2.24k\_VrefMin\_Volt\_f32=Pos TS2.24k\_VrefMin\_Volt\_f32=Pos TS2.28k\_VrefMin\_Volt\_f32=Default

TS2.28k\_VrefMin\_Volt\_f32=Default

TS2.29VrefFltAcc\_Cnt\_M\_u16=Min TS2.30VrefFltAcc\_Cnt\_M\_u16=Max

TS2.31VrefFltAcc\_Cnt\_M\_u16=Pos

TS2.31Vrefritacc\_Cnt\_M\_u1b=Pos
TS2.32k\_VrefDiag\_Cnt\_str.Threshold=Min
TS2.33k\_VrefDiag\_Cnt\_str.Threshold=Max
TS2.34k\_VrefDiag\_Cnt\_str.Threshold=Pos/Default
TS2.35k\_VrefDiag\_Cnt\_str.Pstep=Min
TS2.36k\_VrefDiag\_Cnt\_str.Pstep=Max
TS2.37k\_VrefDiag\_Cnt\_str.Pstep=Pos/Default
TS2.38k\_VrefDiag\_Cnt\_str.Nstep=Min
TS2.39k\_VrefDiag\_Cnt\_str.Nstep=Max
TS2.40k\_VrefDiag\_Cnt\_str.Nstep=Pos/Default

152.4UK_	_vreiblag_	_Cnt_	_sir.ivstep	=Pos/Delault

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
BattSwitched_Volt_M_f32	0
Batt_Volt_M_f32	0
DMAData_G_str.SlowADC_Cnt_u16[0]	0
DMAData_G_str.SlowADC_Cnt_u16[1]	0
DMAData_G_str.SlowADC_Cnt_u16[2]	0
DMAData_G_str.SlowADC_Cnt_u16[3]	0

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Name	Input Value		
DtrmnElapsedTime uS u32(ElapsedTime)	target DtrmnElapsedTime uS u32 Elapsed	Time	
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTim		
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value			
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\			
SlowADCFltAcc Cnt M u16	0		
SysCVSwitch Volt M f32	0		
Temperature Volt M f32	0		
VrefFltAcc Cnt M u16	0		
k_ReadAdcWaitTimeout_uS_u32	0		
k SlowADCValidDiag Cnt str.Threshold	0		
k SlowADCValidDiag Cnt str.PStep	0		
k SlowADCValidDiag Cnt str.NStep	0		
k TempSensorOffset Volt f32	0		
k TempSensorScale Uls f32	1		
k_VbattScale_Uls_f32	6.74712658		
k VrefDiag Cnt str.Threshold	0		
k_VrefDiag_Cnt_str.PStep	0		
k_VrefDiag_Cnt_str.NStep	0		
k VrefMax Volt f32	0		
k_VrefMin_Volt_f32	0		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	0		
target_GetSystemTime_uS_u32_CurrentTime	0		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0	0	~
Batt_Volt_M_f32	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	0	0	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	0	0	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0	0	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0	0	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	0	0	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper	0	0	~
SlowADCFltAcc_Cnt_M_u16	0	0	•
SysCVSwitch_Volt_M_f32	0	0	~
Temperature_Volt_M_f32	0	0	~
VrefFltAcc_Cnt_M_u16	0	0	~
NTC	0x46	0x46	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.2 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	31
Batt_Volt_M_f32	31
DMAData_G_str.SlowADC_Cnt_u16[0]	65535
DMAData_G_str.SlowADC_Cnt_u16[1]	65535
DMAData_G_str.SlowADC_Cnt_u16[2]	65535
DMAData_G_str.SlowADC_Cnt_u16[3]	65535
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	65535
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	65535
SlowADCFltAcc_Cnt_M_u16	65535
SysCVSwitch_Volt_M_f32	31
Temperature_Volt_M_f32	5
VrefFltAcc_Cnt_M_u16	65535
k_ReadAdcWaitTimeout_uS_u32	65535
k_SlowADCValidDiag_Cnt_str.Threshold	65535
k_SlowADCValidDiag_Cnt_str.PStep	65535
k_SlowADCValidDiag_Cnt_str.NStep	65535

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Name	Input Value		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_UIs_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	255		
k_VrefDiag_Cnt_str.PStep	255		
k_VrefDiag_Cnt_str.NStep	255		
k_VrefMax_Volt_f32	6		
k_VrefMin_Volt_f32	6		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4294967295		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	31	31	~
Batt_Volt_M_f32	31	31	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	31	31	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	31	31	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysCV	31	31	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB	5	5	~
SlowADCFltAcc_Cnt_M_u16	65535	65535	•
SysCVSwitch_Volt_M_f32	31	31	~
Temperature_Volt_M_f32	5	5	~
VrefFltAcc_Cnt_M_u16	65535	65535	•

est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	•
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~

Test Step 2.3 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	28.0038395
Batt_Volt_M_f32	4.64012289
DMAData_G_str.SlowADC_Cnt_u16[0]	0
DMAData_G_str.SlowADC_Cnt_u16[1]	0
DMAData_G_str.SlowADC_Cnt_u16[2]	0
DMAData_G_str.SlowADC_Cnt_u16[3]	0
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	5246
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	5246
SlowADCFItAcc_Cnt_M_u16	0
SysCVSwitch_Volt_M_f32	30.3865681
Temperature_Volt_M_f32	2.34604096
VrefFltAcc_Cnt_M_u16	524
k_ReadAdcWaitTimeout_uS_u32	400
k_SlowADCValidDiag_Cnt_str.Threshold	119
k_SlowADCValidDiag_Cnt_str.PStep	84
k_SlowADCValidDiag_Cnt_str.NStep	166
k_TempSensorOffset_Volt_f32	0
k_TempSensorScale_Uls_f32	1
k_VbattScale_Uls_f32	6.74712658
k_VrefDiag_Cnt_str.Threshold	25
k_VrefDiag_Cnt_str.PStep	157
k_VrefDiag_Cnt_str.NStep	54
k_VrefMax_Volt_f32	0.398358107
k_VrefMin_Volt_f32	2.09247947
k_VrefScale_Uls_f32	1





Name	Input Value		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2157471337		
target_GetSystemTime_uS_u32_CurrentTime	3826103446		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0	0	~
Batt_Volt_M_f32	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0	0	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0	0	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	4.73152447	4.73152447	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature.	0	0	~
SlowADCFltAcc_Cnt_M_u16	0	0	•
SysCVSwitch_Volt_M_f32	4.73152447	4.73152447	~
Temperature_Volt_M_f32	0	0	•
VrefFltAcc_Cnt_M_u16	25	25	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	loHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched Volt M f32	30.3547401		
Batt Volt M f32	18.6845856		
DMAData G str.SlowADC Cnt u16[0]	65535		
DMAData_G_str.SlowADC_Cnt_u16[1]	65535		
DMAData G str.SlowADC Cnt u16[2]	65535		
DMAData G str.SlowADC Cnt u16[3]	65535		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target DtrmnElapsedTime uS u32 Elapsed	Time	
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTim		
Rte Task 2ms 10.Rte TB.Rte I CDDInterface SysCVSwitchADC Cnt u16.value			
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\			
SlowADCFltAcc Cnt M u16	65535		
SysCVSwitch_Volt_M_f32	28.8872509		
Temperature_Volt_M_f32	4.8639884		
VrefFltAcc_Cnt_M_u16	525		
k_ReadAdcWaitTimeout_uS_u32	39582		
k_SlowADCValidDiag_Cnt_str.Threshold	159		
k_SlowADCValidDiag_Cnt_str.PStep	50		
k_SlowADCValidDiag_Cnt_str.NStep	73		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	26		
k_VrefDiag_Cnt_str.PStep	158		
k_VrefDiag_Cnt_str.NStep	55		
k_VrefMax_Volt_f32	1.5506171		
k_VrefMin_Volt_f32	3.65667725		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2089692451		
target_GetSystemTime_uS_u32_CurrentTime	2195757816		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	30.3547401	30.3547401	~
Batt_Volt_M_f32	18.6845856	18.6845856	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	18.6845856	18.6845856	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	30.3547401	30.3547401	•
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	28.8872509	28.8872509	~

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Name	Actual Value	Expected Value	Result
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temperature	4.8639884	4.8639884	~
SlowADCFltAcc_Cnt_M_u16	159	159	•
SysCVSwitch_Volt_M_f32	28.8872509	28.8872509	~
Temperature_Volt_M_f32	4.8639884	4.8639884	•
VrefFltAcc_Cnt_M_u16	525	525	~

Test Step Call Trace	st Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	•
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	•
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~

Name	Input Value			
BattSwitched_Volt_M_f32	4.30806255			
Batt Volt M f32	25.8107109			
DMAData G str.SlowADC Cnt u16[0]	1			
DMAData G str.SlowADC Cnt u16[1]	2			
DMAData G str.SlowADC Cnt u16[2]	3			
DMAData G str.SlowADC Cnt u16[3]	4			
DtrmnElapsedTime uS u32(ElapsedTime)	target DtrmnElapsedTime uS u3	32 FlansedTime		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_			
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value		Ganonerano		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC				
SlowADCFItAcc_Cnt_M_u16	43580			
SysCVSwitch_Volt_M_f32	27.9898472			
Femperature_Volt_M_f32	1.26439631			
/refFltAcc Cnt M u16	526			
ReadAdcWaitTimeout uS u32	18562			
SlowADCValidDiag Cnt str.Threshold	149			
SlowADCValidDiag Cnt str.PStep	237			
SlowADCValidDiag Cnt str.NStep	146			
C_TempSensorOffset_Volt_f32	0			
	1			
:_VbattScale_Uls_f32	6.74712658			
VrefDiag Cnt str.Threshold	27			
VrefDiag Cnt str.PStep	159			
C VrefDiag Cnt str.NStep	56			
C VrefMax Volt f32	4.85695553			
C VrefMin Volt f32	0.426252633			
v VrefScale Uls f32	1			
<pre>C_VswitchScale_Uls_f32</pre>	3.36966825			
arget_DtrmnElapsedTime_uS_u32_ElapsedTime	2615883870			
arget_GetSystemTime_uS_u32_CurrentTime	20507433			
Name	Actual Value	Expected Value	Res	
	0.0164574776	0.0164574757	Res	
BattSwitched_Volt_M_f32	0.0164574776	0.0164574757		
Batt_Volt_M_f32	54	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	32	*none*		
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	1	*none*		
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)				
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0.0164765008	0.0164764989		
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.valu		0.0164574757		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC		4.73975325		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temp		0.00122100126		
SlowADCFItAcc_Cnt_M_u16	42996	42996		
SysCVSwitch_Volt_M_f32	4.73975325	4.73975325		
remperature_Volt_M_f32	0.00122100126	0.00122100126		
/refFltAcc_Cnt_M_u16	27	27		
NTC	0x46	0x46		
Param	0x02	0x02		



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.6 (Repeat Count = 1)			~
Name	Input Value		
BattSwitched_Volt_M_f32	19.5796547		
Batt_Volt_M_f32	25.3909492		
DMAData_G_str.SlowADC_Cnt_u16[0]	1		
DMAData_G_str.SlowADC_Cnt_u16[1]	2		
DMAData_G_str.SlowADC_Cnt_u16[2]	3		
DMAData_G_str.SlowADC_Cnt_u16[3]	4		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	edTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentT	ime	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	0		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	0		
SlowADCFltAcc_Cnt_M_u16	43925		
SysCVSwitch_Volt_M_f32	8.8453722		
Temperature_Volt_M_f32	4.77035999		
VrefFltAcc_Cnt_M_u16	527		
k_ReadAdcWaitTimeout_uS_u32	43696		
k_SlowADCValidDiag_Cnt_str.Threshold	102		
k SlowADCValidDiag Cnt str.PStep	185		
k SlowADCValidDiag Cnt str.NStep	134		
k TempSensorOffset Volt f32	0		
k_TempSensorScale_Uls_f32	1		
k VbattScale Uls f32	6.74712658		
k VrefDiag Cnt str.Threshold	28		
k VrefDiag Cnt str.PStep	160		
k VrefDiag Cnt str.NStep	57		
k_VrefMax_Volt_f32	1.14667892		
k_VrefMin_Volt_f32	3.62730908		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1908826491		
target_GetSystemTime_uS_u32_CurrentTime	3634607182		
Name	Actual Value	Expected Value	Result
BattSwitched Volt M f32	0.0164574776	0.0164574757	- TCSuit
Batt Volt M f32	0.0164765008	0.0164764989	~
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr Batt Volt f32.value	0.0164765008	0.0164764989	-
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr BattSwitched Volt f32.value		0.0164574757	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\		0	-
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper		0.00122100126	
SlowADCFltAcc_Cnt_M_u16	43389	43389	-
SysCVSwitch Volt M f32	0	0	
·		-	-
Temperature_Volt_M_f32	0.00122100126	0.00122100126	
VrefFltAcc_Cnt_M_u16	0.46	28	-
NTC	0x46 0x02	0x46 0x02	
Param			~
Status	0x01	0x01	<b>V</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	



Name	Input Value			
BattSwitched_Volt_M_f32	1.42019367			
Batt_Volt_M_f32	10.7669411			
DMAData G str.SlowADC Cnt u16[0]	15			
DMAData_G_str.SlowADC_Cnt_u16[1]	16			
DMAData G str.SlowADC Cnt u16[2]	17			
DMAData G str.SlowADC Cnt u16[3]	18			
DtrmnElapsedTime uS u32(ElapsedTime)		ndTime		
GetSystemTime_u3_u32(CurrentTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime target_GetSystemTime_uS_u32_CurrentTime			
		me		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\				
	10			
SlowADCFItAcc_Cnt_M_u16	17.3533077			
SysCVSwitch_Volt_M_f32				
Temperature_Volt_M_f32	0.463570893			
VrefFltAcc_Cnt_M_u16	528			
k_ReadAdcWaitTimeout_uS_u32	24585			
k_SlowADCValidDiag_Cnt_str.Threshold	255			
k_SlowADCValidDiag_Cnt_str.PStep	10			
k_SlowADCValidDiag_Cnt_str.NStep	253			
k_TempSensorOffset_Volt_f32	0			
k_TempSensorScale_Uls_f32	1			
k_VbattScale_Uls_f32	6.74712658			
k_VrefDiag_Cnt_str.Threshold	29			
k_VrefDiag_Cnt_str.PStep	161			
k_VrefDiag_Cnt_str.NStep	58			
k_VrefMax_Volt_f32	1.13776076			
k_VrefMin_Volt_f32	1.25570905			
k_VrefScale_Uls_f32	1			
k_VswitchScale_Uls_f32	3.36966825			
target_DtrmnElapsedTime_uS_u32_ElapsedTime	887031526			
target_GetSystemTime_uS_u32_CurrentTime	3489753329			
Name	Actual Value	Expected Value	Resul	
BattSwitched_Volt_M_f32	0.0740586445	0.0740586445		
Batt_Volt_M_f32	0.131812006	0.131811991	•	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*		
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	•	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0.131812006	0.131811991	· · · · · ·	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.0740586445	0.0740586445		
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	16.848341	16.848341	•	
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempa	0.0183150191	0.0183150191		
SlowADCFitAcc_Cnt_M_u16	0	0		
SysCVSwitch_Volt_M_f32	16.848341	16.848341		
Temperature_Volt_M_f32	0.0183150191	0.0183150191		
VrefFltAcc_Cnt_M_u16	29	29	•	
NTC	0x46	0x46	•	
Param	0x02	0x02	•	
Status	0x01	0x01		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.8 (Repeat Count = 1)	🗸
Name	Input Value
BattSwitched_Volt_M_f32	25.9065113
Batt_Volt_M_f32	28.1904907
DMAData_G_str.SlowADC_Cnt_u16[0]	29
DMAData_G_str.SlowADC_Cnt_u16[1]	30
DMAData_G_str.SlowADC_Cnt_u16[2]	31
DMAData_G_str.SlowADC_Cnt_u16[3]	32
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	15525
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\columnwidth{\columnwidth}{C} and a substractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\columnwidth{\columnwidth}{C} and and a substractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\columnw$	15525

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Name	Input Value		
SlowADCFltAcc_Cnt_M_u16	1286		
SysCVSwitch Volt M f32	14.6357908		
Temperature Volt M f32	0.246733636		
VrefFltAcc Cnt M u16	529		
k ReadAdcWaitTimeout uS u32	26770		
k SlowADCValidDiag Cnt str.Threshold	17		
k_SlowADCValidDiag_Cnt_str.PStep	217		
k_SlowADCValidDiag_Cnt_str.NStep	178		
k TempSensorOffset Volt f32	0		
k TempSensorScale Uls f32	1		
k_VbattScale_Uls_f32	6.74712658		
k VrefDiag Cnt str.Threshold	30		
k_VrefDiag_Cnt_str.PStep	162		
k_VrefDiag_Cnt_str.NStep	59		
k_VrefMax_Volt_f32	1.1271081		
k_VrefMin_Volt_f32	4.75260639		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3576754686		
target_GetSystemTime_uS_u32_CurrentTime	861546824		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.131659821	0.131659806	~
Batt_Volt_M_f32	0.247147501	0.247147486	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0.247147501	0.247147486	~
$Rte\_Task\_2ms\_10. Rte\_TB. Rte\_I\_loHwAbstractionUsr\_BattSwitched\_Volt\_f32. value for the property of the prope$	0.131659821	0.131659806	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	13.3182135	13.3182125	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.035409037	0.035409037	~
SlowADCFltAcc_Cnt_M_u16	574	574	~
SysCVSwitch_Volt_M_f32	13.3182135	13.3182125	~
Temperature_Volt_M_f32	0.035409037	0.035409037	~
VrefFltAcc_Cnt_M_u16	30	30	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>✓</b>

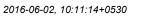
Test Step 2.9 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	0
Batt_Volt_M_f32	29.0710506
DMAData_G_str.SlowADC_Cnt_u16[0]	211
DMAData_G_str.SlowADC_Cnt_u16[1]	212
DMAData_G_str.SlowADC_Cnt_u16[2]	213
DMAData_G_str.SlowADC_Cnt_u16[3]	214
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	591
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC	591
SlowADCFltAcc_Cnt_M_u16	60482
SysCVSwitch_Volt_M_f32	9.19895458
Temperature_Volt_M_f32	3.33381295
VrefFltAcc_Cnt_M_u16	2554
k_ReadAdcWaitTimeout_uS_u32	0
k_SlowADCValidDiag_Cnt_str.Threshold	181
k_SlowADCValidDiag_Cnt_str.PStep	85
k_SlowADCValidDiag_Cnt_str.NStep	217
k_TempSensorOffset_Volt_f32	0
k_TempSensorScale_Uls_f32	1
k_VbattScale_Uls_f32	6.74712658
k_VrefDiag_Cnt_str.Threshold	43



Name	Input Value		
k_VrefDiag_Cnt_str.PStep	175		
k_VrefDiag_Cnt_str.NStep	72		
k_VrefMax_Volt_f32	2.73158336		
k_VrefMin_Volt_f32	0.889596105		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	79494048		
target_GetSystemTime_uS_u32_CurrentTime	4139727429		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.880475044	0.880474985	~
Batt_Volt_M_f32	1.74650908	1.74650896	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.74650908	1.74650896	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.880475044	0.880474985	•
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	2.43159223	2.43159199	•
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe	0.257631272	0.257631272	•
SlowADCFltAcc_Cnt_M_u16	59614	59614	✓
SysCVSwitch_Volt_M_f32	2.43159223	2.43159199	~
Temperature_Volt_M_f32	0.257631272	0.257631272	✓
VrefFltAcc_Cnt_M_u16	43	43	~
NTC	0x46	0x46	✓
Param	0x02	0x02	~
Status	0x01	0x01	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched Volt M f32	31		
Batt Volt M f32	3.52833652		
DMAData G str.SlowADC Cnt u16[0]	225		
DMAData G str.SlowADC Cnt u16[1]	226		
DMAData_G_str.SlowADC_Cnt_u16[2]	227		
DMAData_G_str.SlowADC_Cnt_u16[3]	228		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	e	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	592		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysCV	592		
SlowADCFitAcc_Cnt_M_u16	49261		
SysCVSwitch_Volt_M_f32	17.0789413		
Temperature_Volt_M_f32	1.19054377		
VrefFltAcc_Cnt_M_u16	2555		
k_ReadAdcWaitTimeout_uS_u32	65535		
k_SlowADCValidDiag_Cnt_str.Threshold	234		
k_SlowADCValidDiag_Cnt_str.PStep	22		
k_SlowADCValidDiag_Cnt_str.NStep	219		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	44		
k_VrefDiag_Cnt_str.PStep	176		
k_VrefDiag_Cnt_str.NStep	73		
k_VrefMax_Volt_f32	0.780729175		
k_VrefMin_Volt_f32	0.377253652		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1576855212		
target_GetSystemTime_uS_u32_CurrentTime	2517312924		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.938076198	0.938076138	~
Batt_Volt_M_f32	1.86184454	1.86184442	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~





Name	Actual Value	Expected Value	Result
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.86184454	1.86184442	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.938076198	0.938076138	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	2.43570662	2.43570638	•
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temperature	0.274725288	0.274725288	~
SlowADCFltAcc_Cnt_M_u16	48385	48385	•
SysCVSwitch_Volt_M_f32	2.43570662	2.43570638	~
Temperature_Volt_M_f32	0.274725288	0.274725288	<b>✓</b>
VrefFltAcc_Cnt_M_u16	44	44	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched Volt M f32	4.5815568		
Batt Volt M f32	19.8421535		
DMAData_G_str.SlowADC_Cnt_u16[0]	239		
DMAData G str.SlowADC Cnt u16[1]	240		
DMAData G str.SlowADC Cnt u16[2]	241		
DMAData G str.SlowADC Cnt u16[3]	242		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTim		
Rte Task 2ms 10.Rte TB.Rte I CDDInterface SysCVSwitchADC Cnt u16.value	593		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	593		
SlowADCFItAcc Cnt M u16	16572		
SysCVSwitch Volt M f32	0.891750693		
Temperature Volt M f32	3.43647194		
VrefFltAcc Cnt M u16	24578		
k_ReadAdcWaitTimeout_uS_u32	400		
k_SlowADCValidDiag_Cnt_str.Threshold	146		
k SlowADCValidDiag Cnt str.PStep	146		
k SlowADCValidDiag Cnt str.NStep	112		
k TempSensorOffset Volt f32	0		
k_TempSensorScale_UIs_f32	1		
k VbattScale Uls f32	6.74712658		
k VrefDiag Cnt str.Threshold	45		
k VrefDiag Cnt str.PStep	177		
k_VrefDiag_Cnt_str.NStep	74		
k VrefMax Volt f32	1.25062776		
k VrefMin Volt f32	1.85848808		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1905314420		
target GetSystemTime uS u32 CurrentTime	3086448313		
Name	Actual Value	Expected Value	Result
	0.995677352	0.995677292	Kesuit
BattSwitched_Volt_M_f32			~
Batt_Volt_M_f32	1.97718	1.97717988	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32 1	*none*	, v
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)		1	<b>*</b>
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.97718	1.97717988	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value		0.995677292	<b>*</b>
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\		2.43982077	<b>*</b>
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper		0.291819304	-
SlowADCFltAcc_Cnt_M_u16	16124	16124	<b>V</b>
SysCVSwitch_Volt_M_f32	2.439821	2.43982077	<b>V</b>
Temperature_Volt_M_f32	0.291819304	0.291819304	•
VrefFltAcc_Cnt_M_u16	45	45	<b>V</b>
NTC	0x46	0x46	~
Param	0x02	0x02	~

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Name	Actual Value	Expected Value	Result
Status	0x01	0x01	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>~</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>~</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.12 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	24.5132751		
Batt_Volt_M_f32	0		
DMAData_G_str.SlowADC_Cnt_u16[0]	295		
DMAData_G_str.SlowADC_Cnt_u16[1]	296		
DMAData_G_str.SlowADC_Cnt_u16[2]	297		
DMAData_G_str.SlowADC_Cnt_u16[3]	298		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	e	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	597		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	597		
SlowADCFltAcc_Cnt_M_u16	38463		
SysCVSwitch_Volt_M_f32	4.46260881		
Temperature_Volt_M_f32	0.791613579		
VrefFltAcc_Cnt_M_u16	24582		
k_ReadAdcWaitTimeout_uS_u32	56454		
k_SlowADCValidDiag_Cnt_str.Threshold	236		
k_SlowADCValidDiag_Cnt_str.PStep	186		
k_SlowADCValidDiag_Cnt_str.NStep	0		
k_TempSensorOffset_Volt_f32	0		
k TempSensorScale Uls f32	1		
k_VbattScale_Uls_f32	6.74712658		
k VrefDiag Cnt str.Threshold	49		
k VrefDiag Cnt str.PStep	181		
k VrefDiag Cnt str.NStep	154		
k VrefMax Volt f32	1.2409085		
k VrefMin Volt f32	3.66786385		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2243170835		
target_GetSystemTime_uS_u32_CurrentTime	2046175492		
Name	Actual Value	Expected Value	Result
BattSwitched Volt M f32	1.22608197	1.22608197	~
Batt Volt M f32	2.4385221	2.43852186	~
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	~
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10 ReportNTCStatus(Status Cnt T enum)	1	1	~
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr Batt Volt f32.value	2.4385221	2.43852186	<b>✓</b>
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr BattSwitched Volt f32.value		1.22608197	~
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\		2.45627832	•
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temper		0.360195369	~
SlowADCFitAcc Cnt M u16	38463	38463	<b>✓</b>
SysCVSwitch Volt M f32	2.45627832	2.45627832	<b>✓</b>
Temperature Volt M f32	0.360195369	0.360195369	•
VrefFltAcc Cnt M u16	49	49	~
NTC	0x46	0x46	<b>✓</b>
Param	0x02	0x02	_
Status	0x01	0x01	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~





Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	6.31574869		
Batt_Volt_M_f32	31		
DMAData_G_str.SlowADC_Cnt_u16[0]	309		
DMAData_G_str.SlowADC_Cnt_u16[1]	310		
DMAData G str.SlowADC Cnt u16[2]	311		
DMAData_G_str.SlowADC_Cnt_u16[3]	312		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	e	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	598		
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	598		
SlowADCFltAcc_Cnt_M_u16	35912		
SysCVSwitch_Volt_M_f32	15.8778629		
Temperature_Volt_M_f32	2.79404235		
VrefFitAcc_Cnt_M_u16	24583		
k_ReadAdcWaitTimeout_uS_u32	61733		
k_SlowADCValidDiag_Cnt_str.Threshold	197		
k_SlowADCValidDiag_Cnt_str.PStep	230		
k_SlowADCValidDiag_Cnt_str.NStep	255		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	50		
k_VrefDiag_Cnt_str.PStep	182		
k_VrefDiag_Cnt_str.NStep	155		
k_VrefMax_Volt_f32	2.076509		
k_VrefMin_Volt_f32	0.76046747		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2722463454		
target_GetSystemTime_uS_u32_CurrentTime	57471248		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.28368318	1.28368318	~
Batt_Volt_M_f32	2.55385756	2.55385733	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	2.55385756	2.55385733	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.28368318	1.28368318	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	2.46039271	2.46039271	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.377289385	0.377289385	~
SlowADCFltAcc_Cnt_M_u16	34892	34892	~
SysCVSwitch_Volt_M_f32	2.46039271	2.46039271	~
Temperature_Volt_M_f32	0.377289385	0.377289385	~
VrefFltAcc_Cnt_M_u16	50	50	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	<b>✓</b>

Test Step 2.14 (Repeat Count = 1)	🗸
Name	Input Value
BattSwitched_Volt_M_f32	25.7284222
Batt_Volt_M_f32	15.1094589
DMAData_G_str.SlowADC_Cnt_u16[0]	323
DMAData_G_str.SlowADC_Cnt_u16[1]	324
DMAData_G_str.SlowADC_Cnt_u16[2]	325
DMAData_G_str.SlowADC_Cnt_u16[3]	326
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	599
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\columnwidth{\columnwidth}{C} and a substractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\columnwidth{\columnwidth}{C} and and a substractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\columnw$	599

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Name	Input Value		
SlowADCFltAcc_Cnt_M_u16	60285		
SysCVSwitch_Volt_M_f32	0.576090574		
Temperature_Volt_M_f32	1.23858559		
VrefFltAcc_Cnt_M_u16	24584		
k_ReadAdcWaitTimeout_uS_u32	48478		
k_SlowADCValidDiag_Cnt_str.Threshold	247		
k_SlowADCValidDiag_Cnt_str.PStep	53		
k_SlowADCValidDiag_Cnt_str.NStep	1		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	51		
k_VrefDiag_Cnt_str.PStep	183		
k_VrefDiag_Cnt_str.NStep	156		
k_VrefMax_Volt_f32	4.64651155		
k_VrefMin_Volt_f32	3.28734779		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2033941355		
target_GetSystemTime_uS_u32_CurrentTime	3285270500		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.34128428	1.34128428	~
Batt_Volt_M_f32	2.66919303	2.66919279	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	2.66919303	2.66919279	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.34128428	1.34128428	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	2.4645071	2.4645071	~
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temperature	0.394383401	0.394383401	~
SlowADCFltAcc_Cnt_M_u16	60281	60281	~
SysCVSwitch_Volt_M_f32	2.4645071	2.4645071	~
Temperature_Volt_M_f32	0.394383401	0.394383401	~
VrefFltAcc_Cnt_M_u16	51	51	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>✓</b>

Test Step 2.15 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	3.13798022
Batt_Volt_M_f32	1.27581835
DMAData_G_str.SlowADC_Cnt_u16[0]	379
DMAData_G_str.SlowADC_Cnt_u16[1]	380
DMAData_G_str.SlowADC_Cnt_u16[2]	381
DMAData_G_str.SlowADC_Cnt_u16[3]	382
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	603
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	603
SlowADCFltAcc_Cnt_M_u16	18022
SysCVSwitch_Volt_M_f32	0
Temperature_Volt_M_f32	1.2864784
VrefFltAcc_Cnt_M_u16	24588
k_ReadAdcWaitTimeout_uS_u32	52013
k_SlowADCValidDiag_Cnt_str.Threshold	30
k_SlowADCValidDiag_Cnt_str.PStep	0
k_SlowADCValidDiag_Cnt_str.NStep	100
k_TempSensorOffset_Volt_f32	0
k_TempSensorScale_Uls_f32	1
k_VbattScale_Uls_f32	6.74712658
k_VrefDiag_Cnt_str.Threshold	55





Name	Input Value		
k_VrefDiag_Cnt_str.PStep	187		
k_VrefDiag_Cnt_str.NStep	160		
k_VrefMax_Volt_f32	2.37490797		
k_VrefMin_Volt_f32	0.416241318		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3081270104		
target_GetSystemTime_uS_u32_CurrentTime	2133832437		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.57168901	1.57168901	~
Batt_Volt_M_f32	3.13053489	3.13053489	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.13053489	3.13053489	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.57168901	1.57168901	•
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	2.48096466	2.48096442	•
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe	0.462759465	0.462759465	•
SlowADCFltAcc_Cnt_M_u16	17622	17622	<b>✓</b>
SysCVSwitch_Volt_M_f32	2.48096466	2.48096442	~
Temperature_Volt_M_f32	0.462759465	0.462759465	~
VrefFltAcc_Cnt_M_u16	24428	24428	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC Dma InvalidateSlowADCGroup	1	TWrapC Dma InvalidateSlowADCGroup	1	<b>✓</b>

Test Step 2.16 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	23.6290665		
Batt_Volt_M_f32	28.50704		
DMAData_G_str.SlowADC_Cnt_u16[0]	393		
DMAData_G_str.SlowADC_Cnt_u16[1]	394		
DMAData_G_str.SlowADC_Cnt_u16[2]	395		
DMAData_G_str.SlowADC_Cnt_u16[3]	396		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	dTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTir	ne	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	e 58742		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC	58742		
SlowADCFltAcc_Cnt_M_u16	59599		
SysCVSwitch_Volt_M_f32	31		
Temperature_Volt_M_f32	3.03321457		
VrefFltAcc_Cnt_M_u16	24589		
k_ReadAdcWaitTimeout_uS_u32	41113		
k_SlowADCValidDiag_Cnt_str.Threshold	121		
k_SlowADCValidDiag_Cnt_str.PStep	255		
k_SlowADCValidDiag_Cnt_str.NStep	122		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	56		
k_VrefDiag_Cnt_str.PStep	188		
k_VrefDiag_Cnt_str.NStep	161		
k_VrefMax_Volt_f32	0.710318327		
k_VrefMin_Volt_f32	0.596259058		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1396056638		
target_GetSystemTime_uS_u32_CurrentTime	2176681352		
Name	Actual Value	Expected Value	Result
BattSwitched Volt M f32	1.62929022	1.6292901	-
Batt Volt M f32	3.24587059	3.24587035	-
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	-
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr Batt Volt f32.value	3.24587059	3.24587035	-
Rte Task 2ms 10.Rte TB.Rte   IoHwAbstractionUsr BattSwitched Volt f32.valu		1.6292901	-

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Name	Actual Value	Expected Value	Result
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5.7518878	5.7518878	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.479853511	0.479853481	~
SlowADCFltAcc_Cnt_M_u16	59111	59111	~
SysCVSwitch_Volt_M_f32	5.7518878	5.7518878	~
Temperature_Volt_M_f32	0.479853511	0.479853481	~
VrefFltAcc_Cnt_M_u16	56	56	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>✓</b>

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Test Step 2.17 (Repeat Count = 1)			~
Name	Input Value		
BattSwitched_Volt_M_f32	30.0535297		
Batt_Volt_M_f32	13.0206537		
DMAData_G_str.SlowADC_Cnt_u16[0]	407		
DMAData_G_str.SlowADC_Cnt_u16[1]	408		
DMAData_G_str.SlowADC_Cnt_u16[2]	409		
DMAData_G_str.SlowADC_Cnt_u16[3]	410		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	dTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	ne	
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	58743		
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	58743		
SlowADCFltAcc_Cnt_M_u16	15637		
SysCVSwitch_Volt_M_f32	11.0298319		
Temperature_Volt_M_f32	4.9330678		
VrefFltAcc_Cnt_M_u16	24590		
k_ReadAdcWaitTimeout_uS_u32	53295		
k_SlowADCValidDiag_Cnt_str.Threshold	80		
k_SlowADCValidDiag_Cnt_str.PStep	4		
k_SlowADCValidDiag_Cnt_str.NStep	172		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	57		
k_VrefDiag_Cnt_str.PStep	189		
k_VrefDiag_Cnt_str.NStep	162		
k_VrefMax_Volt_f32	0.650771558		
k_VrefMin_Volt_f32	0.606542706		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3847345397		
target_GetSystemTime_uS_u32_CurrentTime	3507376676		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.68689144	1.68689132	~
Batt_Volt_M_f32	3.36120605	3.36120582	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.36120605	3.36120582	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.68689144	1.68689132	~
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	5.75600243	5.75600243	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper	0.496947527	0.496947497	~
SlowADCFltAcc_Cnt_M_u16	14949	14949	•
SysCVSwitch_Volt_M_f32	5.75600243	5.75600243	~
Temperature_Volt_M_f32	0.496947527	0.496947497	<b>✓</b>
VrefFltAcc_Cnt_M_u16	57	57	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	•





Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.18 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	16.7797127		
Batt_Volt_M_f32	15.7400465		
DMAData_G_str.SlowADC_Cnt_u16[0]	421		
DMAData_G_str.SlowADC_Cnt_u16[1]	422		
DMAData_G_str.SlowADC_Cnt_u16[2]	423		
DMAData_G_str.SlowADC_Cnt_u16[3]	424		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	dTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTir	me	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	58744		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	58744		
SlowADCFltAcc_Cnt_M_u16	7995		
SysCVSwitch_Volt_M_f32	2.59969521		
Temperature_Volt_M_f32	0		
VrefFltAcc_Cnt_M_u16	24591		
k_ReadAdcWaitTimeout_uS_u32	38002		
k_SlowADCValidDiag_Cnt_str.Threshold	0		
k SlowADCValidDiag Cnt str.PStep	186		
k SlowADCValidDiag Cnt str.NStep	128		
k TempSensorOffset Volt f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	58		
k_VrefDiag_Cnt_str.PStep	190		
k_VrefDiag_Cnt_str.NStep	163		
k_VrefMax_Volt_f32	0.767083645		
k_VrefMin_Volt_f32	3.20896411		
k_VrefScale_Uls_f32	1		
k VswitchScale Uls f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1498024395		
target_GetSystemTime_uS_u32_CurrentTime	2530847871		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.74449253	1.74449253	~
Batt_Volt_M_f32	3.47654152	3.47654128	<b>~</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.47654152	3.47654128	<b>~</b>
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr BattSwitched Volt f32.value		1.74449253	_
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\		5.76011658	<b>✓</b>
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe		0.514041543	~
SlowADCFItAcc Cnt M u16	7483	7483	<b>~</b>
SysCVSwitch Volt M f32	5.76011658	5.76011658	•
Temperature Volt M f32	0.514041543	0.514041543	•
VrefFltAcc_Cnt_M_u16	58	58	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	





Test Step 2.19 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
BattSwitched_Volt_M_f32	26.4607525			
Batt Volt M f32	22.7770042	22.7770042		
DMAData_G_str.SlowADC_Cnt_u16[0]	435			
DMAData G str.SlowADC Cnt u16[1]	436			
DMAData G str.SlowADC Cnt u16[2]	437			
DMAData G str.SlowADC Cnt u16[3]	438			
DtrmnElapsedTime uS u32(ElapsedTime)	target DtrmnElapsedTime uS u32 Elapsed	Time		
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTim			
Rte Task 2ms 10.Rte TB.Rte I CDDInterface SysCVSwitchADC Cnt u16.value	58745			
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	58745			
SlowADCFItAcc Cnt M u16	59561			
SysCVSwitch_Volt_M_f32	20.3263893			
Temperature Volt M f32	5			
VrefFltAcc Cnt M u16	24592			
k ReadAdcWaitTimeout uS u32	2840			
k SlowADCValidDiag Cnt str.Threshold	255			
k SlowADCValidDiag Cnt str.PStep	68			
k_SlowADCValidDiag_Cnt_str.NStep	218			
k TempSensorOffset Volt f32	0			
k TempSensorScale UIs f32	1			
k_VbattScale_Uls_f32	6.74712658			
k VrefDiag Cnt str.Threshold	59			
k VrefDiag Cnt str.PStep	191			
k VrefDiag Cnt str.NStep	164			
k_VrefMax_Volt_f32	4.15743685			
k VrefMin Volt f32	0.354314417			
k_VrefScale_Uls_f32	1			
k_VswitchScale_Uls_f32	3.36966825			
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4205382334			
target_GetSystemTime_uS_u32_CurrentTime	2171724739			
Name	Actual Value	Expected Value	Result	
BattSwitched_Volt_M_f32	1.80209374	1.80209363	~	
Batt_Volt_M_f32	3.59187698	3.59187675	~	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~	
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.59187698	3.59187675	~	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.80209374	1.80209363	~	
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysCNationUsr\_IoHwAb\_ReadAdc.Rte$	5.7642312	5.7642312	~	
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.531135559	0.531135559	~	
SlowADCFltAcc_Cnt_M_u16	58689	58689	~	
SysCVSwitch_Volt_M_f32	5.7642312	5.7642312	~	
Temperature_Volt_M_f32	0.531135559	0.531135559	~	
VrefFltAcc_Cnt_M_u16	24428	24428	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC Dma InvalidateSlowADCGroup	1	TWrapC Dma InvalidateSlowADCGroup	1	_

Test Step 2.20 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	30.6357689
Batt_Volt_M_f32	29.3858128
DMAData_G_str.SlowADC_Cnt_u16[0]	449
DMAData_G_str.SlowADC_Cnt_u16[1]	450
DMAData_G_str.SlowADC_Cnt_u16[2]	451
DMAData_G_str.SlowADC_Cnt_u16[3]	452
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	58746
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	58746
SlowADCFitAcc_Cnt_M_u16	39345
SysCVSwitch_Volt_M_f32	16.5102043
Temperature_Volt_M_f32	2.64551282
VrefFltAcc_Cnt_M_u16	24593

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IoHwAb\_ReadAdc Input Value k\_ReadAdcWaitTimeout\_uS\_u32 60120  $k\_SlowADCValidDiag\_Cnt\_str.Threshold$ 100 k\_SlowADCValidDiag\_Cnt\_str.PStep 121 k\_SlowADCValidDiag\_Cnt\_str.NStep 73 k\_TempSensorOffset\_Volt\_f32 0 k\_TempSensorScale\_Uls\_f32 1 k\_VbattScale\_Uls\_f32 6.74712658 k\_VrefDiag\_Cnt\_str.Threshold 60 k\_VrefDiag\_Cnt\_str.PStep 192 k\_VrefDiag\_Cnt\_str.NStep 165 k\_VrefMax\_Volt\_f32 0.158807486 k\_VrefMin\_Volt\_f32 1.85650408 k\_VrefScale\_Uls\_f32 3.36966825 k\_VswitchScale\_Uls\_f32 target\_DtrmnElapsedTime\_uS\_u32\_ElapsedTime 630440147

target_GetSystemTime_uS_u32_CurrentTime	17904585		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.85969484	1.85969484	•
Batt_Volt_M_f32	3.70721269	3.70721221	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.70721269	3.70721221	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.85969484	1.85969484	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5.76834536	5.76834536	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.548229575	0.548229575	~
SlowADCFltAcc_Cnt_M_u16	39053	39053	~
SysCVSwitch_Volt_M_f32	5.76834536	5.76834536	~
Temperature_Volt_M_f32	0.548229575	0.548229575	~
VrefFltAcc_Cnt_M_u16	60	60	~
NTC	0x46	0x46	~
Param	0x01	0x01	~
Status	0x01	0x01	•

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>~</b>

Test Step 2.21 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	5.07142639
Batt_Volt_M_f32	15.2209921
DMAData_G_str.SlowADC_Cnt_u16[0]	645
DMAData_G_str.SlowADC_Cnt_u16[1]	646
DMAData_G_str.SlowADC_Cnt_u16[2]	647
DMAData_G_str.SlowADC_Cnt_u16[3]	648
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	256
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	256
SlowADCFltAcc_Cnt_M_u16	2309
SysCVSwitch_Volt_M_f32	0.701105893
Temperature_Volt_M_f32	0.0742249489
VrefFltAcc_Cnt_M_u16	156
k_ReadAdcWaitTimeout_uS_u32	46206
k_SlowADCValidDiag_Cnt_str.Threshold	150
k_SlowADCValidDiag_Cnt_str.PStep	221
k_SlowADCValidDiag_Cnt_str.NStep	69
k_TempSensorOffset_Volt_f32	0
k_TempSensorScale_Uls_f32	1
k_VbattScale_Uls_f32	6.74712658
k_VrefDiag_Cnt_str.Threshold	74
k_VrefDiag_Cnt_str.PStep	37
k_VrefDiag_Cnt_str.NStep	179
k_VrefMax_Volt_f32	0
k_VrefMin_Volt_f32	1.00135124





Name	Input Value		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3550023085		
target_GetSystemTime_uS_u32_CurrentTime	0		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	2.66611123	2.66611123	~
Batt_Volt_M_f32	5.32190943	5.32190943	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	5.32190943	5.32190943	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	2.66611123	2.66611123	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	1.05327857	1.05327845	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.7875458	0.7875458	~
SlowADCFltAcc_Cnt_M_u16	2033	2033	•
SysCVSwitch_Volt_M_f32	1.05327857	1.05327845	~
Temperature_Volt_M_f32	0.7875458	0.7875458	•
VrefFltAcc_Cnt_M_u16	74	74	~
NTC	0x46	0x46	~
Param	0x01	0x01	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
loHwAb SlowADCGroupValidity	1	IoHwAb SlowADCGroupValidity	1	/ ✓
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.22 (Repeat Count = 1)			<b>→</b>
Name	Input Value		
BattSwitched_Volt_M_f32	22.6603279		
Batt_Volt_M_f32	28.3988495		
DMAData_G_str.SlowADC_Cnt_u16[0]	659		
DMAData_G_str.SlowADC_Cnt_u16[1]	660		
DMAData_G_str.SlowADC_Cnt_u16[2]	661		
DMAData_G_str.SlowADC_Cnt_u16[3]	662		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	edTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTi	me	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	257		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC	257		
SlowADCFltAcc_Cnt_M_u16	31759		
SysCVSwitch_Volt_M_f32	27.8346519		
Temperature_Volt_M_f32	1.45927286		
VrefFltAcc_Cnt_M_u16	157		
k_ReadAdcWaitTimeout_uS_u32	26338		
k_SlowADCValidDiag_Cnt_str.Threshold	192		
k_SlowADCValidDiag_Cnt_str.PStep	205		
k_SlowADCValidDiag_Cnt_str.NStep	126		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	75		
k_VrefDiag_Cnt_str.PStep	38		
k_VrefDiag_Cnt_str.NStep	180		
k_VrefMax_Volt_f32	6		
k_VrefMin_Volt_f32	1.64641285		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2374789753		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Resul
BattSwitched_Volt_M_f32	2.72371244	2.72371221	
Batt_Volt_M_f32	5.43724489	5.43724489	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	5.43724489	5.43724489	•
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr BattSwitched Volt f32.value	2.72371244	2.72371221	•

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Name	Actual Value	Expected Value	Result
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	1.05739284	1.05739284	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.804639816	0.804639816	~
SlowADCFltAcc_Cnt_M_u16	31255	31255	~
SysCVSwitch_Volt_M_f32	1.05739284	1.05739284	~
Temperature_Volt_M_f32	0.804639816	0.804639816	~
VrefFltAcc_Cnt_M_u16	75	75	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>✓</b>

Test Step 2.23 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched Volt M f32	6.5345788		
Batt Volt M f32	25.7201786		
DMAData G str.SlowADC Cnt u16[0]	673		
DMAData G str.SlowADC Cnt u16[1]	674		
DMAData G str.SlowADC Cnt u16[2]	675		
DMAData G str.SlowADC Cnt u16[3]	676		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	fTime	
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTim		
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	258		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	258		
SlowADCFltAcc_Cnt_M_u16	46568		
SysCVSwitch_Volt_M_f32	11.3360157		
Temperature_Volt_M_f32	4.18892241		
VrefFltAcc_Cnt_M_u16	158		
k_ReadAdcWaitTimeout_uS_u32	24183		
k_SlowADCValidDiag_Cnt_str.Threshold	232		
k_SlowADCValidDiag_Cnt_str.PStep	250		
k_SlowADCValidDiag_Cnt_str.NStep	145		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	76		
k_VrefDiag_Cnt_str.PStep	39		
k_VrefDiag_Cnt_str.NStep	181		
k_VrefMax_Volt_f32	1.53921175		
k_VrefMin_Volt_f32	3.92358184		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1933342689		
target_GetSystemTime_uS_u32_CurrentTime	1298311081		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	2.78131342	2.78131342	~
Batt_Volt_M_f32	5.55258036	5.55258036	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	5.55258036	5.55258036	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	2.78131342	2.78131342	~
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	1.06150723	1.06150723	<b>✓</b>
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.821733832	0.821733832	~
SlowADCFitAcc_Cnt_M_u16	45988	45988	~
SysCVSwitch_Volt_M_f32	1.06150723	1.06150723	~
Temperature_Volt_M_f32	0.821733832	0.821733832	~
VrefFltAcc_Cnt_M_u16	76	76	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	•





Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.24 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	3.81823254		
Batt_Volt_M_f32	5.84874392		
DMAData_G_str.SlowADC_Cnt_u16[0]	687		
DMAData_G_str.SlowADC_Cnt_u16[1]	688		
DMAData_G_str.SlowADC_Cnt_u16[2]	689		
DMAData_G_str.SlowADC_Cnt_u16[3]	690		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	dTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTir	me	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	259		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	259		
SlowADCFltAcc_Cnt_M_u16	43925		
SysCVSwitch_Volt_M_f32	2.70150685		
Temperature_Volt_M_f32	1.24109292		
VrefFltAcc_Cnt_M_u16	159		
k_ReadAdcWaitTimeout_uS_u32	43696		
k_SlowADCValidDiag_Cnt_str.Threshold	102		
k SlowADCValidDiag Cnt str.PStep	185		
k SlowADCValidDiag Cnt str.NStep	134		
k TempSensorOffset Volt f32	0		
k_TempSensorScale_Uls_f32	1		
k VbattScale Uls f32	6.74712658		
k VrefDiag Cnt str.Threshold	77		
k VrefDiag Cnt str.PStep	40		
k VrefDiag Cnt str.NStep	182		
k_VrefMax_Volt_f32	3		
k VrefMin Volt f32	3.50306416		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	0		
target_GetSystemTime_uS_u32_CurrentTime	3634607182		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	2.83891463	2.83891463	result
Batt Volt M f32	5.66791582	5.66791582	~
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	-
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr Batt Volt f32.value	5.66791582	5.66791582	~
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr BattSwitched Volt f32.value		2.83891463	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\		1.06562161	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper		0.838827848	
SlowADCFltAcc_Cnt_M_u16	43389	43389	~
SysCVSwitch Volt M f32	1.06562161	1.06562161	
Temperature_Volt_M_f32	0.838827848	0.838827848	-
	77	77	
VrefFitAcc_Cnt_M_u16 NTC	0x46	0x46	-
Param	0x02	0x02	
Status	0x02 0x01	0x02	
Status	UAUT	UAUT	_

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	



Test Step 2.25 (Repeat Count = 1) Name	Input Value		
	28.8084602		
BattSwitched_Volt_M_f32	2.58003068		
Batt_Volt_M_f32			
DMAData_G_str.SlowADC_Cnt_u16[0]	701 702		
DMAData_G_str.SlowADC_Cnt_u16[1]	1		
DMAData_G_str.SlowADC_Cnt_u16[2]	703		
DMAData_G_str.SlowADC_Cnt_u16[3]	704		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTir	ne	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value			
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC			
SlowADCFltAcc_Cnt_M_u16	54914		
SysCVSwitch_Volt_M_f32	16.98283		
Temperature_Volt_M_f32	1.84607613		
VrefFltAcc_Cnt_M_u16	160		
k_ReadAdcWaitTimeout_uS_u32	24585		
k_SlowADCValidDiag_Cnt_str.Threshold	39		
k_SlowADCValidDiag_Cnt_str.PStep	107		
k_SlowADCValidDiag_Cnt_str.NStep	253		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	78		
k_VrefDiag_Cnt_str.PStep	41		
k_VrefDiag_Cnt_str.NStep	183		
k_VrefMax_Volt_f32	1.51302028		
k_VrefMin_Volt_f32	0		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4294967295		
target_GetSystemTime_uS_u32_CurrentTime	3489753329		
Name	Actual Value	Expected Value	Resu
BattSwitched Volt M f32	2.89651585	2.89651585	
Batt Volt M f32	5.78325129	5.78325129	
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	5.78325129	5.78325129	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value		2.89651585	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC		1.069736	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Temp		0.855921865	
SlowADCFItAcc_Cnt_M_u16	53902	53902	
SysCVSwitch Volt M f32	1.069736	1.069736	
Temperature Volt M f32	0.855921865	0.855921865	
VrefFitAcc Cnt M u16	0.833921803	0.000921000	
NTC	0 0x46	0x46	
NIO .			
Param	0x00	0x00	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.26 (Repeat Count = 1)	✓
Name	Input Value
BattSwitched_Volt_M_f32	0.68984139
Batt_Volt_M_f32	13.6401043
DMAData_G_str.SlowADC_Cnt_u16[0]	225
DMAData_G_str.SlowADC_Cnt_u16[1]	226
DMAData_G_str.SlowADC_Cnt_u16[2]	227
DMAData_G_str.SlowADC_Cnt_u16[3]	228
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	261
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	261

IoHwAb\_ReadAdc

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Input Value SlowADCFltAcc\_Cnt\_M\_u16 1286 SysCVSwitch\_Volt\_M\_f32 21.9810658 Temperature\_Volt\_M\_f32 4.07619238 VrefFltAcc\_Cnt\_M\_u16 161 k ReadAdcWaitTimeout uS u32 26770  $k\_SlowADCValidDiag\_Cnt\_str.Threshold$ 17 k\_SlowADCValidDiag\_Cnt\_str.PStep 217  $k\_SlowADCValidDiag\_Cnt\_str.NStep$ 178 k\_TempSensorOffset\_Volt\_f32 0  $k\_TempSensorScale\_Uls\_f32$ 1 k\_VbattScale\_Uls\_f32 6.74712658  $k\_VrefDiag\_Cnt\_str.Threshold$ 79 k\_VrefDiag\_Cnt\_str.PStep 42 184 k\_VrefDiag\_Cnt\_str.NStep k\_VrefMax\_Volt\_f32 0.134790257 k\_VrefMin\_Volt\_f32 6 k\_VrefScale\_Uls\_f32 k\_VswitchScale\_Uls\_f32 3.36966825  $target\_DtrmnElapsedTime\_uS\_u32\_ElapsedTime$ 3576754686  $target\_GetSystemTime\_uS\_u32\_CurrentTime$ 861546824 **Actual Value Expected Value** Result Name BattSwitched\_Volt\_M\_f32 0.938076138 0.938076198 1.86184442 Batt\_Volt\_M\_f32 1.86184454 NxtrDiagMgr10\_ReportNTCStatus(NTC\_Cnt\_T\_enum) 54 \*none\* NxtrDiagMgr10\_ReportNTCStatus(Param\_Cnt\_T\_u08) 32 \*none\* NxtrDiagMgr10\_ReportNTCStatus(Status\_Cnt\_T\_enum) Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_loHwAbstractionUsr\_Batt\_Volt\_f32.value 1.86184442 Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_loHwAbstractionUsr\_BattSwitched\_Volt\_f32.value 0.938076198 0.938076138  $Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC \ \ 1.07385039$ 1.07385027 Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_Tempt 0.274725288 0.274725288 SlowADCFltAcc\_Cnt\_M\_u16 574 SysCVSwitch Volt M f32 1.07385039 1.07385027 0.274725288 Temperature\_Volt\_M\_f32 0.274725288 VrefFltAcc Cnt M u16 79 79 NTC 0x46 0x46

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>✓</b>

0x01

0x01

0x01

0x01

Test Step 2.27 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	24.8290863
Batt_Volt_M_f32	29.4313145
DMAData_G_str.SlowADC_Cnt_u16[0]	239
DMAData_G_str.SlowADC_Cnt_u16[1]	240
DMAData_G_str.SlowADC_Cnt_u16[2]	241
DMAData_G_str.SlowADC_Cnt_u16[3]	242
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	262
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	262
SlowADCFltAcc_Cnt_M_u16	35912
SysCVSwitch_Volt_M_f32	11.7482357
Temperature_Volt_M_f32	2.98473811
VrefFltAcc_Cnt_M_u16	162
k_ReadAdcWaitTimeout_uS_u32	61733
k_SlowADCValidDiag_Cnt_str.Threshold	197
k_SlowADCValidDiag_Cnt_str.PStep	230
k_SlowADCValidDiag_Cnt_str.NStep	255
k_TempSensorOffset_Volt_f32	0
k_TempSensorScale_Uls_f32	1
k_VbattScale_Uls_f32	6.74712658
k_VrefDiag_Cnt_str.Threshold	80





N	In and Walter		
Name	Input Value		
k_VrefDiag_Cnt_str.PStep	43		
k_VrefDiag_Cnt_str.NStep	185		
k_VrefMax_Volt_f32	4.07462263		
k_VrefMin_Volt_f32	0.641858637		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2722463454		
target_GetSystemTime_uS_u32_CurrentTime	57471248		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.995677352	0.995677292	•
Batt_Volt_M_f32	1.97718	1.97717988	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.97718	1.97717988	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.995677352	0.995677292	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	1.07796478	1.07796466	<b>✓</b>
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe	0.291819304	0.291819304	•
SlowADCFltAcc_Cnt_M_u16	34892	34892	<b>✓</b>
SysCVSwitch_Volt_M_f32	1.07796478	1.07796466	~
Temperature_Volt_M_f32	0.291819304	0.291819304	•
VrefFltAcc_Cnt_M_u16	80	80	~
NTC	0x46	0x46	<b>✓</b>
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.28 (Repeat Count = 1)			~	
Name	Input Value			
BattSwitched_Volt_M_f32	9.189394			
Batt_Volt_M_f32	16.3501911			
DMAData_G_str.SlowADC_Cnt_u16[0]	253			
DMAData_G_str.SlowADC_Cnt_u16[1]	254			
DMAData_G_str.SlowADC_Cnt_u16[2]	255			
DMAData_G_str.SlowADC_Cnt_u16[3]	256			
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	ie		
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	263			
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC	263			
SlowADCFitAcc_Cnt_M_u16	60285			
SysCVSwitch_Volt_M_f32	14.5291805			
Temperature_Volt_M_f32	1.63690341			
VrefFltAcc_Cnt_M_u16	163			
k_ReadAdcWaitTimeout_uS_u32	48478			
k_SlowADCValidDiag_Cnt_str.Threshold	247			
k_SlowADCValidDiag_Cnt_str.PStep	53			
k_SlowADCValidDiag_Cnt_str.NStep	1			
k_TempSensorOffset_Volt_f32	0			
k_TempSensorScale_Uls_f32	1			
k_VbattScale_Uls_f32	6.74712658			
k_VrefDiag_Cnt_str.Threshold	81			
k_VrefDiag_Cnt_str.PStep	44			
k_VrefDiag_Cnt_str.NStep	186			
k_VrefMax_Volt_f32	3.08257222			
k_VrefMin_Volt_f32	2			
k_VrefScale_Uls_f32	1			
k_VswitchScale_Uls_f32	3.36966825			
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2033941355			
target_GetSystemTime_uS_u32_CurrentTime	3285270500			
Name	Actual Value	Expected Value	Result	
BattSwitched_Volt_M_f32	1.05327857	1.05327845	~	
Batt_Volt_M_f32	2.09251547	2.09251547	~	
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	<b>✓</b>	

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Name	Actual Value	Expected Value	Result
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	2.09251547	2.09251547	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.05327857	1.05327845	~
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	1.08207905	1.08207905	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper	0.30891332	0.30891332	~
SlowADCFltAcc_Cnt_M_u16	60281	60281	~
SysCVSwitch_Volt_M_f32	1.08207905	1.08207905	~
Temperature_Volt_M_f32	0.30891332	0.30891332	•
VrefFltAcc_Cnt_M_u16	81	81	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	<b>✓</b>

Test Step 2.29 (Repeat Count = 1)			•	
Name	Input Value			
BattSwitched_Volt_M_f32	12.1762867			
Batt_Volt_M_f32		12.556304		
DMAData_G_str.SlowADC_Cnt_u16[0]	365			
DMAData_G_str.SlowADC_Cnt_u16[1]	366			
DMAData_G_str.SlowADC_Cnt_u16[2]	367			
DMAData_G_str.SlowADC_Cnt_u16[3]	368			
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u			
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32	_CurrentTime		
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.valu				
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Sys				
SlowADCFItAcc_Cnt_M_u16	18022			
SysCVSwitch_Volt_M_f32	19.2443027			
Temperature_Volt_M_f32	4.47366524			
VrefFltAcc_Cnt_M_u16	0			
k_ReadAdcWaitTimeout_uS_u32	52013			
k_SlowADCValidDiag_Cnt_str.Threshold	30			
k_SlowADCValidDiag_Cnt_str.PStep	0			
k_SlowADCValidDiag_Cnt_str.NStep	100			
k_TempSensorOffset_Volt_f32	0			
k_TempSensorScale_Uls_f32	1			
k_VbattScale_Uls_f32	6.74712658			
k_VrefDiag_Cnt_str.Threshold	82			
k_VrefDiag_Cnt_str.PStep	45			
k_VrefDiag_Cnt_str.NStep	187			
k_VrefMax_Volt_f32	3.23682141			
k_VrefMin_Volt_f32	3.13711953			
k_VrefScale_Uls_f32	1			
k_VswitchScale_Uls_f32	3.36966825			
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3081270104			
target_GetSystemTime_uS_u32_CurrentTime	2133832437			
Name	Actual Value	Expected Value	Resul	
BattSwitched_Volt_M_f32	1.5140878	1.5140878	•	
Batt_Volt_M_f32	3.01519942	3.01519942		
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*		
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*		
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	•	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.01519942	3.01519942		
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.val	ue 1.5140878	1.5140878	•	
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Sys	C\ 1.08619344	1.08619344		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Tem	рі 0.445665449	0.445665449	•	
SlowADCFltAcc_Cnt_M_u16	17622	17622		
SysCVSwitch_Volt_M_f32	1.08619344	1.08619344		
Temperature_Volt_M_f32	0.445665449	0.445665449		
VrefFltAcc Cnt M u16	45	45		



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~

Satish with red_Volt M_152	Test Step 2.30 (Repeat Count = 1)  ✓			
Batt_Volt_M_T32	Name	Input Value		
Batt_Voll_M_122	BattSwitched Volt M f32	24.7913418		
DMAData G_ str. SlowADC_Cnt_u16[0]   114		29.9216099		
DMAData_G_str.SlowADC_Cnt_u16[1]   114		113		
DMAData G. str. SlowADC_Cnt_u16[2]   116   DMDAData G. str. SlowADC_Cnt_u16[2]   116   DMDAData G. str. SlowADC_Cnt_u16[2]   116   DMDAData G. str. SlowADC_Index_G. Sys.CVSwitchADC_Cnt_u16 value   126   Rt. Task. Zms. 10 Rte. T. Rte.   DDIInterface. Sys.CVSwitchADC_Cnt_u16 value   126   Sys.CVSwitch_Volt_M. [32		114		
DMDmBLagoedTime_US_U32[ClapsedTime)		115		
DrimnElapsedTime_uS_u32(ElapsedTime)		116		
GetSystemTime_uS_u32_CurrentTime     Rte_Task_zms_10.Rie_TBRteCDDInterface_SystCVSwitchADC_Cnt_u16.value     Rte_Task_zms_10.Rie_RB.RteLotHwabstraccionUsr_JotHwAb_ReadAdc.Rte_SystC     SostOSWichCotNac_Cnt_M_u16     SystCVSwitch_V0.ttm_/132     Temperature_V0.ttm_M132     VrelFiAdc_Cnt_M_u16     ReadAdcWaltTimeout_uS_u32     Rea		target DtrmnElapsedTime uS u32 Elapsed	dTime	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value   265				
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc Rte_SysC   265				
SlowADCFILACE_Cnt_M_u16		265		
SysCVSwitch_Voit_M_132   3.93445283   3.93				
Temperature_Volt_M_f32		1.47977149		
VerFilAcc_Cnt_M_u16		3.93445253		
k_ReadAdcWaitTimeout_uS_u32         41113           k_SlowADCVaildDiag_Cnt_str.Threshold         121           k_SlowADCVaildDiag_Cnt_str.PStep         255           k_SlowADCVaildDiag_Cnt_str.NStep         122           k_TempSensorOffset_Volt_f32         0           k_TempSensorScale_Uls_f32         1           k_VobattScale_Uls_f32         6.74712658           k_VerBilag_Cnt_str.Threshold         83           k_VerBilag_Cnt_str.PStep         46           k_VerBilag_Cnt_str.NStep         188           k_VerBilag_Cnt_		65535		
K_SlowADCValidDiag_Cnt_str.Pstep		41113		
k_SlowADCValidDiag_Cnt_str.PStep         255           k_SlowADCValidDiag_Cnt_str.NStep         122           k_TempSensorGste_Uis_f32         0           k_TempSensorScale_Uis_f32         1           k_VrefDiag_Cnt_str.Threshold         83           k_VrefDiag_Cnt_str.NStep         46           k_VrefDiag_Cnt_str.NStep         48           k_VrefDiag_Cnt_str.NStep         4.36457777           k_VrefDiag_Cnt_str.NStep         4.36457777           k_VrefDiag_Cnt_str.Str.Dstep         4.36457777           k_VrefDiag_Cnt_str.Dstage         1.0738770589           k_VrefDiag_Cnt_str.Dstage         1.074770589		121		
L2   L2   L2   L3   L3   L3   L3   L3		255		
k_TempSensorOffset_Voll_f32         0           k_TempSensorScale_Uls_f32         1           k_VbattScale_Uls_f32         6.74712658           k_VverDiag_Cnt_str.Threshold         83           k_VverDiag_Cnt_str.PStep         46           k_VverDiag_Cnt_str.NStep         188           k_VverMax_Volt_f32         4.36457777           k_VverMin_Volt_f32         0.0738770589           k_VverScale_Uls_f32         1           k_VswitchScale_Uls_f32         4           k_VswitchScale_Uls_f32         1           k_VswitchScale_Uls_f32         0           k_VswitchScale_Uls_f32		122		
k_TempSensorScale_Uls_f32         1           k_VotatScale_Uls_f32         6.74712658           k_VrefDlag_Cnt_str.Threshold         83           k_VrefDlag_Cnt_str.NStep         46           k_VrefDlag_Cnt_str.NStep         188           k_VrefMlax_Volt_f32         4.36457777           k_VrefMlax_Volt_f32         0.0738770589           k_VrefScale_Uls_f32         1           k_VswitchScale_Uls_f32         1           k_VswitchScale_Uls_f32         1396056638           target_DtrmnElapsedTime_uS_u32_CurrentTime         1396056638           Actual Value         Expected Value         Result           BattSwitched_Volt_M_f32         0.477266818         0.477266818         ✓           BattSwitched_Volt_M_f32         0.939160526         0.939160466         ✓           NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)         54         'none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         'none*         ✓           Nxtr_ask_Zms_10.Rte_TB.Rte_IoHwAbstractionUsr_Batt_Volt_f32.value         0.939160526         0.939160466         ✓           VRte_Task_Zms_10.Rte_TB.Rte_IoHwAbstractionUsr_BattSwitched_Volt_f32.value         0.477266818         0.477266818         ✓           Rte_Ta	·- ·- ·- ·- ·- ·- ·- ·- ·- ·- ·- ·- ·- ·	0		
k_VrefDiag_Cnt_str.Threshold 83 k_VrefDiag_Cnt_str.NStep 46 k_VrefDiag_Cnt_str.NStep 48 k_VrefMax_Volt_f32 4.36457777 k_VrefMin_Volt_f32 4.36457777 k_VrefMin_Volt_f32 4.366625 k_VrefScale_Uls_f32 1.3666625 target_DirmcFlapsedTime_uS_u32_ElapsedTime 1396056638 target_GetSystemTime_uS_u32_CurrentTime 2176681352  Name Actual Value Expected Value Result BattSwitched_Volt_M_f32 0.477266818 0.477266818 0.477266818 0.477266618 Batt_Volt_M_f32 0.939160526 0.939160466 ↓ NxtDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum) 1 none* ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 32 none* ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 1 none* ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 0.939160526 0.939160466 ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 1 none* ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 0.939160526 0.939160466 ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 1 none* ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 0.939160526 0.939160466 ↓ NxtDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08) 1 none* ↓ NxtDiagMgr10_ReportN		1		
k_VrefDiag_Cnt_str.Threshold         83           k_VrefDiag_Cnt_str.PStep         46           k_VrefDiag_Cnt_str.NStep         188           k_VrefMax_Volt_f32         4.36457777           k_VrefMin_Volt_f32         0.0738770589           k_VrefScale_Uls_f32         1           k_VswitchScale_Uls_f32         3.3696825           target_DtrmnElapsedTime_uS_u32_ElapsedTime         1396056638           target_DtrmnElapsedTime_uS_u32_CurrentTime         2176681352           Name         Actual Value         Expected Value         Result           Batt_Volt_M_f32         0.477266818         0.477266818         ✓           Batt_Volt_M_f32         0.939160526         0.939160466         ✓           NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)         54         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)         32         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Param_cnt_u08)         2         *none*         ✓		6.74712658		
k_VrefDiag_Cnt_str.PStep         46           k_VrefDiag_Cnt_str.NStep         188           k_VrefMax_Volt_f32         4.36457777           k_VrefMax_Volt_f32         0.0738770589           k_VrefScale_Uls_f32         1           k_VswitchScale_Uls_f32         3.36966825           target_DtrmnElapsedTime_uS_u32_ElapsedTime         1396056638           target_DetSystemTime_uS_u32_CurrentTime         2176681352           Name         Actual Value         Expected Value         Result           BattSwitched_Volt_M_f32         0.477266818         0.477266818         •           Batt_Volt_M_f32         0.939160526         0.939160466         •           NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)         54         *none*         •           NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)         32         *none*         •           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*         •           Ntr_Task_Zms_10.Rte_TB_Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value         0.939160526         0.939160466         •           Rte_Task_Zms_10.Rte_TB_Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value         0.477266818         0.477266818         •           Rte_Task_Zms_10.Rte_RB_Rte_loHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Tempi         0.137973145	k VrefDiag Cnt str.Threshold			
k_VrefDiag_Cnt_str.NStep       188         k_VrefMax_Volt_f32       4.36457777         k_VrefMin_Volt_f32       0.0738770589         k_VrefScale_Uls_f32       1         k_VswitchScale_Uls_f32       3.36966825         target_DtmnElapsedTime_uS_u32_ElapsedTime       1396056638         target_GetSystemTime_uS_u32_CurrentTime       2176681352         Name       Actual Value       Expected Value       Result         Batt_Volt_M_f32       0.477266818       0.477266818       ✓         Batt_Volt_M_f32       0.939160526       0.939160466       ✓         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_cntenum)       1       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_cntenum)       1       *none*       ✓         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466       ✓         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818       ✓         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_batch_cnt_tm_t16       59111       59111       59111		46		
k_VrefScale_Uls_f32       1         k_VswitchScale_Uls_f32       3.36966825         target_DtrmnElapsedTime_uS_u32_ElapsedTime       1396056638         target_GetSystemTime_uS_u32_CurrentTime       2176681352         Name       Actual Value       Expected Value       Result         BattSwitched_Volt_M_f32       0.477266818       0.477266818       4.77266818         Batt_Volt_M_f32       0.939160526       0.939160466       ✓         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466       ✓         Rte_Task_zms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818       ✓         Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb ReadAdc.Rte SysCV       Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temport       0.137973145       0.137973145       0.137973145       ✓         SlowADCFItAcc_Cnt_M_u16591115911159111       59111       59111       59111       59111       59111       59111       59111		188		
k_VrefScale_Uls_f32       1         k_VswitchScale_Uls_f32       3.36966825         target_DtrmnElapsedTime_uS_u32_ElapsedTime       1396056638         target_GetSystemTime_uS_u32_CurrentTime       2176681352         Name       Actual Value       Expected Value       Result         BattSwitched_Volt_M_f32       0.477266818       0.477266818       4.77266818         Batt_Volt_M_f32       0.939160526       0.939160466       ✓         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466       ✓         Rte_Task_zms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818       ✓         Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb ReadAdc.Rte SysCV       Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temport       0.137973145       0.137973145       0.137973145       ✓         SlowADCFItAcc_Cnt_M_u16591115911159111       59111       59111       59111       59111       59111       59111       59111	k VrefMax Volt f32	4.36457777		
k_VswitchScale_UIs_f32       3.36966825         target_DtrmnElapsedTime_uS_u32_ElapsedTime       1396056638         target_GetSystemTime_uS_u32_CurrentTime       2176681352         Name       Actual Value       Expected Value       Result         BattSwitched_Volt_M_f32       0.477266818       0.477266818       ✓         Batt_Volt_M_f32       0.939160526       0.939160466       ✓         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         Rte_Task_2ms_10.Rte_IB.Rte_IoHwAbstractionUsr_Batt_Volt_f32.value       0.477266818       0.939160466       ✓         Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Tempt	k_VrefMin_Volt_f32	0.0738770589		
k_VswitchScale_Uls_f32       3.36966825         target_DtrmnElapsedTime_uS_u32_ElapsedTime       1396056638         target_GetSystemTime_uS_u32_CurrentTime       2176681352         Name       Actual Value       Expected Value       Result         BattSwitched_Volt_M_f32       0.477266818       0.477266818       0.477266818       ✓         Batt_Volt_M_f32       0.939160526       0.939160466       ✓         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*       ✓         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       0.939160526       0.939160466       ✓         Rte_Task_2ms_10.Rte_TB.Rte_IoHwAbstractionUsr_Batt_Volt_f32.value       0.477266818       0.477266818       0.477266818         Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\       1.09030783       1.09030783       0.137973145	k VrefScale Uls f32	1		
Name         Actual Value         Expected Value         Result           BattSwitched_Volt_M_f32         0.477266818         0.477266818	k_VswitchScale_Uls_f32			
Name         Actual Value         Expected Value         Result           BattSwitched_Volt_M_f32         0.477266818         0.477266818         ✓           Batt_Volt_M_f32         0.939160526         0.939160466         ✓           NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)         54         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_enum)         1         *none*         ✓           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*         ✓           Rte_Task_2ms_10.Rte_TB.Rte_!_loHwAbstractionUsr_Batt_Volt_f32.value         0.939160526         0.939160466         ✓           Rte_Task_2ms_10.Rte_TB.Rte_!_loHwAbstractionUsr_BattSwitched_Volt_f32.value         0.477266818         0.477266818         ✓           Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\         1.09030783         1.09030783         1.09030783         ✓           Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempi         59111         59111         59111           SysCVSwitch_Volt_M_f32         1.09030783         1.09030783         1.09030783         ✓           Temperature_Volt_M_f32         0.137973145         0.137973145         0.137973145         ✓	target DtrmnElapsedTime uS u32 ElapsedTime	1396056638		
BattSwitched_Volt_M_f32       0.477266818       0.477266818         Batt_Volt_M_f32       0.939160526       0.939160466         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818         Rte Task 2ms 10.Rte RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Sysc\\ Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempt       1.09030783       1.09030783         SlowADCFitAcc_Cnt_M_u16       59111       59111         SyscVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145	target_GetSystemTime_uS_u32_CurrentTime	2176681352		
BattSwitched_Volt_M_f32       0.477266818       0.477266818         Batt_Volt_M_f32       0.939160526       0.939160466         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       32       *none*         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818         Rte Task 2ms 10.Rte RB.Rte loHwAbstractionUsr loHwAb ReadAdc.Rte Sysc\\ Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempt       1.09030783       1.09030783         SlowADCFitAcc_Cnt_M_u16       59111       59111         SyscVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145	Name	Actual Value	Expected Value	Result
Batt_Volt_M_f32       0.939160526       0.939160466         NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)       54       *none*         NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)       32       *none*         NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818         Rte Task 2ms 10.Rte RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Sysc\       1.09030783       1.09030783         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempt       59111       59111         SlowADCFitAcc_Cnt_M_u16       59111       59111         SyscVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145		0.477266818	· ·	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)         54         *none*           NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)         32         *none*           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*           Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value         0.939160526         0.939160466           Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value         0.477266818         0.477266818           Rte Task 2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Sysc\\ Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempt         0.137973145         0.137973145           SlowADCFitAcc_Cnt_M_u16         59111         59111         59111           SyscVSwitch_Volt_M_f32         1.09030783         1.09030783           Temperature_Volt_M_f32         0.137973145         0.137973145				<b>~</b>
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)         32         *none*           NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)         1         *none*           Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value         0.939160526         0.939160466           Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value         0.477266818         0.477266818           Rte Task 2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Sysc\         1.09030783         1.09030783           Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempt         0.137973145         0.137973145           SlowADCFitAcc_Cnt_M_u16         59111         59111           SysCVSwitch_Volt_M_f32         1.09030783         1.09030783           Temperature_Volt_M_f32         0.137973145         0.137973145			*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)       1       *none*         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466         Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818         Rte Task 2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Sysc\       1.09030783       1.09030783         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Tempt       0.137973145       0.137973145         SlowADCFitAcc_Cnt_M_u16       59111       59111         SysCVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145				<b>~</b>
Rte_Task_2ms_10.Rte_TB.Rte_I loHwAbstractionUsr_Batt_Volt_f32.value       0.939160526       0.939160466         Rte_Task_2ms_10.Rte_TB.Rte_I loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\       1.09030783       1.09030783         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temp       0.137973145       0.137973145         SlowADCFitAcc_Cnt_M_u16       59111       59111         SysCVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145		1		~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value       0.477266818       0.477266818         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\       1.09030783       1.09030783         Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temp       0.137973145       0.137973145         SlowADCFitAcc_Cnt_M_u16       59111       59111         SysCVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145		0.939160526		<b>~</b>
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC       1.09030783       1.09030783         Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temple       0.137973145       0.137973145         SlowADCFitAcc_Cnt_M_u16       59111       59111         SysCVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145		0.477266818	0.477266818	_
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Templ       0.137973145       0.137973145         SlowADCFltAcc_Cnt_M_u16       59111       59111         SysCVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145				<b>~</b>
SlowADCFltAcc_Cnt_M_u16         59111         \$911				~
SysCVSwitch_Volt_M_f32       1.09030783       1.09030783         Temperature_Volt_M_f32       0.137973145       0.137973145				<b>~</b>
Temperature_Volt_M_f32 0.137973145 0.137973145 ✓				~
				~
		65347	65347	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC Dma InvalidateSlowADCGroup	1	TWrapC Dma InvalidateSlowADCGroup	1	_

Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
BattSwitched_Volt_M_f32	21.2281227
Batt_Volt_M_f32	21.3614731
DMAData_G_str.SlowADC_Cnt_u16[0]	127
DMAData_G_str.SlowADC_Cnt_u16[1]	128

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Name	Input Value		
DMAData_G_str.SlowADC_Cnt_u16[2]	129		
DMAData_G_str.SlowADC_Cnt_u16[3]	130		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse	dTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTin	ne	
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	266		
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC \\ \\$	266		
SlowADCFltAcc_Cnt_M_u16	15637		
SysCVSwitch_Volt_M_f32	19.4959545		
Temperature_Volt_M_f32	1.11849427		
VrefFltAcc_Cnt_M_u16	166		
k_ReadAdcWaitTimeout_uS_u32	53295		
k_SlowADCValidDiag_Cnt_str.Threshold	80		
k_SlowADCValidDiag_Cnt_str.PStep	4		
k_SlowADCValidDiag_Cnt_str.NStep	172		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	84		
k_VrefDiag_Cnt_str.PStep	47		
k_VrefDiag_Cnt_str.NStep	189		
k_VrefMax_Volt_f32	2.13537765		
k_VrefMin_Volt_f32	0.473402113		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3847345397		
target_GetSystemTime_uS_u32_CurrentTime	3507376676		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.534868002	0.534868002	~
Batt_Volt_M_f32	1.05449605	1.05449593	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.05449605	1.05449593	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.534868002	0.534868002	-
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	1.09442222	1.09442222	•
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe		0.155067161	-
SlowADCFltAcc_Cnt_M_u16	14949	14949	•
SysCVSwitch_Volt_M_f32	1.09442222	1.09442222	-
Temperature_Volt_M_f32	0.155067161	0.155067161	•
VrefFltAcc_Cnt_M_u16	84	84	•
NTC	0x46	0x46	•
Param	0x02	0x02	-
Status	0x01	0x01	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 2.32 (Repeat Count = 1)	✓
Name	Input Value
BattSwitched_Volt_M_f32	20.3504543
Batt_Volt_M_f32	25.1074829
DMAData_G_str.SlowADC_Cnt_u16[0]	141
DMAData_G_str.SlowADC_Cnt_u16[1]	142
DMAData_G_str.SlowADC_Cnt_u16[2]	143
DMAData_G_str.SlowADC_Cnt_u16[3]	144
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	267
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	267
SlowADCFltAcc_Cnt_M_u16	7995
SysCVSwitch_Volt_M_f32	30.4799118
Temperature_Volt_M_f32	2.52917743
VrefFltAcc_Cnt_M_u16	167
k_ReadAdcWaitTimeout_uS_u32	38002
k_SlowADCValidDiag_Cnt_str.Threshold	0



Name	Input Value		
k_SlowADCValidDiag_Cnt_str.PStep	186		
k_SlowADCValidDiag_Cnt_str.NStep	128		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	0		
k_VrefDiag_Cnt_str.PStep	48		
k_VrefDiag_Cnt_str.NStep	190		
k_VrefMax_Volt_f32	2.99047828		
k_VrefMin_Volt_f32	2.63544846		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1498024395		
target_GetSystemTime_uS_u32_CurrentTime	2530847871		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.592469156	0.592469156	~
Batt_Volt_M_f32	1.16983151	1.1698314	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.16983151	1.1698314	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.592469156	0.592469156	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	1.09853661	1.09853649	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.172161177	0.172161177	~
SlowADCFltAcc_Cnt_M_u16	7483	7483	~
SysCVSwitch_Volt_M_f32	1.09853661	1.09853649	~
Temperature_Volt_M_f32	0.172161177	0.172161177	~
VrefFltAcc_Cnt_M_u16	0	0	~
NTC	0x46	0x46	~
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	-

Test Step 2.33 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	8.3228569
Batt_Volt_M_f32	28.6493378
DMAData_G_str.SlowADC_Cnt_u16[0]	155
DMAData_G_str.SlowADC_Cnt_u16[1]	156
DMAData_G_str.SlowADC_Cnt_u16[2]	157
DMAData_G_str.SlowADC_Cnt_u16[3]	158
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	5489
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysCNationUsr\_IoHwAb\_ReadAdc.Rte$	5489
SlowADCFltAcc_Cnt_M_u16	59561
SysCVSwitch_Volt_M_f32	28.736084
Temperature_Volt_M_f32	4.16996622
VrefFltAcc_Cnt_M_u16	168
k_ReadAdcWaitTimeout_uS_u32	2840
k_SlowADCValidDiag_Cnt_str.Threshold	255
k_SlowADCValidDiag_Cnt_str.PStep	68
k_SlowADCValidDiag_Cnt_str.NStep	218
k_TempSensorOffset_Volt_f32	0
k_TempSensorScale_Uls_f32	1
k_VbattScale_Uls_f32	6.74712658
k_VrefDiag_Cnt_str.Threshold	255
k_VrefDiag_Cnt_str.PStep	49
k_VrefDiag_Cnt_str.NStep	191
k_VrefMax_Volt_f32	0.0864890367
k_VrefMin_Volt_f32	3.37638402
k_VrefScale_Uls_f32	1
k_VswitchScale_Uls_f32	3.36966825

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Name	Input Value		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4205382334		
target_GetSystemTime_uS_u32_CurrentTime	2171724739		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.65007031	0.65007031	~
Batt_Volt_M_f32	1.28516698	1.28516698	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.28516698	1.28516698	•
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_loHwAbstractionUsr\_BattSwitched\_Volt\_f32.value and the property of the property$	0.65007031	0.65007031	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysCNationUsr\_IoHwAb\_ReadAdc.Rte$	5.73131657	5.73131609	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.189255193	0.189255193	~
SlowADCFltAcc_Cnt_M_u16	58689	58689	•
SysCVSwitch_Volt_M_f32	5.73131657	5.73131609	~
Temperature_Volt_M_f32	0.189255193	0.189255193	•
VrefFltAcc_Cnt_M_u16	217	217	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~

Test Step 2.34 (Repeat Count = 1)			·
Name	Input Value		
BattSwitched_Volt_M_f32	3.30420732		
Batt_Volt_M_f32	30.9742775		
DMAData_G_str.SlowADC_Cnt_u16[0]	169		
DMAData_G_str.SlowADC_Cnt_u16[1]	170		
DMAData_G_str.SlowADC_Cnt_u16[2]	171		
DMAData_G_str.SlowADC_Cnt_u16[3]	172		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u3	2_ElapsedTime	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_0	CurrentTime	
$Rte\_Task\_2ms\_10.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	5490		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5490		
SlowADCFltAcc_Cnt_M_u16	39345		
SysCVSwitch_Volt_M_f32	24.1495571		
Temperature_Volt_M_f32	4.73801374		
VrefFltAcc_Cnt_M_u16	169		
k ReadAdcWaitTimeout uS u32	60120		
k SlowADCValidDiag Cnt str.Threshold	100		
k SlowADCValidDiag Cnt str.PStep	121		
k SlowADCValidDiag Cnt str.NStep	73		
k TempSensorOffset Volt f32	0		
k TempSensorScale UIs f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	100		
k VrefDiag Cnt str.PStep	50		
k VrefDiag Cnt str.NStep	192		
k_VrefMax_Volt_f32	1.45514643		
k VrefMin Volt f32	3.23836184		
k VrefScale Uls f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	630440147		
target_Ottfill:Liapsed fille_u3_u32_CurrentTime	17904585		
		Formanda d Walton	Daniel
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.707671463	0.707671463	•
Batt_Volt_M_f32	1.40050244	1.40050244	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	•
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.40050244	1.40050244	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value		0.707671463	•
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\		5.73543024	•
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_Temperature		0.206349209	•
SlowADCFltAcc_Cnt_M_u16	39053	39053	•
SysCVSwitch_Volt_M_f32	5.73543072	5.73543024	•
Temperature_Volt_M_f32	0.206349209	0.206349209	•
VrefFltAcc_Cnt_M_u16	100	100	· · · · · · · · · · · · · · · · · · ·

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Name	Actual Value	Expected Value	Result
NTC	0x46	0x46	<b>✓</b>
Param	0x02	0x02	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	•
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	•

Test Step 2.35 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	5.04460716		
Batt_Volt_M_f32	19.2410851		
DMAData_G_str.SlowADC_Cnt_u16[0]	183		
DMAData_G_str.SlowADC_Cnt_u16[1]	184		
DMAData_G_str.SlowADC_Cnt_u16[2]	185		
DMAData_G_str.SlowADC_Cnt_u16[3]	186		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTim	e	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	5491		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5491		
SlowADCFItAcc Cnt M u16	2309		
SysCVSwitch Volt M f32	6.61069393		
Temperature Volt M f32	4.74957466		
VrefFltAcc_Cnt_M_u16	170		
k_ReadAdcWaitTimeout_uS_u32	46206		
k SlowADCValidDiag Cnt str.Threshold	150		
k SlowADCValidDiag Cnt str.PStep	221		
k SlowADCValidDiag Cnt str.NStep	69		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k VbattScale Uls f32	6.74712658		
k VrefDiag Cnt str.Threshold	88		
k_VrefDiag_Cnt_str.PStep	0		
k_VrefDiag_Cnt_str.NStep	193		
k_VrefMax_Volt_f32	3.03617787		
k VrefMin Volt f32	4.20073986		
	1		
k_VrefScale_Uls_f32	3.36966825		
k_VswitchScale_Uls_f32	3550023085		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	0		
target_GetSystemTime_uS_u32_CurrentTime		1	
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.765272617	0.765272617	~
Batt_Volt_M_f32	1.51583803	1.51583791	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.51583803	1.51583791	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.765272617	0.765272617	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysCNationUsr\_IoHwAb\_ReadAdc.Rte$	5.73954487	5.73954487	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_Task_2ms_10.Rte_RB.Rt	0.223443225	0.223443225	~
SlowADCFltAcc_Cnt_M_u16	2033	2033	~
SysCVSwitch_Volt_M_f32	5.73954487	5.73954487	~
Temperature_Volt_M_f32	0.223443225	0.223443225	•
VrefFltAcc_Cnt_M_u16	88	88	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~



Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	3.9107058		
Batt_Volt_M_f32	17.7402134		
DMAData_G_str.SlowADC_Cnt_u16[0]	197		
DMAData_G_str.SlowADC_Cnt_u16[1]	198		
DMAData_G_str.SlowADC_Cnt_u16[2]	199		
DMAData_G_str.SlowADC_Cnt_u16[3]	200		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime	е	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	5492		
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5492		
SlowADCFltAcc Cnt M u16	31759		
SysCVSwitch Volt M f32	11.372323		
Temperature_Volt_M_f32	2.70241213		
VrefFitAcc Cnt M u16	171		
k ReadAdcWaitTimeout uS u32	26338		
k SlowADCValidDiag Cnt str.Threshold	192		
k SlowADCValidDiag Cnt str.PStep	205		
k SlowADCValidDiag Cnt str.NStep	126		
k TempSensorOffset Volt f32	0		
k TempSensorScale UIs f32	1		
k VbattScale Uls f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	89		
k_VrefDiag_Cnt_str.PStep	255		
k_VrefDiag_Cnt_str.NStep	194		
k VrefMax Volt f32	0.449034721		
k_VrefMin_Volt_f32	1.6331749		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
target DtrmnElapsedTime uS u32 ElapsedTime	2374789753		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
BattSwitched Volt M f32	0.822873831	0.822873831	- Toodit
Batt Volt M f32	1.63117361	1.63117337	<b>1</b>
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	J
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.63117361	1.63117337	-
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value		0.822873831	
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\		5.74365902	-
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe	31255	0.240537241 31255	-
SlowADCFItAcc_Cnt_M_u16 SucCVSwitch_Volt_M_t33		5.74365902	
SysCVSwitch_Volt_M_f32	5.74365902		<b>V</b>
Temperature_Volt_M_f32	0.240537256	0.240537241	
VrefFitAcc_Cnt_M_u16	89	89	
NTC	0x46	0x46	<b>*</b>
Param	0x02	0x02	<b>*</b>
Status	0x01	0x01	_

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	-
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	•

Test Step 2.37 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
BattSwitched_Volt_M_f32	23.0790176
Batt_Volt_M_f32	26.1974277
DMAData_G_str.SlowADC_Cnt_u16[0]	211
DMAData_G_str.SlowADC_Cnt_u16[1]	212
DMAData_G_str.SlowADC_Cnt_u16[2]	213
DMAData_G_str.SlowADC_Cnt_u16[3]	214
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime

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Name	Input Value		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime		
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	5493		
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	5493		
SlowADCFltAcc_Cnt_M_u16	46568		
SysCVSwitch_Volt_M_f32	8.35024643		
Temperature_Volt_M_f32	3.71041822		
VrefFltAcc_Cnt_M_u16	172		
k_ReadAdcWaitTimeout_uS_u32	24183		
k_SlowADCValidDiag_Cnt_str.Threshold	232		
k_SlowADCValidDiag_Cnt_str.PStep	250		
k_SlowADCValidDiag_Cnt_str.NStep	145		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	90		
k_VrefDiag_Cnt_str.PStep	4		
k_VrefDiag_Cnt_str.NStep	195		
k_VrefMax_Volt_f32	0.389562219		
k_VrefMin_Volt_f32	0.177392766		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	1933342689		
target_GetSystemTime_uS_u32_CurrentTime	1298311081		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.880475044	0.880474985	~
Batt_Volt_M_f32	1.74650908	1.74650896	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.74650908	1.74650896	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.880475044	0.880474985	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5.74777365	5.74777365	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte	0.257631272	0.257631272	~
SlowADCFltAcc_Cnt_M_u16	45988	45988	•
SysCVSwitch_Volt_M_f32	5.74777365	5.74777365	~
Temperature_Volt_M_f32	0.257631272	0.257631272	•
VrefFltAcc_Cnt_M_u16	0	0	~
NTC	0x46	0x46	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	<b>✓</b>

Test Step 2.38 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	6.05874729
Batt_Volt_M_f32	29.5515881
DMAData_G_str.SlowADC_Cnt_u16[0]	225
DMAData_G_str.SlowADC_Cnt_u16[1]	226
DMAData_G_str.SlowADC_Cnt_u16[2]	227
DMAData_G_str.SlowADC_Cnt_u16[3]	228
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	5494
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysCtricks (Application For Community C$	5494
SlowADCFltAcc_Cnt_M_u16	43925
SysCVSwitch_Volt_M_f32	26.460104
Temperature_Volt_M_f32	0.170259282
VrefFltAcc_Cnt_M_u16	173
k_ReadAdcWaitTimeout_uS_u32	43696
k_SlowADCValidDiag_Cnt_str.Threshold	102
k_SlowADCValidDiag_Cnt_str.PStep	185
k_SlowADCValidDiag_Cnt_str.NStep	134
k_TempSensorOffset_Volt_f32	0

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Name	Input Value		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	91		
k_VrefDiag_Cnt_str.PStep	54		
k_VrefDiag_Cnt_str.NStep	0		
k_VrefMax_Volt_f32	2.05074668		
k_VrefMin_Volt_f32	4.84529829		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	0		
target_GetSystemTime_uS_u32_CurrentTime	3634607182		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0.938076198	0.938076138	~
Batt_Volt_M_f32	1.86184454	1.86184442	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.86184454	1.86184442	<b>✓</b>
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	0.938076198	0.938076138	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	5.7518878	5.7518878	<b>✓</b>
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.274725288	0.274725288	~
SlowADCFltAcc_Cnt_M_u16	43389	43389	~
SysCVSwitch_Volt_M_f32	5.7518878	5.7518878	~
Temperature_Volt_M_f32	0.274725288	0.274725288	<b>✓</b>
VrefFltAcc_Cnt_M_u16	91	91	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	-



Test Step 2.39 (Repeat Count = 1) Name	Input Value		
	Input Value 1,28014803		
BattSwitched_Volt_M_f32	21.956377		
Batt_Volt_M_f32	239		
DMAData_G_str.SlowADC_Cnt_u16[0]	240		
DMAData_G_str.SlowADC_Cnt_u16[1]			
DMAData_G_str.SlowADC_Cnt_u16[2]	241		
DMAData_G_str.SlowADC_Cnt_u16[3]	242	1991	
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapse		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTi	me	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value			
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\			
SlowADCFItAcc_Cnt_M_u16	54914		
SysCVSwitch_Volt_M_f32	22.2590008		
Temperature_Volt_M_f32	1.9258697		
VrefFltAcc_Cnt_M_u16	174		
k_ReadAdcWaitTimeout_uS_u32	24585		
k_SlowADCValidDiag_Cnt_str.Threshold	39		
k_SlowADCValidDiag_Cnt_str.PStep	107		
k_SlowADCValidDiag_Cnt_str.NStep	253		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	152		
k_VrefDiag_Cnt_str.PStep	55		
k_VrefDiag_Cnt_str.NStep	255		
k_VrefMax_Volt_f32	2.45898724		
k_VrefMin_Volt_f32	2.23474526		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4294967295		
target_GetSystemTime_uS_u32_CurrentTime	3489753329		
Name	Actual Value	Expected Value	Resul
BattSwitched_Volt_M_f32	0.995677352	0.995677292	
Batt Volt M f32	1.97718	1.97717988	
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	1.97718	1.97717988	
	0.995677352	0.995677292	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\		5.75600243	
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Tempe		0.291819304	
SlowADCFltAcc_Cnt_M_u16	53902	53902	
SysCVSwitch Volt M f32	5.75600243	5.75600243	
Temperature Volt M f32	0.291819304	0.291819304	
VrefFitAcc Cnt M u16	152	152	
NTC	0x46	0x46	
Param	0x02	0x02	
	,		I ,

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IoHwAb_SlowADCGroupValidity	1	loHwAb_SlowADCGroupValidity	1	~	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~	
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•	
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~	

Test Step 2.40 (Repeat Count = 1)	🗸
Name	Input Value
BattSwitched_Volt_M_f32	7.45811367
Batt_Volt_M_f32	15.4351273
DMAData_G_str.SlowADC_Cnt_u16[0]	253
DMAData_G_str.SlowADC_Cnt_u16[1]	254
DMAData_G_str.SlowADC_Cnt_u16[2]	255
DMAData_G_str.SlowADC_Cnt_u16[3]	256
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	5496
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_loHwAbstractionUsr\_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb\_ReadAdc.Rte\_SysCNationUsr_loHwAb_ReadAdc.Rte\_SysCNationUsr_loHwAb_ReadAdc.Rte\_SysCNationUsr_loHwAb_ReadAdc.Rte\_SysCNationUsr_loHwAb_ReadAdc.Rte$	5496

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Name	Input Value		
SlowADCFltAcc_Cnt_M_u16	1286		
SysCVSwitch Volt M f32	18.6822491		
Temperature Volt M f32	3.29033518		
VrefFltAcc_Cnt_M_u16	175		
k_ReadAdcWaitTimeout_uS_u32	26770		
k_SlowADCValidDiag_Cnt_str.Threshold	17		
k_SlowADCValidDiag_Cnt_str.PStep	217		
k_SlowADCValidDiag_Cnt_str.NStep	178		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	153		
k_VrefDiag_Cnt_str.PStep	56		
k_VrefDiag_Cnt_str.NStep	1		
k_VrefMax_Volt_f32	2.72578239		
k_VrefMin_Volt_f32	3.16318583		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3576754686		
target_GetSystemTime_uS_u32_CurrentTime	861546824		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.05327857	1.05327845	~
Batt_Volt_M_f32	2.09251547	2.09251547	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	2.09251547	2.09251547	<b>✓</b>
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.05327857	1.05327845	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysCNationUsr\_IoHwAb\_ReadAdc.Rte$	5.76011658	5.76011658	<b>✓</b>
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temporal Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_RB.R	0.30891332	0.30891332	~
SlowADCFltAcc_Cnt_M_u16	574	574	<b>✓</b>
SysCVSwitch_Volt_M_f32	5.76011658	5.76011658	~
Temperature_Volt_M_f32	0.30891332	0.30891332	•
VrefFltAcc_Cnt_M_u16	153	153	~
NTC	0x46	0x46	•
Param	0x02	0x02	~
Status	0x01	0x01	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~	
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>	
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	•	
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•	
NxtrDiagMgr10 SetNTCStatus	1	NxtrDiagMgr10 SetNTCStatus	1	<b>✓</b>	

Test Step 3.1 (Repeat Count = 1)

BattSwitched Volt M f32



Input Value

n

Batt_Volt_M_f32	0		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]	0		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	e	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	0		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysCV	0		
SlowADCFltAcc_Cnt_M_u16	0		
SysCVSwitch_Volt_M_f32	0		
Temperature_Volt_M_f32	0		
VrefFitAcc_Cnt_M_u16	0		
k_ReadAdcWaitTimeout_uS_u32	0		
k SlowADCValidDiag Cnt str.Threshold	0		
k_SlowADCValidDiag_Cnt_str.PStep	0		
k SlowADCValidDiag Cnt str.NStep	0		
k TempSensorOffset Volt f32	0		
k_TempSensorScale_Uls_f32	1		
k VbattScale Uls f32	6.74712658		
k VrefDiag Cnt str.Threshold	0		
k VrefDiag Cnt str.PStep	0		
k_VrefDiag_Cnt_str.NStep	0		
k_VrefMax_Volt_f32	0		
k VrefMin Volt f32	0		
k_VrefScale_Uls_f32	1		
k VswitchScale Uls f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	0		
target GetSystemTime uS u32 CurrentTime	0		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0	0	✓ ×
Batt Volt M f32	0	0	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	0	*none*	_
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	0	0	<b>~</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	0	0	
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	0	0	•
Rte Task 2ms 10.Rte TB.Rte I IoHwAbstractionUsr BattSwitched Volt f32.value	*	0	_
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\		0	<b>✓</b>
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Temp		0	
SlowADCFItAcc Cnt M u16	0	0	~
SysCVSwitch Volt M f32	0	0	-
Temperature_Volt_M_f32	0	0	~
VrefFltAcc_Cnt_M_u16	0	0	_
NTC	0x46	0x46	~
			-

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Name	Actual Value	Expected Value	Result
Param	0x00	0x00	
Status	0x00	0x00	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 3.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
BattSwitched_Volt_M_f32	31		
Batt Volt M f32	31		
DMAData_G_str.SlowADC_Cnt_u16[0]	65535		
DMAData G str.SlowADC Cnt u16[1]	65535		
DMAData_G_str.SlowADC_Cnt_u16[2]	65535		
DMAData G str.SlowADC Cnt u16[3]	65535		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim		
Rte Task 2ms 10.Rte TB.Rte I CDDInterface SysCVSwitchADC Cnt u16.value	65535		
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	65535		
SlowADCFItAcc Cnt M u16	65535		
SysCVSwitch_Volt_M_f32	31		
Temperature Volt M f32	5		
VrefFltAcc_Cnt_M_u16	65535		
k ReadAdcWaitTimeout uS u32	65535		
k_SlowADCValidDiag_Cnt_str.Threshold	255		
k SlowADCValidDiag Cnt str.PStep	255		
k SlowADCValidDiag Cnt str.NStep	255		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	255		
k_VrefDiag_Cnt_str.PStep	255		
k_VrefDiag_Cnt_str.NStep	255		
k_VrefMax_Volt_f32	6		
k_VrefMin_Volt_f32	6		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	4294967295		
target_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	31	31	~
Batt_Volt_M_f32	31	31	~
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	54	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	32	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	1	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	31	31	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	31	31	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\color=0.00000000000000000000000000000000000$	31	31	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	5	5	~
SlowADCFltAcc_Cnt_M_u16	255	255	~
SysCVSwitch_Volt_M_f32	31	31	~
Temperature_Volt_M_f32	5	5	~
VrefFltAcc_Cnt_M_u16	65535	65535	~



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	~
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	-
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	•
DtrmnElapsedTime_uS_u32	1	DtrmnElapsedTime_uS_u32	1	•
NxtrDiagMgr10_ReportNTCStatus	1	NxtrDiagMgr10_ReportNTCStatus	1	<b>✓</b>
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	•

Test Step 3.3 (Repeat Count = 1)			<b>9</b>
Name	Input Value		Ť
BattSwitched_Volt_M_f32	28.0038395		
	4.64012289		
Batt_Volt_M_f32	0		
DMAData_G_str.SlowADC_Cnt_u16[0]	0		
DMAData_G_str.SlowADC_Cnt_u16[1]	0		
DMAData_G_str.SlowADC_Cnt_u16[2]	0		
DMAData_G_str.SlowADC_Cnt_u16[3]			
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed		
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTin	ne	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value			
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\			
SlowADCFltAcc_Cnt_M_u16	0		
SysCVSwitch_Volt_M_f32	30.3865681		
Temperature_Volt_M_f32	2.34604096		
VrefFltAcc_Cnt_M_u16	524		
k_ReadAdcWaitTimeout_uS_u32	400		
k_SlowADCValidDiag_Cnt_str.Threshold	119		
k_SlowADCValidDiag_Cnt_str.PStep	84		
k_SlowADCValidDiag_Cnt_str.NStep	166		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	25		
k_VrefDiag_Cnt_str.PStep	157		
k_VrefDiag_Cnt_str.NStep	54		
k_VrefMax_Volt_f32	0.398358107		
k_VrefMin_Volt_f32	2.09247947		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	2157471337		
target_GetSystemTime_uS_u32_CurrentTime	3826103446		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	0	0	~
Batt Volt M f32	0	0	<b>✓</b>
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	-
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>~</b>
NxtrDiagMgr10 ReportNTCStatus(Status Cnt T enum)	1	1	-
Rte Task 2ms 10.Rte TB.Rte I loHwAbstractionUsr Batt Volt f32.value	0	0	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	•	0	
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\		4.73152447	~
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte Temp		0	-
SlowADCFItAcc_Cnt_M_u16	0	0	-
SysCVSwitch_Volt_M_f32	4.73152447	4.73152447	
Temperature Volt M f32	0	0	~
VrefFltAcc Cnt M u16	25	25	
NTC	0x46	0x46	-
Param	0x46 0x02	0x40 0x02	
Status	0x02	0x02	
otatus	UAUT	UAUT	_



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	~
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	~
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	~

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
BattSwitched_Volt_M_f32	3.13798022		
Batt_Volt_M_f32	1.27581835		
DMAData_G_str.SlowADC_Cnt_u16[0]	379		
DMAData_G_str.SlowADC_Cnt_u16[1]	380		
DMAData_G_str.SlowADC_Cnt_u16[2]	381		
DMAData_G_str.SlowADC_Cnt_u16[3]	382		
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_Elapsed	Time	
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTim	e	
Rte_Task_2ms_10.Rte_TB.Rte_I_CDDInterface_SysCVSwitchADC_Cnt_u16.value	603		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\	603		
SlowADCFltAcc_Cnt_M_u16	18022		
SysCVSwitch_Volt_M_f32	0		
Temperature_Volt_M_f32	1.2864784		
VrefFitAcc_Cnt_M_u16	24588		
k_ReadAdcWaitTimeout_uS_u32	52013		
k_SlowADCValidDiag_Cnt_str.Threshold	30		
k_SlowADCValidDiag_Cnt_str.PStep	0		
k_SlowADCValidDiag_Cnt_str.NStep	100		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	55		
k_VrefDiag_Cnt_str.PStep	187		
k_VrefDiag_Cnt_str.NStep	160		
k_VrefMax_Volt_f32	2.37490797		
k_VrefMin_Volt_f32	0.416241318		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3081270104		
target_GetSystemTime_uS_u32_CurrentTime	2133832437		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.57168901	1.57168901	~
Batt_Volt_M_f32	3.13053489	3.13053489	•
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.13053489	3.13053489	~
Rte Task 2ms 10.Rte TB.Rte I loHwAbstractionUsr BattSwitched Volt f32.value	1.57168901	1.57168901	~
$Rte\_Task\_2ms\_10.Rte\_RB.Rte\_IoHwAbstractionUsr\_IoHwAb\_ReadAdc.Rte\_SysC\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	2.48096466	2.48096442	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.462759465	0.462759465	~
SlowADCFItAcc_Cnt_M_u16	17622	17622	•
SysCVSwitch_Volt_M_f32	2.48096466	2.48096442	~
Temperature_Volt_M_f32	0.462759465	0.462759465	•
VrefFltAcc_Cnt_M_u16	24428	24428	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC Dma InvalidateSlowADCGroup	1	TWrapC Dma InvalidateSlowADCGroup	1	<b>/</b>

Test Step 3.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	30.6357689
Batt_Volt_M_f32	29.3858128
DMAData_G_str.SlowADC_Cnt_u16[0]	449

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Name	Input Value		
DMAData G str.SlowADC Cnt u16[1]	450		
DMAData G str.SlowADC Cnt u16[2]	451		
DMAData_G_str.SlowADC_Cnt_u16[3]	452		
DtrmnElapsedTime uS u32(ElapsedTime)	target DtrmnElapsedTime uS u32 Elapse	edTime	
GetSystemTime uS u32(CurrentTime)	target GetSystemTime uS u32 CurrentTi		
Rte Task 2ms 10.Rte TB.Rte I CDDInterface SysCVSwitchADC Cnt u16.value	58746		
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\			
SlowADCFItAcc Cnt M u16	39345		
SysCVSwitch Volt M f32	16.5102043		
Temperature Volt M f32	2.64551282		
VrefFltAcc Cnt M u16	24593		
k ReadAdcWaitTimeout uS u32	60120		
k SlowADCValidDiag Cnt str.Threshold	100		
k SlowADCValidDiag Cnt str.PStep	121		
k SlowADCValidDiag Cnt str.NStep	73		
k TempSensorOffset Volt f32	0		
k TempSensorScale Uls f32	1		
k VbattScale Uls f32	6.74712658		
k VrefDiag Cnt str.Threshold	60		
k VrefDiag Cnt str.PStep	192		
k VrefDiag Cnt str.NStep	165		
k VrefMax Volt f32	0.158807486		
k VrefMin Volt f32	1.85650408		
k VrefScale Uls f32	1		
k VswitchScale Uls f32	3.36966825		
target DtrmnElapsedTime uS u32 ElapsedTime	630440147		
target_GetSystemTime_uS_u32_CurrentTime	17904585		
Name	Actual Value	Expected Value	Result
BattSwitched Volt M f32	1.85969484	1.85969484	~
Batt Volt M f32	3.70721269	3.70721221	•
NxtrDiagMgr10 ReportNTCStatus(NTC Cnt T enum)	54	*none*	-
NxtrDiagMgr10 ReportNTCStatus(Param Cnt T u08)	32	*none*	•
NxtrDiagMgr10 ReportNTCStatus(Status Cnt T enum)	1	1	-
Rte Task 2ms 10.Rte TB.Rte I loHwAbstractionUsr Batt Volt f32.value	3.70721269	3.70721221	•
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value		1.85969484	-
Rte Task 2ms 10.Rte RB.Rte IoHwAbstractionUsr IoHwAb ReadAdc.Rte SysC\		5.76834536	<b>✓</b>
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temper		0.548229575	~
SlowADCFItAcc Cnt M u16	39053	39053	<b>✓</b>
SysCVSwitch_Volt_M_f32	5.76834536	5.76834536	~
Temperature_Volt_M_f32	0.548229575	0.548229575	<b>~</b>
VrefFltAcc_Cnt_M_u16	60	60	~
NTC	0x46	0x46	<b>~</b>
Param	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	-
TWrapC_Dma_InvalidateSlowADCGroup	1	TWrapC_Dma_InvalidateSlowADCGroup	1	<b>✓</b>
NxtrDiagMgr10_SetNTCStatus	1	NxtrDiagMgr10_SetNTCStatus	1	<b>✓</b>

Test Step 3.6 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
BattSwitched_Volt_M_f32	12.1762867
Batt_Volt_M_f32	12.556304
DMAData_G_str.SlowADC_Cnt_u16[0]	365
DMAData_G_str.SlowADC_Cnt_u16[1]	366
DMAData_G_str.SlowADC_Cnt_u16[2]	367
DMAData_G_str.SlowADC_Cnt_u16[3]	368
DtrmnElapsedTime_uS_u32(ElapsedTime)	target_DtrmnElapsedTime_uS_u32_ElapsedTime
GetSystemTime_uS_u32(CurrentTime)	target_GetSystemTime_uS_u32_CurrentTime
$Rte\_Task\_2ms\_10.Rte\_TB.Rte\_I\_CDDInterface\_SysCVSwitchADC\_Cnt\_u16.value$	264
Rte_Task_2ms_10.Rte_RB.Rte_loHwAbstractionUsr_loHwAb_ReadAdc.Rte_SysC\	264
SlowADCFltAcc_Cnt_M_u16	18022
SysCVSwitch_Volt_M_f32	19.2443027
Temperature_Volt_M_f32	4.47366524
VrefFltAcc_Cnt_M_u16	0
k_ReadAdcWaitTimeout_uS_u32	52013

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Name	Input Value		
k_SlowADCValidDiag_Cnt_str.Threshold	30		
k_SlowADCValidDiag_Cnt_str.PStep	0		
k_SlowADCValidDiag_Cnt_str.NStep	100		
k_TempSensorOffset_Volt_f32	0		
k_TempSensorScale_Uls_f32	1		
k_VbattScale_Uls_f32	6.74712658		
k_VrefDiag_Cnt_str.Threshold	82		
k_VrefDiag_Cnt_str.PStep	45		
k_VrefDiag_Cnt_str.NStep	187		
k_VrefMax_Volt_f32	3.23682141		
k_VrefMin_Volt_f32	3.13711953		
k_VrefScale_Uls_f32	1		
k_VswitchScale_Uls_f32	3.36966825		
target_DtrmnElapsedTime_uS_u32_ElapsedTime	3081270104		
target_GetSystemTime_uS_u32_CurrentTime	2133832437		
Name	Actual Value	Expected Value	Result
BattSwitched_Volt_M_f32	1.5140878	1.5140878	~
Batt_Volt_M_f32	3.01519942	3.01519942	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(NTC_Cnt_T_enum)	54	*none*	~
NxtrDiagMgr10_ReportNTCStatus(Param_Cnt_T_u08)	32	*none*	<b>✓</b>
NxtrDiagMgr10_ReportNTCStatus(Status_Cnt_T_enum)	1	*none*	~
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_Batt_Volt_f32.value	3.01519942	3.01519942	<b>✓</b>
Rte_Task_2ms_10.Rte_TB.Rte_I_loHwAbstractionUsr_BattSwitched_Volt_f32.value	1.5140878	1.5140878	~
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_SysC\	1.08619344	1.08619344	•
Rte_Task_2ms_10.Rte_RB.Rte_IoHwAbstractionUsr_IoHwAb_ReadAdc.Rte_Temperature	0.445665449	0.445665449	~
SlowADCFltAcc_Cnt_M_u16	17622	17622	~
SysCVSwitch_Volt_M_f32	1.08619344	1.08619344	~
Temperature_Volt_M_f32	0.445665449	0.445665449	•
VrefFltAcc_Cnt_M_u16	45	45	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IoHwAb_SlowADCGroupValidity	1	IoHwAb_SlowADCGroupValidity	1	~
GetSystemTime_uS_u32	1	GetSystemTime_uS_u32	1	<b>✓</b>
DtrmnElapsedTime_uS_u32	4	DtrmnElapsedTime_uS_u32	4	~
TWrapC Dma InvalidateSlowADCGroup	1	TWrapC Dma InvalidateSlowADCGroup	1	<b>✓</b>