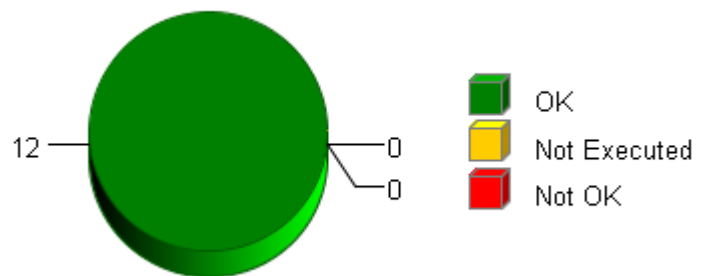


Summary

Total Test Objects: 12
Successful: 12
Failed: 0
Not Executed: 0
Date: 2014-10-14
Time: 18:25:00+0530

Overall Test Object Results (including Coverage)



Selected Project Items

Test Object "CBD_UnitTest/DigColPs/ComputeRoughTurns"
Test Object "CBD_UnitTest/DigColPs/ConstrainOneRev"
Test Object "CBD_UnitTest/DigColPs/DiagnosticThreshold"
Test Object "CBD_UnitTest/DigColPs/DigColPs_Init1"
Test Object "CBD_UnitTest/DigColPs/DigColPs_Per1"
Test Object "CBD_UnitTest/DigColPs/DigColPs_Per2"
Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_CustClrTrim"
Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_CustSetTrim"
Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_NxtClrTrim"
Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_NxtSetTrim"
Test Object "CBD_UnitTest/DigColPs/OddParityFault"
Test Object "CBD_UnitTest/DigColPs/VernierLookup"

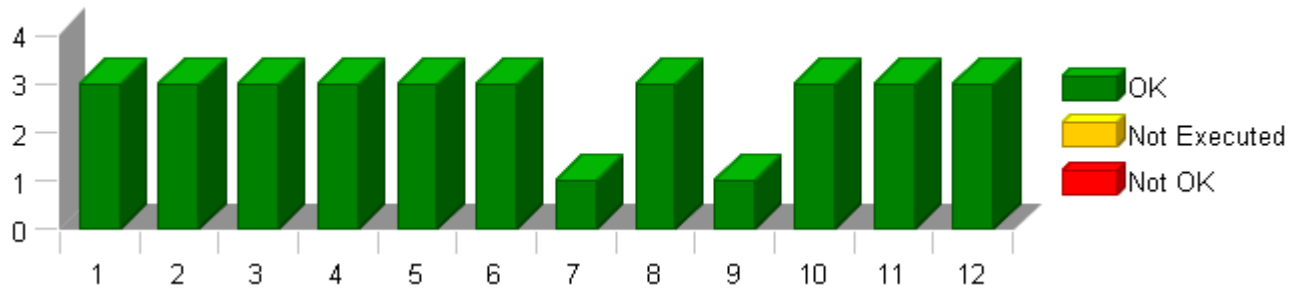
Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

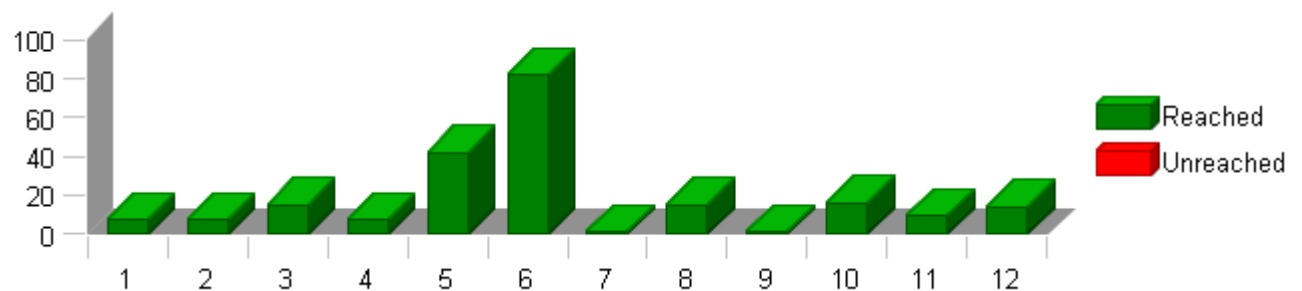
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No
Instrumentation: Test Object Only
Coverage: Statement Coverage, Branch Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)



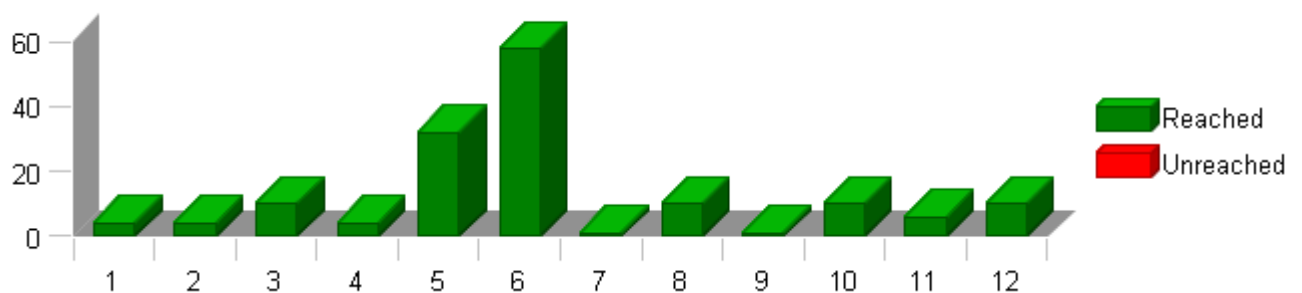
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



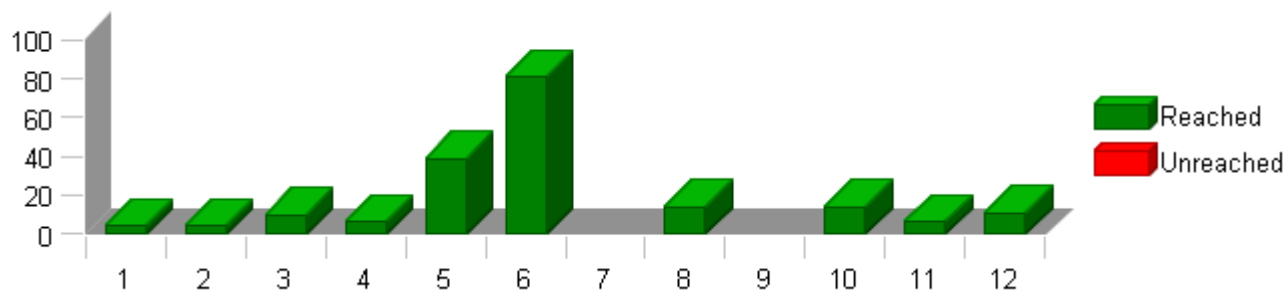
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

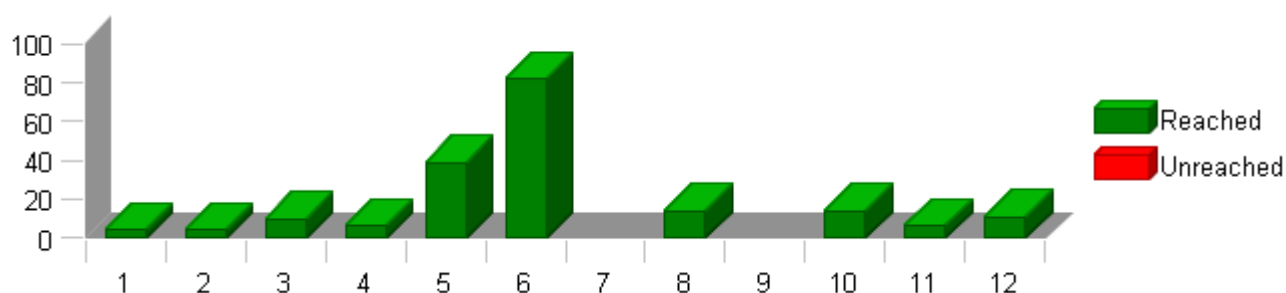
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Project DigColPs



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	MC/DC	MCC	Test Cases	Result
	DigColPs	100 %	100 %	100 %	100 %	32 of 32 passed	✓
	CBD_UnitTest	100 %	100 %	100 %	100 %	32 of 32 passed	✓
	DigColPs	100 %	100 %	100 %	100 %	32 of 32 passed	✓
1	ComputeRoughTurns	100 %	100 %	100 %	100 %	3 of 3 passed	✓
2	ConstrainOneRev	100 %	100 %	100 %	100 %	3 of 3 passed	✓
3	DiagnosticThreshold	100 %	100 %	100 %	100 %	3 of 3 passed	✓
4	DigColPs_Init1	100 %	100 %	100 %	100 %	3 of 3 passed	✓
5	DigColPs_Per1	100 %	100 %	100 %	100 %	3 of 3 passed	✓
6	DigColPs_Per2	100 %	100 %	100 %	100 %	3 of 3 passed	✓
7	DigColPs_SCom_CustClrTrim	100 %	100 %	-	-	1 of 1 passed	✓
8	DigColPs_SCom_CustSetTrim	100 %	100 %	100 %	100 %	3 of 3 passed	✓
9	DigColPs_SCom_NxtClrTrim	100 %	100 %	-	-	1 of 1 passed	✓
10	DigColPs_SCom_NxtSetTrim	100 %	100 %	100 %	100 %	3 of 3 passed	✓
11	OddParityFault	100 %	100 %	100 %	100 %	3 of 3 passed	✓
12	VernierLookup	100 %	100 %	100 %	100 %	3 of 3 passed	✓

TEST DETAILS REPORT

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ConstrainOneRev



Project	DigColPs
Module	DigColPs
Test Object	ConstrainOneRev

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)
	CPU Cycles: TS1.1 41.00 Cycles Longest Execution Path TS1.2 9.00 Cycles Shortest Execution Path
Description	VECTOR DESCRIPTION: TS1.1 "Longest Execution Path => (Input_Deg_T_f32 < D_ZERO_ULS_F32)=>TRUE" TS1.2 "Shortest Execution Path => (Input_Deg_T_f32 > D_ONEREV_DEGREESPREV_F32)=>FALSE (Input_Deg_T_f32 < D_ZERO_ULS_F32)=>FALSE"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Input_Deg_T_f32	-1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
Input_Deg_T_f32	0		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	✓

Test Case 2: Boundary Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)
	CPU Cycles: TS2.1 41.00 Cycles TS2.2 42.00 Cycles TS2.3 9.00 Cycles TS2.4 16.00 Cycles TS2.5 21.00 Cycles
Description	VECTOR DESCRIPTION: TS2.1 Input_Deg_T_f32=Min TS2.2 Input_Deg_T_f32=Max TS2.3 Input_Deg_T_f32=Zero TS2.4 Input_Deg_T_f32=Pos TS2.5 Input_Deg_T_f32=Neg

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
Input_Deg_T_f32	-1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
Input_Deg_T_f32	1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	360	360 ± 0.00048828125	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
Input_Deg_T_f32	0		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	✓

Test Step 2.4 (Repeat Count = 1)				✓
Name		Input Value		
Input_Deg_T_f32		800.5		
Name	Actual Value	Expected Value	Result	
ConstrainOneRev()	80.5	80.5 ± 0.00048828125	✓	

Test Step 2.5 (Repeat Count = 1)				✓
Name		Input Value		
Input_Deg_T_f32		-750.2		
Name	Actual Value	Expected Value	Result	
ConstrainOneRev()	329.799988	329.8 ± 0.00048828125	✓	

Test Case 3: Path Test		✓
Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment) CPU Cycles: TS3.1 3.00 Cycles TS3.2 11.00 Cycles	
Description	VECTOR DESCRIPTION: TS3.1 "(Input_Deg_T_f32 > D_ONEREV_DEGREESPREV_F32)=>TRUE (Input_Deg_T_f32 > D_ONEREV_DEGREESPREV_F32)=>FALSE (Input_Deg_T_f32 < D_ZERO_ULS_F32)=>FALSE" TS3.2 (Input_Deg_T_f32 < D_ZERO_ULS_F32)=>TRUE	

Test Step 3.1 (Repeat Count = 1)				✓
Name		Input Value		
Input_Deg_T_f32		500		
Name	Actual Value	Expected Value	Result	
ConstrainOneRev()	140	140 ± 0.00048828125	✓	

Test Step 3.2 (Repeat Count = 1)				✓
Name		Input Value		
Input_Deg_T_f32		-500		
Name	Actual Value	Expected Value	Result	
ConstrainOneRev()	220	220 ± 0.00048828125	✓	

TEST DETAILS REPORT

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DiagnosticThreshold



Project	DigColPs
Module	DigColPs
Test Object	DiagnosticThreshold

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS1.1 19.00 Cycles Longest Execution Path
TS1.2 8.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Longest Execution Path =>
if (FaultPresent_Cnt_T_lgc == TRUE)=>True
if (DiagFailed_m(*AccumulatorPtr_Cnt_T_u16, DiagSettings_Cnt_T_str) == TRUE)=>True"
TS1.2 "Shortest Execution Path =>
if (FaultPresent_Cnt_T_lgc == TRUE)=>False"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	✓
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0	✓

TEST DETAILS REPORT

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DiagnosticThreshold



Test Case 2: Boundary Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)
	CPU Cycles: TS2.1 7.00 Cycles TS2.2 19.00 Cycles TS2.3 8.00 Cycles TS2.4 20.00 Cycles TS2.5 8.00 Cycles TS2.6 19.00 Cycles TS2.7 8.00 Cycles TS2.8 19.00 Cycles TS2.9 8.00 Cycles TS2.10 20.00 Cycles TS2.11 8.00 Cycles TS2.12 19.00 Cycles TS2.13 8.00 Cycles TS2.14 20.00 Cycles TS2.15 8.00 Cycles TS2.16 19.00 Cycles
Description	VECTOR DESCRIPTION: TS2.1 All Min TS2.2 All Max TS2.3 FaultPresent_Cnt_T_lgc=Min TS2.4 FaultPresent_Cnt_T_lgc=Max TS2.5 AccumulatorPtr_Cnt_T_u16=Min TS2.6 AccumulatorPtr_Cnt_T_u16=Max TS2.7 AccumulatorPtr_Cnt_T_u16=Pos TS2.8 DiagSettings_Cnt_T_str.Threshold=Min TS2.9 DiagSettings_Cnt_T_str.Threshold=Max TS2.10 DiagSettings_Cnt_T_str.Threshold=Pos TS2.11 DiagSettings_Cnt_T_str.Pstep=Min TS2.12 DiagSettings_Cnt_T_str.Pstep=Max TS2.13 DiagSettings_Cnt_T_str.Pstep=Pos TS2.14 DiagSettings_Cnt_T_str.Nstep=Min TS2.15 DiagSettings_Cnt_T_str.Nstep=Max TS2.16 DiagSettings_Cnt_T_str.Nstep=Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	0		
DiagSettings_Cnt_T_str.PStep	0		
DiagSettings_Cnt_T_str.NStep	0		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	✓
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0	✓

TEST DETAILS REPORT

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DiagnosticThreshold



Test Step 2.4 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	200			
DiagSettings_Cnt_T_str.PStep	82			
DiagSettings_Cnt_T_str.NStep	1020			
FaultPresent_Cnt_T_lgc	1			
tgt_AccumulatorPtr_Cnt_T_u16	24			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	106	106		✓

Test Step 2.5 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	300			
DiagSettings_Cnt_T_str.PStep	116			
DiagSettings_Cnt_T_str.NStep	1188			
FaultPresent_Cnt_T_lgc	0			
tgt_AccumulatorPtr_Cnt_T_u16	0			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0		✓

Test Step 2.6 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	400			
DiagSettings_Cnt_T_str.PStep	150			
DiagSettings_Cnt_T_str.NStep	1356			
FaultPresent_Cnt_T_lgc	1			
tgt_AccumulatorPtr_Cnt_T_u16	65535			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	1	1		✓
tgt_AccumulatorPtr_Cnt_T_u16	400	400		✓

Test Step 2.7 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	500			
DiagSettings_Cnt_T_str.PStep	184			
DiagSettings_Cnt_T_str.NStep	1524			
FaultPresent_Cnt_T_lgc	0			
tgt_AccumulatorPtr_Cnt_T_u16	54			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0		✓

Test Step 2.8 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	0			
DiagSettings_Cnt_T_str.PStep	218			
DiagSettings_Cnt_T_str.NStep	1692			
FaultPresent_Cnt_T_lgc	1			
tgt_AccumulatorPtr_Cnt_T_u16	95			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	1	1		✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0		✓

TEST DETAILS REPORT

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DiagnosticThreshold



Test Step 2.9 (Repeat Count = 1)				✓
Name		Input Value		
AccumulatorPtr_Cnt_T_u16		tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold		65535		
DiagSettings_Cnt_T_str.PStep		252		
DiagSettings_Cnt_T_str.NStep		1860		
FaultPresent_Cnt_T_lgc		0		
tgt_AccumulatorPtr_Cnt_T_u16		136		
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0		✓

Test Step 2.10 (Repeat Count = 1)				✓
Name		Input Value		
AccumulatorPtr_Cnt_T_u16		tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold		1023		
DiagSettings_Cnt_T_str.PStep		286		
DiagSettings_Cnt_T_str.NStep		2028		
FaultPresent_Cnt_T_lgc		1		
tgt_AccumulatorPtr_Cnt_T_u16		177		
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	463	463		✓

Test Step 2.11 (Repeat Count = 1)				✓
Name		Input Value		
AccumulatorPtr_Cnt_T_u16		tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold		1		
DiagSettings_Cnt_T_str.PStep		0		
DiagSettings_Cnt_T_str.NStep		2196		
FaultPresent_Cnt_T_lgc		0		
tgt_AccumulatorPtr_Cnt_T_u16		218		
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0		✓

Test Step 2.12 (Repeat Count = 1)				✓
Name		Input Value		
AccumulatorPtr_Cnt_T_u16		tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold		488		
DiagSettings_Cnt_T_str.PStep		65535		
DiagSettings_Cnt_T_str.NStep		2364		
FaultPresent_Cnt_T_lgc		1		
tgt_AccumulatorPtr_Cnt_T_u16		259		
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	1	1		✓
tgt_AccumulatorPtr_Cnt_T_u16	488	488		✓

Test Step 2.13 (Repeat Count = 1)				✓
Name		Input Value		
AccumulatorPtr_Cnt_T_u16		tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold		928		
DiagSettings_Cnt_T_str.PStep		1045		
DiagSettings_Cnt_T_str.NStep		2532		
FaultPresent_Cnt_T_lgc		0		
tgt_AccumulatorPtr_Cnt_T_u16		300		
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0		✓
tgt_AccumulatorPtr_Cnt_T_u16	0	0		✓

TEST DETAILS REPORT

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DiagnosticThreshold



Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1368		
DiagSettings_Cnt_T_str.PStep	645		
DiagSettings_Cnt_T_str.NStep	0		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	341		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	✔
tgt_AccumulatorPtr_Cnt_T_u16	986	986	✔

Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1808		
DiagSettings_Cnt_T_str.PStep	152		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	382		
Name	Actual Value	Expected Value	Result
DagnosticThreshold()	0	0	✔
tgt_AccumulatorPtr_Cnt_T_u16	0	0	✔

Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	2248		
DiagSettings_Cnt_T_str.PStep	8524		
DiagSettings_Cnt_T_str.NStep	2046		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	423		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	✓
tgt_AccumulatorPtr_Cnt_T_u16	2248	2248	✓

Test Case 3: Path Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment) CPU Cycles: TS3.1 19.00 Cycles TS3.2 8.00 Cycles TS3.3 20.00 Cycles TS3.4 9.00 Cycles
Description	VECTOR DESCRIPTION: TS3.1 "if (FaultPresent_Cnt_T_lgc == TRUE)=>True if (DiagFailed_m(*AccumulatorPtr_Cnt_T_u16, DiagSettings_Cnt_T_str) == TRUE)=>True" TS3.2 if (FaultPresent_Cnt_T_lgc == TRUE)=>False TS3.3 "if (FaultPresent_Cnt_T_lgc == TRUE)=>True if (DiagFailed_m(*AccumulatorPtr_Cnt_T_u16, DiagSettings_Cnt_T_str) == TRUE)=>False" TS3.4 ((*AccumulatorPtr_Cnt_T_u16)>(DiagSettings_Cnt_T_str.NStep))=>TRUE

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	✓
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	✓

TEST DETAILS REPORT

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DiagnosticThreshold



Test Step 3.2 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	100			
DiagSettings_Cnt_T_str.PStep	48			
DiagSettings_Cnt_T_str.NStep	852			
FaultPresent_Cnt_T_lgc	0			
tgt_AccumulatorPtr_Cnt_T_u16	1			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0	✓	
tgt_AccumulatorPtr_Cnt_T_u16	0	0	✓	

Test Step 3.3 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	200			
DiagSettings_Cnt_T_str.PStep	82			
DiagSettings_Cnt_T_str.NStep	1020			
FaultPresent_Cnt_T_lgc	1			
tgt_AccumulatorPtr_Cnt_T_u16	24			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0	✓	
tgt_AccumulatorPtr_Cnt_T_u16	106	106	✓	

Test Step 3.4 (Repeat Count = 1)				✓
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	100			
DiagSettings_Cnt_T_str.PStep	48			
DiagSettings_Cnt_T_str.NStep	852			
FaultPresent_Cnt_T_lgc	0			
tgt_AccumulatorPtr_Cnt_T_u16	1500			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0	✓	
tgt_AccumulatorPtr_Cnt_T_u16	648	648	✓	

TEST DETAILS REPORT

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DigColPs_SCom_NxtClrTrim



Project	DigColPs
Module	DigColPs
Test Object	DigColPs_SCom_NxtClrTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>"</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd

TEST DETAILS REPORT

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DigColPs_SCom_NxtClrTrim



Attributes	
Name	Value
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:
TS1.1 18.00 Cycles

Description Vector Description:

TS1.1 Clear All The Trim Variables

Test Step 1.1 (Repeat Count = 1)

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓

TEST DETAILS REPORT

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OddParityFault



Project	DigColPs
Module	DigColPs
Test Object	OddParityFault

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)
	CPU Cycles: TS1.1 168.00 Cycles Longest Execution Path TS1.2 10.00 Cycles Shortest Execution Path
Description	VECTOR DESCRIPTION: TS1.1 "Longest Execution Path => while (Input_Cnt_T_u16 > 0U)=>TRUE if ((Input_Cnt_T_u16 & 1U) != 0U)=>TRUE if ((Parity_Cnt_T_u08 & 1U) == 0U)=>TRUE" TS1.2 "Shortest Execution Path => while (Input_Cnt_T_u16 > 0U)=>FALSE if ((Parity_Cnt_T_u08 & 1U) == 0U)=>TRUE"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Case 2: Boundary Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)
	CPU Cycles: TS2.1 10.00 Cycles TS2.2 168.00 Cycles TS2.3 58.00 Cycles
Description	VECTOR DESCRIPTION: TS2.1 Input_Cnt_T_u16=Min TS2.2 Input_Cnt_T_u16=Max TS2.3 Input_Cnt_T_u16=Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	44		
Name	Actual Value	Expected Value	Result
OddParityFault()	0	0	✓

Test Case 3: Path Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS3.1 10.00 Cycles
TS3.2 168.00 Cycles
TS3.3 33.00 Cycles
TS3.4 1.00 Cycles

Description

VECTOR DESCRIPTION:

TS3.1 "while (Input_Cnt_T_u16 > 0U)=>FALSE
if ((Parity_Cnt_T_u08 & 1U) == 0U)=>TRUE"
TS3.2 "while (Input_Cnt_T_u16 > 0U)=>TRUE
if ((Input_Cnt_T_u16 & 1U) != 0U)=>TRUE"
TS3.3 if ((Input_Cnt_T_u16 & 1U) != 0U)=>FALSE
TS3.4 if ((Parity_Cnt_T_u08 & 1U) == 0U)=>FALSE

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	10		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value		
Input_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
OddParityFault()	0	0	✓

TEST DETAILS REPORT

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DigColPs_SCom_CustClrTrim



Project	DigColPs
Module	DigColPs
Test Object	DigColPs_SCom_CustClrTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>"</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd

TEST DETAILS REPORT

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DigColPs_SCom_CustClrTrim



Attributes	
Name	Value
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

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DigColPs_SCom_CustClrTrim



Test Case 1: Boundary Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)
	CPU Cycles: TS1.1 515.00 Cycles
Description	Vector Description:
	TS1.1 Clear all the Trim variables

Test Step 1.1 (Repeat Count = 1)

Name		Input Value		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32		0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32		4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32		0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32		4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16		61047	61047	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

TEST DETAILS REPORT

2014-10-14, 18:16:06+0530

DigColPs_Per2



Project	DigColPs
Module	DigColPs
Test Object	DigColPs_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS1.1 8951.00 Cycles Longest Execution Path
TS1.2 12143.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

```
TS1.1 "Longest Execution Path =>
if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if ((I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08) &&
(I2CColSensorFault_Cnt_T_lgc == FALSE) &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE) &&
(TrimCompleteEOL_Cnt_T_lgc == TRUE) )=>True
if (HwAVernCorrFault_Cnt_T_lgc == FALSE)=>True
if (k_SelectFromColumn_Cnt_lgc == TRUE)=>False
if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32) && (AngleDataAvailable_Cnt_T_lgc == TRUE))=>True
if ((AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
(AngleDataAvailable_Cnt_T_lgc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) ==
TRUE)=>False
if ((AbsColPosDiff_Deg_T_f32 > k_VernOORangeThresh_Deg_f32) &&
(AngleDataAvailable_Cnt_T_lgc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFitDetect_Cnt_T_lgc == TRUE) ||
(DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>False
if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U) && (DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 == 0U))=>False
if (I2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>False
if ((ColParityError_Cnt_T_lgc == TRUE) ||
(SpurParityError_Cnt_T_lgc == TRUE) ||
(I2CSensCommFlts_Cnt_T_u08 != 0U) )=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>False
else if ((I2CColSensorFault_Cnt_T_lgc == TRUE) || (I2CSpurSensorFault_Cnt_T_lgc == TRUE)) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) )=>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) &&
(I2CColSensorFault_Cnt_T_lgc == TRUE) )=>False
if (DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) &&
(I2CSpurSensorFault_Cnt_T_lgc == TRUE) )=>False
if (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U)=>True"
TS1.2 "Shortest Execution Path =>
if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if ((I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08) &&
(I2CColSensorFault_Cnt_T_lgc == FALSE) &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE) &&
(TrimCompleteEOL_Cnt_T_lgc == TRUE) )=>False
if (k_SelectFromColumn_Cnt_lgc == TRUE)=>True
if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32) && (AngleDataAvailable_Cnt_T_lgc == TRUE))=>False
if ((AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
(AngleDataAvailable_Cnt_T_lgc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) ==
TRUE)=>True
if ((AbsColPosDiff_Deg_T_f32 > k_VernOORangeThresh_Deg_f32) &&
(AngleDataAvailable_Cnt_T_lgc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFitDetect_Cnt_T_lgc == TRUE) ||
(DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>True
if (I2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>True
if ((ColParityError_Cnt_T_lgc == TRUE) ||
(SpurParityError_Cnt_T_lgc == TRUE) ||
(I2CSensCommFlts_Cnt_T_u08 != 0U) )=>False
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) &&
(I2CColSensorFault_Cnt_T_lgc == TRUE) )=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) &&
(I2CSpurSensorFault_Cnt_T_lgc == TRUE) )=>True
"
```

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	255
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	33.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	55		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	3.54		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371.030273	371.0302938 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4	4	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	4	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-528.969727	-528.9697062 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6E	0x6E	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x6F	0x6F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

TEST DETAILS REPORT

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	414
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	360
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

TEST DETAILS REPORT

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x04	0x04	✓
Status	0x01	0x01	✓
NTC	0x6E	0x6E	✓
Param	0x7F	0x7F	✓
Status	0x01	0x01	✓
NTC	0x6F	0x6F	✓
Param	0x7F	0x7F	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Case 2: Boundary Test

TEST DETAILS REPORT

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DigColPs_Per2

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS2.1	12132.00 Cycles
TS2.2	5985.00 Cycles
TS2.3	5849.00 Cycles
TS2.4	5935.00 Cycles
TS2.5	5906.00 Cycles
TS2.6	5921.00 Cycles
TS2.7	2806.00 Cycles
TS2.8	2658.00 Cycles
TS2.9	5843.00 Cycles
TS2.10	5773.00 Cycles
TS2.11	8997.00 Cycles
TS2.12	2696.00 Cycles
TS2.13	8863.00 Cycles
TS2.14	8857.00 Cycles
TS2.15	8868.00 Cycles
TS2.16	8856.00 Cycles
TS2.17	8849.00 Cycles
TS2.18	8854.00 Cycles
TS2.19	5822.00 Cycles
TS2.20	12189.00 Cycles
TS2.21	12185.00 Cycles
TS2.22	5712.00 Cycles
TS2.23	3039.00 Cycles
TS2.24	3080.00 Cycles
TS2.25	2696.00 Cycles
TS2.26	8863.00 Cycles
TS2.27	8857.00 Cycles
TS2.28	8868.00 Cycles
TS2.29	8856.00 Cycles
TS2.30	8849.00 Cycles
TS2.31	8854.00 Cycles
TS2.32	5822.00 Cycles
TS2.33	12189.00 Cycles
TS2.34	12185.00 Cycles
TS2.35	5712.00 Cycles
TS2.36	3039.00 Cycles
TS2.37	3080.00 Cycles
TS2.38	2696.00 Cycles
TS2.39	8863.00 Cycles
TS2.40	8857.00 Cycles
TS2.41	8868.00 Cycles
TS2.42	8856.00 Cycles
TS2.43	8849.00 Cycles
TS2.44	8854.00 Cycles
TS2.45	5822.00 Cycles
TS2.46	12189.00 Cycles
TS2.47	12185.00 Cycles
TS2.48	5712.00 Cycles
TS2.49	3039.00 Cycles
TS2.50	3080.00 Cycles
TS2.51	8951.00 Cycles
TS2.52	12143.00 Cycles
TS2.53	12132.00 Cycles
TS2.54	5985.00 Cycles
TS2.55	5849.00 Cycles
TS2.56	5935.00 Cycles
TS2.57	5906.00 Cycles
TS2.58	5921.00 Cycles
TS2.59	2806.00 Cycles
TS2.60	2658.00 Cycles
TS2.61	5843.00 Cycles
TS2.62	5773.00 Cycles
TS2.63	8997.00 Cycles
TS2.64	2696.00 Cycles
TS2.65	8863.00 Cycles
TS2.66	8857.00 Cycles
TS2.67	8868.00 Cycles
TS2.68	8856.00 Cycles
TS2.69	8849.00 Cycles
TS2.70	8854.00 Cycles
TS2.71	5822.00 Cycles
TS2.72	12189.00 Cycles
TS2.73	12185.00 Cycles
TS2.74	5712.00 Cycles
TS2.75	3039.00 Cycles
TS2.76	3080.00 Cycles
TS2.77	2696.00 Cycles
TS2.78	8863.00 Cycles
TS2.79	8857.00 Cycles
TS2.80	8868.00 Cycles
TS2.81	8856.00 Cycles
TS2.82	8849.00 Cycles
TS2.83	8854.00 Cycles
TS2.84	5822.00 Cycles
TS2.85	12189.00 Cycles
TS2.86	12185.00 Cycles
TS2.87	5712.00 Cycles
TS2.88	3039.00 Cycles
TS2.89	3080.00 Cycles
TS2.90	2696.00 Cycles
TS2.91	8863.00 Cycles
TS2.92	8857.00 Cycles
TS2.93	8868.00 Cycles
TS2.94	8856.00 Cycles
TS2.95	8849.00 Cycles
TS2.96	8854.00 Cycles
TS2.97	5822.00 Cycles
TS2.98	12189.00 Cycles
TS2.99	12185.00 Cycles
TS2.100	5712.00 Cycles
TS2.101	3039.00 Cycles
TS2.102	3080.00 Cycles
TS2.103	5712.00 Cycles
TS2.104	3039.00 Cycles
TS2.105	3080.00 Cycles
TS2.106	3039.00 Cycles
TS2.107	3080.00 Cycles
TS2.108	5712.00 Cycles

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DigColPs_Per2

TS2.109	3039.00 Cycles
TS2.110	3080.00 Cycles

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DigColPs_Per2

Description	VECTOR DESCRIPTION:
	TS2.1All Min
	TS2.2All Max
	TS2.3DigColPs_I2CColSensorFault_Cnt_M_Igc=Min
	TS2.4DigColPs_I2CColSensorFault_Cnt_M_Igc=Max
	TS2.5DigColPs_I2CSpurSensorFault_Cnt_M_Igc=Min
	TS2.6DigColPs_I2CSpurSensorFault_Cnt_M_Igc=Max
	TS2.7DigColPs_ColParityError_Cnt_M_Igc=Min
	TS2.8DigColPs_ColParityError_Cnt_M_Igc=Max
	TS2.9DigColPs_SpurParityError_Cnt_M_Igc=Min
	TS2.10DigColPs_SpurParityError_Cnt_M_Igc=Max
	TS2.11DigColPs_I2CHwColAngle_Cnt_M_u16=Min
	TS2.12DigColPs_I2CHwColAngle_Cnt_M_u16=Max
	TS2.13DigColPs_I2CHwColAngle_Cnt_M_u16=Pos
	TS2.14DigColPs_I2CHwSpurAngle_Cnt_M_u16=Min
	TS2.15DigColPs_I2CHwSpurAngle_Cnt_M_u16=Max
	TS2.16DigColPs_I2CHwSpurAngle_Cnt_M_u16=Pos
	TS2.17DigColPs_I2CHwDataType_Cnt_M_u08=Min
	TS2.18DigColPs_I2CHwDataType_Cnt_M_u08=Max
	TS2.19DigColPs_I2CHwDataType_Cnt_M_u08=Pos
	TS2.20DigColPs_I2CSensCommFlts_Cnt_M_u08=Min
	TS2.21DigColPs_I2CSensCommFlts_Cnt_M_u08=Max
	TS2.22DigColPs_I2CSensCommFlts_Cnt_M_u08=Pos
	TS2.23DigColPs_I2CHwColAngleTrim_Deg_M_f32=Min
	TS2.24DigColPs_I2CHwColAngleTrim_Deg_M_f32=Max
	TS2.25DigColPs_I2CHwColAngleTrim_Deg_M_f32=Pos
	TS2.26DigColPs_I2CHwColAngleTrim_Deg_M_f32=Neg
	TS2.27DigColPs_I2CHwColAngleTrim_Deg_M_f32=Zero
	TS2.28DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Min
	TS2.29DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Max
	TS2.30DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Pos
	TS2.31DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Neg
	TS2.32DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Zero
	TS2.33Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Min
	TS2.34Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Max
	TS2.35Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Pos
	TS2.36k_SelectFromColumn_Cnt_Igc=Min
	TS2.37k_SelectFromColumn_Cnt_Igc=Max
	TS2.38k_VernCorrErrorThresh_Deg_f32=Min
	TS2.39k_VernCorrErrorThresh_Deg_f32=Max
	TS2.40k_VernCorrErrorThresh_Deg_f32=Pos
	TS2.41DigColPs_VernCorrDetectAcc_Cnt_M_u16=Min
	TS2.42DigColPs_VernCorrDetectAcc_Cnt_M_u16=Max
	TS2.43DigColPs_VernCorrDetectAcc_Cnt_M_u16=Pos
	TS2.44DigColPs_PrevVernierLevelNo_Cnt_M_u08=Min
	TS2.45DigColPs_PrevVernierLevelNo_Cnt_M_u08=Max
	TS2.46DigColPs_PrevVernierLevelNo_Cnt_M_u08=Pos
	TS2.47DigColPs_PrevAngleDataAvailable_Cnt_M_Igc=Min
	TS2.48DigColPs_PrevAngleDataAvailable_Cnt_M_Igc=Max
	TS2.49DigColPs_SkipStepFitDetectAcc_Cnt_M_u16=Min
	TS2.50DigColPs_SkipStepFitDetectAcc_Cnt_M_u16=Max
	TS2.51DigColPs_SkipStepFitDetectAcc_Cnt_M_u16=Pos
	TS2.52DigColPs_PrevColPos_Deg_M_f32=Min
	TS2.53DigColPs_PrevColPos_Deg_M_f32=Max
	TS2.54DigColPs_PrevColPos_Deg_M_f32=Pos
	TS2.55DigColPs_VernierAngleOORange_Cnt_M_Igc=Min
	TS2.56DigColPs_VernierAngleOORange_Cnt_M_Igc=Max
	TS2.57DigColPs_ColSensorFaultAcc_Cnt_M_u16=Min
	TS2.58DigColPs_ColSensorFaultAcc_Cnt_M_u16=Max
	TS2.59DigColPs_ColSensorFaultAcc_Cnt_M_u16=Pos
	TS2.60DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Min
	TS2.61DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Max
	TS2.62DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Pos
	TS2.63k_VernCorrErrorDiag_Cnt_str.Pstep=Min
	TS2.64k_VernCorrErrorDiag_Cnt_str.Pstep=Max
	TS2.65k_VernCorrErrorDiag_Cnt_str.Pstep=Pos
	TS2.66k_VernCorrErrorDiag_Cnt_str.Nstep=Min
	TS2.67k_VernCorrErrorDiag_Cnt_str.Nstep=Max
	TS2.68k_VernCorrErrorDiag_Cnt_str.Nstep=Pos
	TS2.69k_VernCorrErrorDiag_Cnt_str.Threshold=Min
	TS2.70k_VernCorrErrorDiag_Cnt_str.Threshold=Max
	TS2.71k_VernCorrErrorDiag_Cnt_str.Threshold=pos
	TS2.72k_SkipStepErrDiag_Cnt_str.Threshold=Min
	TS2.73k_SkipStepErrDiag_Cnt_str.Threshold=Max
	TS2.74k_SkipStepErrDiag_Cnt_str.Threshold=Pos
	TS2.75k_SkipStepErrDiag_Cnt_str.Pstep=Min
	TS2.76k_SkipStepErrDiag_Cnt_str.Pstep=Max
	TS2.77k_SkipStepErrDiag_Cnt_str.Pstep=Pos
	TS2.78k_SkipStepErrDiag_Cnt_str.Nstep=Min
	TS2.79k_SkipStepErrDiag_Cnt_str.Nstep=Max
	TS2.80k_SkipStepErrDiag_Cnt_str.Nstep=Pos
	TS2.81k_VernOORangeThresh_Deg_f32=Min
	TS2.82k_VernOORangeThresh_Deg_f32=Max
	TS2.83k_VernOORangeThresh_Deg_f32=Pos
	TS2.84MecState_Cnt_enum=>ProductionMode
	TS2.85MecState_Cnt_enum=>ManufacturingMode
	TS2.86MecState_Cnt_enum=>EngineeringMode
	TS2.87DigColPs_ColTrimStatic_Deg_M_f32=>Min
	TS2.88DigColPs_ColTrimStatic_Deg_M_f32=>Max
	TS2.89DigColPs_ColTrimStatic_Deg_M_f32=>Pos
	TS2.90DigColPs_ColTrimStatic_Deg_M_f32=>Neg
	TS2.91DigColPs_ColTrimStatic_Deg_M_f32=>Zero
	TS2.92DigColPs_SpurTrimStatic_Deg_M_f32=>Min
	TS2.93DigColPs_SpurTrimStatic_Deg_M_f32=>Max
	TS2.94DigColPs_SpurTrimStatic_Deg_M_f32=>Pos

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TS2.95DigColPs_SpurTrimStatic_Deg_M_f32=>Neg
 TS2.96DigColPs_SpurTrimStatic_Deg_M_f32=>Zero
 TS2.97DigColPs_TrimCompStatic_Cnt_M_u16=>Min
 TS2.98DigColPs_TrimCompStatic_Cnt_M_u16=>Max
 TS2.99DigColPs_TrimCompStatic_Cnt_M_u16=>Pos
 TS2.100DigColPs_I2CHwColAngle_Deg_M_f32=>Min
 TS2.101DigColPs_I2CHwColAngle_Deg_M_f32=>Max
 TS2.102DigColPs_I2CHwColAngle_Deg_M_f32=>Pos
 TS2.103DigColPs_I2CHwSpurAngle_Deg_M_f32=>Min
 TS2.104DigColPs_I2CHwSpurAngle_Deg_M_f32=>Max
 TS2.105DigColPs_I2CHwSpurAngle_Deg_M_f32=>Pos
 TS2.106DigColPs_HwAVernCorrFault_Cnt_M_lgc=>Min
 TS2.107DigColPs_HwAVernCorrFault_Cnt_M_lgc=>Max
 TS2.108DigColPs_I2CHwTrimTransCnts_Uls_M_u08=>Min
 TS2.109DigColPs_I2CHwTrimTransCnts_Uls_M_u08=>Max
 TS2.110DigColPs_I2CHwTrimTransCnts_Uls_M_u08=>Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataTypes_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x6E	0x6E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓	
ReleaseResource	1	ReleaseResource	1	✓	
ConstrainOneRev	2	ConstrainOneRev	2	✓	
VernierLookup	1	VernierLookup	1	✓	
DiagnosticThreshold	1	DiagnosticThreshold	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	✓	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓	

Test Step 2.2 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	511	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255	
DigColPs_ColTrimStatic_Deg_M_f32	360	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	
DigColPs_I2CHwColAngle_Deg_M_f32	360	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535	
DigColPs_I2CHwSpurAngle_Deg_M_f32	360	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	31	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1800	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	21	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255	
DigColPs_SpurTrimStatic_Deg_M_f32	360	
DigColPs_TrimCompStatic_Cnt_M_u16	4488	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131	
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65	
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98	
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130	
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163	
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196	
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229	

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	255
k_SkipStepErrDiag_Cnt_str.PStep	50

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DigColPs_Per2

Name	Input Value		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4

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DigColPs_Per2

Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.14		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x04	0x04	✔
Status	0x01	0x01	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406

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Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_Igc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernierAngleOORange_Cnt_M_Igc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_ColTrimStatic_Deg_M_f32	25
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.4548138
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	80
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFits_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	80
DigColPs_TrimCompStatic_Cnt_M_u16	196
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.11		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1443.65869	1443.658758 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	543.658691	543.6587581 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	9.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	232
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.19		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	✓

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	312.661438	312.6614647 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-587.338562	-587.3385353 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	45.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	14286
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921
DigColPs_I2CHwSpurAngle_Deg_M_f32	10.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	10.2
DigColPs_TrimCompStatic_Cnt_M_u16	268
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	125
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	38
k_VernCorrErrorDiag_Cnt_str.Threshold	64
k_VernCorrErrorDiag_Cnt_str.PStep	8
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32	78.40277648
k_VernOORangeThresh_Deg_f32	547.33
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	298.7894		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	973.389404	973.3894 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	106
DigColPs_ColTrimStatic_Deg_M_f32	55.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHWColAngle_Cnt_M_u16	29294
DigColPs_I2CHWColAngle_Deg_M_f32	199.9994296
DigColPs_I2CHWDataType_Cnt_M_u08	3
DigColPs_I2CHWSpurAngle_Cnt_M_u16	49318
DigColPs_I2CHWSpurAngle_Deg_M_f32	11.3
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1048.767936
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	11.3
DigColPs_TrimCompStatic_Cnt_M_u16	304
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	191		
k_SkipStepErrDiag_Cnt_str.PStep	16		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	24		
k_VernCorrErrorDiag_Cnt_str.PStep	21		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	67.6606307		
k_VernOORangeThresh_Deg_f32	664.42		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	199.9994296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	301.9312882		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2922		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	504.399445	504.3994296 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-395.600555	-395.6005704 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	65.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFits_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	12.4
DigColPs_TrimCompStatic_Cnt_M_u16	340
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10

TEST DETAILS REPORT

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.55		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	153.27533	153.2753346 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-746.72467	-746.7246654 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	76

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	13.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	13.5
DigColPs_TrimCompStatic_Cnt_M_u16	376
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.16		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1065.17773	1065.177819 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFiltDetectAcc_Cnt_M_u16	11	11	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	165.177734	165.177819 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	86.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	325.6206695
DigColPs_I2CHwDataTypes_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	14.6
DigColPs_TrimCompStatic_Cnt_M_u16	412
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	99		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	13		
k_VernCorrErrorDiag_Cnt_str.Threshold	74		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	78.75594592		
k_VernOORangeThresh_Deg_f32	1151.77		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1319.42065	1319.42067 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	419.420654	419.4206695 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	96.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129
DigColPs_I2CHwSpurAngle_Deg_M_f32	15.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	15.7
DigColPs_TrimCompStatic_Cnt_M_u16	448
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	70
k_SkipStepErrDiag_Cnt_str.PStep	47
k_SkipStepErrDiag_Cnt_str.NStep	44
k_VernCorrErrorDiag_Cnt_str.Threshold	88
k_VernCorrErrorDiag_Cnt_str.PStep	0
k_VernCorrErrorDiag_Cnt_str.NStep	38
k_VernCorrErrorThresh_Deg_f32	78.63725519
k_VernOORangeThresh_Deg_f32	1720.3
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	115.010748

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1458.61072	1458.610748 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	106.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25526
DigColPs_I2CHwColAngle_Deg_M_f32	216.7759984
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674
DigColPs_I2CHwSpurTransCnts_Deg_M_f32	16.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	16.8
DigColPs_TrimCompStatic_Cnt_M_u16	484
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	99		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.58		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	830.176025	830.1759984 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-69.8239746	-69.82400159 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	116.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	17.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241

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DigColPs_Per2

Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	17.9
DigColPs_TrimCompStatic_Cnt_M_u16	520
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.29		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	818.811279	818.8112897 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	196
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	127
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	276.8997883
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	19
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	625.0201091
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	5
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	19
DigColPs_TrimCompStatic_Cnt_M_u16	556
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	225		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	96		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	57.46032691		
k_VernOORangeThresh_Deg_f32	1341.97		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	276.8997883		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	232.8930412		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	720.957642	720.9577085 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-179.042358	-179.0422915 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	128
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	137.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	258.0886749
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	12329
DigColPs_I2CHwSpurAngle_Deg_M_f32	20.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	30
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	87.60431278
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	20.1
DigColPs_TrimCompStatic_Cnt_M_u16	592
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	254		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	47		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	17.45087004		
k_VernOORangeThresh_Deg_f32	517.23		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	258.0886749		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.1939993		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1851		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	480.888672	480.8886749 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7	7	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-419.111328	-419.1113251 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	163
DigColPs_ColTrimStatic_Deg_M_f32	147.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	5894
DigColPs_I2CHwColAngle_Deg_M_f32	248.0463682
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64194
DigColPs_I2CHwSpurAngle_Deg_M_f32	21.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1737.555742
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	21.2
DigColPs_TrimCompStatic_Cnt_M_u16	628
DigColPs_VernCorrDetectAcc_Cnt_M_u16	5
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	111
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	48
k_VernCorrErrorDiag_Cnt_str.Threshold	75
k_VernCorrErrorDiag_Cnt_str.PStep	34
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	32.15087152
k_VernOORangeThresh_Deg_f32	1098.48
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	248.0463682

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	112.600146		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	530		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	820.646362	820.6463682 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-79.3536377	-79.35363182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x04	0x04	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	205
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	157.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	21646
DigColPs_I2CHwColAngle_Deg_M_f32	274.5377293
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	22.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1064.526832
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205
DigColPs_SpurTrimStatic_Deg_M_f32	22.3
DigColPs_TrimCompStatic_Cnt_M_u16	664
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	200		
k_SkipStepErrDiag_Cnt_str.PStep	24		
k_SkipStepErrDiag_Cnt_str.NStep	31		
k_VernCorrErrorDiag_Cnt_str.Threshold	95		
k_VernCorrErrorDiag_Cnt_str.PStep	48		
k_VernCorrErrorDiag_Cnt_str.NStep	10		
k_VernCorrErrorThresh_Deg_f32	47.7859745		
k_VernOORangeThresh_Deg_f32	674.82		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.5377293		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	86.89214289		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	655		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	476.937714	476.9377293 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	210
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158
DigColPs_ColTrimStatic_Deg_M_f32	167.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	56914
DigColPs_I2CHwColAngle_Deg_M_f32	93.15782326
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	23.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	496.3249275
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	210

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DigColPs_Per2

Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	23.4
DigColPs_TrimCompStatic_Cnt_M_u16	700
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	13		
k_SkipStepErrDiag_Cnt_str.PStep	33		
k_SkipStepErrDiag_Cnt_str.NStep	14		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	9		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	90.42534328		
k_VernOORangeThresh_Deg_f32	855.99		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	93.15782326		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	317.1723412		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1412		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	645.357788	645.3578233 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-254.642212	-254.6421767 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	220
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	178
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0

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Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	16977
DigColPs_I2CHwColAngle_Deg_M_f32	198.4525095
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43743
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	845.2340471
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	15
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	220
DigColPs_SpurTrimStatic_Deg_M_f32	24.5
DigColPs_TrimCompStatic_Cnt_M_u16	736
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	199		
k_SkipStepErrDiag_Cnt_str.PStep	6		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	76		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	87.62320375		
k_VernOORangeThresh_Deg_f32	1774.591192		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	198.4525095		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	46.80067945		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4447		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1460.45251	1460.45251 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	560.452515	560.4525095 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	214
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	188.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	17009
DigColPs_I2CHwColAngle_Deg_M_f32	183.5
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	9502
DigColPs_I2CHwSpurAngle_Deg_M_f32	25.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	601.2839711
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	214
DigColPs_SpurTrimStatic_Deg_M_f32	25.6
DigColPs_TrimCompStatic_Cnt_M_u16	772
DigColPs_VernCorrDetectAcc_Cnt_M_u16	9
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	191		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	43		
k_VernCorrErrorDiag_Cnt_str.Threshold	47		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	8		
k_VernCorrErrorThresh_Deg_f32	39.43172193		
k_VernOORangeThresh_Deg_f32	1755.401681		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	183.5		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	205.6963653		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2712		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓

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Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1795.30005	1795.3 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	198.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	11710
DigColPs_I2CHwColAngle_Deg_M_f32	204.045151
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16894
DigColPs_I2CHwSpurAngle_Deg_M_f32	26.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1513.739851
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	10
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	26.7
DigColPs_TrimCompStatic_Cnt_M_u16	808
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	27
k_SkipStepErrDiag_Cnt_str.PStep	20
k_SkipStepErrDiag_Cnt_str.NStep	4
k_VernCorrErrorDiag_Cnt_str.Threshold	15
k_VernCorrErrorDiag_Cnt_str.PStep	49
k_VernCorrErrorDiag_Cnt_str.NStep	10
k_VernCorrErrorThresh_Deg_f32	97.54986858
k_VernOORangeThresh_Deg_f32	1151.320251
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	204.045151

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Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	219.1047057		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	730.725098	730.7251338 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	27	27	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-169.274902	-169.2748662 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0E	0x0E	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	208.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22738
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1851
DigColPs_I2CHwSpurAngle_Deg_M_f32	27.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1273.742756
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	27.8
DigColPs_TrimCompStatic_Cnt_M_u16	844
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	150		
k_SkipStepErrDiag_Cnt_str.PStep	8		
k_SkipStepErrDiag_Cnt_str.NStep	29		
k_VernCorrErrorDiag_Cnt_str.Threshold	35		
k_VernCorrErrorDiag_Cnt_str.PStep	37		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	51.31432509		
k_VernOORangeThresh_Deg_f32	1014.951933		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220.2809907		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	894.326782	894.3268224 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	900	900 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-5.67321777	-5.673217573 ± 0.000009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	30
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	28.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30

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DigColPs_Per2

Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	501.200012	501.2 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x04	0x04	✔
Status	0x01	0x01	✔

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓	
ReleaseResource	1	ReleaseResource	1	✓	
ConstrainOneRev	2	ConstrainOneRev	2	✓	
VernierLookup	1	VernierLookup	1	✓	
DiagnosticThreshold	1	DiagnosticThreshold	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓	

Test Step 2.25 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	50	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131	
DigColPs_ColTrimStatic_Deg_M_f32	229	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	

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DigColPs_Per2

Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	48650
DigColPs_I2CHwColAngle_Deg_M_f32	126.5327979
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51339
DigColPs_I2CHwSpurAngle_Deg_M_f32	30
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	373.8183561
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50
DigColPs_SpurTrimStatic_Deg_M_f32	30
DigColPs_TrimCompStatic_Cnt_M_u16	916
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	123		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	64		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	45.68451142		
k_VernOORangeThresh_Deg_f32	735.0528789		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	126.5327979		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	306.8582928		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	365.064392	365.0644124 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	3	3	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.26 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	30
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	28.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-74.24		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	501.200012	501.2 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x04	0x04	✓
Status	0x01	0x01	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	208.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22738
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1851
DigColPs_I2CHwSpurAngle_Deg_M_f32	27.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1273.742756
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	27.8
DigColPs_TrimCompStatic_Cnt_M_u16	844
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	150
k_SkipStepErrDiag_Cnt_str.PStep	8
k_SkipStepErrDiag_Cnt_str.NStep	29
k_VernCorrErrorDiag_Cnt_str.Threshold	35
k_VernCorrErrorDiag_Cnt_str.PStep	37

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	51.31432509		
k_VernOORangeThresh_Deg_f32	1014.951933		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220.2809907		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	730.69043	730.6904588 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-169.30957	-169.3095412 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.28 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.8777566
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	31.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	12		
k_SkipStepErrDiag_Cnt_str.NStep	41		
k_VernCorrErrorDiag_Cnt_str.Threshold	48		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	78.9135704		
k_VernOORangeThresh_Deg_f32	1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1077.77271	1077.772727 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.29 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	144
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	182
DigColPs_ColTrimStatic_Deg_M_f32	249.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	18257
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	21803
DigColPs_I2CHwSpurAngle_Deg_M_f32	32.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	845.517553
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	15
DigColPs_SpurParityError_Cnt_M_lgc	1

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DigColPs_Per2

Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
DigColPs_SpurTrimStatic_Deg_M_f32	32.2
DigColPs_TrimCompStatic_Cnt_M_u16	988
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	179		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	11		
k_VernCorrErrorDiag_Cnt_str.Threshold	8		
k_VernCorrErrorDiag_Cnt_str.PStep	1		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	31.8570087		
k_VernOORangeThresh_Deg_f32	390.7995283		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	14.6363688	14.63636364 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-885.363647	-885.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	33.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371.030273	371.0302938 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-528.969727	-528.9697062 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.8777566
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	31.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	12		
k_SkipStepErrDiag_Cnt_str.NStep	41		
k_VernCorrErrorDiag_Cnt_str.Threshold	48		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	78.9135704		
k_VernOORangeThresh_Deg_f32	1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-74.24		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	375.15451	375.1545455 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	144
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	182
DigColPs_ColTrimStatic_Deg_M_f32	249.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	18257
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	21803
DigColPs_I2CHwSpurAngle_Deg_M_f32	32.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	845.517553
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	15
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
DigColPs_SpurTrimStatic_Deg_M_f32	32.2
DigColPs_TrimCompStatic_Cnt_M_u16	988
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	179
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	11
k_VernCorrErrorDiag_Cnt_str.Threshold	8
k_VernCorrErrorDiag_Cnt_str.PStep	1
k_VernCorrErrorDiag_Cnt_str.NStep	2
k_VernCorrErrorThresh_Deg_f32	31.8570087
k_VernOORangeThresh_Deg_f32	390.7995283
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	14.636363	14.63636364 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-885.363647	-885.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	106
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141
DigColPs_ColTrimStatic_Deg_M_f32	269.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHWColAngle_Cnt_M_u16	26533
DigColPs_I2CHWColAngle_Deg_M_f32	60.248
DigColPs_I2CHWDataType_Cnt_M_u08	0
DigColPs_I2CHWSpurAngle_Cnt_M_u16	36379
DigColPs_I2CHWSpurAngle_Deg_M_f32	34.4
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	284.4795149
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106
DigColPs_SpurTrimStatic_Deg_M_f32	34.4
DigColPs_TrimCompStatic_Cnt_M_u16	1060
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	22		
k_SkipStepErrDiag_Cnt_str.PStep	8		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	4		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	88.97686696		
k_VernOORangeThresh_Deg_f32	706.5625857		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	96.79570621		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_Igc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	150.448013	150.448 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_Igc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-749.552002	-749.552 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	103
DigColPs_ColParityError_Cnt_M_Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	280
DigColPs_HwAVernCorrFault_Cnt_M_Igc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	53187
DigColPs_I2CHwColAngle_Deg_M_f32	11.56588054
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	52647
DigColPs_I2CHwSpurAngle_Deg_M_f32	35.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFits_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0
DigColPs_PrevColPos_Deg_M_f32	899.7103484
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	103
DigColPs_SpurTrimStatic_Deg_M_f32	35.5
DigColPs_TrimCompStatic_Cnt_M_u16	1096
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15

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DigColPs_Per2

Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	131		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	40		
k_VernCorrErrorDiag_Cnt_str.PStep	34		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	58.9241991		
k_VernOORangeThresh_Deg_f32	866.7677131		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	11.56588054		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	811.565918	811.5658805 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	11	11	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11	11	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.35 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	151
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	145
DigColPs_ColTrimStatic_Deg_M_f32	290.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	48613
DigColPs_I2CHwColAngle_Deg_M_f32	136.3651175

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36961
DigColPs_I2CHwSpurAngle_Deg_M_f32	36.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	28
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1505.659877
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151
DigColPs_SpurTrimStatic_Deg_M_f32	36.6
DigColPs_TrimCompStatic_Cnt_M_u16	1132
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_Igc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	184		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	26		
k_VernCorrErrorDiag_Cnt_str.Threshold	20		
k_VernCorrErrorDiag_Cnt_str.PStep	46		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	87.16203666		
k_VernOORangeThresh_Deg_f32	1105.319018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	136.3651175		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2271		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_Igc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1646.16504	1646.165117 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_Igc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 2.36 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	165
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	300.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	28682
DigColPs_I2CHwColAngle_Deg_M_f32	49.7053827
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	13341
DigColPs_I2CHwSpurAngle_Deg_M_f32	37.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFits_Cnt_M_u08	7
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1225.322705
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
DigColPs_SpurTrimStatic_Deg_M_f32	37.7
DigColPs_TrimCompStatic_Cnt_M_u16	1168
DigColPs_VernCorrDetectAcc_Cnt_M_u16	9
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	223		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	11		
k_VernCorrErrorDiag_Cnt_str.PStep	6		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	81.95902205		
k_VernOORangeThresh_Deg_f32	1527.852543		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	49.7053827		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	341		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1542		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	829.30542	829.3053827 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.37 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	175
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128
DigColPs_ColTrimStatic_Deg_M_f32	310.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	5110
DigColPs_I2CHwColAngle_Deg_M_f32	236.1581588
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	13604
DigColPs_I2CHwSpurAngle_Deg_M_f32	38.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1393.487479
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175
DigColPs_SpurTrimStatic_Deg_M_f32	38.8
DigColPs_TrimCompStatic_Cnt_M_u16	1204
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	59		
k_VernCorrErrorDiag_Cnt_str.PStep	46		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	77.78657174		
k_VernOORangeThresh_Deg_f32	566.6271515		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	236.1581588		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	156.2506101		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	645.558167	645.5581588 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-254.441833	-254.4418412 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	185
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	168
DigColPs_ColTrimStatic_Deg_M_f32	320.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26911
DigColPs_I2CHwColAngle_Deg_M_f32	55.3166151
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	23900
DigColPs_I2CHwSpurAngle_Deg_M_f32	39.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	93.47087908
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	10
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	185
DigColPs_SpurTrimStatic_Deg_M_f32	39.9
DigColPs_TrimCompStatic_Cnt_M_u16	1240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	96
k_SkipStepErrDiag_Cnt_str.PStep	42
k_SkipStepErrDiag_Cnt_str.NStep	34
k_VernCorrErrorDiag_Cnt_str.Threshold	98
k_VernCorrErrorDiag_Cnt_str.PStep	34
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32	1
k_VernOORangeThresh_Deg_f32	1574.365275
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	55.3166151		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	301.781571		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	814.516602	814.5166151 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	195
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_ColTrimStatic_Deg_M_f32	331
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHWColAngle_Cnt_M_u16	22241
DigColPs_I2CHWColAngle_Deg_M_f32	71.4783923
DigColPs_I2CHWDataType_Cnt_M_u08	0
DigColPs_I2CHWSpurAngle_Cnt_M_u16	37586
DigColPs_I2CHWSpurAngle_Deg_M_f32	141
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	414.2750131
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	195
DigColPs_SpurTrimStatic_Deg_M_f32	141
DigColPs_TrimCompStatic_Cnt_M_u16	1276
DigColPs_VernCorrDetectAcc_Cnt_M_u16	16
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	67		
k_SkipStepErrDiag_Cnt_str.PStep	39		
k_SkipStepErrDiag_Cnt_str.NStep	27		
k_VernCorrErrorDiag_Cnt_str.Threshold	5		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	245.4025523		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	71.4783923		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.98159581		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1481		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	820.478394	820.4783923 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_Igc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-79.5216064	-79.5216077 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123
DigColPs_ColTrimStatic_Deg_M_f32	1.5
DigColPs_HwAVernCorrFault_Cnt_M_Igc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	46566
DigColPs_I2CHwColAngle_Deg_M_f32	135.5191227
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22556
DigColPs_I2CHwSpurAngle_Deg_M_f32	42.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	1
DigColPs_PrevColPos_Deg_M_f32	1072.03711
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	14
DigColPs_SpurParityError_Cnt_M_Igc	1

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DigColPs_Per2

Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	42.1
DigColPs_TrimCompStatic_Cnt_M_u16	1312
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	71		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	38		
k_VernCorrErrorDiag_Cnt_str.Threshold	7		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	83.48664141		
k_VernOORangeThresh_Deg_f32	744.223277		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	135.5191227		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	263.9402983		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	302		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	494.019104	494.0191227 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_ColTrimStatic_Deg_M_f32	5.6

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	20466
DigColPs_I2CHwColAngle_Deg_M_f32	34.50624543
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	34618
DigColPs_I2CHwSpurAngle_Deg_M_f32	43.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1779.91482
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	18
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	43.2
DigColPs_TrimCompStatic_Cnt_M_u16	1348
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	228		
k_SkipStepErrDiag_Cnt_str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	40		
k_VernCorrErrorDiag_Cnt_str.PStep	32		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	23.81406522		
k_VernOORangeThresh_Deg_f32	1525.900935		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	34.50624543		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.5753602		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2878		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1468.90625	1468.906245 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	568.90625	568.906245 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	163
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158
DigColPs_ColTrimStatic_Deg_M_f32	9.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	52348
DigColPs_I2CHwColAngle_Deg_M_f32	222.1544354
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27884
DigColPs_I2CHwSpurAngle_Deg_M_f32	44.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1405.727187
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	163
DigColPs_SpurTrimStatic_Deg_M_f32	44.3
DigColPs_TrimCompStatic_Cnt_M_u16	1384
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	222		
k_SkipStepErrDiag_Cnt_str.PStep	8		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	68		
k_VernCorrErrorDiag_Cnt_str.PStep	30		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k_VernCorrErrorThresh_Deg_f32	28.48486996		
k_VernOORangeThresh_Deg_f32	1595.635967		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	222.1544354		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.57416618		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	148.05719	148.0571972 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	68	68	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	50	50	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	13.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	9945
DigColPs_I2CHwColAngle_Deg_M_f32	253.5686912
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	37553
DigColPs_I2CHwSpurAngle_Deg_M_f32	45.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	4
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	319.1410994
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	45.4
DigColPs_TrimCompStatic_Cnt_M_u16	1420
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	124
k_SkipStepErrDiag_Cnt_str.PStep	50
k_SkipStepErrDiag_Cnt_str.NStep	31
k_VernCorrErrorDiag_Cnt_str.Threshold	80
k_VernCorrErrorDiag_Cnt_str.PStep	3
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	1
k_VernOORangeThresh_Deg_f32	414.5643529
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	253.5686912		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.5532733		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2452		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1319.76868	1319.768691 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6	6	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6	6	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	409.09082	409.0909091 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.44 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	158
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	17.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHWColAngle_Cnt_M_u16	37674
DigColPs_I2CHWColAngle_Deg_M_f32	266.7729402
DigColPs_I2CHWDataType_Cnt_M_u08	0
DigColPs_I2CHWSpurAngle_Cnt_M_u16	64843
DigColPs_I2CHWSpurAngle_Deg_M_f32	46.5
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	12
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1117.242519
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158
DigColPs_SpurTrimStatic_Deg_M_f32	46.5
DigColPs_TrimCompStatic_Cnt_M_u16	1456
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	132		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	27		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	43		
k_VernCorrErrorThresh_Deg_f32	59.61001611		
k_VernOORangeThresh_Deg_f32	220.0944071		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	266.7729402		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.5881469		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1187		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	968.872925	968.8729402 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	125
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	22
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	16067
DigColPs_I2CHwColAngle_Deg_M_f32	272.6490288
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16937
DigColPs_I2CHwSpurAngle_Deg_M_f32	47.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1

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DigColPs_Per2

Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	1733.007516
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	47.6
DigColPs_TrimCompStatic_Cnt_M_u16	1492
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	80		
k_SkipStepErrDiag_Cnt_str.PStep	43		
k_SkipStepErrDiag_Cnt_str.NStep	7		
k_VernCorrErrorDiag_Cnt_str.Threshold	6		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	14		
k_VernCorrErrorThresh_Deg_f32	86.69760323		
k_VernOORangeThresh_Deg_f32	1173.76136		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	272.6490288		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.17228091		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	621		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	970.649048	970.6490288 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7	7	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	70.6490479	70.64902878 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	165
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	26.1

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56371
DigColPs_I2CHwColAngle_Deg_M_f32	296.9508778
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11434
DigColPs_I2CHwSpurAngle_Deg_M_f32	48.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	267.2598278
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
DigColPs_SpurTrimStatic_Deg_M_f32	48.7
DigColPs_TrimCompStatic_Cnt_M_u16	1528
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	57		
k_SkipStepErrDiag_Cnt_str.PStep	9		
k_SkipStepErrDiag_Cnt_str.NStep	18		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	11		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	9.823269606		
k_VernOORangeThresh_Deg_f32	664.8207433		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	296.9508778		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	182.5995052		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	189		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	990.85083	990.8508778 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	90.8508301	90.85087785 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.47 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	144
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	30.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	4911
DigColPs_I2CHwColAngle_Deg_M_f32	75.69248641
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	45521
DigColPs_I2CHwSpurAngle_Deg_M_f32	49.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	21
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1606.298487
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
DigColPs_SpurTrimStatic_Deg_M_f32	49.8
DigColPs_TrimCompStatic_Cnt_M_u16	1564
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	27		
k_SkipStepErrDiag_Cnt_str.PStep	36		
k_SkipStepErrDiag_Cnt_str.NStep	31		
k_VernCorrErrorDiag_Cnt_str.Threshold	8		
k_VernCorrErrorDiag_Cnt_str.PStep	37		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	21.03098726		
k_VernOORangeThresh_Deg_f32	132.1493682		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.69248641		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.71645284		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	13.6743383	13.67433962 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.48 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	34.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	28758
DigColPs_I2CHwColAngle_Deg_M_f32	169.2136934
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1113
DigColPs_I2CHwSpurAngle_Deg_M_f32	50.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	1
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1698.48323
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	50.9
DigColPs_TrimCompStatic_Cnt_M_u16	1600
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	90
k_SkipStepErrDiag_Cnt_str.PStep	34
k_SkipStepErrDiag_Cnt_str.NStep	39
k_VernCorrErrorDiag_Cnt_str.Threshold	10
k_VernCorrErrorDiag_Cnt_str.PStep	33

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	64.4036839		
k_VernOORangeThresh_Deg_f32	1423.580669		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	169.2136934		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	135.3572482		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3790		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	494.913696	494.9136934 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-405.086304	-405.0863066 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	143
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184
DigColPs_ColTrimStatic_Deg_M_f32	38.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	49805
DigColPs_I2CHwColAngle_Deg_M_f32	313.3494742
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22222
DigColPs_I2CHwSpurAngle_Deg_M_f32	52
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	687.96434
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	143
DigColPs_SpurTrimStatic_Deg_M_f32	52
DigColPs_TrimCompStatic_Cnt_M_u16	1636
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	218		
k_SkipStepErrDiag_Cnt_str.PStep	31		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	47.04804516		
k_VernOORangeThresh_Deg_f32	914.2227411		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	313.3494742		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	162.8974475		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2322		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	994.949463	994.9494742 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	94.9494629	94.94947419 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	131
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	42.5
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	16100
DigColPs_I2CHwColAngle_Deg_M_f32	299.0314264
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	45386
DigColPs_I2CHwSpurAngle_Deg_M_f32	53.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFits_Cnt_M_u08	28
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1756.602492
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	255
DigColPs_SpurParityError_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	131
DigColPs_SpurTrimStatic_Deg_M_f32	53.1
DigColPs_TrimCompStatic_Cnt_M_u16	1672
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	161		
k_SkipStepErrDiag_Cnt_str.PStep	1		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	79		
k_VernCorrErrorDiag_Cnt_str.PStep	19		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	29.57760787		
k_VernOORangeThresh_Deg_f32	320.9261016		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	299.0314264		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	107.5765935		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3593		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	976.531433	976.5314264 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	161	161	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0E	0x0E	✔
Status	0x01	0x01	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.51 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	46.6

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	24812
DigColPs_I2CHwColAngle_Deg_M_f32	148.1551865
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7234
DigColPs_I2CHwSpurAngle_Deg_M_f32	54.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	724.8810905
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	11
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	54.2
DigColPs_TrimCompStatic_Cnt_M_u16	1708
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	195		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	10		
k_VernCorrErrorDiag_Cnt_str.Threshold	58		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	84.35098028		
k_VernOORangeThresh_Deg_f32	1554.614787		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	148.1551865		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	349.5774245		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	356.646606	356.6466252 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.52 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	134
DigColPs_ColTrimStatic_Deg_M_f32	50.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26033
DigColPs_I2CHwColAngle_Deg_M_f32	166.9625559
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7191
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	3
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	55.3
DigColPs_TrimCompStatic_Cnt_M_u16	1744
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	8		
k_VernCorrErrorDiag_Cnt_str.PStep	41		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k_VernCorrErrorThresh_Deg_f32	31.69468141		
k_VernOORangeThresh_Deg_f32	1512.089929		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	166.9625559		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4.647624195		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2354		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	476.262573	476.2625559 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-423.737427	-423.7374441 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.53 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	184
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135
DigColPs_ColTrimStatic_Deg_M_f32	54.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	34509
DigColPs_I2CHwColAngle_Deg_M_f32	125.941998
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	19043
DigColPs_I2CHwSpurAngle_Deg_M_f32	56.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFits_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	18
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184
DigColPs_SpurTrimStatic_Deg_M_f32	56.4
DigColPs_TrimCompStatic_Cnt_M_u16	1780
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	223
k_SkipStepErrDiag_Cnt_str.PStep	11
k_SkipStepErrDiag_Cnt_str.NStep	45
k_VernCorrErrorDiag_Cnt_str.Threshold	41
k_VernCorrErrorDiag_Cnt_str.PStep	39
k_VernCorrErrorDiag_Cnt_str.NStep	0
k_VernCorrErrorThresh_Deg_f32	98.42212296
k_VernOORangeThresh_Deg_f32	1614.228407

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DigColPs_Per2

Name	Input Value		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	125.941998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	234.6564466		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	305		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1151.14197	1151.141998 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	245.454468	245.4545455 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	138
DigColPs_ColTrimStatic_Deg_M_f32	58.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	17718
DigColPs_I2CHwColAngle_Deg_M_f32	89.98652095
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26140
DigColPs_I2CHwSpurAngle_Deg_M_f32	57.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	686.9139401
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	57.5
DigColPs_TrimCompStatic_Cnt_M_u16	1816
DigColPs_VernCorrDetectAcc_Cnt_M_u16	16
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	168		
k_SkipStepErrDiag_Cnt_str.PStep	25		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	56		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	40.71416354		
k_VernOORangeThresh_Deg_f32	852.5587618		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	89.98652095		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	221.6592153		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2805		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1471.08655	1471.086521 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.55 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	156
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	63
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	38087
DigColPs_I2CHwColAngle_Deg_M_f32	291.3419048
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36636
DigColPs_I2CHwSpurAngle_Deg_M_f32	58.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	886.4049975
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0

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DigColPs_Per2

Name	Input Value
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156
DigColPs_SpurTrimStatic_Deg_M_f32	58.6
DigColPs_TrimCompStatic_Cnt_M_u16	1852
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	171		
k_SkipStepErrDiag_Cnt_str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	4		
k_VernCorrErrorDiag_Cnt_str.Threshold	61		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	80.16494608		
k_VernOORangeThresh_Deg_f32	995.0178322		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	291.3419048		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	192.5007017		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1084.59058	1084.59059 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	184.590576	184.5905902 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.56 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	134
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195

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DigColPs_Per2

Name	Input Value
DigColPs_ColTrimStatic_Deg_M_f32	67.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	13742
DigColPs_I2CHwColAngle_Deg_M_f32	196.4963954
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	20378
DigColPs_I2CHwSpurAngle_Deg_M_f32	59.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1340.457155
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	134
DigColPs_SpurTrimStatic_Deg_M_f32	59.7
DigColPs_TrimCompStatic_Cnt_M_u16	1888
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	72		
k_SkipStepErrDiag_Cnt_str.PStep	22		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	17		
k_VernCorrErrorDiag_Cnt_str.PStep	14		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k_VernCorrErrorThresh_Deg_f32	81.58188558		
k_VernOORangeThresh_Deg_f32	510.2277182		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	196.4963954		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	10.61436504		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1249		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	489.396393	489.3963954 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x04	0x04	✓
Status	0x01	0x01	✓

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.57 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	71.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	5158
DigColPs_I2CHwColAngle_Deg_M_f32	194.3084972
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	19371
DigColPs_I2CHwSpurAngle_Deg_M_f32	60.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1210.662194
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	14
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	60.8
DigColPs_TrimCompStatic_Cnt_M_u16	1924
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	128		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	80		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	61.77320576		
k_VernOORangeThresh_Deg_f32	1180.024269		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	194.3084972		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	258.7965072		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3065		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	483.108521	483.1084972 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6E	0x6E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.58 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	75.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58683
DigColPs_I2CHwColAngle_Deg_M_f32	226.2329707
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	52949
DigColPs_I2CHwSpurAngle_Deg_M_f32	61.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	61.9
DigColPs_TrimCompStatic_Cnt_M_u16	1960
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	240
k_SkipStepErrDiag_Cnt_str.PStep	11
k_SkipStepErrDiag_Cnt_str.NStep	37
k_VernCorrErrorDiag_Cnt_str.Threshold	70
k_VernCorrErrorDiag_Cnt_str.PStep	34

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	36.6228292		
k_VernOORangeThresh_Deg_f32	992.7934918		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.2329707		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	113.3681837		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1804		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	150.932968	150.9329707 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363647	-736.3636364 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓	
ReleaseResource	1	ReleaseResource	1	✓	
ConstrainOneRev	2	ConstrainOneRev	2	✓	
VernierLookup	1	VernierLookup	1	✓	
DiagnosticThreshold	1	DiagnosticThreshold	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓	

Test Step 2.59 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	175	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156	
DigColPs_ColTrimStatic_Deg_M_f32	79.4	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	7302	
DigColPs_I2CHwColAngle_Deg_M_f32	31.81471384	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	20291	
DigColPs_I2CHwSpurAngle_Deg_M_f32	63	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	23	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1800	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175	
DigColPs_SpurTrimStatic_Deg_M_f32	63	
DigColPs_TrimCompStatic_Cnt_M_u16	1996	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	161
k_SkipStepErrDiag_Cnt_str.PStep	44
k_SkipStepErrDiag_Cnt_str.NStep	14
k_VernCorrErrorDiag_Cnt_str.Threshold	63
k_VernCorrErrorDiag_Cnt_str.PStep	40
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32	59.55320692
k_VernOORangeThresh_Deg_f32	1084.696699
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	31.81471384
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.2363453
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	358.528931	358.528934 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_ReqI2CSnrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	56	56	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.60 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	83.5
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	42754
DigColPs_I2CHwColAngle_Deg_M_f32	256.6914936
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	24629
DigColPs_I2CHwSpurAngle_Deg_M_f32	64.1

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	16
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	688
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	3
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	64.1
DigColPs_TrimCompStatic_Cnt_M_u16	2032
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	5		
k_SkipStepErrDiag_Cnt_str.NStep	11		
k_VernCorrErrorDiag_Cnt_str.Threshold	11		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	67.07432961		
k_VernOORangeThresh_Deg_f32	836.2919484		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	256.6914936		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105.0697877		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	921		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	173.191498	173.1914936 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363647	-736.3636364 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.61 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	255
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	243
DigColPs_ColTrimStatic_Deg_M_f32	87.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	59004
DigColPs_I2CHwColAngle_Deg_M_f32	93.13262653
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26282
DigColPs_I2CHwSpurAngle_Deg_M_f32	65.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1600.2344
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	14
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	65.2
DigColPs_TrimCompStatic_Cnt_M_u16	2068
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	222		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	31		
k_VernCorrErrorDiag_Cnt_str.Threshold	22		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	59.32419395		
k_VernOORangeThresh_Deg_f32	542.7790878		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	93.13262653		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	131.6931788		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3932		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	5.53263092	5.532626534 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.62 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	155
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	91.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	15177
DigColPs_I2CHwColAngle_Deg_M_f32	20.78231114
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	55143
DigColPs_I2CHwSpurAngle_Deg_M_f32	66.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFits_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	596.9864027
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155
DigColPs_SpurTrimStatic_Deg_M_f32	66.3
DigColPs_TrimCompStatic_Cnt_M_u16	2104
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	19
k_SkipStepErrDiag_Cnt_str.PStep	22
k_SkipStepErrDiag_Cnt_str.NStep	49
k_VernCorrErrorDiag_Cnt_str.Threshold	91
k_VernCorrErrorDiag_Cnt_str.PStep	48
k_VernCorrErrorDiag_Cnt_str.NStep	19
k_VernCorrErrorThresh_Deg_f32	6.884903669
k_VernOORangeThresh_Deg_f32	605.936505
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	20.78231114		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	116.1393507		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	678		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	649.082275	649.0823111 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.63 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	125
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	95.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHWColAngle_Cnt_M_u16	62253
DigColPs_I2CHWColAngle_Deg_M_f32	99.35150683
DigColPs_I2CHWDataType_Cnt_M_u08	1
DigColPs_I2CHWSpurAngle_Cnt_M_u16	26843
DigColPs_I2CHWSpurAngle_Deg_M_f32	67.4
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	822.8970824
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	67.4
DigColPs_TrimCompStatic_Cnt_M_u16	2140
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	206		
k_SkipStepErrDiag_Cnt_str.PStep	31		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	80		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	85.02186632		
k_VernOORangeThresh_Deg_f32	1061.295247		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	99.35150683		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	127.7892992		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1445.27759	1445.277591 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	32	32	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.64 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	243
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	99.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	55455
DigColPs_I2CHwColAngle_Deg_M_f32	346.7766712
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	47484
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFits_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1630.352482
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	243
DigColPs_SpurTrimStatic_Deg_M_f32	68.5
DigColPs_TrimCompStatic_Cnt_M_u16	2176
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	98		
k_SkipStepErrDiag_Cnt_str.PStep	47		
k_SkipStepErrDiag_Cnt_str.NStep	39		
k_VernCorrErrorDiag_Cnt_str.Threshold	47		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	6.903702974		
k_VernOORangeThresh_Deg_f32	1481.66354		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	346.7766712		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	85.06156057		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2733		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	966.876709	966.8766712 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	18	18	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18	18	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.65 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123
DigColPs_ColTrimStatic_Deg_M_f32	104

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	28915
DigColPs_I2CHwColAngle_Deg_M_f32	118.0404236
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43172
DigColPs_I2CHwSpurAngle_Deg_M_f32	69.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	943.3614662
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	69.6
DigColPs_TrimCompStatic_Cnt_M_u16	2212
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	37		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	27		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	16.13001919		
k_VernOORangeThresh_Deg_f32	708.4126034		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0404236		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	214.0159558		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2465		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	14.0404205	14.04042363 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.66 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	108.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	32244
DigColPs_I2CHwColAngle_Deg_M_f32	233.8189296
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26632
DigColPs_I2CHwSpurAngle_Deg_M_f32	70.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	19
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	837.3964648
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	70.7
DigColPs_TrimCompStatic_Cnt_M_u16	2248
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	188		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	32		
k_VernCorrErrorDiag_Cnt_str.Threshold	36		
k_VernCorrErrorDiag_Cnt_str.PStep	9		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	32.58559203		
k_VernOORangeThresh_Deg_f32	1033.041085		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	233.8189296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	253.5267325		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3274		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	485.718933	485.7189296 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	3	3	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-414.281067	-414.2810704 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.67 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	127
DigColPs_ColTrimStatic_Deg_M_f32	112.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50430
DigColPs_I2CHwColAngle_Deg_M_f32	131.2116221
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	62280
DigColPs_I2CHwSpurAngle_Deg_M_f32	71.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	339.5431169
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	14
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	71.8
DigColPs_TrimCompStatic_Cnt_M_u16	2284
DigColPs_VernCorrDetectAcc_Cnt_M_u16	5
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	182
k_SkipStepErrDiag_Cnt_str.PStep	8
k_SkipStepErrDiag_Cnt_str.NStep	30
k_VernCorrErrorDiag_Cnt_str.Threshold	90
k_VernCorrErrorDiag_Cnt_str.PStep	7
k_VernCorrErrorDiag_Cnt_str.NStep	50

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorThresh_Deg_f32	44.56530905		
k_VernOORangeThresh_Deg_f32	835.2161256		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	131.2116221		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	96.8100152		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	152.268173	152.2681749 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	34	34	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12	12	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.68 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	116.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	54715
DigColPs_I2CHwColAngle_Deg_M_f32	133.5364133
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42308
DigColPs_I2CHwSpurAngle_Deg_M_f32	72.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	27
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1016.035717
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	72.9
DigColPs_TrimCompStatic_Cnt_M_u16	2320
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	34		
k_VernCorrErrorDiag_Cnt_str.Threshold	96		
k_VernCorrErrorDiag_Cnt_str.PStep	41		
k_VernCorrErrorDiag_Cnt_str.NStep	33		
k_VernCorrErrorThresh_Deg_f32	43.33685136		
k_VernOORangeThresh_Deg_f32	1120.447047		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	133.5364133		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	207.7008287		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1458		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1457.23645	1457.236413 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.69 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	149
DigColPs_ColTrimStatic_Deg_M_f32	120.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25072
DigColPs_I2CHwColAngle_Deg_M_f32	117.9909339
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	33822
DigColPs_I2CHwSpurAngle_Deg_M_f32	74
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	424.6977491

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DigColPs_Per2

Name	Input Value
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	74
DigColPs_TrimCompStatic_Cnt_M_u16	2356
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	105		
k_SkipStepErrDiag_Cnt_str.PStep	34		
k_SkipStepErrDiag_Cnt_str.NStep	24		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	86.64014435		
k_VernOORangeThresh_Deg_f32	232.6736557		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	117.9909339		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	208.2439033		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	35		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1797.59094	1797.590934 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6	6	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6	6	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	897.590942	897.5909339 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.70 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	124.5
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	12814
DigColPs_I2CHwColAngle_Deg_M_f32	77.52818984
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	14635
DigColPs_I2CHwSpurAngle_Deg_M_f32	75.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	923.4796569
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	75.1
DigColPs_TrimCompStatic_Cnt_M_u16	2392
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	148		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	3		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	6.626505613		
k_VernOORangeThresh_Deg_f32	759.6732113		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	77.52818984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	357.6556342		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3516		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	313.028198	313.0281898 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-586.971802	-586.9718102 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x04	0x04	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.71 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	126
DigColPs_ColTrimStatic_Deg_M_f32	128.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21375
DigColPs_I2CHwColAngle_Deg_M_f32	76.6514684
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	38191
DigColPs_I2CHwSpurAngle_Deg_M_f32	76.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1339.267418
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	76.2
DigColPs_TrimCompStatic_Cnt_M_u16	2428
DigColPs_VernCorrDetectAcc_Cnt_M_u16	5
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	118		
k_SkipStepErrDiag_Cnt_str.PStep	15		
k_SkipStepErrDiag_Cnt_str.NStep	42		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	90.72870111		
k_VernOORangeThresh_Deg_f32	378.3238977		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	76.6514684		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	336.2350776		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	372.711304	372.7113284 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.72 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	127
DigColPs_ColTrimStatic_Deg_M_f32	132.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	27081
DigColPs_I2CHwColAngle_Deg_M_f32	152.7639936
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49055
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	96.19118387
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	77.3
DigColPs_TrimCompStatic_Cnt_M_u16	2464
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	10
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	36
k_VernCorrErrorDiag_Cnt_str.Threshold	3
k_VernCorrErrorDiag_Cnt_str.PStep	33

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Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	24.98827672		
k_VernOORangeThresh_Deg_f32	1644.361279		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	152.7639936		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.24033874		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1344		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1460.06396	1460.063994 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.73 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	136.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	10005
DigColPs_I2CHwColAngle_Deg_M_f32	222.9168355
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29915
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	998.4962399
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	78.4
DigColPs_TrimCompStatic_Cnt_M_u16	2500
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	95		
k_VernCorrErrorDiag_Cnt_str.PStep	18		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	6.261063576		
k_VernOORangeThresh_Deg_f32	1626.468312		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	222.9168355		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	292.4312814		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1014		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	806.116821	806.1168355 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.74 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	241
DigColPs_ColTrimStatic_Deg_M_f32	140.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	40621
DigColPs_I2CHwColAngle_Deg_M_f32	87.17455715
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	59269
DigColPs_I2CHwSpurAngle_Deg_M_f32	79.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0

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Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	806.5395069
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	79.5
DigColPs_TrimCompStatic_Cnt_M_u16	2536
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	193		
k_SkipStepErrDiag_Cnt_str.PStep	35		
k_SkipStepErrDiag_Cnt_str.NStep	18		
k_VernCorrErrorDiag_Cnt_str.Threshold	3		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	79.03816199		
k_VernOORangeThresh_Deg_f32	1714.927183		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	87.17455715		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	277.9257751		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4278		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	666.274536	666.2745571 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4	4	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-233.725464	-233.7254429 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.75 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	196
DigColPs_ColTrimStatic_Deg_M_f32	145

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	6514
DigColPs_I2CHwColAngle_Deg_M_f32	247.6901497
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18547
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	14
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	928.9719008
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	80.6
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	71		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	43		
k_VernCorrErrorDiag_Cnt_str.Threshold	18		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	4.670489788		
k_VernOORangeThresh_Deg_f32	1238.898165		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	247.6901497		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	147.1778288		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1442.4646	1442.464623 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8	8	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.76 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128
DigColPs_ColTrimStatic_Deg_M_f32	149.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	13437
DigColPs_I2CHwColAngle_Deg_M_f32	212.9646001
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43747
DigColPs_I2CHwSpurAngle_Deg_M_f32	81.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	19
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	458.6756344
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	81.7
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	23		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	14.63263726		
k_VernOORangeThresh_Deg_f32	215.8799315		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	212.9646001		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	227.4025638		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1079.22607	1079.226107 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	23	23	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0E	0x0E	✓
Status	0x01	0x01	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.77 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	153.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	45384
DigColPs_I2CHwColAngle_Deg_M_f32	217.6150646
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58335
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1165.499187
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	82.8
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	162
k_SkipStepErrDiag_Cnt_str.PStep	20
k_SkipStepErrDiag_Cnt_str.NStep	48
k_VernCorrErrorDiag_Cnt_str.Threshold	100
k_VernCorrErrorDiag_Cnt_str.PStep	38

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	67.91880226		
k_VernOORangeThresh_Deg_f32	1176.43799		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	217.6150646		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	178.2231709		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1429.35303	1429.353104 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	42	42	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.78 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	196
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	205
DigColPs_ColTrimStatic_Deg_M_f32	157.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	33060
DigColPs_I2CHwColAngle_Deg_M_f32	28.42972344
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18440
DigColPs_I2CHwSpurAngle_Deg_M_f32	83.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	594.4117691
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	83.9
DigColPs_TrimCompStatic_Cnt_M_u16	2680
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	50		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	57		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	27.292485		
k_VernOORangeThresh_Deg_f32	686.6912438		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	28.42972344		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	179.9644135		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1458		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1311.12976	1311.129723 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7	7	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	411.129761	411.1297234 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.79 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	128
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	210
DigColPs_ColTrimStatic_Deg_M_f32	161.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	11998
DigColPs_I2CHwColAngle_Deg_M_f32	16.12509024
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	24120
DigColPs_I2CHwSpurAngle_Deg_M_f32	285
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	4
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	1148.961804
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	5
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	285
DigColPs_TrimCompStatic_Cnt_M_u16	2716
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	52		
k_VernCorrErrorDiag_Cnt_str.PStep	20		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	85.22490358		
k_VernOORangeThresh_Deg_f32	1677.836695		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	16.12509024		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	48.06899381		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4397		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1294.7251	1294.72509 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✔
DigColPs_ReqI2CSnrDataTpe_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	409.09082	409.0909091 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.80 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	220
DigColPs_ColTrimStatic_Deg_M_f32	165.5

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DigColPs_Per2

Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	12814
DigColPs_I2CHwColAngle_Deg_M_f32	117.9909339
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	14635
DigColPs_I2CHwSpurAngle_Deg_M_f32	86.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	86.1
DigColPs_TrimCompStatic_Cnt_M_u16	2752
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	117.9909339		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	208.2439033		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1089.93457	1089.934589 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.81 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	205
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	214
DigColPs_ColTrimStatic_Deg_M_f32	169.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	21375
DigColPs_I2CHwColAngle_Deg_M_f32	77.52818984
DigColPs_I2CHwDataTypes_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	38191
DigColPs_I2CHwSpurAngle_Deg_M_f32	87.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205
DigColPs_SpurTrimStatic_Deg_M_f32	87.2
DigColPs_TrimCompStatic_Cnt_M_u16	2788
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	77.52818984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	357.6556342		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2646		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	987.928223	987.9281898 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.82 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	196
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	173.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	27081
DigColPs_I2CHwColAngle_Deg_M_f32	76.6514684
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49055
DigColPs_I2CHwSpurAngle_Deg_M_f32	88.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	88.3
DigColPs_TrimCompStatic_Cnt_M_u16	2824
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	160
k_SkipStepErrDiag_Cnt_str.PStep	23
k_SkipStepErrDiag_Cnt_str.NStep	16
k_VernCorrErrorDiag_Cnt_str.Threshold	82
k_VernCorrErrorDiag_Cnt_str.PStep	43
k_VernCorrErrorDiag_Cnt_str.NStep	12

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	76.6514684		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	336.2350776		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	982.951477	982.9514684 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	82.9514771	82.9514684 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.83 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	128
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	177.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	10005
DigColPs_I2CHwColAngle_Deg_M_f32	152.7639936
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29915
DigColPs_I2CHwSpurAngle_Deg_M_f32	89.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	89.4
DigColPs_TrimCompStatic_Cnt_M_u16	2860
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	125		
k_SkipStepErrDiag_Cnt_str.PStep	10		
k_SkipStepErrDiag_Cnt_str.NStep	38		
k_VernCorrErrorDiag_Cnt_str.Threshold	64		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	78.40277648		
k_VernOORangeThresh_Deg_f32	547.3349351		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	152.7639936		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.24033874		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	334.963989	334.9639936 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-572.727295	-572.7272727 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.84 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_ColTrimStatic_Deg_M_f32	190.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	92.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0

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DigColPs_Per2

Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	92.7
DigColPs_TrimCompStatic_Cnt_M_u16	2968
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.143258		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	647.932129	647.9321395 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-252.067871	-252.0678605 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.85 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	194.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	93.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	93.8
DigColPs_TrimCompStatic_Cnt_M_u16	3004
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1160.1637	1160.163741 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	260.163696	260.1637406 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 2.86 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_ColTrimStatic_Deg_M_f32	198.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.4548138
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	94.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	94.9
DigColPs_TrimCompStatic_Cnt_M_u16	3040
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.1102527		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1450.4314	1450.431485 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	550.431396	550.4314854 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.87 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	-360
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	96
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	96
DigColPs_TrimCompStatic_Cnt_M_u16	3076
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1787.86145	1787.861465 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	887.86145	887.8614647 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.88 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	360
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	14286
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921
DigColPs_I2CHwSpurAngle_Deg_M_f32	97.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	97.1
DigColPs_TrimCompStatic_Cnt_M_u16	3112
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	125
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	38
k_VernCorrErrorDiag_Cnt_str.Threshold	64
k_VernCorrErrorDiag_Cnt_str.PStep	8
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32	78.40277648
k_VernOORangeThresh_Deg_f32	547.3349351
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	298.7894

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	658.789429	658.7894 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.89 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	106
DigColPs_ColTrimStatic_Deg_M_f32	180.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	29294
DigColPs_I2CHwColAngle_Deg_M_f32	199.9994296
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49318
DigColPs_I2CHwSpurAngle_Deg_M_f32	98.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1048.767936
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	98.2
DigColPs_TrimCompStatic_Cnt_M_u16	3148
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	191		
k_SkipStepErrDiag_Cnt_str.PStep	16		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	24		
k_VernCorrErrorDiag_Cnt_str.PStep	21		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	67.6606307		
k_VernOORangeThresh_Deg_f32	664.4244195		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	199.9994296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	301.9312882		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2922		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1459.39941	1459.39943 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	559.399414	559.3994296 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.90 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	-82.29
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	96
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFits_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	96
DigColPs_TrimCompStatic_Cnt_M_u16	3076
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13

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DigColPs_Per2

Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	160
k_SkipStepErrDiag_Cnt_str.PStep	23
k_SkipStepErrDiag_Cnt_str.NStep	16
k_VernCorrErrorDiag_Cnt_str.Threshold	82
k_VernCorrErrorDiag_Cnt_str.PStep	43
k_VernCorrErrorDiag_Cnt_str.NStep	12
k_VernCorrErrorThresh_Deg_f32	16.35241604
k_VernOORangeThresh_Deg_f32	106.1935596
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1150.15149	1150.151465 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	250.151489	250.1514647 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.91 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	14286
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921
DigColPs_I2CHwSpurAngle_Deg_M_f32	97.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4

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Name	Input Value
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	97.1
DigColPs_TrimCompStatic_Cnt_M_u16	3112
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	125		
k_SkipStepErrDiag_Cnt_str.PStep	10		
k_SkipStepErrDiag_Cnt_str.NStep	38		
k_VernCorrErrorDiag_Cnt_str.Threshold	64		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	78.40277648		
k_VernOORangeThresh_Deg_f32	547.3349351		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	298.7894		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	658.789429	658.7894 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 2.92 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFits_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	-360
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.552634		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	4.37533569	4.375334609 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-895.624634	-895.6246654 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

TEST DETAILS REPORT

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.93 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	3220
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1423		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1649.86719	1649.867139 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	11	11	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.94 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	222.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	325.6206695
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	180.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	180.6
DigColPs_TrimCompStatic_Cnt_M_u16	3256
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	99
k_SkipStepErrDiag_Cnt_str.PStep	3
k_SkipStepErrDiag_Cnt_str.NStep	13
k_VernCorrErrorDiag_Cnt_str.Threshold	74
k_VernCorrErrorDiag_Cnt_str.PStep	33
k_VernCorrErrorDiag_Cnt_str.NStep	6
k_VernCorrErrorThresh_Deg_f32	78.75594592
k_VernOORangeThresh_Deg_f32	1151.771932
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	822.720703	822.7206695 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-77.2792969	-77.27933046 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.95 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHWColAngle_Cnt_M_u16	15468
DigColPs_I2CHWColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHWDataType_Cnt_M_u08	1
DigColPs_I2CHWSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHWSpurAngle_Deg_M_f32	0
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	-82.29
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.552634		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_Igc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	364.677246	364.6772727 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	364.375336	364.3753346 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_Igc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-535.624634	-535.6246654 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.96 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_Igc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_Igc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	3220

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DigColPs_Per2

Name	Input Value
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1423		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1649.86719	1649.867139 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.97 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	227
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.1

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	297.1
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	70		
k_SkipStepErrDiag_Cnt_str.PStep	47		
k_SkipStepErrDiag_Cnt_str.NStep	44		
k_VernCorrErrorDiag_Cnt_str.Threshold	88		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	38		
k_VernCorrErrorThresh_Deg_f32	78.63725519		
k_VernOORangeThresh_Deg_f32	1720.30508		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	115.010748		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	968.010742	968.010748 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 2.98 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	231.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25526
DigColPs_I2CHwColAngle_Deg_M_f32	216.7759984
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFits_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	298.2
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	99		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.5857018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1785.67603	1785.675998 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	885.676025	885.6759984 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.99 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataTyep_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	99.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	340.411285	340.4112897 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-572.727295	-572.7272727 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.100 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	35
k_SkipStepErrDiag_Cnt_str.PStep	2
k_SkipStepErrDiag_Cnt_str.NStep	28
k_VernCorrErrorDiag_Cnt_str.Threshold	42
k_VernCorrErrorDiag_Cnt_str.PStep	16
k_VernCorrErrorDiag_Cnt_str.NStep	18
k_VernCorrErrorThresh_Deg_f32	92.41026139
k_VernOORangeThresh_Deg_f32	1413.552634
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	135.045456	135.0454545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	145.300003	145.3 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-754.700012	-754.7 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.101 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	3220
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1358.22327	1358.223274 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1371.33289	1371.332861 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	11	11	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	458.223267	458.2232736 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.102 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	222.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	60.482
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	99.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFits_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	180.6
DigColPs_TrimCompStatic_Cnt_M_u16	3256
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7

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DigColPs_Per2

Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	99
k_SkipStepErrDiag_Cnt_str.PStep	3
k_SkipStepErrDiag_Cnt_str.NStep	13
k_VernCorrErrorDiag_Cnt_str.Threshold	74
k_VernCorrErrorDiag_Cnt_str.PStep	33
k_VernCorrErrorDiag_Cnt_str.NStep	6
k_VernCorrErrorThresh_Deg_f32	78.75594592
k_VernOORangeThresh_Deg_f32	1151.771932
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1272.13635	1272.13636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1277.58203	1277.582 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	377.582031	377.582 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.103 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	227
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1

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DigColPs_Per2

Name	Input Value
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	297.1
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	70		
k_SkipStepErrDiag_Cnt_str.PStep	47		
k_SkipStepErrDiag_Cnt_str.NStep	44		
k_VernCorrErrorDiag_Cnt_str.Threshold	88		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	38		
k_VernCorrErrorThresh_Deg_f32	78.63725519		
k_VernOORangeThresh_Deg_f32	1720.30508		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	115.010748		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1337.68176	1337.681818 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1328.01074	1328.010748 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	437.681763	437.6818182 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 2.104 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	231.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25526
DigColPs_I2CHwColAngle_Deg_M_f32	216.7759984
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFits_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	298.2
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	99		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.5857018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	355.363617	355.3636364 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	345.675995	345.6759984 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-554.323975	-554.3240016 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.105 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	60.482
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1782.35547	1782.355455 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1780.41125	1780.41129 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	882.355469	882.355455 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.106 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	167
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	33
k_VernCorrErrorDiag_Cnt_str.Threshold	97
k_VernCorrErrorDiag_Cnt_str.PStep	13
k_VernCorrErrorDiag_Cnt_str.NStep	3
k_VernCorrErrorThresh_Deg_f32	82.93280101
k_VernOORangeThresh_Deg_f32	1028.14
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.107 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.108 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	60.482
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241

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DigColPs_Per2

Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1782.35547	1782.355455 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1780.41125	1780.41129 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	882.355469	882.3554545 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.109 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395
DigColPs_I2CHwDataType_Cnt_M_u08	0

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFits_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.14		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.110 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓



Test Case 3: Path Test





Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)	
	CPU Cycles:	
	TS3.1	12132.00 Cycles
	TS3.2	5985.00 Cycles
	TS3.3	5849.00 Cycles
	TS3.4	5935.00 Cycles
	TS3.5	5906.00 Cycles
	TS3.6	5921.00 Cycles
	TS3.7	2806.00 Cycles
	TS3.8	2658.00 Cycles
	TS3.9	5843.00 Cycles
	TS3.10	5773.00 Cycles
	TS3.11	8997.00 Cycles
	TS3.12	2696.00 Cycles
	TS3.13	8863.00 Cycles
	TS3.14	8857.00 Cycles
	TS3.15	8868.00 Cycles
	TS3.16	8856.00 Cycles
	TS3.17	8849.00 Cycles
	TS3.18	8854.00 Cycles
	TS3.19	5822.00 Cycles
	TS3.20	12189.00 Cycles
	TS3.21	12185.00 Cycles
	TS3.22	5712.00 Cycles
	TS3.23	3039.00 Cycles
	TS3.24	3080.00 Cycles

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DigColPs_Per2

Description VECTOR DESCRIPTION:

```
TS3.1 " if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>FALSE
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>False &&
  (I2CColSensorFault_Cnt_T_lgc == FALSE)=>False &&
  (I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
  (TrimCompleteEOL_Cnt_T_lgc == TRUE)=>False
if (k_SelectFromColumn_Cnt_lgc == TRUE)=>False
if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32) && (AngleDataAvailable_Cnt_T_lgc == TRUE))=>False
if ( (AbsVernLevelDiff_Cnt_T_u08 > 1U)=>True&&
  (AngleDataAvailable_Cnt_T_lgc == TRUE)=>False &&
  (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE)=>False
if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) ==
TRUE)=>False
if ( (AbsColPosDiff_Deg_T_f32 > k_VernOORangeThresh_Deg_f32) &&
  (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
  (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if ( (VernCorrDetect_Cnt_T_lgc == TRUE) ||
  (SkipStepFitDetect_Cnt_T_lgc == TRUE) ||
  (DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>False
if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U) && (DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 == 0U))=>True
if (I2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>False
if ( (ColParityError_Cnt_T_lgc == TRUE) ||
  (SpurParityError_Cnt_T_lgc == TRUE) ||
  (I2CSensCommFlts_Cnt_T_u08 != 0U) )=>False
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>False
else if (I2CColSensorFault_Cnt_T_lgc == TRUE)=>False|| (I2CSpurSensorFault_Cnt_T_lgc == TRUE)=>False &&
  (ParityOrCommFault_Cnt_T_lgc == FALSE) )=>False
if ( (ErrorDataReady_Cnt_T_lgc == TRUE) &&
  (ParityOrCommFault_Cnt_T_lgc == FALSE) &&
  (I2CColSensorFault_Cnt_T_lgc == TRUE) )=>False
if (DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U)=>True
if ( (ErrorDataReady_Cnt_T_lgc == TRUE) &&
  (ParityOrCommFault_Cnt_T_lgc == FALSE) &&
  (I2CSpurSensorFault_Cnt_T_lgc == TRUE) )=>False
if (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U)=>True"
TS3.2 "if (k_SelectFromColumn_Cnt_lgc == TRUE)=>True
if ( (VernCorrDetect_Cnt_T_lgc == TRUE) ||
  (SkipStepFitDetect_Cnt_T_lgc == TRUE) ||
  (DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE)=>True )=>True
if(ColParityError_Cnt_T_lgc == TRUE)=>True||
  (SpurParityError_Cnt_T_lgc == TRUE)=>True ||
  (I2CSensCommFlts_Cnt_T_u08 != 0U) =>True
If (DigColPs_ColSensorFaultAcc_Cnt_M_u16 = 0)=>False
If (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 = 0)=False"
TS3.3 " if ( (ColParityError_Cnt_T_lgc == TRUE) ||
  (SpurParityError_Cnt_T_lgc == TRUE) ||
  (I2CSensCommFlts_Cnt_T_u08 != 0U)=>True )=>True
"
TS3.4 " if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
  (I2CColSensorFault_Cnt_T_lgc == FALSE)=>False &&
  (I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
  (TrimCompleteEOL_Cnt_T_lgc == TRUE)=False"
TS3.5 " if ( (ColParityError_Cnt_T_lgc == TRUE) ||
  (SpurParityError_Cnt_T_lgc == TRUE)=>True ||
  (I2CSensCommFlts_Cnt_T_u08 != 0U) )=>True"
TS3.6 "if ( (AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
  (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
  (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if (I2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True &&
  (ParityOrCommFault_Cnt_T_lgc == FALSE)=>False &&
  (I2CColSensorFault_Cnt_T_lgc == TRUE)=>False"
TS3.7 if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U)=>True&& (DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 == 0U))=>False
TS3.8 if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U)=>False && (DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 == 0U))=>True
TS3.9 "else if ( ((I2CColSensorFault_Cnt_T_lgc == TRUE)=>True || (I2CSpurSensorFault_Cnt_T_lgc == TRUE)=>False) &&
  (ParityOrCommFault_Cnt_T_lgc == FALSE)=>True )"
TS3.10 if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32)=>True && (AngleDataAvailable_Cnt_T_lgc ==
TRUE)=>False)=>False
TS3.11 " if ( (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
  (I2CColSensorFault_Cnt_T_lgc == FALSE)=>True &&
  (I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>True &&
  (TrimCompleteEOL_Cnt_T_lgc == TRUE)=>False"
TS3.12 " if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True&&
  (I2CColSensorFault_Cnt_T_lgc == FALSE)=>False &&
  (I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>True &&
  (TrimCompleteEOL_Cnt_T_lgc == TRUE)=>False"
TS3.13 " if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
  (I2CColSensorFault_Cnt_T_lgc == FALSE)=>True &&
  (I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>True &&
  (TrimCompleteEOL_Cnt_T_lgc == TRUE)=>True
if (HwAVernCorrFault_Cnt_T_lgc == TRUE)=>True
if ( (AbsVernLevelDiff_Cnt_T_u08 > 1U)=>True&&
  (AngleDataAvailable_Cnt_T_lgc == TRUE)=>True &&
  (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE)=>False)
if ( (AbsColPosDiff_Deg_T_f32 > k_VernOORangeThresh_Deg_f32)=>True &&
  (AngleDataAvailable_Cnt_T_lgc == TRUE) )=>True&&
  (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE)=>False )=>False"
TS3.14 if (HwAVernCorrFault_Cnt_T_lgc == TRUE)=>False
TS3.15 if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32) && (AngleDataAvailable_Cnt_T_lgc == TRUE))=>True
TS3.16 "if ( (AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
  (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
  (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>True
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 = DiagPStep_m(DigColPs_SkipStepFitDetectAcc_Cnt_M_u16, k_SkipStepErrDiag_Cnt_str)=>True
```

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if ( (AbsColPosDiff_Deg_T_f32 > k_VernOORangeThresh_Deg_f32) &&
    (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
    (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>True"
TS3.17 "DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 = DiagPStep_m(DigColPs_SkipStepFitDetectAcc_Cnt_M_u16,
k_SkipStepErrDiag_Cnt_str)=>False
if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) ==
TRUE)=>True
if ( (VernCorrDetect_Cnt_T_lgc == TRUE) ||
    (SkipStepFitDetect_Cnt_T_lgc == TRUE)=>True||
    (DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>True"
TS3.18 " if ( (VernCorrDetect_Cnt_T_lgc == TRUE)=>True ||
    (SkipStepFitDetect_Cnt_T_lgc == TRUE)||
    (DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>True"
TS3.19 "else if ( ((I2CColSensorFault_Cnt_T_lgc == TRUE)=>False || (I2CSpurSensorFault_Cnt_T_lgc == TRUE)=>True) &&
    (ParityOrCommFault_Cnt_T_lgc == FALSE)=>True )"
TS3.20 " if ( (ErrorDataReady_Cnt_T_lgc == TRUE) &&
    (ParityOrCommFault_Cnt_T_lgc == FALSE) &&
    (I2CColSensorFault_Cnt_T_lgc == TRUE) )=>True
if ( (ErrorDataReady_Cnt_T_lgc == TRUE)=>True &&
    (ParityOrCommFault_Cnt_T_lgc == FALSE)=>True &&
    (I2CSpurSensorFault_Cnt_T_lgc == TRUE)=>False )=>False"
TS3.21 " if ( (ErrorDataReady_Cnt_T_lgc == TRUE)=>True &&
    (ParityOrCommFault_Cnt_T_lgc == FALSE)=>True &&
    (I2CColSensorFault_Cnt_T_lgc == TRUE)=>False )=>False
if ( (ErrorDataReady_Cnt_T_lgc == TRUE) &&
    (ParityOrCommFault_Cnt_T_lgc == FALSE) &&
    (I2CSpurSensorFault_Cnt_T_lgc == TRUE) )=>True"
TS3.22 "if ( (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>TRUE &&
    (I2CColSensorFault_Cnt_T_lgc == FALSE)=>TRUE &&
    (I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
    (TrimCompleteEOL_Cnt_T_lgc == TRUE) )=>False"
TS3.23 if ( (AngleDataAvailable_Cnt_T_lgc == TRUE) && (DigColPs_HwAVernCorrFault_Cnt_M_lgc == FALSE) ) ==>TRUE
TS3.24 (I2CAbsHwPos_HwDeg_T_f32 >= 900) ==>TRUE

```

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	20
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	10
k_SkipStepErrDiag_Cnt_str.PStep	0
k_SkipStepErrDiag_Cnt_str.NStep	0
k_VernCorrErrorDiag_Cnt_str.Threshold	0
k_VernCorrErrorDiag_Cnt_str.PStep	0
k_VernCorrErrorDiag_Cnt_str.NStep	0
k_VernCorrErrorThresh_Deg_f32	1
k_VernOORangeThresh_Deg_f32	100
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0

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DigColPs_Per2

Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x6E	0x6E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	414
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	10
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHWColAngle_Cnt_M_u16	65535
DigColPs_I2CHWColAngle_Deg_M_f32	360
DigColPs_I2CHWDataType_Cnt_M_u08	1
DigColPs_I2CHWSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHWSpurAngle_Deg_M_f32	360
DigColPs_I2CHWTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	100
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1

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DigColPs_Per2

Name	Input Value
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	60		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1427.27271	1427.272727 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	1430	1430 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	131	131	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	60	60	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	530	530 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0D	0x0D	✔
Status	0x01	0x01	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	12
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	212
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.032
DigColPs_I2CHwDataType_Cnt_M_u08	0

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Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.308
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.143258		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	840.685425	840.6854545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	833.432007	833.432 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5679932	-66.568 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x04	0x04	✔
Status	0x01	0x01	✔

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DigColPs_Per2

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	24
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs_I2CHwColAngle_Deg_M_f32	274.36
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	88.88
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1346.35449	1346.354545 ± 0.00048828125	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1339.56006	1339.56 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	439.560059	439.56 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	62
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	25
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.45
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	143.95
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	80
DigColPs_TrimCompStatic_Cnt_M_u16	196
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	214
k_SkipStepErrDiag_Cnt_str.PStep	38
k_SkipStepErrDiag_Cnt_str.NStep	23
k_VernCorrErrorDiag_Cnt_str.Threshold	66
k_VernCorrErrorDiag_Cnt_str.PStep	39

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Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	10		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.1102527		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2646		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	192.704544	192.7045455 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	201.449997	201.45 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-707.295471	-707.2954545 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	100
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.86
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	210.79
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	160
k_SkipStepErrDiag_Cnt_str.PStep	23
k_SkipStepErrDiag_Cnt_str.NStep	16
k_VernCorrErrorDiag_Cnt_str.Threshold	82
k_VernCorrErrorDiag_Cnt_str.PStep	43
k_VernCorrErrorDiag_Cnt_str.NStep	34
k_VernCorrErrorThresh_Deg_f32	16.35241604
k_VernOORangeThresh_Deg_f32	106.1935596
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1400.76807	1400.768182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1392.65991	1392.66 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_ReqI2CSnrDataTypes_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	492.659912	492.66 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	138
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	76
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.667
DigColPs_I2CHwDataTypes_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	190.108

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DigColPs_Per2

Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	13.5
DigColPs_TrimCompStatic_Cnt_M_u16	376
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	48		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3501		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1062.09448	1062.094545 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	1072.66699	1072.667 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	162.094482	162.0945455 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	2
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	116.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.61
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	58.784
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFits_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	17.9
DigColPs_TrimCompStatic_Cnt_M_u16	520
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1491.31091	1491.3109 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1489.40503	1489.405 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	18	18	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4	4	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	4	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	591.310913	591.31091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

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Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.9 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	214
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	241
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataTypes_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.48
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	42		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2109		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1573.44543	1573.445455 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1581.19995	1581.2 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	673.445435	673.4454545 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.10 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	252
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.877
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	175
k_SkipStepErrDiag_Cnt_str.PStep	12
k_SkipStepErrDiag_Cnt_str.NStep	41
k_VernCorrErrorDiag_Cnt_str.Threshold	48
k_VernCorrErrorDiag_Cnt_str.PStep	12
k_VernCorrErrorDiag_Cnt_str.NStep	32

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DigColPs_Per2

Name	Input Value		
k_VernCorrErrorThresh_Deg_f32	78.9135704		
k_VernOORangeThresh_Deg_f32	1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2056		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	476.772736	476.7727273 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	471.677002	471.677 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-428.322998	-428.323 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.11 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	290
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Deg_M_u16	46069
DigColPs_I2CHwColAngle_Cnt_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4242		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	447.151367	447.1513636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	460.399994	460.4 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-452.848633	-452.8486364 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.12 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	3
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	22
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	16067
DigColPs_I2CHwColAngle_Deg_M_f32	272.64
DigColPs_I2CHwDataType_Cnt_M_u08	1

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Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16937
DigColPs_I2CHwSpurAngle_Deg_M_f32	19.172
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFits_Cnt_M_u08	15
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1733.007516
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	47.6
DigColPs_TrimCompStatic_Cnt_M_u16	1492
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	80		
k_SkipStepErrDiag_Cnt_str.PStep	43		
k_SkipStepErrDiag_Cnt_str.NStep	7		
k_VernCorrErrorDiag_Cnt_str.Threshold	6		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	49		
k_VernCorrErrorThresh_Deg_f32	86.69760323		
k_VernOORangeThresh_Deg_f32	1173.76136		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	272.6490288		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.17228091		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	621		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	968.896362	968.8963636 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	970.640015	970.64 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	70.6400146	70.64 ± 0.00009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 3.13 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	7
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFits_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	41	41	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0E	0x0E	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	54
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	41
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	50
k_VernCorrErrorDiag_Cnt_str.Threshold	85
k_VernCorrErrorDiag_Cnt_str.PStep	4
k_VernCorrErrorDiag_Cnt_str.NStep	46
k_VernCorrErrorThresh_Deg_f32	8.884848118
k_VernOORangeThresh_Deg_f32	1087.934204
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.033536
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.15 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.03
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0

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DigColPs_Per2

Name	Input Value		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	3		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.634827	163.6348393 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	41	41	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.365173	-736.3651607 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0E	0x0E	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	148
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4

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DigColPs_Per2

Name	Input Value
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	41	41	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x0E	0x0E	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 3.17 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	195
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFits_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	41	41	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✔
NTC	0x6C	0x6C	✔
Param	0x06	0x06	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.18 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	242
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	41
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	50
k_VernCorrErrorDiag_Cnt_str.Threshold	85
k_VernCorrErrorDiag_Cnt_str.PStep	4
k_VernCorrErrorDiag_Cnt_str.NStep	46
k_VernCorrErrorThresh_Deg_f32	3
k_VernOORangeThresh_Deg_f32	1087.934204
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.033536
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	24	24	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	24	24	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.19 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	289
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.48
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0

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DigColPs_Per2

Name	Input Value		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	42		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2109		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	98.7181778	98.71818182 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	100.399994	100.4 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✔
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-801.281799	-801.2818182 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x0C	0x0C	✔
Status	0x01	0x01	✔

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓	
ReleaseResource	1	ReleaseResource	1	✓	
ConstrainOneRev	2	ConstrainOneRev	2	✓	
VernierLookup	1	VernierLookup	1	✓	
DiagnosticThreshold	1	DiagnosticThreshold	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓	

Test Step 3.20 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	336	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	0	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	0	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0	
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	
DigColPs_SpurTrimStatic_Deg_M_f32	0	

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DigColPs_Per2

Name	Input Value
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x6E	0x6E	✔
Param	0x00	0x00	✔
Status	0x01	0x01	✔
NTC	0x6F	0x6F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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DigColPs_Per2

Test Step 3.21 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	383
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFits_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6

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DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✔
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✔
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✔
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✔
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✔
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✔
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✔
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✔
NTC	0x6C	0x6C	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x6E	0x6E	✔

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DigColPs_Per2

Name	Actual Value	Expected Value	Result
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	430
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.075
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	324.208
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	35
k_SkipStepErrDiag_Cnt_str.PStep	2
k_SkipStepErrDiag_Cnt_str.NStep	28
k_VernCorrErrorDiag_Cnt_str.Threshold	42
k_VernCorrErrorDiag_Cnt_str.PStep	16
k_VernCorrErrorDiag_Cnt_str.NStep	31
k_VernCorrErrorThresh_Deg_f32	92.41026139
k_VernOORangeThresh_Deg_f32	1413.552634
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32

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DigColPs_Per2

Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1456.45813	1456.458182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1444.375	1444.375 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	544.375	544.375 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	64
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.86
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	210.79
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130

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DigColPs_Per2

Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21

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DigColPs_Per2

Name	Input Value		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	34		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1400.76807	1400.768182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1392.65991	1392.66 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	492.659912	492.66 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	53
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFits_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20

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DigColPs_Per2

Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18

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DigColPs_Per2

Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	255
k_SkipStepErrDiag_Cnt_str.PStep	50
k_SkipStepErrDiag_Cnt_str.NStep	50
k_VernCorrErrorDiag_Cnt_str.Threshold	100
k_VernCorrErrorDiag_Cnt_str.PStep	50
k_VernCorrErrorDiag_Cnt_str.NStep	50
k_VernCorrErrorThresh_Deg_f32	100
k_VernOORangeThresh_Deg_f32	1800
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1800	1800 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFitDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	900	900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	✓
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

ComputeRoughTurns

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Project	DigColPs
Module	DigColPs
Test Object	ComputeRoughTurns

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 5.00 Cycles Longest Execution Path
TS1.2 3.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Longest Execution Path =>
if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=FALSE
else if (Delta_Deg_T_f32 < -k_StepDetect_Deg_f32)=FALSE"
TS1.2 "Shortest Execution Path =>
if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=TRUE"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	30		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	30		
tgt_RoughTurnAccPtr_Cnt_T_s16	-3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1080	-1080 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	-3	-3	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1080	1080 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3	✓

TEST DETAILS REPORT

ComputeRoughTurns

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Test Case 2: Boundary Test

Specification Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 8.00 Cycles
TS2.2 3.00 Cycles
TS2.3 8.00 Cycles
TS2.4 3.00 Cycles
TS2.5 5.00 Cycles
TS2.6 3.00 Cycles
TS2.7 8.00 Cycles
TS2.8 3.00 Cycles
TS2.9 5.00 Cycles
TS2.10 5.00 Cycles
TS2.11 5.00 Cycles
TS2.12 3.00 Cycles
TS2.13 5.00 Cycles
TS2.14 5.00 Cycles
TS2.15 5.00 Cycles

Description VECTOR DESCRIPTION:

TS2.1 All Min
TS2.2 All Max
TS2.3 Delta_Deg_T_f32=Min
TS2.4 Delta_Deg_T_f32=Max
TS2.5 Delta_Deg_T_f32=Zero
TS2.6 Delta_Deg_T_f32=Pos
TS2.7 Delta_Deg_T_f32=Neg
TS2.8 k_StepDetect_Deg_f32=Min
TS2.9 k_StepDetect_Deg_f32=Max
TS2.10 k_StepDetect_Deg_f32=Pos
TS2.11 RoughTurnAccPtr_Cnt_T_s08=Min
TS2.12 RoughTurnAccPtr_Cnt_T_s08=Max
TS2.13 RoughTurnAccPtr_Cnt_T_s08=Zero
TS2.14 RoughTurnAccPtr_Cnt_T_s08=Pos
TS2.15 RoughTurnAccPtr_Cnt_T_s08=Neg

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	20		
tgt_RoughTurnAccPtr_Cnt_T_s16	-5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1440	-1440 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	-4	-4	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1440	1440 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	4	4	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	24.5		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	28		

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Name	Input Value		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	0	0 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	0	0	✓

Test Step 2.5 (Repeat Count = 1) ✓

Name	Input Value		
Delta_Deg_T_f32	0		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	44.4		
tgt_RoughTurnAccPtr_Cnt_T_s16	2		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	✓

Test Step 2.6 (Repeat Count = 1) ✓

Name	Input Value		
Delta_Deg_T_f32	180.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	68		
tgt_RoughTurnAccPtr_Cnt_T_s16	3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	✓

Test Step 2.7 (Repeat Count = 1) ✓

Name	Input Value		
Delta_Deg_T_f32	-150.6		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	90.2		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1800	1800 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	5	5	✓

Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value		
Delta_Deg_T_f32	50		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	20		
tgt_RoughTurnAccPtr_Cnt_T_s16	3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	✓

Test Step 2.9 (Repeat Count = 1) ✓

Name	Input Value		
Delta_Deg_T_f32	-30.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓

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ComputeRoughTurns

Test Step 2.10 (Repeat Count = 1)				✓
Name	Input Value			
Delta_Deg_T_f32	0			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16			
k_StepDetect_Deg_f32	100.8			
tgt_RoughTurnAccPtr_Cnt_T_s16	2			
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	720	720 ± 0.00048828125		✓
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2		✓

Test Step 2.11 (Repeat Count = 1)				✓
Name	Input Value			
Delta_Deg_T_f32	154.2			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16			
k_StepDetect_Deg_f32	155			
tgt_RoughTurnAccPtr_Cnt_T_s16	-5			
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	-1800	-1800 ± 0.00048828125		✓
tgt_RoughTurnAccPtr_Cnt_T_s16	-5	-5		✓

Test Step 2.12 (Repeat Count = 1)				✓
Name	Input Value			
Delta_Deg_T_f32	40			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16			
k_StepDetect_Deg_f32	25.4			
tgt_RoughTurnAccPtr_Cnt_T_s16	5			
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	1440	1440 ± 0.00048828125		✓
tgt_RoughTurnAccPtr_Cnt_T_s16	4	4		✓

Test Step 2.13 (Repeat Count = 1)				✓
Name	Input Value			
Delta_Deg_T_f32	-300			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16			
k_StepDetect_Deg_f32	300			
tgt_RoughTurnAccPtr_Cnt_T_s16	0			
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	0	0 ± 0.00048828125		✓
tgt_RoughTurnAccPtr_Cnt_T_s16	0	0		✓

Test Step 2.14 (Repeat Count = 1)				✓
Name	Input Value			
Delta_Deg_T_f32	10.5			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16			
k_StepDetect_Deg_f32	150.1			
tgt_RoughTurnAccPtr_Cnt_T_s16	1			
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	360	360 ± 0.00048828125		✓
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1		✓

Test Step 2.15 (Repeat Count = 1)				✓
Name	Input Value			
Delta_Deg_T_f32	150			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16			
k_StepDetect_Deg_f32	200			
tgt_RoughTurnAccPtr_Cnt_T_s16	-2			
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	-720	-720 ± 0.00048828125		✓
tgt_RoughTurnAccPtr_Cnt_T_s16	-2	-2		✓

Test Case 3: Path Test

Specification Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 3.00 Cycles
TS3.2 8.00 Cycles
TS3.3 5.00 Cycles

Description VECTOR DESCRIPTION:

TS3.1 if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=TRUE
TS3.2 "if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=FALSE
else if (Delta_Deg_T_f32 < -k_StepDetect_Deg_f32)=TRUE"
TS3.3 "if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=FALSE
else if (Delta_Deg_T_f32 < -k_StepDetect_Deg_f32)=FALSE"

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1080	1080 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	24		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
Delta_Deg_T_f32	30		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	30		
tgt_RoughTurnAccPtr_Cnt_T_s16	-3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1080	-1080 ± 0.00048828125	✓
tgt_RoughTurnAccPtr_Cnt_T_s16	-3	-3	✓

TEST DETAILS REPORT

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DigColPs_SCom_CustSetTrim



Project	DigColPs
Module	DigColPs
Test Object	DigColPs_SCom_CustSetTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

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DigColPs_SCom_CustSetTrim



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

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DigColPs_SCom_CustSetTrim



Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 1027.00 Cycles
TS1.2 1499.00 Cycles

Description

Vector Description:

TS1.1 "Shortest Execution Path =>
(I2CColSensorFault_Cnt_T_lgc = False) And (I2CSpurSensorFault_Cnt_T_lgc = False) _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16) _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16) _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16))=>FALSE;
(Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16) ==> TRUE"
TS1.2 "Longest ExecutionPath =>
(I2CColSensorFault_Cnt_T_lgc = False) And (I2CSpurSensorFault_Cnt_T_lgc = False) _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16) _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16) _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16))=>TRUE;
(I2CHwTrimColOffset_Deg_T_f32 >= 0)==>TRUE;
(Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16) ==> FALSE"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360	360 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	✔

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DigColPs_SCom_CustSetTrim



Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	44		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	✓

TEST DETAILS REPORT

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DigColPs_SCom_CustSetTrim



Test Case 2: Boundary Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS2.1 1356.00 Cycles
TS2.2 1026.00 Cycles
TS2.3 1028.00 Cycles
TS2.4 1027.00 Cycles
TS2.5 1033.00 Cycles
TS2.6 1037.00 Cycles
TS2.7 1048.00 Cycles
TS2.8 1027.00 Cycles
TS2.9 1356.00 Cycles
TS2.10 1027.00 Cycles
TS2.11 1356.00 Cycles
TS2.12 1027.00 Cycles
TS2.13 1492.00 Cycles
TS2.14 1027.00 Cycles
TS2.15 1432.00 Cycles
TS2.16 1027.00 Cycles
TS2.17 1356.00 Cycles
TS2.18 1027.00 Cycles
TS2.19 1481.00 Cycles
TS2.20 1027.00 Cycles
TS2.21 1356.00 Cycles
TS2.22 1027.00 Cycles

Description

Vector Description:

TS2.1All Min
TS2.2All Max
TS2.3DigColPs_I2CHwSpurAngle_Deg_M_f32=Min
TS2.4DigColPs_I2CHwSpurAngle_Deg_M_f32=Max
TS2.5DigColPs_I2CHwSpurAngle_Deg_M_f32=Pos
TS2.6DigColPs_I2CHwColAngle_Deg_M_f32=Min
TS2.7DigColPs_I2CHwColAngle_Deg_M_f32=Max
TS2.8DigColPs_I2CHwColAngle_Deg_M_f32=Pos
TS2.9DigColPs_I2CSpurSensorFault_Cnt_M_lgc=Min
TS2.10DigColPs_I2CSpurSensorFault_Cnt_M_lgc=Max
TS2.11DigColPs_I2CColSensorFault_Cnt_M_lgc=Min
TS2.12DigColPs_I2CColSensorFault_Cnt_M_lgc=Max
TS2.13DigColPs_I2CHwColAngleForTrim_Deg_M_f32=Min
TS2.14DigColPs_I2CHwColAngleForTrim_Deg_M_f32=Max
TS2.15DigColPs_I2CHwColAngleForTrim_Deg_M_f32=Pos
TS2.16DigColPs_TrimCompStatic_Cnt_M_u16=Min
TS2.17DigColPs_TrimCompStatic_Cnt_M_u16=Max
TS2.18DigColPs_TrimCompStatic_Cnt_M_u16=Pos
TS2.19DigColPsInt_GetCustData=Min
TS2.20DigColPsInt_GetCustData=Max
TS2.21DigColPsInt_GetCustData=Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R.ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R.SpurTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_CustSetTrim()	0	0	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R.ColTrim_Cnt_u32	1020002303	1020002303	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R.SpurTrim_Cnt_u32	1020002303	1020002303	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65534	65534	✔

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DigColPs_SCom_CustSetTrim



Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360	360 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	✔

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	455		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	254.2		
DigColPs_I2CHwColAngle_Deg_M_f32	50		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	965		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1	1 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1	1	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4	4 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456	2456	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142	2142	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525	525	✔

Test Step 2.4 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	245
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	11.2
DigColPs_I2CHwColAngle_Deg_M_f32	1.2

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DigColPs_SCom_CustSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	785		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12.3		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2144		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12.3000002	12.3 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000	1000	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8	8 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564	3564	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2144	2144	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625	625	✔

Test Step 2.5 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	455		
DigColPs_I2CHwColAngle_Deg_M_f32	44		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	142		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24	24 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999	1999	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.3999996	12.4 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672	4672	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146	2146	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725	725	✔

Test Step 2.6 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	211
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	965.2
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_TrimCompStatic_Cnt_M_u16	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	36.6

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DigColPs_SCom_CustSetTrim

Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	16		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2148		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	36.5999985	36.6 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998	2998	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	16	16 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780	5780	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2148	2148	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	825	825	✓

Test Step 2.7 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	356		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	751.2		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48	48 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997	3997	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20	20 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888	6888	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	925	925	✓

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DigColPs_SCom_CustSetTrim



Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetCustData()	478		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	25.3		
DigColPs_I2CHwColAngle_Deg_M_f32	2.5		
DigColPs_I2CHwSpurAngle_Deg_M_f32	44		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	854		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4996		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	7996		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2152		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	60	60 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4996	4996	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	24	24 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	7996	7996	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2152	2152	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1025	1025	✓

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetCustData()	356		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	751.2		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48	48 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997	3997	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20	20 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888	6888	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925	925	✓

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	14
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	9.58
DigColPs_I2CHwColAngle_Deg_M_f32	196.2

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DigColPs_SCom_CustSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	84		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4000		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R.ColTrim_Cnt_u32	6994		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2156		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84	84 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R.ColTrim_Cnt_u32	6994	6994	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32	32 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212	10212	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2156	2156	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1225	1225	✓

Test Step 2.11 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	2		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5		
DigColPs_I2CHwColAngle_Deg_M_f32	14.23		
DigColPs_I2CHwSpurAngle_Deg_M_f32	255		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2000		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.52		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3021458		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.54		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10254		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3200		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	344		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_CustSetTrim()	0	0	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	10	10 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3202351103	3202351103	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	✔

Test Step 2.12 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	147
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	87.25
DigColPs_I2CHwColAngle_Deg_M_f32	36
DigColPs_I2CHwSpurAngle_Deg_M_f32	124.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_TrimCompStatic_Cnt_M_u16	2548
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	108

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DigColPs_SCom_CustSetTrim

Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	40		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2160		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	108	108 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992	8992	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	40	40 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428	12428	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2160	2160	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1425	1425	✓

Test Step 2.13 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	365		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	65.25		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	1258		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	32.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	98574		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	64.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	98574		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	965		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	9857		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180	180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3167485951	3167485951	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65534	65534	✓

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DigColPs_SCom_CustSetTrim



Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	147		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	24.8		
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.23		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	78.54		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	154785		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	352414		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	785		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	78.5400009	78.54 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	154785	154785	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84	84 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	352414	352414	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	785	785	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425	1425	✔

Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	985.5		
DigColPs_I2CHwColAngle_Deg_M_f32	98.25		
DigColPs_I2CHwSpurAngle_Deg_M_f32	325.1		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	3658		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.254		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCstatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.25400019	6.254 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541	7859541	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547	789547	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548	2548	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	911	911	✔

Test Step 2.16 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	325
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	95
DigColPs_I2CHwColAngle_Deg_M_f32	124.7

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DigColPs_SCom_CustSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	62.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14258		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	108		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	951247		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4417		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	25		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	190	190 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3166830591	3166830591	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	29	29 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3189243903	3189243903	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	✓

Test Step 2.17 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	145		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	7.254		
DigColPs_I2CHwColAngle_Deg_M_f32	65.25		
DigColPs_I2CHwSpurAngle_Deg_M_f32	15.25		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1.036		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	35789		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32.2		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	35789		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4000		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	8		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1.03600001	1.036 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	35789	35789	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32.2000008	32.2 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	35789	35789	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4000	4000	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	8	8	✔

Test Step 2.18 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	2
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5
DigColPs_I2CHwColAngle_Deg_M_f32	14.23
DigColPs_I2CHwSpurAngle_Deg_M_f32	255
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_TrimCompStatic_Cnt_M_u16	2000
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.52

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DigColPs_SCom_CustSetTrim

Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3021458		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.54		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10254		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3200		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	344		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	10	10 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3202351103	3202351103	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65534	65534	✓

Test Step 2.19 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	985.5		
DigColPs_I2CHwColAngle_Deg_M_f32	98.25		
DigColPs_I2CHwSpurAngle_Deg_M_f32	325.1		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	3658		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.254		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.25400019	6.254 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541	7859541	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547	789547	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548	2548	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	911	911	✓

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DigColPs_SCom_CustSetTrim



Test Step 2.20 (Repeat Count = 1)

Name	Input Value	Actual Value	Expected Value	Result
DigColPsInt_GetCustData()	511			
DigColPs_I2CColSensorFault_Cnt_M_lgc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	745			
DigColPs_I2CHwColAngle_Deg_M_f32	115.3			
DigColPs_I2CHwSpurAngle_Deg_M_f32	147.8			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0			
DigColPs_TrimCompStatic_Cnt_M_u16	4025			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0.9857			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	152			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	332.5			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2145			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1258			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	789			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2		✓
DigColPs_SCom_CustSetTrim()	34	34		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1		✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0.985700011	0.9857 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	152	152		✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	332.5	332.5 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2145	2145		✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1258	1258		✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	789	789		✓

Test Step 2.21 (Repeat Count = 1)

Name	Input Value	Actual Value	Expected Value	Result
DigColPsInt_GetCustData()	255			
DigColPs_I2CColSensorFault_Cnt_M_lgc	1			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	357.2			
DigColPs_I2CHwColAngle_Deg_M_f32	58.21			
DigColPs_I2CHwSpurAngle_Deg_M_f32	196.5			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1			
DigColPs_TrimCompStatic_Cnt_M_u16	4152			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.1			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	148			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	124.7			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	158			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	154			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3		✓
DigColPs_SCom_CustSetTrim()	34	34		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1		✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.0999985	75.1 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	148	148		✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	124.699997	124.7 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	158	158		✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4	4		✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	154	154		✓

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DigColPs_SCom_CustSetTrim

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Test Case 3: Path Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 1027.00 Cycles
TS3.2 1031.00 Cycles
TS3.3 1356.00 Cycles
TS3.4 1492.00 Cycles
TS3.5 1500.00 Cycles
TS3.6 1032.00 Cycles
TS3.7 1036.00 Cycles
TS3.8 1047.00 Cycles

Description

Vector Description:

TS3.1 (I2CColSensorFault_Cnt_T_Igc = False)==>TRUE And (I2CSpurSensorFault_Cnt_T_Igc = False)==>FALSE &&
(Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16)==>True
TS3.2 (I2CColSensorFault_Cnt_T_Igc = False)==>FALSE And (I2CSpurSensorFault_Cnt_T_Igc = False)==>FALSE &&
(Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16)==>False
TS3.3 "((I2CColSensorFault_Cnt_T_Igc = False) And (I2CSpurSensorFault_Cnt_T_Igc = False) _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16) _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16) _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16))=>TRUE &&
(BitwiseAND(DigColPs_I2CHwCustData_Uls_M_u16, D_I2CHWTRIMINSENSOR_CNT_U16) <>
D_I2CHWTRIMINSENSOR_CNT_U16)==>True"
TS3.4 "((I2CColSensorFault_Cnt_T_Igc == FALSE) && (I2CSpurSensorFault_Cnt_T_Igc == FALSE)
&& (DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)
&& (DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)
&& (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16)) ==>True &&
((DigColPs_I2CHwCustData_Uls_M_u16 & D_I2CHWTRIMINSENSOR_CNT_U16) != D_I2CHWTRIMINSENSOR_CNT_U16)==>False &&
(I2CHwTrimColOffset_Deg_T_f32 >= 0)==>False"
TS3.5 "((I2CColSensorFault_Cnt_T_Igc == FALSE) && (I2CSpurSensorFault_Cnt_T_Igc == FALSE)
&& (DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)
&& (DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)
&& (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16)) ==>True &&
((DigColPs_I2CHwCustData_Uls_M_u16 & D_I2CHWTRIMINSENSOR_CNT_U16) != D_I2CHWTRIMINSENSOR_CNT_U16)==>False &&
(I2CHwTrimColOffset_Deg_T_f32 >= 0)==>True"
TS3.6 "((I2CColSensorFault_Cnt_T_Igc = False)==>TRUE And (I2CSpurSensorFault_Cnt_T_Igc = False)==>TRUE _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16)==>FALSE _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16) _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16))"
TS3.7 "((I2CColSensorFault_Cnt_T_Igc = False)==>TRUE And (I2CSpurSensorFault_Cnt_T_Igc = False)==>TRUE _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16)==>TRUE _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16)==>FALSE _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16))"
TS3.8 "((I2CColSensorFault_Cnt_T_Igc = False)==>TRUE And (I2CSpurSensorFault_Cnt_T_Igc = False)==>TRUE _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16)==>TRUE _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16)==>TRUE _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16)==>FALSE)"

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	96.58		
DigColPs_I2CHwColAngle_Deg_M_f32	50		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_SCom_CustSetTrim()	34	34	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1	1 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1	1	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4	4 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456	2456	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142	2142	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525	525	✔

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DigColPs_SCom_CustSetTrim



Test Step 3.2 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetCustData()	511			
DigColPs_I2CColSensorFault_Cnt_M_lgc	1			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	7.254			
DigColPs_I2CHwColAngle_Deg_M_f32	1			
DigColPs_I2CHwSpurAngle_Deg_M_f32	360			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1			
DigColPs_TrimCompStatic_Cnt_M_u16	1			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6		✓
DigColPs_SCom_CustSetTrim()	34	34		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0		✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12	12 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000	1000		✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8	8 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564	3564		✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1		✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625	625		✓

Test Step 3.3 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetCustData()	2			
DigColPs_I2CColSensorFault_Cnt_M_lgc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5			
DigColPs_I2CHwColAngle_Deg_M_f32	14.23			
DigColPs_I2CHwSpurAngle_Deg_M_f32	255			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0			
DigColPs_TrimCompStatic_Cnt_M_u16	2000			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.52			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3021458			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.54			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10254			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3200			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	344			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6		✓
DigColPs_SCom_CustSetTrim()	0	0		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0		✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	10	10 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3202351103	3202351103		✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125		✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055		✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1		✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534		✓

Test Step 3.4 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetCustData()	1			
DigColPs_I2CColSensorFault_Cnt_M_lgc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5			
DigColPs_I2CHwColAngle_Deg_M_f32	44			

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DigColPs_SCom_CustSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	185	185 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167158271	3167158271	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	✓

Test Step 3.5 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	44		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_CustSetTrim()	0	0	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	✔

Test Step 3.6 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	511
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	455
DigColPs_I2CHwColAngle_Deg_M_f32	44
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_TrimCompStatic_Cnt_M_u16	142
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24

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DigColPs_SCom_CustSetTrim

Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24	24 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999	1999	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.3999996	12.4 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672	4672	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146	2146	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	725	725	✓

Test Step 3.7 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65534	65534	✓

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DigColPs_SCom_CustSetTrim



Test Step 3.8 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetCustData()	356
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	751.2
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_TrimCompStatic_Cnt_M_u16	2
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_CustSetTrim()	34	34	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48	48 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997	3997	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20	20 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888	6888	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925	925	✓

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DigColPs_Init1



Project	DigColPs
Module	DigColPs
Test Object	DigColPs_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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DigColPs_Init1



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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DigColPs_Init1



Test Case 1: Metrics Test

Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment) CPU Cycles: TS1.1 1098.00 Cycles Shortest Execution Path TS1.2 821.00 Cycles Longest Execution Path
Description	VECTOR DESCRIPTION: TS1.1 "Shortest Execution Path => (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32)=FALSE (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)=FALSE ((uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16)=FALSE" TS1.2 "Longest Execution Path => (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32)=FALSE (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)=FALSE ((uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16)=TRUE "

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	2		
k_SpurAngSenseLPFFc_Hz_f32	9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	124		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2500		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0248195529	0.024819543 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✔

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DigColPs_Init1

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180	180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951	3167485951	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200	200 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231	3166175231	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100	100	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435	65435	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Case 2: Boundary Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS2.1 1068.00 Cycles
TS2.2 1070.00 Cycles
TS2.3 1071.00 Cycles
TS2.4 824.00 Cycles
TS2.5 1072.00 Cycles
TS2.6 1072.00 Cycles
TS2.7 1072.00 Cycles
TS2.8 825.00 Cycles
TS2.9 1071.00 Cycles
TS2.10 1073.00 Cycles
TS2.11 1074.00 Cycles
TS2.12 1072.00 Cycles
TS2.13 825.00 Cycles
TS2.14 1072.00 Cycles
TS2.15 1070.00 Cycles
TS2.16 1071.00 Cycles
TS2.17 824.00 Cycles
TS2.18 1072.00 Cycles
TS2.19 1072.00 Cycles
TS2.20 1072.00 Cycles
TS2.21 825.00 Cycles
TS2.22 1072.00 Cycles
TS2.23 824.00 Cycles
TS2.24 1071.00 Cycles
TS2.25 824.00 Cycles
TS2.26 1071.00 Cycles
TS2.27 1071.00 Cycles
TS2.28 824.00 Cycles
TS2.29 1071.00 Cycles
TS2.30 1073.00 Cycles
TS2.31 1071.00 Cycles
TS2.32 1073.00 Cycles
TS2.33 826.00 Cycles
TS2.34 1073.00 Cycles
TS2.35 826.00 Cycles
TS2.36 1073.00 Cycles

Description

VECTOR DESCRIPTION:

TS2.1All Min
TS2.2All Max
TS2.3k_ColAngSenseLPFFc_Hz_f32=Min
TS2.4k_ColAngSenseLPFFc_Hz_f32=Max
TS2.5k_ColAngSenseLPFFc_Hz_f32=pos
TS2.6DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Min
TS2.7DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Max
TS2.8DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Pos
TS2.9k_SpurAngSenseLPFFc_Hz_f32=Min
TS2.10k_SpurAngSenseLPFFc_Hz_f32=Max
TS2.11k_SpurAngSenseLPFFc_Hz_f32=Pos
TS2.12DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Min
TS2.13DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Max
TS2.14DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Pos
TS2.15Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Min
TS2.16Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Max
TS2.17Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Pos
TS2.18Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Neg
TS2.19Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Zero
TS2.20Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Min
TS2.21Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Max
TS2.22Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Pos
TS2.23Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Neg
TS2.24Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Zero
TS2.25Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Min
TS2.26Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Max
TS2.27Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Pos
TS2.28Rte_Pim_DigColPsEOL.R.ColTrim_Cnt_u32=Min
TS2.29Rte_Pim_DigColPsEOL.R.ColTrim_Cnt_u32=Max
TS2.30Rte_Pim_DigColPsEOL.R.ColTrim_Cnt_u32=Pos
TS2.31Rte_Pim_DigColPsEOL.R.SpurTrim_Cnt_u32=Min
TS2.32Rte_Pim_DigColPsEOL.R.SpurTrim_Cnt_u32=Max
TS2.33Rte_Pim_DigColPsEOL.R.SpurTrim_Cnt_u32=Pos
TS2.34Rte_Pim_DigColPsEOL.R.TrimCom_Cnt_u16=Min
TS2.35Rte_Pim_DigColPsEOL.R.TrimCom_Cnt_u16=Max
TS2.36Rte_Pim_DigColPsEOL.R.TrimCom_Cnt_u16=Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	1
k_SpurAngSenseLPFFc_Hz_f32	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180
tgt_Pim_DigColPsEOL.R.ColTrim_Cnt_u32	0
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180
tgt_Pim_DigColPsEOL.R.SpurTrim_Cnt_u32	0
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	0

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DigColPs_Init1

Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✓
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.2 (Repeat Count = 1)				
Name		Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		1		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32		60		
k_SpurAngSenseLPFFc_Hz_f32		60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32		360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32		4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32		360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32		4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		65535		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16		65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✓	
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✓	
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓	
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓	
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.3 (Repeat Count = 1)				
Name	Input Value			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.012			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
k_ColAngSenseLPFFc_Hz_f32	1			
k_SpurAngSenseLPFFc_Hz_f32	8.5			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	124			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525			

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DigColPs_Init1

Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✓
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.101307333	0.101307321 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.4 (Repeat Count = 1)				✔
Name		Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.015		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32		60		
k_SpurAngSenseLPFFc_Hz_f32		16		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32		5		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32		3210739711		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32		36.2		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32		3186570034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		224		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16		65311		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.529510796	0.529510782 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32		0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.182138205	0.18213822 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32		0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16		4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32		5	5 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32		3210739711	3210739711	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32		36.2000008	36.2 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32		3186570034	3186570034	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		224	224	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16		65311	65311	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.018		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	30.5		
k_SpurAngSenseLPFFc_Hz_f32	24		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.9		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	324		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.318374097	0.318374099 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.260360897	0.260360885 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.021		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	6		
k_SpurAngSenseLPFFc_Hz_f32	32.9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	13		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	424		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0726258755	0.07262589 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.338624597	0.338624547 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.7 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	12		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	17.4		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	108		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	524		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.139977276	0.139977259 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.8 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.027		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	18.4		
k_SpurAngSenseLPFFc_Hz_f32	48		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	21		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3193438207		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.1		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3170625125		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.206436098	0.206436105 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.452934027	0.45293398 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	21	21 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3193438207	3193438207	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.100006	132.1 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3170625125	3170625125	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	624	624	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64911	64911	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.9 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.01		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.03		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	24		
k_SpurAngSenseLPFFc_Hz_f32	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	25		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	5995		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	156		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	9104		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	724		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.260360897	0.260360885 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.033		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	30		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	29.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6994		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	824		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.314077795	0.314077834 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.11 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.03		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.036		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	36		
k_SpurAngSenseLPFFc_Hz_f32	30		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	33		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7993		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	204.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	11320		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	924		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1325		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.363893569	0.363893542 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.314077795	0.314077834 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.04		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	42.9		
k_SpurAngSenseLPFFc_Hz_f32	5		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	37		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	228		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1024		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.416725516	0.416725463 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.13 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.05		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	48		
k_SpurAngSenseLPFFc_Hz_f32	10.5		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	41.1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3185285529		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	252		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3162767359		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1124		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	64411		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.452934027	0.45293398 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.123612463	0.123612462 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	41.0999985	41.1 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3185285529	3185285529	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	252	252 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3162767359	3162767359	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1124	1124	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	64411	64411	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.06		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	54		
k_SpurAngSenseLPFFc_Hz_f32	15		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	45		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	10990		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	276		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	14644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1224		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.492665172	0.492665137 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.171795845	0.171795819 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.07		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.014		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	20.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	11989		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	300.5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	15752		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1324		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.230012119	0.230012123 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1.1		
k_SpurAngSenseLPFFc_Hz_f32	25		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	12988		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16860		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1424		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0137279034	0.013727909 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.17 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180.25		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.18 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.07		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.014		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	20.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-74.29		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	11989		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	300.5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	15752		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1324		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.230012119	0.230012123 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1

Test Step 2.19 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1.1		
k_SpurAngSenseLPFFc_Hz_f32	25		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	12988		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16860		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1424		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	1825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0137279034	0.013727909 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.20 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.044		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	5		
k_SpurAngSenseLPFFc_Hz_f32	35.3		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14986		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	19076		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1624		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.358273387	0.358273374 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1

Test Step 2.21 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.11		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.054		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	7.7		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	44		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	15985		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	20184		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1724		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0922271013	0.092227111 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.22 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.064		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	9		
k_SpurAngSenseLPFFc_Hz_f32	45		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	64.6		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	16984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180.25		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	21292		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1824		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.431916416	0.431916394 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.23 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.044
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	5
k_SpurAngSenseLPFFc_Hz_f32	35.3
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14986
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-74.29
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	19076
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1624
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2025
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.358273387	0.358273374 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.24 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.11		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.054		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	7.7		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	44		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	15985		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	20184		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1724		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0922271013	0.092227111 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.25 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.13
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.074
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	11
k_SpurAngSenseLPFFc_Hz_f32	50.8
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3176660991
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	5
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3210739711
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65535
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.129101694	0.129101705 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.471848249	0.471848248 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84	84 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3176660991	3176660991	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	5	5 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3210739711	3210739711	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	0	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65535	65535	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.26 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.14		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.084		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	13		
k_SpurAngSenseLPFFc_Hz_f32	55		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	104		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	18982		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	15.1		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	23508		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	65535		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.15071702	0.150717003 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.499000609	0.499000604 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.27 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.15
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.094
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	15.1
k_SpurAngSenseLPFFc_Hz_f32	60
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	124
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171418111
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	25
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3191341055
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	63291
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.172835946	0.172835917 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	124	124 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171418111	3171418111	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	25	25 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3191341055	3191341055	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244	2244	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	63291	63291	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.28 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.16		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.104		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	17		
k_SpurAngSenseLPFFc_Hz_f32	2.2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	144.4		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	35		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	25724		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.19235146	0.192351468 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0272673368	0.027267362 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.29 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.17
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.114
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	19
k_SpurAngSenseLPFFc_Hz_f32	4
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	45.8
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3184053452
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	500
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65035
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.21239692	0.212396936 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0490230918	0.049023077 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	45.7999992	45.8 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3184053452	3184053452	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	500	500	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65035	65035	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.30 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.18		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.124		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	21		
k_SpurAngSenseLPFFc_Hz_f32	6		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	184		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2451658		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	27940		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	550		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.231944919	0.231944884 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0726258755	0.07262589 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.31 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.19
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.134
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	23.3
k_SpurAngSenseLPFFc_Hz_f32	8
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	204
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4444
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	65
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	600
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	2925
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.253826022	0.25382598 ± 0.00048828125	✓
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.32 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.144		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	25		
k_SpurAngSenseLPFFc_Hz_f32	10		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	224		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3164602367		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	650		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	64885		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.118088603	0.118088622 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	224	224 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3164602367	3164602367	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	650	650	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	64885	64885	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1

Test Step 2.33 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.21		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.154		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	27		
k_SpurAngSenseLPFFc_Hz_f32	12.4		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	244.7		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	85		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2145623		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	700		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	3125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.287725627	0.28772557 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.144289374	0.144289358 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.34 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.22		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.164		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	29.8		
k_SpurAngSenseLPFFc_Hz_f32	14		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	264		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	95		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	750		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312351763	0.312351755 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.161322653	0.161322631 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 2.35 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.23
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.174
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	31
k_SpurAngSenseLPFFc_Hz_f32	16
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	284
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3161587711
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3173908479
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65535
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.322643459	0.322643454 ± 0.00048828125	✓
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.182138205	0.18213822 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	284	284 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3161587711	3161587711	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105	105 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3173908479	3173908479	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	0	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	65535	65535	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.36 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.184
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	33
k_SpurAngSenseLPFFc_Hz_f32	18
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	304
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	9444
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	115
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	25846
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	850
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	254
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.339455187	0.339455134 ± 0.00048828125	✓
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.202437222	0.202437176 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1

Test Case 3: Path Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS3.1 821.00 Cycles
TS3.2 823.00 Cycles
TS3.3 1084.00 Cycles
TS3.4 1071.00 Cycles

Description

VECTOR DESCRIPTION:

```
TS3.1 "( (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32) ||  
(Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32) ||  
(uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16) )F"  
TS3.2 "( (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32) ||  
(Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32) ||  
(uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16) )F"  
TS3.3 "( (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32) ||  
(Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32) ||  
(uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16) )F"  
TS3.4 "( (Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32) ||  
(Redundant_Format_1_m*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32) ||  
(uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16) )F"
```

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180	180 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951	3167485951	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200	200 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231	3166175231	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100	100	✔
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435	65435	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 3.2 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	2
k_SpurAngSenseLPFFc_Hz_f32	9
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0248195529	0.024819543 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220	220 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511	3164864511	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	3		
k_SpurAngSenseLPFFc_Hz_f32	10		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	120		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171942399		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	150		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4265000		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	550		
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	655		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0369973183	0.036997347 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.118088603	0.118088622 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_Init1



Test Step 3.4 (Repeat Count = 1)

Name	Input Value
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
k_ColAngSenseLPFFc_Hz_f32	4
k_SpurAngSenseLPFFc_Hz_f32	11
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	260
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6548212
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	340
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16598742
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1321
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	43625
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0490230918	0.049023077 ± 0.00048828125	✔
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.129101694	0.129101705 ± 0.00048828125	✔
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✔
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✔
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✔
tgt_Pim_DigColPsEOL.R.TrimCom_Cnt_u16	61047	61047	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	✓
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓

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DigColPs_SCom_NxtSetTrim



Project	DigColPs
Module	DigColPs
Test Object	DigColPs_SCom_NxtSetTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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DigColPs_SCom_NxtSetTrim



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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DigColPs_SCom_NxtSetTrim



Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 765.00 Cycles
TS1.2 1429.00 Cycles

Description

Vector Description:

TS1.1 "Shortest Execution Path:

((I2CColSensorFault_Cnt_T_lgc = False) And (I2CSpurSensorFault_Cnt_T_lgc = False) _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16) _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16) _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16)) ==> FALSE;
(DigColPs_TrimCompStatic_Cnt_M_u16 = D_TRIMCOMPLETE_CNT_U16) ==> FALSE"

TS1.2 "Longest Execution Path:

((I2CColSensorFault_Cnt_T_lgc = False) And (I2CSpurSensorFault_Cnt_T_lgc = False) _
And (DigColPs_I2CHwCustData_Uls_M_u16 <> D_I2CHWCUSTDATAUNKWN_CNT_U16) _
And (DigColPs_TrimCompStatic_Cnt_M_u16 <> D_TRIMCOMPLETE_CNT_U16) _
And (Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16 <> D_TRIMCOMPLETE_CNT_U16)) ==> TRUE;
(BitwiseAND(DigColPs_I2CHwCustData_Uls_M_u16, D_I2CHWTRIMINSENSOR_CNT_U16) <> D_I2CHWTRIMINSENSOR_CNT_U16) ==> FALSE;
(I2CHwTrimColOffset_Deg_T_f32 >= 0) ==> TRUE;
(DigColPs_TrimCompStatic_Cnt_M_u16 = D_TRIMCOMPLETE_CNT_U16) ==> TRUE"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	511
DigColPs_ColTrimStatic_Deg_M_f32	360
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_NxtSetTrim()	34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	1
DigColPs_ColTrimStatic_Deg_M_f32	48
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800
DigColPs_I2CHwColAngle_Deg_M_f32	89.1
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_SpurTrimStatic_Deg_M_f32	41
DigColPs_TrimCompStatic_Cnt_M_u16	300
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓

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DigColPs_SCom_NxtSetTrim

Name	Actual Value	Expected Value	Result
DigColPs_SCom_NxtSetTrim()	0	0	✓
DigColPs_SpurTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓

Test Case 2: Boundary Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS2.1 812.00 Cycles
TS2.2 765.00 Cycles
TS2.3 812.00 Cycles
TS2.4 765.00 Cycles
TS2.5 812.00 Cycles
TS2.6 765.00 Cycles
TS2.7 812.00 Cycles
TS2.8 765.00 Cycles
TS2.9 767.00 Cycles
TS2.10 765.00 Cycles
TS2.11 767.00 Cycles
TS2.12 765.00 Cycles
TS2.13 767.00 Cycles
TS2.14 765.00 Cycles
TS2.15 767.00 Cycles
TS2.16 765.00 Cycles
TS2.17 767.00 Cycles
TS2.18 765.00 Cycles
TS2.19 767.00 Cycles
TS2.20 765.00 Cycles
TS2.21 767.00 Cycles

Description

Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 DigColPs_I2CColSensorFault_Cnt_M_lgc=>Min
TS2.4 DigColPs_I2CColSensorFault_Cnt_M_lgc=>Max
TS2.5 DigColPs_I2CSpurSensorFault_Cnt_M_lgc=>Min
TS2.6 DigColPs_I2CSpurSensorFault_Cnt_M_lgc=>Max
TS2.7 DigColPs_I2CHwColAngle_Deg_M_f32=>Min
TS2.8 DigColPs_I2CHwColAngle_Deg_M_f32=>Max
TS2.9 DigColPs_I2CHwColAngle_Deg_M_f32=>Pos
TS2.10 DigColPs_I2CHwSpurAngle_Deg_M_f32=>Min
TS2.11 DigColPs_I2CHwSpurAngle_Deg_M_f32=>Max
TS2.12 DigColPs_I2CHwSpurAngle_Deg_M_f32=>Pos
TS2.13 DigColPs_I2CHwColAngleForTrim_Deg_M_f32=>Min
TS2.14 DigColPs_I2CHwColAngleForTrim_Deg_M_f32=>Max
TS2.15 DigColPs_I2CHwColAngleForTrim_Deg_M_f32=>Pos
TS2.16 Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=>Min
TS2.17 Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=>Max
TS2.18 Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=>Pos
TS2.19 DigColPsInt_GetCustData=>Min
TS2.20 DigColPsInt_GetCustData=>Max
TS2.21 DigColPsInt_GetCustData=>Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	0
DigColPs_ColTrimStatic_Deg_M_f32	-360
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_SpurTrimStatic_Deg_M_f32	-360
DigColPs_TrimCompStatic_Cnt_M_u16	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	0	0	✔

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DigColPs_SCom_NxtSetTrim

Name	Actual Value	Expected Value	Result
DigColPs_SpurTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.2 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_ColTrimStatic_Deg_M_f32	360		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	360		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✔

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.3 (Repeat Count = 1) ✓

Name	Input Value		
DigColPsInt_GetCustData()	12		
DigColPs_ColTrimStatic_Deg_M_f32	12		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	156		
DigColPs_I2CHwColAngle_Deg_M_f32	24.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32	64.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	54		
DigColPs_TrimCompStatic_Cnt_M_u16	100		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1854		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	312	312.0002 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	0	0	✔
DigColPs_SpurTrimStatic_Deg_M_f32	163.199951	163.199456 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✔

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓

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DigColPs_SCom_NxtSetTrim



Test Step 2.4 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	44
DigColPs_ColTrimStatic_Deg_M_f32	24
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	199
DigColPs_I2CHwColAngle_Deg_M_f32	68.6
DigColPs_I2CHwSpurAngle_Deg_M_f32	63.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_SpurTrimStatic_Deg_M_f32	65
DigColPs_TrimCompStatic_Cnt_M_u16	200
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2456
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	24	24 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_SCom_NxtSetTrim()	34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32	65	65 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	200	200	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.5 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	76
DigColPs_ColTrimStatic_Deg_M_f32	48
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	242
DigColPs_I2CHwColAngle_Deg_M_f32	89.1
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_SpurTrimStatic_Deg_M_f32	41
DigColPs_TrimCompStatic_Cnt_M_u16	300
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	124	124.0002 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_NxtSetTrim()	0	0	✓
DigColPs_SpurTrimStatic_Deg_M_f32	352.400024	352.4000062 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓

Test Step 2.6 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetCustData()	108
DigColPs_ColTrimStatic_Deg_M_f32	96
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	285
DigColPs_I2CHwColAngle_Deg_M_f32	154

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DigColPs_SCom_NxtSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	182.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	121		
DigColPs_TrimCompStatic_Cnt_M_u16	451		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3660		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	96	96 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	121	121 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	451	451	✔

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓	
ReleaseResource	1	ReleaseResource	1	✓	

Test Step 2.7 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetCustData()		140		
DigColPs_ColTrimStatic_Deg_M_f32		192		
DigColPs_I2CColSensorFault_Cnt_M_lgc		0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		328		
DigColPs_I2CHwColAngle_Deg_M_f32		0		
DigColPs_I2CHwSpurAngle_Deg_M_f32		1.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0		
DigColPs_SpurTrimStatic_Deg_M_f32		204		
DigColPs_TrimCompStatic_Cnt_M_u16		620		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		4262		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		296	296.00004 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		6	6	✓
DigColPs_SCom_NxtSetTrim()		0	0	✓
DigColPs_SpurTrimStatic_Deg_M_f32		181.599976	181.6004 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16		1	1	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓	
ReleaseResource	1	ReleaseResource	1	✓	
ConstrainOneRev	2	ConstrainOneRev	2	✓	

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DigColPs_SCom_NxtSetTrim



Test Step 2.8 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	172
DigColPs_ColTrimStatic_Deg_M_f32	218
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwSpurAngle_Deg_M_f32	244.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_SpurTrimStatic_Deg_M_f32	14
DigColPs_TrimCompStatic_Cnt_M_u16	780
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4464
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	218	218 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_SCom_NxtSetTrim()	34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32	14	14 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	780	780	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.9 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	204
DigColPs_ColTrimStatic_Deg_M_f32	8
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	414
DigColPs_I2CHwColAngle_Deg_M_f32	180.4
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_SpurTrimStatic_Deg_M_f32	117
DigColPs_TrimCompStatic_Cnt_M_u16	54
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4466
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	8	8 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_NxtSetTrim()	34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32	117	117 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	54	54	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	236
DigColPs_ColTrimStatic_Deg_M_f32	16
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	457
DigColPs_I2CHwColAngle_Deg_M_f32	296.5
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0

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DigColPs_SCom_NxtSetTrim

Name	Input Value		
DigColPs_SpurTrimStatic_Deg_M_f32	46		
DigColPs_TrimCompStatic_Cnt_M_u16	946		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4068		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	16	16 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	46	46 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	946	946	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.11 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetCustData()		268		
DigColPs_ColTrimStatic_Deg_M_f32		57		
DigColPs_I2CColSensorFault_Cnt_M_lgc		0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		500		
DigColPs_I2CHwColAngle_Deg_M_f32		176.3		
DigColPs_I2CHwSpurAngle_Deg_M_f32		360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1		
DigColPs_SpurTrimStatic_Deg_M_f32		94		
DigColPs_TrimCompStatic_Cnt_M_u16		1122		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		3670		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		57	57 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		1	1	✓
DigColPs_SCom_NxtSetTrim()		34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32		94	94 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16		1122	1122	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.12 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetCustData()		300		
DigColPs_ColTrimStatic_Deg_M_f32		39		
DigColPs_I2CColSensorFault_Cnt_M_lgc		1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		543		
DigColPs_I2CHwColAngle_Deg_M_f32		184.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32		180.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0		
DigColPs_SpurTrimStatic_Deg_M_f32		23		
DigColPs_TrimCompStatic_Cnt_M_u16		846		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		4272		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		39	39 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		2	2	✓
DigColPs_SCom_NxtSetTrim()		34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32		23	23 ± 0.00048828125	✓

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DigColPs_SCom_NxtSetTrim

Name	Actual Value	Expected Value	Result
DigColPs_TrimCompStatic_Cnt_M_u16	846	846	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.13 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetCustData()	332			
DigColPs_ColTrimStatic_Deg_M_f32	48			
DigColPs_I2CColSensorFault_Cnt_M_lgc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0			
DigColPs_I2CHwColAngle_Deg_M_f32	192.5			
DigColPs_I2CHwSpurAngle_Deg_M_f32	101.1			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1			
DigColPs_SpurTrimStatic_Deg_M_f32	46			
DigColPs_TrimCompStatic_Cnt_M_u16	300			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3874			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_ColTrimStatic_Deg_M_f32	48	48 ± 0.00048828125	✓	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓	
DigColPs_SCom_NxtSetTrim()	34	34	✓	
DigColPs_SpurTrimStatic_Deg_M_f32	46	46 ± 0.00048828125	✓	
DigColPs_TrimCompStatic_Cnt_M_u16	300	300	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.14 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetCustData()	364			
DigColPs_ColTrimStatic_Deg_M_f32	96			
DigColPs_I2CColSensorFault_Cnt_M_lgc	1			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800			
DigColPs_I2CHwColAngle_Deg_M_f32	200.6			
DigColPs_I2CHwSpurAngle_Deg_M_f32	119.8			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0			
DigColPs_SpurTrimStatic_Deg_M_f32	94			
DigColPs_TrimCompStatic_Cnt_M_u16	451			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4476			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_ColTrimStatic_Deg_M_f32	96	96 ± 0.00048828125	✓	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓	
DigColPs_SCom_NxtSetTrim()	34	34	✓	
DigColPs_SpurTrimStatic_Deg_M_f32	94	94 ± 0.00048828125	✓	
DigColPs_TrimCompStatic_Cnt_M_u16	451	451	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

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DigColPs_SCom_NxtSetTrim



Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	396		
DigColPs_ColTrimStatic_Deg_M_f32	144		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	900		
DigColPs_I2CHwColAngle_Deg_M_f32	208.7		
DigColPs_I2CHwSpurAngle_Deg_M_f32	138.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	142		
DigColPs_TrimCompStatic_Cnt_M_u16	602		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4078		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	144	144 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	142	142 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	602	602	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	428		
DigColPs_ColTrimStatic_Deg_M_f32	192		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	586		
DigColPs_I2CHwColAngle_Deg_M_f32	216.8		
DigColPs_I2CHwSpurAngle_Deg_M_f32	157.2		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	190		
DigColPs_TrimCompStatic_Cnt_M_u16	753		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	192	192 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	190	190 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	753	753	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.17 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	460
DigColPs_ColTrimStatic_Deg_M_f32	240
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	629
DigColPs_I2CHwColAngle_Deg_M_f32	224.9
DigColPs_I2CHwSpurAngle_Deg_M_f32	175.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1

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DigColPs_SCom_NxtSetTrim

Name	Input Value		
DigColPs_SpurTrimStatic_Deg_M_f32	238		
DigColPs_TrimCompStatic_Cnt_M_u16	904		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	240	240 ± 0.00048828125	✔
DigColPs_I2ChwTrimTransCnts_Uls_M_u08	0	0	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	238	238 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	904	904	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.18 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetCustData()		492		
DigColPs_ColTrimStatic_Deg_M_f32		288		
DigColPs_I2CColSensorFault_Cnt_M_lgc		1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		672		
DigColPs_I2CHwColAngle_Deg_M_f32		233		
DigColPs_I2CHwSpurAngle_Deg_M_f32		194.6		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0		
DigColPs_SpurTrimStatic_Deg_M_f32		286		
DigColPs_TrimCompStatic_Cnt_M_u16		1055		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		2244		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		288	288 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		1	1	✓
DigColPs_SCom_NxtSetTrim()		34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32		286	286 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16		1055	1055	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.19 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetCustData()		0		
DigColPs_ColTrimStatic_Deg_M_f32		336		
DigColPs_I2CColSensorFault_Cnt_M_lgc		0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		715		
DigColPs_I2CHwColAngle_Deg_M_f32		241.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32		213.3		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1		
DigColPs_SpurTrimStatic_Deg_M_f32		334		
DigColPs_TrimCompStatic_Cnt_M_u16		1206		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		3680		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		336	336 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		2	2	✓
DigColPs_SCom_NxtSetTrim()		34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32		334	334 ± 0.00048828125	✓

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DigColPs_SCom_NxtSetTrim

Name	Actual Value	Expected Value	Result
DigColPs_TrimCompStatic_Cnt_M_u16	1206	1206	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.20 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetCustData()	511			
DigColPs_ColTrimStatic_Deg_M_f32	192			
DigColPs_I2CColSensorFault_Cnt_M_lgc	1			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	758			
DigColPs_I2CHwColAngle_Deg_M_f32	249.2			
DigColPs_I2CHwSpurAngle_Deg_M_f32	232			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0			
DigColPs_SpurTrimStatic_Deg_M_f32	121			
DigColPs_TrimCompStatic_Cnt_M_u16	1357			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1028			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_ColTrimStatic_Deg_M_f32	192	192 ± 0.00048828125	✓	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓	
DigColPs_SCom_NxtSetTrim()	34	34	✓	
DigColPs_SpurTrimStatic_Deg_M_f32	121	121 ± 0.00048828125	✓	
DigColPs_TrimCompStatic_Cnt_M_u16	1357	1357	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 2.21 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetCustData()	365			
DigColPs_ColTrimStatic_Deg_M_f32	218			
DigColPs_I2CColSensorFault_Cnt_M_lgc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	801			
DigColPs_I2CHwColAngle_Deg_M_f32	257.3			
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.7			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1			
DigColPs_SpurTrimStatic_Deg_M_f32	204			
DigColPs_TrimCompStatic_Cnt_M_u16	1508			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1088			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_ColTrimStatic_Deg_M_f32	218	218 ± 0.00048828125	✓	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓	
DigColPs_SCom_NxtSetTrim()	34	34	✓	
DigColPs_SpurTrimStatic_Deg_M_f32	204	204 ± 0.00048828125	✓	
DigColPs_TrimCompStatic_Cnt_M_u16	1508	1508	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

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DigColPs_SCom_NxtSetTrim

Test Case 3: Path Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 812.00 Cycles
TS3.2 1338.00 Cycles
TS3.3 1381.00 Cycles
TS3.4 781.00 Cycles
TS3.5 778.00 Cycles
TS3.6 772.00 Cycles
TS3.7 769.00 Cycles
TS3.8 767.00 Cycles

Description

Vector Description:

```
TS3.1 "if ( (I2CColSensorFault_Cnt_T_Igc == FALSE) && (I2CSpurSensorFault_Cnt_T_Igc == FALSE)
&& (DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)
&& (DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)
&& (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16) )=>TRUE;
( (DigColPs_I2CHwCustData_Uls_M_u16 & D_I2CHWTRIMINSENSOR_CNT_U16) != D_I2CHWTRIMINSENSOR_CNT_U16 ) ==> TRUE;
(DigColPs_TrimCompStatic_Cnt_M_u16 == D_TRIMCOMPLETE_CNT_U16) ==>TRUE"
TS3.2 "if ( (I2CColSensorFault_Cnt_T_Igc == FALSE) && (I2CSpurSensorFault_Cnt_T_Igc == FALSE)
&& (DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)
&& (DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)
&& (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16) )=>TRUE;
( (DigColPs_I2CHwCustData_Uls_M_u16 & D_I2CHWTRIMINSENSOR_CNT_U16) != D_I2CHWTRIMINSENSOR_CNT_U16 ) ==> FALSE;
(I2CHWTrimColOffset_Deg_T_f32 >= 0.0f) ==> TRUE;"
TS3.3 "if ( (I2CColSensorFault_Cnt_T_Igc == FALSE) && (I2CSpurSensorFault_Cnt_T_Igc == FALSE)
&& (DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)
&& (DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)
&& (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16) )=>TRUE;
( (DigColPs_I2CHwCustData_Uls_M_u16 & D_I2CHWTRIMINSENSOR_CNT_U16) != D_I2CHWTRIMINSENSOR_CNT_U16 ) ==> FALSE;
(I2CHWTrimColOffset_Deg_T_f32 >= 0.0f) ==> FALSE"
TS3.4 " ((I2CColSensorFault_Cnt_T_Igc == FALSE)==> TRUE) && ((I2CSpurSensorFault_Cnt_T_Igc == FALSE)==>TRUE)
&& ((DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)==>TRUE)
&& ((DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)==>TRUE)
&& ((Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE) "
TS3.5 " ((I2CColSensorFault_Cnt_T_Igc == FALSE)==> TRUE) && ((I2CSpurSensorFault_Cnt_T_Igc == FALSE)==>TRUE)
&& ((DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)==>TRUE)
&& ((DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE) "
TS3.6 " ((I2CColSensorFault_Cnt_T_Igc == FALSE)==> TRUE) && ((I2CSpurSensorFault_Cnt_T_Igc == FALSE)==>TRUE)
&& ((DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)==>FALSE) "
&& ((DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE) "
TS3.7 " ((I2CColSensorFault_Cnt_T_Igc == FALSE)==> TRUE) && ((I2CSpurSensorFault_Cnt_T_Igc == FALSE)==>FALSE)
&& ((DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)==>FALSE)
&& ((DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE)
&& ((Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE) "
TS3.8 " ((I2CColSensorFault_Cnt_T_Igc == FALSE)==> FALSE) && ((I2CSpurSensorFault_Cnt_T_Igc == FALSE)==>FALSE)
&& ((DigColPs_I2CHwCustData_Uls_M_u16 != D_I2CHWCUSTDATAUNKWN_CNT_U16)==>FALSE)
&& ((DigColPs_TrimCompStatic_Cnt_M_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE)
&& ((Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 != D_TRIMCOMPLETE_CNT_U16)==>FALSE) "
```

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	12		
DigColPs_ColTrimStatic_Deg_M_f32	12		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	156		
DigColPs_I2CHwColAngle_Deg_M_f32	24.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32	64.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	54		
DigColPs_TrimCompStatic_Cnt_M_u16	100		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1854		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	312	312 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	0	0	✔
DigColPs_SpurTrimStatic_Deg_M_f32	163.199951	163.2 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓

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DigColPs_SCom_NxtSetTrim



Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1000		
DigColPs_I2CHwColAngle_Deg_M_f32	89.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	41		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-260	-260 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_NxtSetTrim()	0	0	✓
DigColPs_SpurTrimStatic_Deg_M_f32	-140	-140 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	500		
DigColPs_I2CHwColAngle_Deg_M_f32	89.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	41		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	320	320 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	0	0	✔
DigColPs_SpurTrimStatic_Deg_M_f32	200	200 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetCustData()	120
DigColPs_ColTrimStatic_Deg_M_f32	8
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	414
DigColPs_I2CHwColAngle_Deg_M_f32	180.4

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DigColPs_SCom_NxtSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	117		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	8	8 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	117	117 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	2	2	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 3.5 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetCustData()		120		
DigColPs_ColTrimStatic_Deg_M_f32		8		
DigColPs_I2CColSensorFault_Cnt_M_lgc		0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		414		
DigColPs_I2CHwColAngle_Deg_M_f32		180.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32		78.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0		
DigColPs_SpurTrimStatic_Deg_M_f32		117		
DigColPs_TrimCompStatic_Cnt_M_u16		1		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		4466		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		8	8 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		6	6	✓
DigColPs_SCom_NxtSetTrim()		34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32		117	117 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16		1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 3.6 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetCustData()		511		
DigColPs_ColTrimStatic_Deg_M_f32		218		
DigColPs_I2CColSensorFault_Cnt_M_lgc		0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32		371		
DigColPs_I2CHwColAngle_Deg_M_f32		360		
DigColPs_I2CHwSpurAngle_Deg_M_f32		244.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08		5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0		
DigColPs_SpurTrimStatic_Deg_M_f32		14		
DigColPs_TrimCompStatic_Cnt_M_u16		780		
Rte_Inst_Sa_DigColPs		tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		4464		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		tgt_Pim_DigColPsEOL		
Name		Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32		218	218 ± 0.00048828125	

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DigColPs_SCom_NxtSetTrim

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_SCom_NxtSetTrim()	34	34	✓
DigColPs_SpurTrimStatic_Deg_M_f32	14	14 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	780	780	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 3.7 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_ColTrimStatic_Deg_M_f32	360		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	360		
DigColPs_TrimCompStatic_Cnt_M_u16	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	✔
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✔
DigColPs_SCom_NxtSetTrim()	34	34	✔
DigColPs_SpurTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	✔
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

Test Step 3.8 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_ColTrimStatic_Deg_M_f32	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_NxtSetTrim()	0	0	✓
DigColPs_SpurTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	✓

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DigColPs_SCom_NxtSetTrim



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	✓

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DigColPs_Per1

Project	DigColPs
Module	DigColPs
Test Object	DigColPs_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 4792.00 Cycles Longest Execution Path
TS1.2 4457.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Longest Execution Path =>
if (I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>False
if (I2CSensCommFlts_Cnt_T_u08 != 0U)=>False
if ((I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if ((I2CHwColAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>True
if ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((ColSensorFault_Cnt_T_lgc == TRUE) ||
 (SpurSensorFault_Cnt_T_lgc == TRUE) ||
 (ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
 (SpurParityErrorEvt_Cnt_T_lgc == TRUE))=>False
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True
if (SensorsSampleOK_Cnt_T_lgc == TRUE)=>True
if (DigColPs_ColLPFInitDone_Cnt_M_lgc == FALSE)=>True
if (DigColPs_SpurLPFInitDone_Cnt_M_lgc == FALSE)=>True
if ((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=>True"
TS1.2 "Shortest Execution Path =>
if (I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>False
if (I2CSensCommFlts_Cnt_T_u08 != 0U)=>True
if ((I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if ((I2CHwColAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((ColSensorFault_Cnt_T_lgc == TRUE) ||
 (SpurSensorFault_Cnt_T_lgc == TRUE) ||
 (ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
 (SpurParityErrorEvt_Cnt_T_lgc == TRUE))=>True
if (SensorsSampleOK_Cnt_T_lgc == TRUE)=>False
if ((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=>False"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	250		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	512		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	122		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	920		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	210.6		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓

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Name	Actual Value	Expected Value	Result
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	502	502	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	7
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-160
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.324
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	256
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	12
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1561
DigColPs_PrevI2CHwColAngle_Deg_M_f32	190
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3500
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	225
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	406
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.344
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	254
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168
k_SenseDetErrDiag_Cnt_str.Threshold	80
k_SenseDetErrDiag_Cnt_str.PStep	38
k_SenseDetErrDiag_Cnt_str.NStep	13

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Name	Input Value		
k_SenseParityErrDiag_Cnt_str.Threshold	710		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-413.628082	-413.6280859 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	281	281	✓
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143	143	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	306.371918	306.3719141 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	112.956299	112.9563125 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1561	1561	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	137.197266	137.1972656 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3500	3500	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	307.617188	307.6171875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnrDataTypes_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-247.043701	-247.0436875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	279	279	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-5	-5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155	155	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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Test Case 2: Boundary Test



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DigColPs_Per1

Specification

Performance Metrics:
(With "None" instrumentation and WithPS
Environment)

CPU Cycles:

TS2.1	5109.00	Cycles
TS2.2	5174.00	Cycles
TS2.3	4830.00	Cycles
TS2.4	5043.00	Cycles
TS2.5	5043.00	Cycles
TS2.6	5026.00	Cycles
TS2.7	5010.00	Cycles
TS2.8	5004.00	Cycles
TS2.9	4994.00	Cycles
TS2.10	4997.00	Cycles
TS2.11	5003.00	Cycles
TS2.12	5016.00	Cycles
TS2.13	4890.00	Cycles
TS2.14	5026.00	Cycles
TS2.15	5036.00	Cycles
TS2.16	5016.00	Cycles
TS2.17	4994.00	Cycles
TS2.18	4881.00	Cycles
TS2.19	4982.00	Cycles
TS2.20	5001.00	Cycles
TS2.21	5015.00	Cycles
TS2.22	5028.00	Cycles
TS2.23	4962.00	Cycles
TS2.24	5041.00	Cycles
TS2.25	5015.00	Cycles
TS2.26	5028.00	Cycles
TS2.27	5016.00	Cycles
TS2.28	4962.00	Cycles
TS2.29	5009.00	Cycles
TS2.30	5014.00	Cycles
TS2.31	5004.00	Cycles
TS2.32	5042.00	Cycles
TS2.33	5010.00	Cycles
TS2.34	5044.00	Cycles
TS2.35	5039.00	Cycles
TS2.36	5018.00	Cycles
TS2.37	5027.00	Cycles
TS2.38	4951.00	Cycles
TS2.39	5002.00	Cycles
TS2.40	4986.00	Cycles
TS2.41	4983.00	Cycles
TS2.42	4975.00	Cycles
TS2.43	4885.00	Cycles
TS2.44	5004.00	Cycles
TS2.45	4994.00	Cycles
TS2.46	5015.00	Cycles
TS2.47	5003.00	Cycles
TS2.48	5024.00	Cycles
TS2.49	5030.00	Cycles
TS2.50	4980.00	Cycles
TS2.51	5028.00	Cycles
TS2.52	4898.00	Cycles
TS2.53	5021.00	Cycles
TS2.54	5018.00	Cycles
TS2.55	5011.00	Cycles
TS2.56	5034.00	Cycles
TS2.57	4976.00	Cycles
TS2.58	5018.00	Cycles
TS2.59	4977.00	Cycles
TS2.60	5021.00	Cycles
TS2.61	5042.00	Cycles
TS2.62	4899.00	Cycles
TS2.63	5012.00	Cycles
TS2.64	5086.00	Cycles
TS2.65	5096.00	Cycles
TS2.66	5037.00	Cycles
TS2.67	4987.00	Cycles
TS2.68	5040.00	Cycles
TS2.69	5009.00	Cycles
TS2.70	5014.00	Cycles
TS2.71	4999.00	Cycles
TS2.72	4988.00	Cycles
TS2.73	5041.00	Cycles
TS2.74	4988.00	Cycles
TS2.75	5043.00	Cycles
TS2.76	4988.00	Cycles
TS2.77	4991.00	Cycles
TS2.78	4986.00	Cycles
TS2.79	4992.00	Cycles
TS2.80	4930.00	Cycles
TS2.81	4746.00	Cycles
TS2.82	4729.00	Cycles
TS2.83	4729.00	Cycles
TS2.84	4987.00	Cycles
TS2.85	5015.00	Cycles
TS2.86	5014.00	Cycles
TS2.87	4999.00	Cycles
TS2.88	4988.00	Cycles
TS2.89	5040.00	Cycles
TS2.90	4988.00	Cycles
TS2.91	5014.00	Cycles
TS2.92	4988.00	Cycles
TS2.93	5028.00	Cycles
TS2.94	5014.00	Cycles
TS2.95	4988.00	Cycles
TS2.96	5009.00	Cycles
TS2.97	4988.00	Cycles
TS2.98	4988.00	Cycles
TS2.99	4973.00	Cycles
TS2.100	5028.00	Cycles
TS2.101	4988.00	Cycles
TS2.102	4746.00	Cycles
TS2.103	4726.00	Cycles
TS2.104	4750.00	Cycles
TS2.105	4773.00	Cycles

TEST DETAILS REPORT

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DigColPs_Per1

Description	VECTOR DESCRIPTION:
TS2.1	All Min
TS2.2	All Max
TS2.3	DigColPs_I2CHwColAngle_Cnt_M_u16=Min
TS2.4	DigColPs_I2CHwColAngle_Cnt_M_u16=Max
TS2.5	DigColPs_I2CHwColAngle_Cnt_M_u16=Pos
TS2.6	DigColPs_I2CHwSpurAngle_Cnt_M_u16=Min
TS2.7	DigColPs_I2CHwSpurAngle_Cnt_M_u16=Max
TS2.8	DigColPs_I2CHwSpurAngle_Cnt_M_u16=Pos
TS2.9	DigColPs_I2CHwDataType_Cnt_M_u08=Min
TS2.10	DigColPs_I2CHwDataType_Cnt_M_u08=Max
TS2.11	DigColPs_I2CHwDataType_Cnt_M_u08=Pos
TS2.12	DigColPs_I2CSensCommFlts_Cnt_M_u08=Min
TS2.13	DigColPs_I2CSensCommFlts_Cnt_M_u08=Max
TS2.14	DigColPs_I2CSensCommFlts_Cnt_M_u08=Pos
TS2.15	DigColPs_ColSensorDiagFailed_Cnt_M_lgc=Min
TS2.16	DigColPs_ColSensorDiagFailed_Cnt_M_lgc=Max
TS2.17	k_SenseDetErrDiag_Cnt_str.Threshold=Min
TS2.18	k_SenseDetErrDiag_Cnt_str.Threshold=Max
TS2.19	k_SenseDetErrDiag_Cnt_str.Threshold=Pos
TS2.20	k_SenseDetErrDiag_Cnt_str.Pstep=Min
TS2.21	k_SenseDetErrDiag_Cnt_str.Pstep=Max
TS2.22	k_SenseDetErrDiag_Cnt_str.Pstep=Pos
TS2.23	k_SenseDetErrDiag_Cnt_str.Nstep=Min
TS2.24	k_SenseDetErrDiag_Cnt_str.Nstep=Max
TS2.25	k_SenseDetErrDiag_Cnt_str.Nstep=Pos
TS2.26	DigColPs_ColParityErrorAcc_Cnt_M_u16=Min
TS2.27	DigColPs_ColParityErrorAcc_Cnt_M_u16=Max
TS2.28	DigColPs_ColParityErrorAcc_Cnt_M_u16=Pos
TS2.29	DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Min
TS2.30	DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Max
TS2.31	DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Pos
TS2.32	DigColPs_SpurSensorDiagFailed_Cnt_M_lgc=Min
TS2.33	DigColPs_SpurSensorDiagFailed_Cnt_M_lgc=Max
TS2.34	DigColPs_SpurParityErrorAcc_Cnt_M_u16=Min
TS2.35	DigColPs_SpurParityErrorAcc_Cnt_M_u16=Max
TS2.36	DigColPs_SpurParityErrorAcc_Cnt_M_u16=Pos
TS2.37	DigColPs_PrevI2CHwColAngle_Cnt_M_u16=Min
TS2.38	DigColPs_PrevI2CHwColAngle_Cnt_M_u16=Max
TS2.39	DigColPs_PrevI2CHwColAngle_Cnt_M_u16=Pos
TS2.40	DigColPs_ColSensorFaultAcc_Cnt_M_u16=Min
TS2.41	DigColPs_ColSensorFaultAcc_Cnt_M_u16=Max
TS2.42	DigColPs_ColSensorFaultAcc_Cnt_M_u16=Pos
TS2.43	DigColPs_ColRoughTurns_Cnt_M_s08=Min
TS2.44	DigColPs_ColRoughTurns_Cnt_M_s08=Max
TS2.45	DigColPs_ColRoughTurns_Cnt_M_s08=Zero
TS2.46	DigColPs_ColRoughTurns_Cnt_M_s08=Pos
TS2.47	DigColPs_ColRoughTurns_Cnt_M_s08=Neg
TS2.48	DigColPs_SpurRoughTurns_Cnt_M_s08=Min
TS2.49	DigColPs_SpurRoughTurns_Cnt_M_s08=Max
TS2.50	DigColPs_SpurRoughTurns_Cnt_M_s08=Zero
TS2.51	DigColPs_SpurRoughTurns_Cnt_M_s08=Pos
TS2.52	DigColPs_SpurRoughTurns_Cnt_M_s08=Neg
TS2.53	DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Min
TS2.54	DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Max
TS2.55	DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Pos
TS2.56	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV=Min
TS2.57	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV=Max
TS2.58	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV=Pos
TS2.59	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV=Zero
TS2.60	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV=Neg
TS2.61	DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Min
TS2.62	DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Max
TS2.63	DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Pos
TS2.64	DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV=Min
TS2.65	DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV=Max
TS2.66	DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV=Pos
TS2.67	DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV=Zero
TS2.68	DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV=Neg
TS2.69	Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Min
TS2.70	Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Max
TS2.71	Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Pos
TS2.72	Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Min
TS2.73	Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Max
TS2.74	Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Pos
TS2.75	DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16=Min
TS2.76	DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16=Max
TS2.77	DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16=Pos
TS2.78	k_SenseParityErrDiag_Cnt_str.Threshold=Min
TS2.79	k_SenseParityErrDiag_Cnt_str.Threshold=Max
TS2.80	k_SenseParityErrDiag_Cnt_str.Threshold=Pos
TS2.81	k_StepDetect_Deg_f32=Min
TS2.82	k_StepDetect_Deg_f32=Max
TS2.83	k_StepDetect_Deg_f32=Pos
TS2.84	DigColPs_PrevI2CHwColAngle_Deg_M_f32=Min
TS2.85	DigColPs_PrevI2CHwColAngle_Deg_M_f32=Max
TS2.86	DigColPs_PrevI2CHwColAngle_Deg_M_f32=Pos
TS2.87	DigColPs_PrevI2CHwSpurAngle_Deg_M_f32=Min
TS2.88	DigColPs_PrevI2CHwSpurAngle_Deg_M_f32=Max
TS2.89	DigColPs_PrevI2CHwSpurAngle_Deg_M_f32=Pos
TS2.90	DigColPs_Reql2CSnsrDataType_Cnt_M_u08=Min
TS2.91	DigColPs_Reql2CSnsrDataType_Cnt_M_u08=Max
TS2.92	DigColPs_Reql2CSnsrDataType_Cnt_M_u08=Pos
TS2.93	k_SenseParityErrDiag_Cnt_str.Pstep=Min
TS2.94	k_SenseParityErrDiag_Cnt_str.Pstep=Max

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TS2.95 k_SenseParityErrDiag_Cnt_str.Pstep=Pos
 TS2.96 k_SenseParityErrDiag_Cnt_str.Nstep=Min
 TS2.97 k_SenseParityErrDiag_Cnt_str.Nstep=Max
 TS2.98 k_SenseParityErrDiag_Cnt_str.Nstep=Pos
 TS2.99 DigColPsInt_GetData=Min
 TS2.100 DigColPsInt_GetData=Max
 TS2.101 DigColPsInt_GetData=Pos
 TS2.102 DigColPs_ColLPFInitDone_Cnt_M_lgc=Min
 TS2.103 DigColPs_ColLPFInitDone_Cnt_M_lgc=Max
 TS2.104 DigColPs_SpurLPFInitDone_Cnt_M_lgc=Min
 TS2.105 DigColPs_SpurLPFInitDone_Cnt_M_lgc=Max

Test Step 2.1 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetData()	0			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	0			
DigColPs_ColRoughTurns_Cnt_M_s16	-5			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	0			
DigColPs_I2CHwDataType_Cnt_M_u08	0			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0			
DigColPs_I2CSensCommFits_Cnt_M_u08	0			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0			
DigColPs_SpurRoughTurns_Cnt_M_s16	-11			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	1			
k_SenseDetErrDiag_Cnt_str.PStep	0			
k_SenseDetErrDiag_Cnt_str.NStep	0			
k_SenseParityErrDiag_Cnt_str.Threshold	1			
k_SenseParityErrDiag_Cnt_str.PStep	0			
k_SenseParityErrDiag_Cnt_str.NStep	0			
k_StepDetect_Deg_f32	20			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFits_Cnt_M_u08	0	0	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960	-3960 ± 0.00048828125	✓	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0	0	✓	
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓	
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	✓	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓	

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Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value	Actual Value	Expected Value	Result
DigColPsInt_GetData()	16			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2160	2159.91211	2159.912109 ± 0.00048828125	✓
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1	1	1	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000	1000	1000	✓
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255	205	205	✓
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_I2CHwDataTypes_Cnt_M_u08	4	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535	16	16	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	31	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	4095	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	4095	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataTypes_Cnt_M_u08	4	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4320	4319.91211	4319.912109 ± 0.00048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1	1	1	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000	1000	1000	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	11	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255			
k_SenseDetErrDiag_Cnt_str.Threshold	255			
k_SenseDetErrDiag_Cnt_str.PStep	50			
k_SenseDetErrDiag_Cnt_str.NStep	50			
k_SenseParityErrDiag_Cnt_str.Threshold	1000			
k_SenseParityErrDiag_Cnt_str.PStep	50			
k_SenseParityErrDiag_Cnt_str.NStep	50			
k_StepDetect_Deg_f32	340			

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205	205	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	1
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	999
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	124
DigColPs_I2CSensCommFlts_Cnt_M_u08	1
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1700
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30
k_SenseDetErrDiag_Cnt_str.Threshold	2
k_SenseDetErrDiag_Cnt_str.PStep	5
k_SenseDetErrDiag_Cnt_str.NStep	2
k_SenseParityErrDiag_Cnt_str.Threshold	15
k_SenseParityErrDiag_Cnt_str.PStep	1
k_SenseParityErrDiag_Cnt_str.NStep	1
k_StepDetect_Deg_f32	22.3

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-612.747253	-612.7472656 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	15	15	✓
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	122	122	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	107.252747	107.2527344 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	105.221069	105.2210938 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5	5	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.439453125	0.439453125 ± 0.0001220703125	✓

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Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	12	12	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	1.0546875	1.0546875 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1694.77893	-1694.778906 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	15	15	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	28	28	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.16		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		153		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		128		
DigColPs_I2CHwColAngle_Cnt_M_u16		65535		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		524		
DigColPs_I2CSensCommFlts_Cnt_M_u08		2		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		15		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		15.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		6		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.08		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		126		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		50		
k_SenseDetErrDiag_Cnt_str.Threshold		4		
k_SenseDetErrDiag_Cnt_str.PStep		10		
k_SenseDetErrDiag_Cnt_str.NStep		3		
k_SenseParityErrDiag_Cnt_str.Threshold		10		
k_SenseParityErrDiag_Cnt_str.PStep		3		
k_SenseParityErrDiag_Cnt_str.NStep		2		
k_StepDetect_Deg_f32		24		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-566.189087	-566.1890625 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	10	10	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125	125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	153.810913	153.8109375 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	212.807007	212.8070313 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	15	15	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	1.31835938	1.318359375 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1587.19299	-1587.192969 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	10	10	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	47	47	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.5 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetData()	3			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-300			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	563			
DigColPs_ColRoughTurns_Cnt_M_s16	-3			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	168			
DigColPs_I2CHwColAngle_Cnt_M_u16	2048			
DigColPs_I2CHwDataType_Cnt_M_u08	3			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	924			
DigColPs_I2CSensCommFlts_Cnt_M_u08	3			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.6			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	16			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9			
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1500			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.14			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	142			
DigColPs_SpurRoughTurns_Cnt_M_s16	-3			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101			
k_SenseDetErrDiag_Cnt_str.Threshold	6			
k_SenseDetErrDiag_Cnt_str.PStep	15			
k_SenseDetErrDiag_Cnt_str.NStep	4			
k_SenseParityErrDiag_Cnt_str.Threshold	20			
k_SenseParityErrDiag_Cnt_str.PStep	5			
k_SenseParityErrDiag_Cnt_str.NStep	3			
k_StepDetect_Deg_f32	26			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-455.982422	-455.9824219 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	20	20	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	164	164	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	264.017578	264.0175781 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	358.996826	358.996875 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	16	16	✓	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.40625	1.40625 ± 0.0001220703125	✓	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	✓	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1441.00317	-1441.003125 ± 0.00048828125	✓	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	20	20	✓	
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓	
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	97	97	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	4
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	256
DigColPs_ColRoughTurns_Cnt_M_s16	-2
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_I2CHwColAngle_Cnt_M_u16	124
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	4
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	25
DigColPs_PrevI2CHwColAngle_Deg_M_f32	35
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	20
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	12
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563
DigColPs_SpurRoughTurns_Cnt_M_s16	-2
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
k_SenseDetErrDiag_Cnt_str.Threshold	8
k_SenseDetErrDiag_Cnt_str.PStep	20
k_SenseDetErrDiag_Cnt_str.NStep	5
k_SenseParityErrDiag_Cnt_str.Threshold	30
k_SenseParityErrDiag_Cnt_str.PStep	7
k_SenseParityErrDiag_Cnt_str.NStep	4
k_StepDetect_Deg_f32	28.5

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-237.87265	-237.8726563 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	30	30	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141	141	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	122.12735	122.1273438 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	176.351563	176.3515625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	25	25	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	2.19726563	2.197265625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	20	20	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.7578125	1.7578125 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1263.64844	-1263.648438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	30	30	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	139	139	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.28
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	563
DigColPs_ColRoughTurns_Cnt_M_s16	-1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123
DigColPs_I2CHwColAngle_Cnt_M_u16	628
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	15.5
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	856
DigColPs_SpurRoughTurns_Cnt_M_s16	-1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
k_SenseDetErrDiag_Cnt_str.Threshold	10
k_SenseDetErrDiag_Cnt_str.PStep	25
k_SenseDetErrDiag_Cnt_str.NStep	6
k_SenseParityErrDiag_Cnt_str.Threshold	40
k_SenseParityErrDiag_Cnt_str.PStep	9
k_SenseParityErrDiag_Cnt_str.NStep	5
k_StepDetect_Deg_f32	30

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-71.1386719	-71.13867188 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	40	40	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	117	117	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	288.861328	288.8613281 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.9484863	24.9484375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35	35	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.07617188	3.076171875 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	24	24	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	2.109375	2.109375 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1055.05151	-1055.051563 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	40	40	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.32		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		245		
DigColPs_ColRoughTurns_Cnt_M_s16		0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		146		
DigColPs_I2CHwColAngle_Cnt_M_u16		1132		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		2124		
DigColPs_I2CSensCommFlts_Cnt_M_u08		6		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		45		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		55		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		28		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		18		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.32		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		146		
DigColPs_SpurRoughTurns_Cnt_M_s16		0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		106		
k_SenseDetErrDiag_Cnt_str.Threshold		12		
k_SenseDetErrDiag_Cnt_str.PStep		30		
k_SenseDetErrDiag_Cnt_str.NStep		7		
k_SenseParityErrDiag_Cnt_str.Threshold		50		
k_SenseParityErrDiag_Cnt_str.PStep		11		
k_SenseParityErrDiag_Cnt_str.NStep		6		
k_StepDetect_Deg_f32		32		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		116.465622	116.465625 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		50	50	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		1	1	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		139	139	✔

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	116.465622	116.465625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	264.787476	264.7875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	45	45	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.95507813	3.955078125 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	28	28	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.4609375	2.4609375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-815.212524	-815.2125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	50	50	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	99	99	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.9 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.36		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		256		
DigColPs_ColRoughTurns_Cnt_M_s16		1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		158		
DigColPs_I2CHwColAngle_Cnt_M_u16		1636		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		2524		
DigColPs_I2CSensCommFlts_Cnt_M_u08		7		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		55		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		65		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		32		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		21		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.38		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		756		
DigColPs_SpurRoughTurns_Cnt_M_s16		1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		103		
k_SenseDetErrDiag_Cnt_str.Threshold		14		
k_SenseDetErrDiag_Cnt_str.PStep		35		
k_SenseDetErrDiag_Cnt_str.NStep		8		
k_SenseParityErrDiag_Cnt_str.Threshold		60		
k_SenseParityErrDiag_Cnt_str.PStep		13		
k_SenseParityErrDiag_Cnt_str.NStep		7		
k_StepDetect_Deg_f32		34		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		324.940247	324.9402344 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		60	60	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		2	2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		150	150	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		324.940247	324.9402344 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		175.868713	175.86875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		55	55	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		4.83398438	4.833984375 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		32	32	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		2.8125	2.8125 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-544.131287	-544.13125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		60	60	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		95	95	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		7	7	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	8
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	263
DigColPs_ColRoughTurns_Cnt_M_s16	2
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_I2CHwColAngle_Cnt_M_u16	2140
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2924
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	65
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75.8
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	24
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.44
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	964
DigColPs_SpurRoughTurns_Cnt_M_s16	2
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151
k_SenseDetErrDiag_Cnt_str.Threshold	16
k_SenseDetErrDiag_Cnt_str.PStep	40
k_SenseDetErrDiag_Cnt_str.NStep	9
k_SenseParityErrDiag_Cnt_str.Threshold	70
k_SenseParityErrDiag_Cnt_str.PStep	15
k_SenseParityErrDiag_Cnt_str.NStep	8
k_StepDetect_Deg_f32	36.4

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	554.285156	554.2851563 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	70	70	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	177	177	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	194.285156	194.2851563 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	118.1922	118.1921875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	65	65	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5.71289063	5.712890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36	36	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.1640625	3.1640625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-241.8078	-241.8078125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	70	70	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142	142	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	9			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.44			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	254			
DigColPs_ColRoughTurns_Cnt_M_s16	3			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152			
DigColPs_I2CHwColAngle_Cnt_M_u16	2644			
DigColPs_I2CHwDataType_Cnt_M_u08	2			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	3324			
DigColPs_I2CSensCommFlts_Cnt_M_u08	9			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	75			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	85			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	40			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	27.6			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	746			
DigColPs_SpurRoughTurns_Cnt_M_s16	3			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165			
k_SenseDetErrDiag_Cnt_str.Threshold	18			
k_SenseDetErrDiag_Cnt_str.PStep	45			
k_SenseDetErrDiag_Cnt_str.NStep	10			
k_SenseParityErrDiag_Cnt_str.Threshold	80			
k_SenseParityErrDiag_Cnt_str.PStep	17			
k_SenseParityErrDiag_Cnt_str.NStep	9			
k_StepDetect_Deg_f32	38			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	804.500366	804.5003906 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	80	80	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142	142	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	84.5003662	84.50039063 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	91.7578125	91.7578125 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	75	75	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	6.59179688	6.591796875 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	40	40	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.515625	3.515625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	91.7578125	91.7578125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	80	80	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155	155	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.48		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		214		
DigColPs_ColRoughTurns_Cnt_M_s16		4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		175		
DigColPs_I2CHwColAngle_Cnt_M_u16		3148		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		3724		
DigColPs_I2CSensCommFlts_Cnt_M_u08		0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		85		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		95		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		44		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		30		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.56		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		175		
k_SenseDetErrDiag_Cnt_str.Threshold		20		
k_SenseDetErrDiag_Cnt_str.PStep		50		
k_SenseDetErrDiag_Cnt_str.NStep		11		
k_SenseParityErrDiag_Cnt_str.Threshold		90		
k_SenseParityErrDiag_Cnt_str.PStep		19		
k_SenseParityErrDiag_Cnt_str.NStep		10		
k_StepDetect_Deg_f32		40		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1075.58594	1075.585938 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		90	90	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		5	5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		164	164	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	355.585938	355.5859375 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	96.5656738	96.565625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	85	85	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	7.47070313	7.470703125 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	44	44	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.8671875	3.8671875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	456.565674	456.565625 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	90	90	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	164	164	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.13 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.52		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		256		
DigColPs_ColRoughTurns_Cnt_M_s16		4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		3652		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		2		
DigColPs_I2CSensCommFlts_Cnt_M_u08		31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		95		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		105		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		48		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		33		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.62		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		523		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		185		
k_SenseDetErrDiag_Cnt_str.Threshold		22		
k_SenseDetErrDiag_Cnt_str.PStep		2		
k_SenseDetErrDiag_Cnt_str.NStep		12		
k_SenseParityErrDiag_Cnt_str.Threshold		100		
k_SenseParityErrDiag_Cnt_str.PStep		21		
k_SenseParityErrDiag_Cnt_str.NStep		11		
k_StepDetect_Deg_f32		42		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1180.3418	1180.341797 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		100	100	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		5	5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		174	174	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		100.341797	100.3417969 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		269.415649	269.415625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		11	11	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		95	95	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		8.34960938	8.349609375 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		48	48	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		4.21875	4.21875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		629.415649	629.415625 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		100	100	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		173	173	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		11	11	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.56
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	865
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_I2CHwColAngle_Cnt_M_u16	88
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	105
DigColPs_PrevI2CHwColAngle_Deg_M_f32	115
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	52
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	36
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	195
k_SenseDetErrDiag_Cnt_str.Threshold	24
k_SenseDetErrDiag_Cnt_str.PStep	4
k_SenseDetErrDiag_Cnt_str.NStep	13
k_SenseParityErrDiag_Cnt_str.Threshold	110
k_SenseParityErrDiag_Cnt_str.PStep	23
k_SenseParityErrDiag_Cnt_str.NStep	12
k_StepDetect_Deg_f32	44.2

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-335.632019	-335.6320313 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	110	110	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	129	129	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	24.367981	24.36796875 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	271.907837	271.9078125 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	105	105	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	9.22851563	9.228515625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	52	52	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.5703125	4.5703125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1168.09216	-1168.092188 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	110	110	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	182	182	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	13			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	568			
DigColPs_ColRoughTurns_Cnt_M_s16	-4			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186			
DigColPs_I2CHwColAngle_Cnt_M_u16	244			
DigColPs_I2CHwDataType_Cnt_M_u08	3			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42			
DigColPs_I2CSensCommFlts_Cnt_M_u08	10			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	39			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.74			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	235			
DigColPs_SpurRoughTurns_Cnt_M_s16	-4			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142			
k_SenseDetErrDiag_Cnt_str.Threshold	26			
k_SenseDetErrDiag_Cnt_str.PStep	6			
k_SenseDetErrDiag_Cnt_str.NStep	14			
k_SenseParityErrDiag_Cnt_str.Threshold	120			
k_SenseParityErrDiag_Cnt_str.PStep	25			
k_SenseParityErrDiag_Cnt_str.NStep	13			
k_StepDetect_Deg_f32	46			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-361.935547	-361.9355469 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	120	120	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	172	172	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	358.064453	358.0644531 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	248.042236	248.0421875 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115	115	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.1074219	10.10742188 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56	56	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.921875	4.921875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1191.95776	-1191.957813 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120	120	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128	128	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		14		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.64		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		965		
DigColPs_ColRoughTurns_Cnt_M_s16		-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		184		
DigColPs_I2CHwColAngle_Cnt_M_u16		400		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		62		
DigColPs_I2CSensCommFlts_Cnt_M_u08		11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		125		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		135		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		60		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		42		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.8		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		256		
DigColPs_SpurRoughTurns_Cnt_M_s16		-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		152		
k_SenseDetErrDiag_Cnt_str.Threshold		28		
k_SenseDetErrDiag_Cnt_str.PStep		8		
k_SenseDetErrDiag_Cnt_str.NStep		15		
k_SenseParityErrDiag_Cnt_str.Threshold		130		
k_SenseParityErrDiag_Cnt_str.PStep		27		
k_SenseParityErrDiag_Cnt_str.NStep		14		
k_StepDetect_Deg_f32		48		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-165.768738	-165.76875 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		130	130	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-2	-2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		169	169	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	194.231262	194.23125 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	140.21875	140.21875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	14	14	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	125	125	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.9863281	10.9863281 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	60	60	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	5.2734375	5.2734375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-939.78125	-939.78125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130	130	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	137	137	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	14	14	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.17 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.68		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		456		
DigColPs_ColRoughTurns_Cnt_M_s16		-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		556		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		82		
DigColPs_I2CSensCommFlts_Cnt_M_u08		12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		135		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		145		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		64		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		45.5		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.86		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		142		
DigColPs_SpurRoughTurns_Cnt_M_s16		-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		163		
k_SenseDetErrDiag_Cnt_str.Threshold		1		
k_SenseDetErrDiag_Cnt_str.PStep		10		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		140		
k_SenseParityErrDiag_Cnt_str.PStep		29		
k_SenseParityErrDiag_Cnt_str.NStep		15		
k_StepDetect_Deg_f32		50.5		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		51.2683716	51.26835937 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		140	140	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		170	170	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		51.2683716	51.26835937 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		63.6374512	63.6375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		15	15	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		135	135	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		11.8652344	11.86523438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		64	64	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		5.625	5.625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-656.362549	-656.3625 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		140	140	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		147	147	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		15	15	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	16
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.72
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	256
DigColPs_ColRoughTurns_Cnt_M_s16	-1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_I2CHwColAngle_Cnt_M_u16	712
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	102
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	68
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	48
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253
DigColPs_SpurRoughTurns_Cnt_M_s16	-1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
k_SenseDetErrDiag_Cnt_str.Threshold	255
k_SenseDetErrDiag_Cnt_str.PStep	12
k_SenseDetErrDiag_Cnt_str.NStep	17
k_SenseParityErrDiag_Cnt_str.Threshold	150
k_SenseParityErrDiag_Cnt_str.PStep	31
k_SenseParityErrDiag_Cnt_str.NStep	16
k_StepDetect_Deg_f32	52

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	289.175781	289.1757813 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	150	150	✓
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	139	139	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	289.175781	289.1757813 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	18.2984314	18.2984375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145	145	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	12.7441406	12.74414063 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	68	68	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	5.9765625	5.9765625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-341.701569	-341.7015625 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	150	150	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	0			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.76			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	526			
DigColPs_ColRoughTurns_Cnt_M_s16	0			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	868			
DigColPs_I2CHwDataType_Cnt_M_u08	2			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	122			
DigColPs_I2CSensCommFlts_Cnt_M_u08	14			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	155			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	165			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	72			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	51			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.125			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625			
DigColPs_SpurRoughTurns_Cnt_M_s16	0			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	50			
k_SenseDetErrDiag_Cnt_str.PStep	14			
k_SenseDetErrDiag_Cnt_str.NStep	18			
k_SenseParityErrDiag_Cnt_str.Threshold	160			
k_SenseParityErrDiag_Cnt_str.PStep	33			
k_SenseParityErrDiag_Cnt_str.NStep	17			
k_StepDetect_Deg_f32	54			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	537.600037	537.6 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	509	509	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	177.600037	177.6 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	272.5	272.5 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-87.5	-87.5 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	608	608	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.8		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		286		
DigColPs_ColRoughTurns_Cnt_M_s16		1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		135		
DigColPs_I2CHwColAngle_Cnt_M_u16		1024		
DigColPs_I2CHwDataType_Cnt_M_u08		3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		142		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		165		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		76		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		54		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.135		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		412		
DigColPs_SpurRoughTurns_Cnt_M_s16		1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		125		
k_SenseDetErrDiag_Cnt_str.Threshold		10		
k_SenseDetErrDiag_Cnt_str.PStep		0		
k_SenseDetErrDiag_Cnt_str.NStep		19		
k_SenseParityErrDiag_Cnt_str.Threshold		170		
k_SenseParityErrDiag_Cnt_str.PStep		35		
k_SenseParityErrDiag_Cnt_str.NStep		18		
k_StepDetect_Deg_f32		56		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	827.601563	827.6015625 ± 0.00048828125		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170		
DigColPs_ColParityError_Cnt_M_lgc	0	0		
DigColPs_ColRoughTurns_Cnt_M_s16	2	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	116	116		

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	107.601563	107.6015625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	49.5017586	49.50175781 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165	165	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	14.5019531	14.50195313 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76	76	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	49.5017586	49.50175781 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	170	170	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106	106	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.21 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.84		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		245		
DigColPs_ColRoughTurns_Cnt_M_s16		2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		1180		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		162		
DigColPs_I2CSensCommFlts_Cnt_M_u08		16		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		175		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		185		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		80		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		57		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.145		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		532		
DigColPs_SpurRoughTurns_Cnt_M_s16		2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		165		
k_SenseDetErrDiag_Cnt_str.Threshold		20		
k_SenseDetErrDiag_Cnt_str.PStep		50		
k_SenseDetErrDiag_Cnt_str.NStep		20		
k_SenseParityErrDiag_Cnt_str.Threshold		180		
k_SenseParityErrDiag_Cnt_str.PStep		37		
k_SenseParityErrDiag_Cnt_str.NStep		19		
k_StepDetect_Deg_f32		58		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1128.11987	1128.119922 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		180	180	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		3	3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		10	10	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		48.119873	48.11992188 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		190.919525	190.9195313 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		2	2	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		175	175	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		15.3808594	15.38085938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		80	80	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		7.03125	7.03125 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		190.919525	190.9195313 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		180	180	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		145	145	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	3
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.88
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	863
DigColPs_ColRoughTurns_Cnt_M_s16	3
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_I2CHwColAngle_Cnt_M_u16	1336
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	182
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	185
DigColPs_PrevI2CHwColAngle_Deg_M_f32	195
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	84
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	60
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.155
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652
DigColPs_SpurRoughTurns_Cnt_M_s16	3
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
k_SenseDetErrDiag_Cnt_str.Threshold	30
k_SenseDetErrDiag_Cnt_str.PStep	25
k_SenseDetErrDiag_Cnt_str.NStep	21
k_SenseParityErrDiag_Cnt_str.Threshold	190
k_SenseParityErrDiag_Cnt_str.PStep	39
k_SenseParityErrDiag_Cnt_str.NStep	20
k_StepDetect_Deg_f32	60.8

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1449.50854	1449.508594 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	190	190	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	29	29	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	9.50854492	9.50859375 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	337.544342	337.5443359 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	185	185	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	16.2597656	16.25976563 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	84	84	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.3828125	7.3828125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	337.544342	337.5443359 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	190	190	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123	123	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	4			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	865			
DigColPs_ColRoughTurns_Cnt_M_s16	4			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101			
DigColPs_I2CHwColAngle_Cnt_M_u16	1492			
DigColPs_I2CHwDataType_Cnt_M_u08	1			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	202			
DigColPs_I2CSensCommFlts_Cnt_M_u08	18			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	205			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	63			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.165			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	351			
DigColPs_SpurRoughTurns_Cnt_M_s16	4			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124			
k_SenseDetErrDiag_Cnt_str.Threshold	40			
k_SenseDetErrDiag_Cnt_str.PStep	3			
k_SenseDetErrDiag_Cnt_str.NStep	0			
k_SenseParityErrDiag_Cnt_str.Threshold	200			
k_SenseParityErrDiag_Cnt_str.PStep	41			
k_SenseParityErrDiag_Cnt_str.NStep	21			
k_StepDetect_Deg_f32	62			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1791.76758	1791.767578 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	200	200	✓	
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101	101	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	351.767578	351.7675781 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	129.37616	129.3761719 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195	195	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	17.1386719	17.13867188 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88	88	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.734375	7.734375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	489.37616	489.3761719 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	200	200	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124	124	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.1		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		456		
DigColPs_ColRoughTurns_Cnt_M_s16		4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		144		
DigColPs_I2CHwColAngle_Cnt_M_u16		1648		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		222		
DigColPs_I2CSensCommFlts_Cnt_M_u08		19		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		205		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		215		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		92		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		66.6		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.175		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		256		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		143		
k_SenseDetErrDiag_Cnt_str.Threshold		50		
k_SenseDetErrDiag_Cnt_str.PStep		6		
k_SenseDetErrDiag_Cnt_str.NStep		50		
k_SenseParityErrDiag_Cnt_str.Threshold		210		
k_SenseParityErrDiag_Cnt_str.PStep		43		
k_SenseParityErrDiag_Cnt_str.NStep		22		
k_StepDetect_Deg_f32		64		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1621.80176	1621.801758 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		210	210	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		5	5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		94	94	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	181.801758	181.8017578 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	223.415039	223.4150391 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	205	205	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.0175781	18.01757813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	92	92	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.0859375	8.0859375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	583.415039	583.4150391 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	210	210	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	93	93	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.25 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		235		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		105		
DigColPs_I2CHwColAngle_Cnt_M_u16		1804		
DigColPs_I2CHwDataType_Cnt_M_u08		3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		242		
DigColPs_I2CSensCommFlts_Cnt_M_u08		20		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		215		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		225		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		96		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		69		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.185		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		321		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		131		
k_SenseDetErrDiag_Cnt_str.Threshold		60		
k_SenseDetErrDiag_Cnt_str.PStep		9		
k_SenseDetErrDiag_Cnt_str.NStep		25		
k_SenseParityErrDiag_Cnt_str.Threshold		220		
k_SenseParityErrDiag_Cnt_str.PStep		45		
k_SenseParityErrDiag_Cnt_str.NStep		23		
k_StepDetect_Deg_f32		66.6		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1147.7793	1147.779297 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		220	220	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		80	80	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		67.7792969	67.77929688 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		142.660919	142.6609375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		6	6	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		215	215	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		18.8964844	18.89648438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		96	96	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		8.4375	8.4375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		142.660919	142.6609375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		220	220	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		106	106	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		6	6	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.26 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	7
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1700
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	0
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	106
DigColPs_I2CHwColAngle_Cnt_M_u16	1960
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	262
DigColPs_I2CSensCommFlts_Cnt_M_u08	21
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	225
DigColPs_PrevI2CHwColAngle_Deg_M_f32	235
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	72
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	600
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.195
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	314
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	100
k_SenseDetErrDiag_Cnt_str.Threshold	70
k_SenseDetErrDiag_Cnt_str.PStep	12
k_SenseDetErrDiag_Cnt_str.NStep	2
k_SenseParityErrDiag_Cnt_str.Threshold	230
k_SenseParityErrDiag_Cnt_str.PStep	47
k_SenseParityErrDiag_Cnt_str.NStep	24
k_StepDetect_Deg_f32	68

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1508.06738	-1508.067383 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	47	47	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	104	104	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	291.932617	291.9326172 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	203.913879	203.9138672 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	225	225	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	19.7753906	19.77539063 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100	100	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.7890625	8.7890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	203.913879	203.9138672 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	230	230	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	98	98	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	8			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1600			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000			
DigColPs_ColRoughTurns_Cnt_M_s16	-3			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103			
DigColPs_I2CHwColAngle_Cnt_M_u16	2116			
DigColPs_I2CHwDataType_Cnt_M_u08	0			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	282			
DigColPs_I2CSensCommFlts_Cnt_M_u08	22			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	235			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	245			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	104			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	75			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.205			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	568			
DigColPs_SpurRoughTurns_Cnt_M_s16	-3			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	182			
k_SenseDetErrDiag_Cnt_str.Threshold	80			
k_SenseDetErrDiag_Cnt_str.PStep	15			
k_SenseDetErrDiag_Cnt_str.NStep	3			
k_SenseParityErrDiag_Cnt_str.Threshold	240			
k_SenseParityErrDiag_Cnt_str.PStep	49			
k_SenseParityErrDiag_Cnt_str.NStep	25			
k_StepDetect_Deg_f32	70			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1239.73828	-1239.738281 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100	100	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	200.261719	200.2617188 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	336.973846	336.9738281 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	235	235	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	20.6542969	20.65429688 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	104	104	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	9.140625	9.140625 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336.973846	336.9738281 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240	240	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	179	179	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.28 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.5		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		500		
DigColPs_ColRoughTurns_Cnt_M_s16		-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		151		
DigColPs_I2CHwColAngle_Cnt_M_u16		2272		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		302		
DigColPs_I2CSensCommFlts_Cnt_M_u08		23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		245		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		255		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		108		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		78		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.215		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		425		
DigColPs_SpurRoughTurns_Cnt_M_s16		-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		150		
k_SenseDetErrDiag_Cnt_str.Threshold		90		
k_SenseDetErrDiag_Cnt_str.PStep		18		
k_SenseDetErrDiag_Cnt_str.NStep		4		
k_SenseParityErrDiag_Cnt_str.Threshold		250		
k_SenseParityErrDiag_Cnt_str.PStep		0		
k_SenseParityErrDiag_Cnt_str.NStep		26		
k_StepDetect_Deg_f32		72.2		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-919.233398	-919.2333984 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		250	250	✔
DigColPs_ColParityError_Cnt_M_lgc		1	1	✔
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		147	147	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	160.766602	160.7666016 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	115.240814	115.2408203 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	245	245	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	21.5332031	21.53320313 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	108	108	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.4921875	9.4921875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	475.240814	475.2408203 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	250	250	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146	146	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.29 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		253		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		165		
DigColPs_I2CHwColAngle_Cnt_M_u16		2428		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		322		
DigColPs_I2CSensCommFlts_Cnt_M_u08		24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		255		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		265		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		112		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		81		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.225		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		965		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		100		
k_SenseDetErrDiag_Cnt_str.PStep		21		
k_SenseDetErrDiag_Cnt_str.NStep		5		
k_SenseParityErrDiag_Cnt_str.Threshold		260		
k_SenseParityErrDiag_Cnt_str.PStep		2		
k_SenseParityErrDiag_Cnt_str.NStep		27		
k_StepDetect_Deg_f32		74		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-546.552673	-546.5527344 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		255	255	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		0	0	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		160	160	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32		173.447327	173.4472656 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		258.714844	258.7148438 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		255	255	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		22.4121094	22.41210938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		112	112	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		9.84375	9.84375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		618.714844	618.7148438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		260	260	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	11
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	126
DigColPs_ColRoughTurns_Cnt_M_s16	0
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_I2CHwColAngle_Cnt_M_u16	2584
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412
DigColPs_SpurRoughTurns_Cnt_M_s16	0
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
k_SenseDetErrDiag_Cnt_str.Threshold	110
k_SenseDetErrDiag_Cnt_str.PStep	24
k_SenseDetErrDiag_Cnt_str.NStep	6
k_SenseParityErrDiag_Cnt_str.Threshold	270
k_SenseParityErrDiag_Cnt_str.PStep	4
k_SenseParityErrDiag_Cnt_str.NStep	28
k_StepDetect_Deg_f32	76

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-121.696289	-121.6962891 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	238.303711	238.3037109 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	47.395874	47.39589844 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	116	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	767.395874	767.3958984 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1200
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	142
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_I2CHwColAngle_Cnt_M_u16	2740
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	362
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	275
DigColPs_PrevI2CHwColAngle_Deg_M_f32	285
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	120
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	87.7
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.245
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	523
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155
k_SenseDetErrDiag_Cnt_str.Threshold	120
k_SenseDetErrDiag_Cnt_str.PStep	27
k_SenseDetErrDiag_Cnt_str.NStep	7
k_SenseParityErrDiag_Cnt_str.Threshold	280
k_SenseParityErrDiag_Cnt_str.PStep	6
k_SenseParityErrDiag_Cnt_str.NStep	29
k_StepDetect_Deg_f32	78

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	355.335938	355.3359375 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	148	148	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	178	178	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	355.335938	355.3359375 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	201.283997	201.2839844 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	275	275	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	24.1699219	24.16992188 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	120	120	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	10.546875	10.546875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	921.283997	921.2839844 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	280	280	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	148	148	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		13		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.9		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		563		
DigColPs_ColRoughTurns_Cnt_M_s16		2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		195		
DigColPs_I2CHwColAngle_Cnt_M_u16		2896		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		382		
DigColPs_I2CSensCommFlts_Cnt_M_u08		27		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		285		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		295		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		124		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		90		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.255		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		654		
DigColPs_SpurRoughTurns_Cnt_M_s16		2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		124		
k_SenseDetErrDiag_Cnt_str.Threshold		130		
k_SenseDetErrDiag_Cnt_str.PStep		30		
k_SenseDetErrDiag_Cnt_str.NStep		8		
k_SenseParityErrDiag_Cnt_str.Threshold		290		
k_SenseParityErrDiag_Cnt_str.PStep		8		
k_SenseParityErrDiag_Cnt_str.NStep		30		
k_StepDetect_Deg_f32		80		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	884.543945	884.5439453 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	290	290	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	187	187	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	164.543945	164.5439453 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.379150391	0.379101562 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	285	285	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.0488281	25.04882813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	124	124	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.8984375	10.8984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataTyp_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.37915	1080.379102 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	290	290	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	116	116	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.33 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		14		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		1		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		856		
DigColPs_ColRoughTurns_Cnt_M_s16		3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		142		
DigColPs_I2CHwColAngle_Cnt_M_u16		3052		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		402		
DigColPs_I2CSensCommFlts_Cnt_M_u08		28		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		295		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		305		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		128		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		93		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.265		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		258		
DigColPs_SpurRoughTurns_Cnt_M_s16		3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		128		
k_SenseDetErrDiag_Cnt_str.Threshold		140		
k_SenseDetErrDiag_Cnt_str.PStep		33		
k_SenseDetErrDiag_Cnt_str.NStep		9		
k_SenseParityErrDiag_Cnt_str.Threshold		300		
k_SenseParityErrDiag_Cnt_str.PStep		10		
k_SenseParityErrDiag_Cnt_str.NStep		31		
k_StepDetect_Deg_f32		82		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1465.92773	1465.927734 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		300	300	✓
DigColPs_ColParityError_Cnt_M_lgc		1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16		4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		133	133	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		25.9277344	25.92773438 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		164.681274	164.68125 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		14	14	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		295	295	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		25.9277344	25.92773438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		128	128	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		11.25	11.25 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1244.68127	1244.68125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		268	268	✓
DigColPs_SpurParityError_Cnt_M_lgc		1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		119	119	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		14	14	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.224		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	146		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	3208		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	422		
DigColPs_I2CSensCommFlts_Cnt_M_u08	29		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	305		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	315		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	132		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	96		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.275		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	150		
k_SenseDetErrDiag_Cnt_str.PStep	36		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	310		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	32		
k_StepDetect_Deg_f32	84		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-289.195313	-289.1953125 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	158	158	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	70.8046875	70.8046875 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	73.1904297	73.19042969 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	15	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	305	305	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	26.8066406	26.80664063 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	132	132	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.6015625	11.6015625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1513.19043	1513.19043 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	12	12	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.35 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	16
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-800
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.226
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	756
DigColPs_ColRoughTurns_Cnt_M_s16	4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	163
DigColPs_I2CHwColAngle_Cnt_M_u16	3364
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	442
DigColPs_I2CSensCommFlts_Cnt_M_u08	30
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	315
DigColPs_PrevI2CHwColAngle_Deg_M_f32	325
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	136
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	99.1
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.285
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000
DigColPs_SpurRoughTurns_Cnt_M_s16	4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146
k_SenseDetErrDiag_Cnt_str.Threshold	160
k_SenseDetErrDiag_Cnt_str.PStep	39
k_SenseDetErrDiag_Cnt_str.NStep	11
k_SenseParityErrDiag_Cnt_str.Threshold	320
k_SenseParityErrDiag_Cnt_str.PStep	14
k_SenseParityErrDiag_Cnt_str.NStep	33
k_StepDetect_Deg_f32	86

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-206.143066	-206.1430664 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	320	320	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152	152	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	153.856934	153.8569336 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	148.906616	148.9066406 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	315	315	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	27.6855469	27.68554688 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	136	136	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	11.953125	11.953125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1588.90662	1588.906641 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	320	320	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.36 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	0			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-700			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.228			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	964			
DigColPs_ColRoughTurns_Cnt_M_s16	-4			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	3520			
DigColPs_I2CHwDataType_Cnt_M_u08	4			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	462			
DigColPs_I2CSensCommFlts_Cnt_M_u08	0			
DigColPs_Prevl2CHwColAngle_Cnt_M_u16	325			
DigColPs_Prevl2CHwColAngle_Deg_M_f32	335			
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	140			
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	102			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.295			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	501			
DigColPs_SpurRoughTurns_Cnt_M_s16	-4			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	170			
k_SenseDetErrDiag_Cnt_str.PStep	42			
k_SenseDetErrDiag_Cnt_str.NStep	12			
k_SenseParityErrDiag_Cnt_str.Threshold	330			
k_SenseParityErrDiag_Cnt_str.PStep	16			
k_SenseParityErrDiag_Cnt_str.NStep	34			
k_StepDetect_Deg_f32	88.5			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-786.640015	-786.64 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	930	930	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	293.359985	293.36 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	89.4000244	89.4 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	809.400024	809.4 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	467	467	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.37 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.23		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		746		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		158		
DigColPs_I2CHwColAngle_Cnt_M_u16		3676		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		482		
DigColPs_I2CSensCommFlts_Cnt_M_u08		1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		345		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		144		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		105		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.305		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		235		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		180		
k_SenseDetErrDiag_Cnt_str.PStep		45		
k_SenseDetErrDiag_Cnt_str.NStep		13		
k_SenseParityErrDiag_Cnt_str.Threshold		340		
k_SenseParityErrDiag_Cnt_str.PStep		18		
k_SenseParityErrDiag_Cnt_str.NStep		35		
k_StepDetect_Deg_f32		90		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-710.400024	-710.4 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		340	340	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		145	145	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		9.59997559	9.6 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		135.960144	135.9601563 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		0	0	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0	0 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		144	144	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		12.65625	12.65625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		855.960144	855.9601563 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		253	253	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		133	133	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	2
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.232
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	865
DigColPs_ColRoughTurns_Cnt_M_s16	-3
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_I2CHwColAngle_Cnt_M_u16	3832
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	502
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095
DigColPs_PrevI2CHwColAngle_Deg_M_f32	355
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	148
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	108
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.315
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145
DigColPs_SpurRoughTurns_Cnt_M_s16	-3
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158
k_SenseDetErrDiag_Cnt_str.Threshold	190
k_SenseDetErrDiag_Cnt_str.PStep	48
k_SenseDetErrDiag_Cnt_str.NStep	14
k_SenseParityErrDiag_Cnt_str.Threshold	350
k_SenseParityErrDiag_Cnt_str.PStep	20
k_SenseParityErrDiag_Cnt_str.NStep	36
k_StepDetect_Deg_f32	92

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-551.060364	-551.0603906 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	350	350	✓
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	111	111	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	168.939636	168.9396094 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	137.982468	137.9824609 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	148	148	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	13.0078125	13.0078125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-222.017532	-222.0175391 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	165	165	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144	144	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.39 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	3			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-400			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.234			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	523			
DigColPs_ColRoughTurns_Cnt_M_s16	-2			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165			
DigColPs_I2CHwColAngle_Cnt_M_u16	3988			
DigColPs_I2CHwDataType_Cnt_M_u08	2			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	522			
DigColPs_I2CSensCommFlts_Cnt_M_u08	3			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2047			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	152			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	111			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	10			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.325			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	568			
DigColPs_SpurRoughTurns_Cnt_M_s16	-2			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186			
k_SenseDetErrDiag_Cnt_str.Threshold	200			
k_SenseDetErrDiag_Cnt_str.PStep	0			
k_SenseDetErrDiag_Cnt_str.NStep	15			
k_SenseParityErrDiag_Cnt_str.Threshold	360			
k_SenseParityErrDiag_Cnt_str.PStep	22			
k_SenseParityErrDiag_Cnt_str.NStep	37			
k_StepDetect_Deg_f32	94			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-517.020569	-517.0205664 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	360	360	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150	150	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	202.979431	202.9794336 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	254.091797	254.0917969 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2047	2047	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	179.912109	179.9121094 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	152	152	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	13.359375	13.359375 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-105.908195	-105.9082031 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	360	360	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	171	171	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.236		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		145		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		1		
DigColPs_I2CHwDataType_Cnt_M_u08		3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		542		
DigColPs_I2CSensCommFlts_Cnt_M_u08		4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		7		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		114		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		20		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.335		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		624		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		152		
k_SenseDetErrDiag_Cnt_str.Threshold		210		
k_SenseDetErrDiag_Cnt_str.PStep		1		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		370		
k_SenseParityErrDiag_Cnt_str.PStep		24		
k_SenseParityErrDiag_Cnt_str.NStep		38		
k_StepDetect_Deg_f32		96		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-314.014801	-314.0148047 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	169	169	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	45.985199	45.98519531 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	13.2999992	13.3 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	7	7	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.615234375	0.615234375 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	13.2999992	13.3 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	370	370	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	136	136	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.41 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetData()	5			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.238			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	235			
DigColPs_ColRoughTurns_Cnt_M_s16	0			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255			
DigColPs_I2CHwColAngle_Cnt_M_u16	8			
DigColPs_I2CHwDataType_Cnt_M_u08	4			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	562			
DigColPs_I2CSensCommFlts_Cnt_M_u08	5			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	49			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	117			
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	30			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.345			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	354			
DigColPs_SpurRoughTurns_Cnt_M_s16	0			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175			
k_SenseDetErrDiag_Cnt_str.Threshold	220			
k_SenseDetErrDiag_Cnt_str.PStep	2			
k_SenseDetErrDiag_Cnt_str.NStep	17			
k_SenseParityErrDiag_Cnt_str.Threshold	380			
k_SenseParityErrDiag_Cnt_str.PStep	26			
k_SenseParityErrDiag_Cnt_str.NStep	39			
k_StepDetect_Deg_f32	98			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-151.375015	-151.3750195 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	261	261	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	238	238	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	208.624985	208.6249805 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	19.6196785	19.61967773 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	49	49	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	4.30664063	4.306640625 ± 0.0001220703125	✓	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	✓	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	19.6196785	19.61967773 ± 0.00048828125	✓	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	380	380	✓	
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓	
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158	158	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	6
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	256
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_I2CHwColAngle_Cnt_M_u16	15
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	582
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	91
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.7
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	120
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	40
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.355
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	536
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
k_SenseDetErrDiag_Cnt_str.Threshold	230
k_SenseDetErrDiag_Cnt_str.PStep	3
k_SenseDetErrDiag_Cnt_str.NStep	18
k_SenseParityErrDiag_Cnt_str.Threshold	390
k_SenseParityErrDiag_Cnt_str.PStep	28
k_SenseParityErrDiag_Cnt_str.NStep	40
k_StepDetect_Deg_f32	100

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	12.3195267	12.31953125 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	284	284	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	138	138	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	12.3195267	12.31953125 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	217.468796	217.4687988 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	91	91	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	7.99804688	7.998046875 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047	2047	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	179.912109	179.9121094 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	217.468796	217.4687988 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	7
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.242
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	142
DigColPs_ColRoughTurns_Cnt_M_s16	-5
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131
DigColPs_I2CHwColAngle_Cnt_M_u16	22
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	602
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133
DigColPs_PrevI2CHwColAngle_Deg_M_f32	20
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	123
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	50
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.365
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
k_SenseDetErrDiag_Cnt_str.Threshold	240
k_SenseDetErrDiag_Cnt_str.PStep	4
k_SenseDetErrDiag_Cnt_str.NStep	19
k_SenseParityErrDiag_Cnt_str.Threshold	400
k_SenseParityErrDiag_Cnt_str.PStep	30
k_SenseParityErrDiag_Cnt_str.NStep	41
k_StepDetect_Deg_f32	102

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-432.771149	-432.7711523 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	172	172	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	112	112	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	287.228851	287.2288477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.757995605	0.758007813 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133	133	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	11.6894531	11.68945313 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	100	100	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	8.7890625	8.7890625 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-359.242004	-359.2419922 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	400	400	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123	123	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.44 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.244		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		253		
DigColPs_ColRoughTurns_Cnt_M_s16		5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		100		
DigColPs_I2CHwColAngle_Cnt_M_u16		29		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		622		
DigColPs_I2CSensCommFlts_Cnt_M_u08		8		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		175		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		25.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		126		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		60		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.375		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		586		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		186		
k_SenseDetErrDiag_Cnt_str.Threshold		250		
k_SenseDetErrDiag_Cnt_str.PStep		5		
k_SenseDetErrDiag_Cnt_str.NStep		20		
k_SenseParityErrDiag_Cnt_str.Threshold		410		
k_SenseParityErrDiag_Cnt_str.PStep		32		
k_SenseParityErrDiag_Cnt_str.NStep		42		
k_StepDetect_Deg_f32		104		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		518.552979	518.5529297 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16		285	285	
DigColPs_ColParityError_Cnt_M_lgc		0	0	
DigColPs_ColRoughTurns_Cnt_M_s16		5	5	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16		80	80	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	158.552979	158.5529297 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.091797	359.0917969 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	175	175	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.3808594	15.38085938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	200	200	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	17.578125	17.578125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	719.091797	719.0917969 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	410	410	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	166	166	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.45 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		625		
DigColPs_ColRoughTurns_Cnt_M_s16		0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		182		
DigColPs_I2CHwColAngle_Cnt_M_u16		2140		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		2924		
DigColPs_I2CSensCommFlts_Cnt_M_u08		8		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		65		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		75		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		36		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		24.4		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1000		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.44		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		412		
DigColPs_SpurRoughTurns_Cnt_M_s16		0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		184		
k_SenseDetErrDiag_Cnt_str.Threshold		16		
k_SenseDetErrDiag_Cnt_str.PStep		40		
k_SenseDetErrDiag_Cnt_str.NStep		9		
k_SenseParityErrDiag_Cnt_str.Threshold		70		
k_SenseParityErrDiag_Cnt_str.PStep		15		
k_SenseParityErrDiag_Cnt_str.NStep		8		
k_StepDetect_Deg_f32		36.3		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		266.285156	266.2851563 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		70	70	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		173	173	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		266.285156	266.2851563 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		161.392212	161.3921875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		8	8	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		65	65	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		5.71289063	5.712890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		36	36	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		3.1640625	3.1640625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-558.607788	-558.6078125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		70	70	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		175	175	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150		
DigColPs_I2CHwColAngle_Cnt_M_u16	1024		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	142		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	54		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.135		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	170		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	18		
k_StepDetect_Deg_f32	56		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1403.60156	1403.601563 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131	131	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	323.601563	323.6015625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	195.301773	195.3017578 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165	165	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	14.5019531	14.50195313 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76	76	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	195.301773	195.3017578 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	170	170	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	167	167	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.47 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	9			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.246			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	532			
DigColPs_ColRoughTurns_Cnt_M_s16	-3			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141			
DigColPs_I2CHwColAngle_Cnt_M_u16	36			
DigColPs_I2CHwDataType_Cnt_M_u08	3			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	642			
DigColPs_I2CSensCommFlts_Cnt_M_u08	9			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	217			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	30			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	300			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	129			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	70			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.385			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	286			
DigColPs_SpurRoughTurns_Cnt_M_s16	-4			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156			
k_SenseDetErrDiag_Cnt_str.Threshold	12			
k_SenseDetErrDiag_Cnt_str.PStep	6			
k_SenseDetErrDiag_Cnt_str.NStep	21			
k_SenseParityErrDiag_Cnt_str.Threshold	420			
k_SenseParityErrDiag_Cnt_str.PStep	34			
k_SenseParityErrDiag_Cnt_str.NStep	43			
k_StepDetect_Deg_f32	106			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-110.188232	-110.1882227 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	420	420	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	249.811768	249.8117773 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.801392	218.8013672 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	217	217	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	19.0722656	19.07226563 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	300	300	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	26.3671875	26.3671875 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-501.198608	-501.1986328 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	320	320	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.48 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.248		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		652		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		142		
DigColPs_I2CHwColAngle_Cnt_M_u16		43		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		662		
DigColPs_I2CSensCommFlts_Cnt_M_u08		10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		259		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		35.2		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		132		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		80		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.395		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		253		
DigColPs_SpurRoughTurns_Cnt_M_s16		-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		134		
k_SenseDetErrDiag_Cnt_str.Threshold		14		
k_SenseDetErrDiag_Cnt_str.PStep		7		
k_SenseDetErrDiag_Cnt_str.NStep		22		
k_SenseParityErrDiag_Cnt_str.Threshold		430		
k_SenseParityErrDiag_Cnt_str.PStep		36		
k_SenseParityErrDiag_Cnt_str.NStep		44		
k_StepDetect_Deg_f32		108		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		141.965393	141.9653906 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		430	430	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		120	120	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	141.965393	141.9653906 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.08667	298.0867188 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	259	259	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	22.7636719	22.76367188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	400	400	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	35.15625	35.15625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1501.91333	-1501.913281 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	289	289	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	112	112	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.49 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetData()	11
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	400
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.25
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	351
DigColPs_ColRoughTurns_Cnt_M_s16	-3
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	682
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	301
DigColPs_PrevI2CHwColAngle_Deg_M_f32	40
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	500
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	135
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	90
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.405
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	236
DigColPs_SpurRoughTurns_Cnt_M_s16	11
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
k_SenseDetErrDiag_Cnt_str.Threshold	16
k_SenseDetErrDiag_Cnt_str.PStep	8
k_SenseDetErrDiag_Cnt_str.NStep	23
k_SenseParityErrDiag_Cnt_str.Threshold	440
k_SenseParityErrDiag_Cnt_str.PStep	38
k_SenseParityErrDiag_Cnt_str.NStep	45
k_StepDetect_Deg_f32	110

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	36.6137695	36.61376953 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	389	389	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	36.6137695	36.61376953 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	235.147827	235.1478516 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	301	301	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	26.4550781	26.45507813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	500	500	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	43.9453125	43.9453125 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1675.14783	1675.147852 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	274	274	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

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DigColPs_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	13		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124		
DigColPs_I2CHwColAngle_Cnt_M_u16	244		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	39		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.74		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	452		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30		
k_SenseDetErrDiag_Cnt_str.Threshold	18		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	120		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	13		
k_StepDetect_Deg_f32	46.5		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	70.0644531	70.06445313 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	120	120	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	110	110	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	70.0644531	70.06445313 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	233.642181	233.6421875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115	115	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.1074219	10.10742188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56	56	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.921875	4.921875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-126.357819	-126.3578125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120	120	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.51 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	6
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	321
DigColPs_ColRoughTurns_Cnt_M_s16	-1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128
DigColPs_I2CHwColAngle_Cnt_M_u16	1804
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	242
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215
DigColPs_PrevI2CHwColAngle_Deg_M_f32	225
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	69.2
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.185
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	635
DigColPs_SpurRoughTurns_Cnt_M_s16	3
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50
k_SenseDetErrDiag_Cnt_str.Threshold	20
k_SenseDetErrDiag_Cnt_str.PStep	9
k_SenseDetErrDiag_Cnt_str.NStep	25
k_SenseParityErrDiag_Cnt_str.Threshold	220
k_SenseParityErrDiag_Cnt_str.PStep	45
k_SenseParityErrDiag_Cnt_str.NStep	23
k_StepDetect_Deg_f32	66

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1363.7793	1363.779297 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	220	220	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103	103	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	283.779297	283.7792969 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	248.860962	248.8609375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215	215	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.8964844	18.89648438 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96	96	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.4375	8.4375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	608.860962	608.8609375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220	220	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	25	25	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.52 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		314		
DigColPs_ColRoughTurns_Cnt_M_s16		0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		57		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		702		
DigColPs_I2CSensCommFlts_Cnt_M_u08		12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		343		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		45.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		138		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.415		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		241		
DigColPs_SpurRoughTurns_Cnt_M_s16		-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		22		
k_SenseDetErrDiag_Cnt_str.PStep		9		
k_SenseDetErrDiag_Cnt_str.NStep		24		
k_SenseParityErrDiag_Cnt_str.Threshold		450		
k_SenseParityErrDiag_Cnt_str.PStep		40		
k_SenseParityErrDiag_Cnt_str.NStep		46		
k_StepDetect_Deg_f32		112		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		381.596924	381.5969141 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		354	354	✓
DigColPs_ColParityError_Cnt_M_lgc		1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16		0	0	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	21.5969238	21.59691406 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	352.184784	352.1847656 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	343	343	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	30.1464844	30.14648438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	600	600	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	52.734375	52.734375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-367.815216	-367.8152344 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	281	281	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.53 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetData()	13
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	568
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_I2CHwColAngle_Cnt_M_u16	64
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	722
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	385
DigColPs_PrevI2CHwColAngle_Deg_M_f32	50
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	700
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	141
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	110
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.425
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	523
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
k_SenseDetErrDiag_Cnt_str.Threshold	24
k_SenseDetErrDiag_Cnt_str.PStep	10
k_SenseDetErrDiag_Cnt_str.NStep	25
k_SenseParityErrDiag_Cnt_str.Threshold	460
k_SenseParityErrDiag_Cnt_str.PStep	42
k_SenseParityErrDiag_Cnt_str.NStep	47
k_StepDetect_Deg_f32	114

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	460	460	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	121	121	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	242.397461	242.3974609 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	385	385	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	33.8378906	33.83789063 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	700	700	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	61.5234375	61.5234375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	242.397461	242.3974609 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	460	460	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	119	119	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

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DigColPs_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	14		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	425		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	71		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	742		
DigColPs_I2CSensCommFlts_Cnt_M_u08	14		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	427		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	55		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	144		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	120		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.435		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	365		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	26		
k_SenseDetErrDiag_Cnt_str.PStep	11		
k_SenseDetErrDiag_Cnt_str.NStep	26		
k_SenseParityErrDiag_Cnt_str.Threshold	470		
k_SenseParityErrDiag_Cnt_str.PStep	44		
k_SenseParityErrDiag_Cnt_str.NStep	48		
k_StepDetect_Deg_f32	116		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	757.529297	757.5292969 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	469	469	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	37.5292969	37.52929688 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	51.5859375	51.5859375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	14	14	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	427	427	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	37.5292969	37.52929688 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	800	800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	70.3125	70.3125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	411.585938	411.5859375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	409	409	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.55 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	15			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.258			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	965			
DigColPs_ColRoughTurns_Cnt_M_s16	3			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146			
DigColPs_I2CHwColAngle_Cnt_M_u16	78			
DigColPs_I2CHwDataType_Cnt_M_u08	4			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	762			
DigColPs_I2CSensCommFlts_Cnt_M_u08	15			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	469			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	60			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	900			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	147			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	130			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.445			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256			
DigColPs_SpurRoughTurns_Cnt_M_s16	3			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106			
k_SenseDetErrDiag_Cnt_str.Threshold	28			
k_SenseDetErrDiag_Cnt_str.PStep	12			
k_SenseDetErrDiag_Cnt_str.NStep	27			
k_SenseParityErrDiag_Cnt_str.Threshold	480			
k_SenseParityErrDiag_Cnt_str.PStep	46			
k_SenseParityErrDiag_Cnt_str.NStep	49			
k_StepDetect_Deg_f32	118			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	957.074951	957.0749414 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	480	480	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	119	119	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	237.074951	237.0749414 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	227.950195	227.9501953 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	15	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	469	469	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	41.2207031	41.22070313 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	900	900	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	79.1015625	79.1015625 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	587.950195	587.9501953 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	302	302	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	79	79	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	15	15	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.56 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.26		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		412		
DigColPs_ColRoughTurns_Cnt_M_s16		4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		85		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		782		
DigColPs_I2CSensCommFlts_Cnt_M_u08		16		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		511		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		65		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		150		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		140		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.455		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		365		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		30		
k_SenseDetErrDiag_Cnt_str.PStep		13		
k_SenseDetErrDiag_Cnt_str.NStep		28		
k_SenseParityErrDiag_Cnt_str.Threshold		490		
k_SenseParityErrDiag_Cnt_str.PStep		48		
k_SenseParityErrDiag_Cnt_str.NStep		50		
k_StepDetect_Deg_f32		120		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-945.922913	-945.9228516 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		460	460	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	134.077087	134.0771484 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	51.4902344	51.49023438 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	511	511	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	44.9121094	44.91210938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1000	1000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	87.890625	87.890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	771.490234	771.4902344 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	413	413	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.57 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		2160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.262		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		523		
DigColPs_ColRoughTurns_Cnt_M_s16		4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		92		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		802		
DigColPs_I2CSensCommFlts_Cnt_M_u08		17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		553		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		70		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1100		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		153		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		150		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.465		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		251		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		151		
k_SenseDetErrDiag_Cnt_str.Threshold		32		
k_SenseDetErrDiag_Cnt_str.PStep		14		
k_SenseDetErrDiag_Cnt_str.NStep		29		
k_SenseParityErrDiag_Cnt_str.Threshold		500		
k_SenseParityErrDiag_Cnt_str.PStep		1		
k_SenseParityErrDiag_Cnt_str.NStep		1		
k_StepDetect_Deg_f32		122		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1984.09412	1984.094121 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		500	500	✓
DigColPs_ColParityError_Cnt_M_lgc		1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16		4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		157	157	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		184.094116	184.0941211 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		74.8060303	74.80605469 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		553	553	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		48.6035156	48.60351563 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1100	1100	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		96.6796875	96.6796875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		794.80603	794.8060547 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		252	252	✓
DigColPs_SpurParityError_Cnt_M_lgc		1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		122	122	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.58 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	654		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152		
DigColPs_I2CHwColAngle_Cnt_M_u16	99		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	822		
DigColPs_I2CSensCommFlts_Cnt_M_u08	18		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	595		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	156		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	160		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.475		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	362		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	34		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	30		
k_SenseParityErrDiag_Cnt_str.Threshold	510		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	2		
k_StepDetect_Deg_f32	124		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900	900 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	510	510	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	122	122	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	180	180 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	170.097656	170.0976563 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	595	595	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	52.2949219	52.29492188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1200	1200	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	105.46875	105.46875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-549.902344	-549.9023438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	364	364	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.59 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	2			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	126			
DigColPs_ColRoughTurns_Cnt_M_s16	0			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175			
DigColPs_I2CHwColAngle_Cnt_M_u16	2584			
DigColPs_I2CHwDataType_Cnt_M_u08	3			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342			
DigColPs_I2CSensCommFlts_Cnt_M_u08	25			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412			
DigColPs_SpurRoughTurns_Cnt_M_s16	0			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255			
k_SenseDetErrDiag_Cnt_str.Threshold	110			
k_SenseDetErrDiag_Cnt_str.PStep	24			
k_SenseDetErrDiag_Cnt_str.NStep	6			
k_SenseParityErrDiag_Cnt_str.Threshold	270			
k_SenseParityErrDiag_Cnt_str.PStep	4			
k_SenseParityErrDiag_Cnt_str.NStep	28			
k_StepDetect_Deg_f32	76			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	268.303711	268.3037109 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	268.303711	268.3037109 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	2.39589834	2.395898438 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	116	116	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2.39589834	2.395898438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.60 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.5		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		632		
DigColPs_ColRoughTurns_Cnt_M_s16		-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		113		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		862		
DigColPs_I2CSensCommFlts_Cnt_M_u08		20		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		679		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		85		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		162		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		180		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.495		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		624		
DigColPs_SpurRoughTurns_Cnt_M_s16		-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		185		
k_SenseDetErrDiag_Cnt_str.Threshold		38		
k_SenseDetErrDiag_Cnt_str.PStep		17		
k_SenseDetErrDiag_Cnt_str.NStep		32		
k_SenseParityErrDiag_Cnt_str.Threshold		530		
k_SenseParityErrDiag_Cnt_str.PStep		4		
k_SenseParityErrDiag_Cnt_str.NStep		4		
k_StepDetect_Deg_f32		128		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-960.161133	-960.1611328 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		530	530	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		-3	-3	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		154	154	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	119.838867	119.8388672 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	337.208191	337.2082031 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	679	679	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	59.6777344	59.67773438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1400	1400	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	123.046875	123.046875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-382.791809	-382.7917969 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	530	530	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	153	153	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.61 (Repeat Count = 1)				✓
Name	Input Value			
DigColPsInt_GetData()	4			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	412			
DigColPs_ColRoughTurns_Cnt_M_s16	-2			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	120			
DigColPs_I2CHwDataType_Cnt_M_u08	0			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	882			
DigColPs_I2CSensCommFlts_Cnt_M_u08	21			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	721			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	90			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1500			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	165			
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	125			
DigColPs_SpurRoughTurns_Cnt_M_s16	-2			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	40			
k_SenseDetErrDiag_Cnt_str.PStep	18			
k_SenseDetErrDiag_Cnt_str.NStep	33			
k_SenseParityErrDiag_Cnt_str.Threshold	540			
k_SenseParityErrDiag_Cnt_str.PStep	5			
k_SenseParityErrDiag_Cnt_str.NStep	5			
k_StepDetect_Deg_f32	130.9			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	588.673828	588.6738281 ± 0.00048828125		✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0		✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	417	417		✓
DigColPs_ColParityError_Cnt_M_lgc	0	0		✓
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2		✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0		✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0		✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0		✓
DigColPs_I2CHwColAngle_Deg_M_f32	228.673828	228.6738281 ± 0.0001220703125		✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125		✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4		✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0		✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	721	721		✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	63.3691406	63.36914063 ± 0.0001220703125		✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1500	1500		✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	131.835938	131.8359375 ± 0.0001220703125		✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0	0		✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125		✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1		✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130	130		✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0		✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2		✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0		✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0		✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0		✓

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DigColPs_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.62 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.25		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	127		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	902		
DigColPs_I2CSensCommFlts_Cnt_M_u08	22		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	763		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	95		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	168.5		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	42		
k_SenseDetErrDiag_Cnt_str.PStep	19		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	550		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	6		
k_StepDetect_Deg_f32	132		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	676.765137	676.7651367 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	262	262	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	316.765137	316.7651367 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	140.625	140.625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	763	763	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	67.0605469	67.06054688 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1600	1600	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	140.625	140.625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-219.375	-219.375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	259	259	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.63 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.3		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		235		
DigColPs_ColRoughTurns_Cnt_M_s16		0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		184		
DigColPs_I2CHwColAngle_Cnt_M_u16		134		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		922		
DigColPs_I2CSensCommFlts_Cnt_M_u08		23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		805		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		100.9		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		171		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		625		
DigColPs_SpurRoughTurns_Cnt_M_s16		0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		152		
k_SenseDetErrDiag_Cnt_str.Threshold		44		
k_SenseDetErrDiag_Cnt_str.PStep		20		
k_SenseDetErrDiag_Cnt_str.NStep		35		
k_SenseParityErrDiag_Cnt_str.Threshold		560		
k_SenseParityErrDiag_Cnt_str.PStep		7		
k_SenseParityErrDiag_Cnt_str.NStep		7		
k_StepDetect_Deg_f32		134		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		791.225586	791.2255859 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		242	242	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		0	0	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		149	149	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✔
DigColPs_I2CHwColAngle_Deg_M_f32		71.2255859	71.22558594 ± 0.0001220703125	✔
DigColPs_I2CHwSpurAngle_Deg_M_f32		164.707031	164.7070313 ± 0.0001220703125	✔
DigColPs_I2CSensCommFlts_Cnt_M_u08		6	6	✔
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✔
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		805	805	✔
DigColPs_PrevI2CHwColAngle_Deg_M_f32		70.7519531	70.75195313 ± 0.0001220703125	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	1700	1700	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	149.414063	149.4140625 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	524.707031	524.7070313 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	560	560	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	117	117	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.64 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		253		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		165		
DigColPs_I2CHwColAngle_Cnt_M_u16		2428		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		322		
DigColPs_I2CSensCommFlts_Cnt_M_u08		24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		255		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		265		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		112		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		81		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-3960		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.225		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		965		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		100		
k_SenseDetErrDiag_Cnt_str.PStep		21		
k_SenseDetErrDiag_Cnt_str.NStep		5		
k_SenseParityErrDiag_Cnt_str.Threshold		260		
k_SenseParityErrDiag_Cnt_str.PStep		2		
k_SenseParityErrDiag_Cnt_str.NStep		27		
k_StepDetect_Deg_f32		74		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-546.552673	-546.5527344 ± 0.00048828125		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	255	255		
DigColPs_ColParityError_Cnt_M_lgc	0	0		
DigColPs_ColRoughTurns_Cnt_M_s16	0	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	160	160		

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	173.447327	173.4472656 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	92.2148438	92.21484375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	255	255	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	22.4121094	22.41210938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112	112	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.84375	9.84375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3147.78516	-3147.785156 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	260	260	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.65 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		568		
DigColPs_ColRoughTurns_Cnt_M_s16		2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		148		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		962		
DigColPs_I2CSensCommFlts_Cnt_M_u08		25		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		889		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		110		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1900		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		177		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		4320		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.545		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		241		
DigColPs_SpurRoughTurns_Cnt_M_s16		2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		48		
k_SenseDetErrDiag_Cnt_str.PStep		22		
k_SenseDetErrDiag_Cnt_str.NStep		37		
k_SenseParityErrDiag_Cnt_str.Threshold		580		
k_SenseParityErrDiag_Cnt_str.PStep		9		
k_SenseParityErrDiag_Cnt_str.NStep		9		
k_StepDetect_Deg_f32		138		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1099.25391	1099.253906 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		577	577	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		2	2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32		19.2539063	19.25390625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		289.010742	289.0107422 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		8	8	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		889	889	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		78.1347656	78.13476563 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1900	1900	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		166.992188	166.9921875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		2449.01074	2449.010742 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		250	250	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		0	0	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.66 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	9
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	3
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	134
DigColPs_I2CHwColAngle_Cnt_M_u16	155
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	982
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	931
DigColPs_PrevI2CHwColAngle_Deg_M_f32	115
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145
DigColPs_SpurRoughTurns_Cnt_M_s16	3
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158
k_SenseDetErrDiag_Cnt_str.Threshold	50
k_SenseDetErrDiag_Cnt_str.PStep	23
k_SenseDetErrDiag_Cnt_str.NStep	38
k_SenseParityErrDiag_Cnt_str.Threshold	590
k_SenseParityErrDiag_Cnt_str.PStep	10
k_SenseParityErrDiag_Cnt_str.NStep	10
k_StepDetect_Deg_f32	140

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1292.82178	1292.821777 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	590	590	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	96	96	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	212.821777	212.8217773 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	180	180 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	931	931	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	81.8261719	81.82617188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2000	2000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	175.78125	175.78125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900	900 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	155	155	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	120	120	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.67 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	8			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	126			
DigColPs_ColRoughTurns_Cnt_M_s16	0			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175			
DigColPs_I2CHwColAngle_Cnt_M_u16	2584			
DigColPs_I2CHwDataType_Cnt_M_u08	3			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342			
DigColPs_I2CSensCommFlts_Cnt_M_u08	25			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412			
DigColPs_SpurRoughTurns_Cnt_M_s16	0			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255			
k_SenseDetErrDiag_Cnt_str.Threshold	110			
k_SenseDetErrDiag_Cnt_str.PStep	24			
k_SenseDetErrDiag_Cnt_str.NStep	6			
k_SenseParityErrDiag_Cnt_str.Threshold	270			
k_SenseParityErrDiag_Cnt_str.PStep	4			
k_SenseParityErrDiag_Cnt_str.NStep	28			
k_StepDetect_Deg_f32	76			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-121.696289	-121.6962891 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	238.303711	238.3037109 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	2.39589834	2.395898438 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	116	116	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2.39589834	2.395898438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.68 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.55		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		536		
DigColPs_ColRoughTurns_Cnt_M_s16		4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		184		
DigColPs_I2CHwColAngle_Cnt_M_u16		169		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1022		
DigColPs_I2CSensCommFlts_Cnt_M_u08		28		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1015		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		2200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		186		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		625		
DigColPs_SpurRoughTurns_Cnt_M_s16		4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		165		
k_SenseDetErrDiag_Cnt_str.Threshold		54		
k_SenseDetErrDiag_Cnt_str.PStep		25		
k_SenseDetErrDiag_Cnt_str.NStep		40		
k_SenseParityErrDiag_Cnt_str.Threshold		610		
k_SenseParityErrDiag_Cnt_str.PStep		12		
k_SenseParityErrDiag_Cnt_str.NStep		12		
k_StepDetect_Deg_f32		144		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	144	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200	2200	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	193.359375	193.359375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	366.679688	366.6796875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.69 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		563		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		176		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1042		
DigColPs_I2CSensCommFlts_Cnt_M_u08		0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1057		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		130.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		2300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		189		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		55		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.152		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		652		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		144		
k_SenseDetErrDiag_Cnt_str.Threshold		56		
k_SenseDetErrDiag_Cnt_str.PStep		26		
k_SenseDetErrDiag_Cnt_str.NStep		0		
k_SenseParityErrDiag_Cnt_str.Threshold		620		
k_SenseParityErrDiag_Cnt_str.PStep		13		
k_SenseParityErrDiag_Cnt_str.NStep		13		
k_StepDetect_Deg_f32		146		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		932.130859	932.1308984 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		576	576	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186	186	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32		212.130859	212.1308984 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		218.486572	218.4865625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1057	1057	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		92.9003906	92.90039063 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		2300	2300	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		202.148438	202.1484375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-141.513428	-141.5134375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		620	620	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		144	144	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		12	12	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.70 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	6
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	294
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
k_SenseDetErrDiag_Cnt_str.Threshold	90
k_SenseDetErrDiag_Cnt_str.PStep	43
k_SenseDetErrDiag_Cnt_str.NStep	18
k_SenseParityErrDiag_Cnt_str.Threshold	760
k_SenseParityErrDiag_Cnt_str.PStep	30
k_SenseParityErrDiag_Cnt_str.NStep	30
k_StepDetect_Deg_f32	174

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	654	654	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	852.358521	852.3585 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.71 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	5			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	624			
DigColPs_ColRoughTurns_Cnt_M_s16	1			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101			
DigColPs_I2CHwColAngle_Cnt_M_u16	295			
DigColPs_I2CHwDataType_Cnt_M_u08	2			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382			
DigColPs_I2CSensCommFlts_Cnt_M_u08	17			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965			
DigColPs_SpurRoughTurns_Cnt_M_s16	1			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186			
k_SenseDetErrDiag_Cnt_str.Threshold	90			
k_SenseDetErrDiag_Cnt_str.PStep	43			
k_SenseDetErrDiag_Cnt_str.NStep	18			
k_SenseParityErrDiag_Cnt_str.Threshold	760			
k_SenseParityErrDiag_Cnt_str.PStep	35			
k_SenseParityErrDiag_Cnt_str.NStep	30			
k_StepDetect_Deg_f32	174			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.72 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		360		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		25		
k_StepDetect_Deg_f32		170.7		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		138.148849	138.1488477 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		390	390	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14	14	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.73 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1440		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.276		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		286		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		204		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1122		
DigColPs_I2CSensCommFlts_Cnt_M_u08		4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1225		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		150		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		2700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		201.4		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1275		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.216		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		999		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		100		
k_SenseDetErrDiag_Cnt_str.Threshold		64		
k_SenseDetErrDiag_Cnt_str.PStep		30		
k_SenseDetErrDiag_Cnt_str.NStep		5		
k_SenseParityErrDiag_Cnt_str.Threshold		660		
k_SenseParityErrDiag_Cnt_str.PStep		17		
k_SenseParityErrDiag_Cnt_str.NStep		17		
k_StepDetect_Deg_f32		154		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1112.20422	-1112.20418 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		303	303	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		181	181	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32		327.795776	327.7958203 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		253.097778	253.0978125 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1225	1225	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		107.666016	107.6660156 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		2700	2700	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		237.304688	237.3046875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		973.097778	973.0978125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		660	660	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		95	95	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.74 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.75 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	11
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	536
DigColPs_ColRoughTurns_Cnt_M_s16	4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184
DigColPs_I2CHwColAngle_Cnt_M_u16	169
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022
DigColPs_I2CSensCommFlts_Cnt_M_u08	28
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625
DigColPs_SpurRoughTurns_Cnt_M_s16	4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
k_SenseDetErrDiag_Cnt_str.Threshold	54
k_SenseDetErrDiag_Cnt_str.PStep	25
k_SenseDetErrDiag_Cnt_str.NStep	40
k_SenseParityErrDiag_Cnt_str.Threshold	610
k_SenseParityErrDiag_Cnt_str.PStep	12
k_SenseParityErrDiag_Cnt_str.NStep	12
k_StepDetect_Deg_f32	144

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	144	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	90	90 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	450	450 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.76 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		234		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		28		
k_StepDetect_Deg_f32		170.7		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	✔	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✔	
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✔	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✔	
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✔	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✔	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	✔	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	87.4535522	87.45354688 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	447.453552	447.4535469 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.77 (Repeat Count = 1)



Name	Input Value
DigColPsInt_GetData()	4
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	362
DigColPs_ColRoughTurns_Cnt_M_s16	-1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_I2CHwColAngle_Cnt_M_u16	281
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865
DigColPs_SpurRoughTurns_Cnt_M_s16	-1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146
k_SenseDetErrDiag_Cnt_str.Threshold	86
k_SenseDetErrDiag_Cnt_str.PStep	41
k_SenseDetErrDiag_Cnt_str.NStep	16
k_SenseParityErrDiag_Cnt_str.Threshold	740
k_SenseParityErrDiag_Cnt_str.PStep	28
k_SenseParityErrDiag_Cnt_str.NStep	25
k_StepDetect_Deg_f32	170.7

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	158.01355	158.0135469 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047	2047	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	179.912109	179.9121094 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	518.01355	518.0135469 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

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DigColPs_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.78 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.306		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	241		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	239		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1222		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1435		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	216		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	76		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.296		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	245		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	145		
k_SenseDetErrDiag_Cnt_str.Threshold	74		
k_SenseDetErrDiag_Cnt_str.PStep	35		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	22		
k_SenseParityErrDiag_Cnt_str.NStep	22		
k_StepDetect_Deg_f32	164		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	35.0736694	35.07365234 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	1	1	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	176	176	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	35.0736694	35.07365234 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	202.994019	202.994 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1435	1435	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	126.123047	126.1230469 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3200	3200	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	281.25	281.25 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	562.994019	562.994 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1	1	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.79 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	5			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-480			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312			
DigColPs_ColLPFInitDone_Cnt_M_lgc	0			
DigColPs_ColParityErrorAcc_Cnt_M_u16	523			
DigColPs_ColRoughTurns_Cnt_M_s16	4			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	246			
DigColPs_I2CHwDataType_Cnt_M_u08	0			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1242			
DigColPs_I2CSensCommFlts_Cnt_M_u08	10			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1477			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3300			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	219			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	186			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256			
DigColPs_SpurRoughTurns_Cnt_M_s16	4			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	76			
k_SenseDetErrDiag_Cnt_str.PStep	36			
k_SenseDetErrDiag_Cnt_str.NStep	11			
k_SenseParityErrDiag_Cnt_str.Threshold	1000			
k_SenseParityErrDiag_Cnt_str.PStep	23			
k_SenseParityErrDiag_Cnt_str.NStep	23			
k_StepDetect_Deg_f32	166			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	159.542114	159.5421094 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	546	546	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	159.542114	159.5421094 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	307.740234	307.7401875 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1477	1477	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	129.814453	129.8144531 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	3300	3300	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	290.039063	290.0390625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	667.740234	667.7401875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	279	279	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.80 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	6			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-320			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.318			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	365			
DigColPs_ColRoughTurns_Cnt_M_s16	-4			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	253			
DigColPs_I2CHwDataType_Cnt_M_u08	1			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1262			
DigColPs_I2CSensCommFlts_Cnt_M_u08	11			
DigColPs_Prevl2CHwColAngle_Cnt_M_u16	1519			
DigColPs_Prevl2CHwColAngle_Deg_M_f32	185			
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	3400			
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	222			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	296			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.328			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	263			
DigColPs_SpurRoughTurns_Cnt_M_s16	-4			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	78			
k_SenseDetErrDiag_Cnt_str.PStep	37			
k_SenseDetErrDiag_Cnt_str.NStep	12			
k_SenseParityErrDiag_Cnt_str.Threshold	500			
k_SenseParityErrDiag_Cnt_str.PStep	24			
k_SenseParityErrDiag_Cnt_str.NStep	24			
k_StepDetect_Deg_f32	168			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-633.705139	-633.7051367 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	389	389	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	86.2948608	86.29486328 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	184.607605	184.607625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1519	1519	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	133.505859	133.5058594 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3400	3400	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	298.828125	298.828125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-175.392395	-175.392375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	287	287	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.81 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.324		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		256		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		156		
DigColPs_I2CHwColAngle_Cnt_M_u16		260		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1282		
DigColPs_I2CSensCommFlts_Cnt_M_u08		12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1561		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		190		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3500		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		225		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		406		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.344		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		254		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		168		
k_SenseDetErrDiag_Cnt_str.Threshold		80		
k_SenseDetErrDiag_Cnt_str.PStep		38		
k_SenseDetErrDiag_Cnt_str.NStep		13		
k_SenseParityErrDiag_Cnt_str.Threshold		710		
k_SenseParityErrDiag_Cnt_str.PStep		25		
k_SenseParityErrDiag_Cnt_str.NStep		25		
k_StepDetect_Deg_f32		20		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-413.628082	-413.6280859 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		281	281	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		143	143	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32		306.371918	306.3719141 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		112.956299	112.9563125 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1561	1561	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		137.197266	137.1972656 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3500	3500	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		307.617188	307.6171875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-247.043701	-247.0436875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		279	279	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-5	-5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		155	155	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		0	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		0	*none*	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.82 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.33		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	365		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	134		
DigColPs_I2CHwColAngle_Cnt_M_u16	267		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1302		
DigColPs_I2CSensCommFlts_Cnt_M_u08	13		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1603		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	195		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	228		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	516		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.36		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	214		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	82		
k_SenseDetErrDiag_Cnt_str.PStep	39		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	720		
k_SenseParityErrDiag_Cnt_str.PStep	26		
k_SenseParityErrDiag_Cnt_str.NStep	26		
k_StepDetect_Deg_f32	340		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-309.906738	-309.9067383 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16	391	391	✔
DigColPs_ColParityError_Cnt_M_lgc	0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngle_Deg_M_f32	50.0932617	50.09326172 ± 0.0001220703125	✔
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.3462219	55.34625 ± 0.0001220703125	✔
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✔
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✔
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1603	1603	✔
DigColPs_PrevI2CHwColAngle_Deg_M_f32	140.888672	140.8886719 ± 0.0001220703125	✔
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3600	3600	✔
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	316.40625	316.40625 ± 0.0001220703125	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55.3462219	55.34625 ± 0.00048828125	✔
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✔
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240	240	✔
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✔
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	132	132	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.83 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	9
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	160
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.336
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	251
DigColPs_ColRoughTurns_Cnt_M_s16	-2
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135
DigColPs_I2CHwColAngle_Cnt_M_u16	274
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1322
DigColPs_I2CSensCommFlts_Cnt_M_u08	14
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1645
DigColPs_PrevI2CHwColAngle_Deg_M_f32	200
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3700
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	231
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	626
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.376
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256
DigColPs_SpurRoughTurns_Cnt_M_s16	-2
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
k_SenseDetErrDiag_Cnt_str.Threshold	84
k_SenseDetErrDiag_Cnt_str.PStep	40
k_SenseDetErrDiag_Cnt_str.NStep	15
k_SenseParityErrDiag_Cnt_str.Threshold	730
k_SenseParityErrDiag_Cnt_str.PStep	27
k_SenseParityErrDiag_Cnt_str.NStep	27
k_StepDetect_Deg_f32	180

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-87.1010895	-87.10109375 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	278	278	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	272.898926	272.8989063 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	242.17746	242.1774375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1645	1645	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	144.580078	144.5800781 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3700	3700	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	325.195313	325.1953125 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	242.17746	242.1774375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	283	283	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	108	108	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.84 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		234		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		28		
k_StepDetect_Deg_f32		170.7		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		138.148849	138.1488477 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		390	390	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14	14	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✔
DigColPs_I2CHwColAngle_Deg_M_f32		138.148849	138.1488477 ± 0.0001220703125	✔
DigColPs_I2CHwSpurAngle_Deg_M_f32		77.2898865	77.289875 ± 0.0001220703125	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.271484 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.85 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		624		
DigColPs_ColRoughTurns_Cnt_M_s16		1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		101		
DigColPs_I2CHwColAngle_Cnt_M_u16		295		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08		17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		965		
DigColPs_SpurRoughTurns_Cnt_M_s16		1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		186		
k_SenseDetErrDiag_Cnt_str.Threshold		90		
k_SenseDetErrDiag_Cnt_str.PStep		43		
k_SenseDetErrDiag_Cnt_str.NStep		18		
k_SenseParityErrDiag_Cnt_str.Threshold		760		
k_SenseParityErrDiag_Cnt_str.PStep		35		
k_SenseParityErrDiag_Cnt_str.NStep		30		
k_StepDetect_Deg_f32		174		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		723.421631	723.4216211 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		659	659	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	3.42163086	3.421621094 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.86 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	294
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
k_SenseDetErrDiag_Cnt_str.Threshold	90
k_SenseDetErrDiag_Cnt_str.PStep	43
k_SenseDetErrDiag_Cnt_str.NStep	18
k_SenseParityErrDiag_Cnt_str.Threshold	760

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DigColPs_Per1

Name	Input Value		
k_SenseParityErrDiag_Cnt_str.PStep	30		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	654	654	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	852.358521	852.3585 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.87 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	295
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965

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DigColPs_Per1

Name	Input Value		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.88 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	10
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	362
DigColPs_ColRoughTurns_Cnt_M_s16	-1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_I2CHwColAngle_Cnt_M_u16	281
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0

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DigColPs_Per1

Name	Input Value
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865
DigColPs_SpurRoughTurns_Cnt_M_s16	-1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146
k_SenseDetErrDiag_Cnt_str.Threshold	86
k_SenseDetErrDiag_Cnt_str.PStep	41
k_SenseDetErrDiag_Cnt_str.NStep	16
k_SenseParityErrDiag_Cnt_str.Threshold	740
k_SenseParityErrDiag_Cnt_str.PStep	28
k_SenseParityErrDiag_Cnt_str.NStep	25
k_StepDetect_Deg_f32	170.7

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓	
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓	
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓	
DiagnosticThreshold	2	DiagnosticThreshold	2	✓	
OddParityFault	2	OddParityFault	2	✓	
DiagnosticThreshold	2	DiagnosticThreshold	2	✓	
ComputeRoughTurns	2	ComputeRoughTurns	2	✓	
ConstrainOneRev	2	ConstrainOneRev	2	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.89 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetData()	11	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	536	
DigColPs_ColRoughTurns_Cnt_M_s16	4	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	

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DigColPs_Per1

Name	Input Value
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184
DigColPs_I2CHwColAngle_Cnt_M_u16	169
DigColPs_I2CHwDataTypes_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022
DigColPs_I2CSensCommFlts_Cnt_M_u08	28
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180.4
DigColPs_Reql2CSnsrDataTypes_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625
DigColPs_SpurRoughTurns_Cnt_M_s16	4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
k_SenseDetErrDiag_Cnt_str.Threshold	54
k_SenseDetErrDiag_Cnt_str.PStep	25
k_SenseDetErrDiag_Cnt_str.NStep	40
k_SenseParityErrDiag_Cnt_str.Threshold	610
k_SenseParityErrDiag_Cnt_str.PStep	12
k_SenseParityErrDiag_Cnt_str.NStep	12
k_StepDetect_Deg_f32	144

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.06494 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	144	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.064941 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200	2200	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	193.359375	193.359375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataTypes_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	366.679688	366.6796875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.90 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		234		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		28		
k_StepDetect_Deg_f32		170.7		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		138.148849	138.1488477 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		390	390	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14	14	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.91 (Repeat Count = 1)

Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101		
DigColPs_I2CHwColAngle_Cnt_M_u16	295		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	✔
DigColPs_ColParityError_Cnt_M_lgc	0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✔
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	✔
DigColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	✔
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✔
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✔
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	✔
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✔
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✔
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✔
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✔
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	852.358521	852.3585 ± 0.00048828125	✔
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✔
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✔
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.92 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	1
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	145
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_I2CHwColAngle_Cnt_M_u16	337
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184
k_SenseDetErrDiag_Cnt_str.Threshold	102
k_SenseDetErrDiag_Cnt_str.PStep	49
k_SenseDetErrDiag_Cnt_str.NStep	24
k_SenseParityErrDiag_Cnt_str.Threshold	820
k_SenseParityErrDiag_Cnt_str.PStep	36
k_SenseParityErrDiag_Cnt_str.NStep	36
k_StepDetect_Deg_f32	186

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	181	181	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	921	921	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	✓
DigColPs_Req12CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.93 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1760		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.396		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		523		
DigColPs_ColRoughTurns_Cnt_M_s16		-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		184		
DigColPs_I2CHwColAngle_Cnt_M_u16		344		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1522		
DigColPs_I2CSensCommFlts_Cnt_M_u08		24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2065		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		270		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		956		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		245		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1780		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.536		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		186		
k_SenseDetErrDiag_Cnt_str.Threshold		104		
k_SenseDetErrDiag_Cnt_str.PStep		50		
k_SenseDetErrDiag_Cnt_str.NStep		25		
k_SenseParityErrDiag_Cnt_str.Threshold		830		
k_SenseParityErrDiag_Cnt_str.PStep		0		
k_SenseParityErrDiag_Cnt_str.NStep		37		
k_StepDetect_Deg_f32		188		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	707.231689	707.2316797 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	523	523	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	159	159	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	347.231689	347.2316797 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.2365723	80.2365625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065	2065	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	181.494141	181.4941406 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956	956	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84.0234375	84.0234375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1359.76343	-1359.763438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	830	830	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	161	161	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.94 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		624		
DigColPs_ColRoughTurns_Cnt_M_s16		1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		101		
DigColPs_I2CHwColAngle_Cnt_M_u16		295		
DigColPs_I2CHwDataType_Cnt_M_u08		2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08		17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		280		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		240		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		965		
DigColPs_SpurRoughTurns_Cnt_M_s16		1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		186		
k_SenseDetErrDiag_Cnt_str.Threshold		90		
k_SenseDetErrDiag_Cnt_str.PStep		43		
k_SenseDetErrDiag_Cnt_str.NStep		18		
k_SenseParityErrDiag_Cnt_str.Threshold		760		
k_SenseParityErrDiag_Cnt_str.PStep		50		
k_SenseParityErrDiag_Cnt_str.NStep		30		
k_StepDetect_Deg_f32		174		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		595.981628	595.9816211 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		674	674	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		83	83	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		235.981628	235.9816211 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		132.358521	132.3585 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		3	3	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1771	1771	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		155.654297	155.6542969 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		852.358521	852.3585 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		3	3	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.95 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	4
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	145
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_I2CHwColAngle_Cnt_M_u16	337
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184
k_SenseDetErrDiag_Cnt_str.Threshold	102
k_SenseDetErrDiag_Cnt_str.PStep	49
k_SenseDetErrDiag_Cnt_str.NStep	24
k_SenseParityErrDiag_Cnt_str.Threshold	820
k_SenseParityErrDiag_Cnt_str.PStep	25
k_SenseParityErrDiag_Cnt_str.NStep	36
k_StepDetect_Deg_f32	186

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.96 (Repeat Count = 1)

Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	563
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_I2CHwColAngle_Cnt_M_u16	176
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1042
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057
DigColPs_PrevI2CHwColAngle_Deg_M_f32	130.5
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2300
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	189
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.152
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
k_SenseDetErrDiag_Cnt_str.Threshold	56
k_SenseDetErrDiag_Cnt_str.PStep	26
k_SenseDetErrDiag_Cnt_str.NStep	0
k_SenseParityErrDiag_Cnt_str.Threshold	620
k_SenseParityErrDiag_Cnt_str.PStep	13
k_SenseParityErrDiag_Cnt_str.NStep	1
k_StepDetect_Deg_f32	146

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	932.130859	932.1308984 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	576	576	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	186	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	212.130859	212.1308984 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.486572	218.4865625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057	1057	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	92.9003906	92.90039063 ± 0.0001220703125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	2300	2300	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	202.148438	202.1484375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-141.513428	-141.5134375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	620	620	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144	144	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.97 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		234		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		50		
k_StepDetect_Deg_f32		170.7		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		138.148849	138.1488477 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		390	390	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14	14	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 2.98 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		234		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		25		
k_StepDetect_Deg_f32		170.7		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		138.148849	138.1488477 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		390	390	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14	14	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.99 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	554.800049	554.8 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	109	109	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	194.800049	194.8 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	214.080078	214.08 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	214.080078	214.08 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	827	827	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.100 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1760		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.396		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		523		
DigColPs_ColRoughTurns_Cnt_M_s16		-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		184		
DigColPs_I2CHwColAngle_Cnt_M_u16		344		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1522		
DigColPs_I2CSensCommFlts_Cnt_M_u08		24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2065		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		270		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		956		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		245		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1780		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.536		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		186		
k_SenseDetErrDiag_Cnt_str.Threshold		104		
k_SenseDetErrDiag_Cnt_str.PStep		50		
k_SenseDetErrDiag_Cnt_str.NStep		25		
k_SenseParityErrDiag_Cnt_str.Threshold		830		
k_SenseParityErrDiag_Cnt_str.PStep		0		
k_SenseParityErrDiag_Cnt_str.NStep		37		
k_StepDetect_Deg_f32		188		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		707.231689	707.2316797 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		523	523	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		-3	-3	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		159	159	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	✔
DigColPs_I2CHwColAngle_Deg_M_f32		347.231689	347.2316797 ± 0.0001220703125	✔
DigColPs_I2CHwSpurAngle_Deg_M_f32		80.2365723	80.2365625 ± 0.0001220703125	✔
DigColPs_I2CSensCommFlts_Cnt_M_u08		16	16	✔
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✔
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2065	2065	✔
DigColPs_PrevI2CHwColAngle_Deg_M_f32		181.494141	181.4941406 ± 0.0001220703125	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16	956	956	✓
DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	84.0234375	84.0234375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1359.76343	-1359.76343 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	830	830	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	161	161	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.101 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		145		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		186		
DigColPs_I2CHwColAngle_Cnt_M_u16		337		
DigColPs_I2CHwDataType_Cnt_M_u08		3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08		23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		863		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		184		
k_SenseDetErrDiag_Cnt_str.Threshold		102		
k_SenseDetErrDiag_Cnt_str.PStep		49		
k_SenseDetErrDiag_Cnt_str.NStep		24		
k_SenseParityErrDiag_Cnt_str.Threshold		820		
k_SenseParityErrDiag_Cnt_str.PStep		36		
k_SenseParityErrDiag_Cnt_str.NStep		36		
k_StepDetect_Deg_f32		186		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	✔	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✔	
DigColPs_ColParityErrorAcc_Cnt_M_u16	181	181	✔	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✔	
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✔	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✔	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	✔	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1



Test Step 2.102 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		250		
DigColPs_ColRoughTurns_Cnt_M_s16		-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		1		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1		
DigColPs_I2CSensCommFlts_Cnt_M_u08		12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		240.6		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		512		
DigColPs_SpurRoughTurns_Cnt_M_s16		0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		122		
k_SenseDetErrDiag_Cnt_str.PStep		9		
k_SenseDetErrDiag_Cnt_str.NStep		34		
k_SenseParityErrDiag_Cnt_str.Threshold		920		
k_SenseParityErrDiag_Cnt_str.PStep		6		
k_SenseParityErrDiag_Cnt_str.NStep		10		
k_StepDetect_Deg_f32		210.6		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-719.912109	-719.9121094 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		240	240	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		-2	-2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1	1	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		360.087891	360.0878906 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		502	502	✓
DigColPs_SpurParityError_Cnt_M_lgc		0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		0	0	✓

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DigColPs_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.103 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	378		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.456		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	345		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2485		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1306		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	285.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1020		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.696		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	324		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	124		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	930		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	321.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-122.647919	-122.6479219 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	333	333	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	237.352081	237.3520781 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	200.701172	200.7011719 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	560.701172	560.7011719 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	312	312	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.104 (Repeat Count = 1)				
Name	Input Value			
DigColPsInt_GetData()	0			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	420			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.462			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	625			
DigColPs_ColRoughTurns_Cnt_M_s16	-1			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0			
DigColPs_I2CHwColAngle_Cnt_M_u16	1			
DigColPs_I2CHwDataType_Cnt_M_u08	1			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1			
DigColPs_I2CSensCommFlts_Cnt_M_u08	19			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2527			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	250.9			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1341			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	310.4			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.712			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	645			
DigColPs_SpurRoughTurns_Cnt_M_s16	2			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0			
k_SenseDetErrDiag_Cnt_str.Threshold	126			
k_SenseDetErrDiag_Cnt_str.PStep	11			
k_SenseDetErrDiag_Cnt_str.NStep	36			
k_SenseParityErrDiag_Cnt_str.Threshold	940			
k_SenseParityErrDiag_Cnt_str.PStep	8			
k_SenseParityErrDiag_Cnt_str.NStep	25			
k_StepDetect_Deg_f32	105.8			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	226.000595	226.0006055 ± 0.00048828125	✓	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓	
DigColPs_ColParityErrorAcc_Cnt_M_u16	600	600	✓	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓	
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	✓	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	226.000595	226.0006055 ± 0.0001220703125	✓	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	✓	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	620	620	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.105 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		462		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.468		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		412		
DigColPs_ColRoughTurns_Cnt_M_s16		0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		1		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1		
DigColPs_I2CSensCommFlts_Cnt_M_u08		21		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2569		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		312.8		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1376		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		127.1		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1580		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.728		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		741		
DigColPs_SpurRoughTurns_Cnt_M_s16		3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		128		
k_SenseDetErrDiag_Cnt_str.PStep		12		
k_SenseDetErrDiag_Cnt_str.NStep		37		
k_SenseParityErrDiag_Cnt_str.Threshold		950		
k_SenseParityErrDiag_Cnt_str.PStep		9		
k_SenseParityErrDiag_Cnt_str.NStep		30		
k_StepDetect_Deg_f32		120.4		
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	360.087891	360.0878906 ± 0.00048828125	✔	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✔	
DigColPs_ColParityErrorAcc_Cnt_M_u16	382	382	✔	
DigColPs_ColParityError_Cnt_M_lgc	0	0	✔	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✔	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✔	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✔	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	38.144043	38.14398437 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1478.14404	1478.143984 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	711	711	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓



Test Case 3: Path Test





Specification	Performance Metrics:	
	(With "None" instrumentation and WithPS	
	Environment)	
	CPU Cycles:	
	TS3.1	5049.00 Cycles
	TS3.2	5174.00 Cycles
	TS3.3	4828.00 Cycles
	TS3.4	5030.00 Cycles
	TS3.5	4671.00 Cycles
	TS3.6	5045.00 Cycles
	TS3.7	5082.00 Cycles
	TS3.8	4914.00 Cycles
	TS3.9	4722.00 Cycles
	TS3.10	4791.00 Cycles
	TS3.11	4742.00 Cycles
	TS3.12	5002.00 Cycles

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DigColPs_Per1

Description VECTOR DESCRIPTION:

```
TS3.1 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>TRUE
(I2CSensCommFlts_Cnt_T_u08 != 0U)=>FALSE
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>FALSE
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>FALSE
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE
( (ColSensorFault_Cnt_T_lgc == TRUE) ||
  (SpurSensorFault_Cnt_T_lgc == TRUE) ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE) )=>FALSE
if (I2ChwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>FALSE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE
((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=>TRUE"
TS3.2 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>TRUE
(I2CSensCommFlts_Cnt_T_u08 != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE)F ||
  (SpurSensorFault_Cnt_T_lgc == TRUE)F ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE)T ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE)T )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))T
If (I2CSensCommFlts_Cnt_T_u08 == 0)=>FALSE"
TS3.3 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)F
(I2CSensCommFlts_Cnt_T_u08 != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)F || (ColParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE)F ||
  (SpurSensorFault_Cnt_T_lgc == TRUE)F ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE)T ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE)T )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
(I2CSensCommFlts_Cnt_T_u08 == 0U)F"
TS3.4 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)F
(I2CSensCommFlts_Cnt_T_u08 != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>TRUE
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)F || (ColParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)T || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE)F ||
  (SpurSensorFault_Cnt_T_lgc == TRUE)T ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE)T ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE)T )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))T"
TS3.5 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)F
(I2CSensCommFlts_Cnt_T_u08 != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))F
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE) ||
  (SpurSensorFault_Cnt_T_lgc == TRUE) ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE) )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))F
((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))F"
TS3.6 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)F
(I2CSensCommFlts_Cnt_T_u08 != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>TRUE
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>TRUE
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE) ||
  (SpurSensorFault_Cnt_T_lgc == TRUE) ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE) )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
(I2CSensCommFlts_Cnt_T_u08 == 0U)F"
TS3.7 "(I2ChwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)F
(I2CSensCommFlts_Cnt_T_u08 != 0U)F
((I2ChwColAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2ChwColAngle_Cnt_T_u16 & 0x8000U) != 0U)=>TRUE
((I2ChwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>TRUE
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE) ||
  (SpurSensorFault_Cnt_T_lgc == TRUE) ||
  (ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
  (SpurParityErrorEvt_Cnt_T_lgc == TRUE) )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
(I2CSensCommFlts_Cnt_T_u08 == 0U)F"
```

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```
(ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
(SpurParityErrorEvt_Cnt_T_lgc == TRUE) )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE
(I2CSensCommFlts_Cnt_T_u08 == 0U)>=TRUE"
TS3.8 "(I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)F
(I2CSensCommFlts_Cnt_T_u08 != 0U)F
((I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)F
((I2CHwColAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)T || (ColParityOrCommErr_Cnt_T_lgc == TRUE)F)>=TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)>=TRUE
( (ColSensorFault_Cnt_T_lgc == TRUE)F ||
(SpurSensorFault_Cnt_T_lgc == TRUE)F ||
(ColParityErrorEvt_Cnt_T_lgc == TRUE)F ||
(SpurParityErrorEvt_Cnt_T_lgc == TRUE)T )=>TRUE
((ColParityOrCommErr_Cnt_T_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)>=TRUE
(I2CSensCommFlts_Cnt_T_u08 == 0U)>=TRUE"
TS3.9 (DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U)>=TRUE && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U)F
TS3.10 "( (ColSensorFault_Cnt_T_lgc == TRUE) ||
(SpurSensorFault_Cnt_T_lgc == TRUE) ||
(ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
(SpurParityErrorEvt_Cnt_T_lgc == TRUE) )=>FALSE
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)>=TRUE
if (SensorsSampleOK_Cnt_T_lgc == TRUE)>=TRUE
if (DigColPs_ColLPFInitDone_Cnt_M_lgc == FALSE)>=TRUE
if (DigColPs_SpurLPFInitDone_Cnt_M_lgc == FALSE)>=TRUE"
TS3.11 "if (SensorsSampleOK_Cnt_T_lgc == TRUE)>=TRUE
if (DigColPs_ColLPFInitDone_Cnt_M_lgc == FALSE)>=FALSE
if (DigColPs_SpurLPFInitDone_Cnt_M_lgc == FALSE)>=FALSE"
TS3.12 if (SensorsSampleOK_Cnt_T_lgc == TRUE)>=FALSE
```

Test Step 3.1 (Repeat Count = 1) ✓

Name	Input Value
DigColPsInt_GetData()	0
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	0
DigColPs_ColRoughTurns_Cnt_M_s16	-5
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0
DigColPs_SpurRoughTurns_Cnt_M_s16	-11
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
k_SenseDetErrDiag_Cnt_str.Threshold	1
k_SenseDetErrDiag_Cnt_str.PStep	0
k_SenseDetErrDiag_Cnt_str.NStep	0
k_SenseParityErrDiag_Cnt_str.Threshold	1
k_SenseParityErrDiag_Cnt_str.PStep	0
k_SenseParityErrDiag_Cnt_str.NStep	1
k_StepDetect_Deg_f32	20

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	✓

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0	0	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		1		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		1000		
DigColPs_ColRoughTurns_Cnt_M_s16		5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		255		
DigColPs_I2CHwColAngle_Cnt_M_u16		65535		
DigColPs_I2CHwDataType_Cnt_M_u08		4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08		31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		1000		
DigColPs_SpurRoughTurns_Cnt_M_s16		11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		255		
k_SenseDetErrDiag_Cnt_str.Threshold		255		
k_SenseDetErrDiag_Cnt_str.PStep		50		
k_SenseDetErrDiag_Cnt_str.NStep		50		
k_SenseParityErrDiag_Cnt_str.Threshold		1000		
k_SenseParityErrDiag_Cnt_str.PStep		50		
k_SenseParityErrDiag_Cnt_str.NStep		50		
k_StepDetect_Deg_f32		340		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		2159.91211	2159.912109 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		1000	1000	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		5	5	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		205	205	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✔

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4319.91211	4319.912109 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000	1000	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205	205	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.12		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		100		
DigColPs_ColRoughTurns_Cnt_M_s16		-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		1		
DigColPs_I2CHwColAngle_Cnt_M_u16		0		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		124		
DigColPs_I2CSensCommFlts_Cnt_M_u08		1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		5		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		12		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		3		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.02		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		110		
DigColPs_SpurRoughTurns_Cnt_M_s16		-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		110		
k_SenseDetErrDiag_Cnt_str.Threshold		2		
k_SenseDetErrDiag_Cnt_str.PStep		5		
k_SenseDetErrDiag_Cnt_str.NStep		2		
k_SenseParityErrDiag_Cnt_str.Threshold		1		
k_SenseParityErrDiag_Cnt_str.PStep		1		
k_SenseParityErrDiag_Cnt_str.NStep		1		
k_StepDetect_Deg_f32		22		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-612.747253	-612.7472656 ± 0.00048828125	

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Name	Actual Value	Expected Value	Result
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	1	1	✓
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	107.252747	107.2527344 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	105.221069	105.2210938 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5	5	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.439453125	0.439453125 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12	12	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.0546875	1.0546875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1694.77893	-1694.778906 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1	1	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	108	108	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.28
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	110
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1
DigColPs_I2CHwColAngle_Cnt_M_u16	628
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	15
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	120
k_SenseDetErrDiag_Cnt_str.Threshold	10
k_SenseDetErrDiag_Cnt_str.PStep	25

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DigColPs_Per1

Name	Input Value		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	40		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	30		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-373.538666	-373.5386719 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	40	40	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	346.461334	346.4613281 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	104.148438	104.1484375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35	35	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.07617188	3.076171875 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24	24	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.109375	2.109375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1335.85156	-1335.851563 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	40	40	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	4
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	120
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1
DigColPs_I2CHwColAngle_Cnt_M_u16	1492
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	202
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195
DigColPs_PrevI2CHwColAngle_Deg_M_f32	205
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	63
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.165

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Name	Input Value		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130		
k_SenseDetErrDiag_Cnt_str.Threshold	40		
k_SenseDetErrDiag_Cnt_str.PStep	3		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	200		
k_SenseParityErrDiag_Cnt_str.PStep	41		
k_SenseParityErrDiag_Cnt_str.NStep	21		
k_StepDetect_Deg_f32	62		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-857.83252	-857.8324219 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	161	161	✓
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1	1	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	222.16748	222.1675781 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.1761475	14.17617188 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195	195	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	17.1386719	17.13867188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88	88	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.734375	7.734375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	14.1761475	14.17617188 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	171	171	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	9
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	300
DigColPs_ColRoughTurns_Cnt_M_s16	-2
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	3
DigColPs_I2CHwColAngle_Cnt_M_u16	32767
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	32767
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695

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Name	Input Value
DigColPs_PrevI2CHwColAngle_Deg_M_f32	132
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	296
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.658
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220
DigColPs_SpurRoughTurns_Cnt_M_s16	-2
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	200
k_SenseDetErrDiag_Cnt_str.Threshold	150
k_SenseDetErrDiag_Cnt_str.PStep	30
k_SenseDetErrDiag_Cnt_str.NStep	20
k_SenseParityErrDiag_Cnt_str.Threshold	28
k_SenseParityErrDiag_Cnt_str.PStep	36
k_SenseParityErrDiag_Cnt_str.NStep	39
k_StepDetect_Deg_f32	169

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-630.636353	-630.6363281 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	28	28	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	33	33	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	89.3636475	89.36367188 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	320.592896	320.592832 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	695	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	61.0839844	61.08398438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	769	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	67.5878906	67.58789063 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320.592896	320.592832 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	28	28	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	150	150	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	0
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	310
DigColPs_ColRoughTurns_Cnt_M_s16	-1

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DigColPs_Per1

Name	Input Value
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	3
DigColPs_I2CHwColAngle_Cnt_M_u16	32768
DigColPs_I2CHwDataTypes_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	32768
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695
DigColPs_PrevI2CHwColAngle_Deg_M_f32	132
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	296
DigColPs_ReqI2CSnsrDataTypes_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.658
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	230
DigColPs_SpurRoughTurns_Cnt_M_s16	-1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	210
k_SenseDetErrDiag_Cnt_str.Threshold	150
k_SenseDetErrDiag_Cnt_str.PStep	30
k_SenseDetErrDiag_Cnt_str.NStep	20
k_SenseParityErrDiag_Cnt_str.Threshold	28
k_SenseParityErrDiag_Cnt_str.PStep	36
k_SenseParityErrDiag_Cnt_str.NStep	39
k_StepDetect_Deg_f32	169

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-443.43634	-443.4363281 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	28	28	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	276.56366	276.5636719 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	197.472839	197.472832 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	695	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	61.0839844	61.08398438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	769	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	67.5878906	67.58789063 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataTypes_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	557.472839	557.472832 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	28	28	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	190	190	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	65	65	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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DigColPs_Per1

Test Step 3.8 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.52		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		320		
DigColPs_ColRoughTurns_Cnt_M_s16		0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		3		
DigColPs_I2CHwColAngle_Cnt_M_u16		256		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		255		
DigColPs_I2CSensCommFlts_Cnt_M_u08		0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		695		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		132		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		769		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		296		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.658		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		240		
DigColPs_SpurRoughTurns_Cnt_M_s16		0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		220		
k_SenseDetErrDiag_Cnt_str.Threshold		150		
k_SenseDetErrDiag_Cnt_str.PStep		30		
k_SenseDetErrDiag_Cnt_str.NStep		20		
k_SenseParityErrDiag_Cnt_str.Threshold		28		
k_SenseParityErrDiag_Cnt_str.PStep		36		
k_SenseParityErrDiag_Cnt_str.NStep		39		
k_StepDetect_Deg_f32		169		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		-256.236328	-256.2363281 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc		0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16		281	281	✓
DigColPs_ColParityError_Cnt_M_lgc		0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16		0	0	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32		103.763672	103.7636719 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32		74.3528442	74.35283203 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08		0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		695	695	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32		61.0839844	61.08398438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		769	769	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		67.5878906	67.58789063 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08		3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		794.352844	794.352832 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16		28	28	✓
DigColPs_SpurParityError_Cnt_M_lgc		1	1	✓
DigColPs_SpurRoughTurns_Cnt_M_s16		1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		200	200	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		64	64	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1	1	✓

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DigColPs_Per1

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.9 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	168		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.426		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	379		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1622		
DigColPs_I2CSensCommFlts_Cnt_M_u08	29		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2275		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	320		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1131		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	250		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-380		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.616		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	114		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	30		
k_SenseParityErrDiag_Cnt_str.Threshold	880		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	198		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	334.971191	334.9711992 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	258	258	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	334.971191	334.9711992 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	137.073059	137.0730469 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2275	2275	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	199.951172	199.9511719 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1131	1131	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	99.4042969	99.40429688 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	137.073059	137.0730469 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	255	255	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156	156	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	64	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.10 (Repeat Count = 1)				✔
Name		Input Value		
DigColPsInt_GetData()		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc		0		
DigColPs_ColParityErrorAcc_Cnt_M_u16		250		
DigColPs_ColRoughTurns_Cnt_M_s16		2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		1		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1		
DigColPs_I2CSensCommFlts_Cnt_M_u08		12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		240.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		512		
DigColPs_SpurRoughTurns_Cnt_M_s16		2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		122		
k_SenseDetErrDiag_Cnt_str.PStep		9		
k_SenseDetErrDiag_Cnt_str.NStep		34		
k_SenseParityErrDiag_Cnt_str.Threshold		920		
k_SenseParityErrDiag_Cnt_str.PStep		6		
k_SenseParityErrDiag_Cnt_str.NStep		10		
k_StepDetect_Deg_f32		210.6		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1080.08789	1080.087891 ± 0.00048828125	✔
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	✔
DigColPs_ColParityErrorAcc_Cnt_M_u16		240	240	✔
DigColPs_ColParityError_Cnt_M_lgc		0	0	✔
DigColPs_ColRoughTurns_Cnt_M_s16		3	3	✔
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	1	✔
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	✔
DigColPs_I2CColSensorFault_Cnt_M_lgc		1	1	✔
DigColPs_I2CHwColAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✔
DigColPs_I2CHwSpurAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✔
DigColPs_I2CSensCommFlts_Cnt_M_u08		0	0	✔
DigColPs_I2CSpurSensorFault_Cnt_M_lgc		0	0	✔
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1	1	✔
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0.087890625	0.087890625 ± 0.0001220703125	✔
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1	1	✔

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	502	502	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.11 (Repeat Count = 1)				
Name		Input Value		
DigColPsInt_GetData()		0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		378		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.456		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		345		
DigColPs_ColRoughTurns_Cnt_M_s16		2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0		
DigColPs_I2CHwColAngle_Cnt_M_u16		1		
DigColPs_I2CHwDataType_Cnt_M_u08		1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		2485		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		125.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1306		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		285.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		1020		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.696		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		324		
DigColPs_SpurRoughTurns_Cnt_M_s16		2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		0		
k_SenseDetErrDiag_Cnt_str.Threshold		124		
k_SenseDetErrDiag_Cnt_str.PStep		10		
k_SenseDetErrDiag_Cnt_str.NStep		35		
k_SenseParityErrDiag_Cnt_str.Threshold		930		
k_SenseParityErrDiag_Cnt_str.PStep		7		
k_SenseParityErrDiag_Cnt_str.NStep		12		
k_StepDetect_Deg_f32		321.4		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		533.992065	533.9920781 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc		1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16		333	333	
DigColPs_ColParityError_Cnt_M_lgc		0	0	
DigColPs_ColRoughTurns_Cnt_M_s16		2	2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16		0	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc		0	0	

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwColAngle_Deg_M_f32	173.992065	173.9920781 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	91.2611694	91.26117188 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	811.261169	811.2611719 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	312	312	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 3.12 (Repeat Count = 1)				✓
Name		Input Value		
DigColPsInt_GetData()		10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc		1		
DigColPs_ColParityErrorAcc_Cnt_M_u16		362		
DigColPs_ColRoughTurns_Cnt_M_s16		1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16		30		
DigColPs_I2CHwColAngle_Cnt_M_u16		281		
DigColPs_I2CHwDataType_Cnt_M_u08		0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16		1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08		15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16		1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32		234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08		3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32		736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32		0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc		0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16		865		
DigColPs_SpurRoughTurns_Cnt_M_s16		1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16		146		
k_SenseDetErrDiag_Cnt_str.Threshold		86		
k_SenseDetErrDiag_Cnt_str.PStep		41		
k_SenseDetErrDiag_Cnt_str.NStep		16		
k_SenseParityErrDiag_Cnt_str.Threshold		740		
k_SenseParityErrDiag_Cnt_str.PStep		28		
k_SenseParityErrDiag_Cnt_str.NStep		25		
k_StepDetect_Deg_f32		170.7		
Name		Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		384.388855	384.3888477 ± 0.00048828125	✓

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DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	24.388855	24.38884766 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.529846	359.529875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	719.529846	719.529875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	✓
DisableI2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	✓
EnableI2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	✓
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

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VernierLookup



Project	DigColPs
Module	DigColPs
Test Object	VernierLookup

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D__inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'DigColPs'	<p>-----Unit_Test_Information-----</p> <p>Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.</p> <p>Note 2 : In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.</p> <p>Note 3 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>Note 4 : In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro.</p> <p>-----</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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VernierLookup



Test Case 1: Metrics Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 358.00 Cycles Longest Execution Path
TS1.2 16.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Longest Execution Path =>
If (Level_Deg_T_f32 <= T2_VernierLUT_Cnt_u16(D_VERNIERLEVEL_CNT_U08, 0))=>FALSE
For Search_Cnt_T_u08 = 0 To (TableSize_Cnt_T_u08 - 2)=>TRUE
If (MatchFound_Cnt_T_lgc = False)=>TRUE
If (Level_Deg_T_f32 < T2_VernierLUT_Cnt_u16(D_VERNIERLEVEL_CNT_U08, (Search_Cnt_T_u08 + 1)))=>TRUE
If (Level_Deg_T_f32 < Middle_Cnt_T_f32)=>FALSE
If (MatchFound_Cnt_T_lgc = False)=>FALSE"
TS1.2 "Shortest Execution Path =>
If (Level_Deg_T_f32 <= T2_VernierLUT_Cnt_u16(D_VERNIERLEVEL_CNT_U08, 0)) =>TRUE"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2
VernierLUT_Cnt_T_s16[36]	3
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[42]	9
VernierLUT_Cnt_T_s16[43]	0
VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✔
tgt_SpurRevPtr_Cnt_T_u08	0	0	✔
tgt_VernierLevelNo_Cnt_T_u08	1	1	✔

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[20]	2		
VernierLUT_Cnt_T_s16[21]	1		
VernierLUT_Cnt_T_s16[22]	0		
VernierLUT_Cnt_T_s16[23]	4		
VernierLUT_Cnt_T_s16[24]	3		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	1		
VernierLUT_Cnt_T_s16[27]	0		
VernierLUT_Cnt_T_s16[28]	4		
VernierLUT_Cnt_T_s16[29]	3		
VernierLUT_Cnt_T_s16[30]	2		
VernierLUT_Cnt_T_s16[31]	1		
VernierLUT_Cnt_T_s16[32]	0		
VernierLUT_Cnt_T_s16[33]	4		
VernierLUT_Cnt_T_s16[34]	0		
VernierLUT_Cnt_T_s16[35]	8		
VernierLUT_Cnt_T_s16[36]	6		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	2		
VernierLUT_Cnt_T_s16[39]	0		
VernierLUT_Cnt_T_s16[40]	9		
VernierLUT_Cnt_T_s16[41]	7		
VernierLUT_Cnt_T_s16[42]	5		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✔
tgt_SpurRevPtr_Cnt_T_u08	0	0	✔
tgt_VernierLevelNo_Cnt_T_u08	1	1	✔

Test Case 2: Boundary Test

Specification Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 16.00 Cycles
TS2.2 378.00 Cycles
TS2.3 203.00 Cycles
TS2.4 358.00 Cycles
TS2.5 16.00 Cycles
TS2.6 203.00 Cycles
TS2.7 358.00 Cycles
TS2.8 16.00 Cycles
TS2.9 358.00 Cycles

Description VECTOR DESCRIPTION:

TS2.1Level_Deg_T_f32=Min
TS2.2Level_Deg_T_f32=Max
TS2.3Level_Deg_T_f32=Zero
TS2.4Level_Deg_T_f32=Pos
TS2.5Level_Deg_T_f32=Neg
TS2.6LookupTableXSize_Cnt_T_u08=Min
TS2.7LookupTableXSize_Cnt_T_u08=Max
TS2.8All Min
TS2.9All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[42]	5		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	1	1	✓

Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08		
Level_Deg_T_f32	360		
LookupTableXSize_Cnt_T_u08	22		
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08		
VernierLUT_Cnt_T_s16[0]	-396		
VernierLUT_Cnt_T_s16[1]	-360		
VernierLUT_Cnt_T_s16[2]	-324		
VernierLUT_Cnt_T_s16[3]	-288		
VernierLUT_Cnt_T_s16[4]	-252		
VernierLUT_Cnt_T_s16[5]	-216		
VernierLUT_Cnt_T_s16[6]	-180		
VernierLUT_Cnt_T_s16[7]	-144		
VernierLUT_Cnt_T_s16[8]	-108		
VernierLUT_Cnt_T_s16[9]	-72		
VernierLUT_Cnt_T_s16[10]	-36		
VernierLUT_Cnt_T_s16[11]	0		
VernierLUT_Cnt_T_s16[12]	36		

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VernierLookup

Name	Input Value
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2
VernierLUT_Cnt_T_s16[36]	3
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[42]	9
VernierLUT_Cnt_T_s16[43]	0
VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	5
VernierLUT_Cnt_T_s16[50]	6
VernierLUT_Cnt_T_s16[51]	7
VernierLUT_Cnt_T_s16[52]	8
VernierLUT_Cnt_T_s16[53]	9
VernierLUT_Cnt_T_s16[54]	10
VernierLUT_Cnt_T_s16[55]	0
VernierLUT_Cnt_T_s16[56]	1
VernierLUT_Cnt_T_s16[57]	2
VernierLUT_Cnt_T_s16[58]	3
VernierLUT_Cnt_T_s16[59]	4
VernierLUT_Cnt_T_s16[60]	5
VernierLUT_Cnt_T_s16[61]	6
VernierLUT_Cnt_T_s16[62]	7
VernierLUT_Cnt_T_s16[63]	8
VernierLUT_Cnt_T_s16[64]	9
VernierLUT_Cnt_T_s16[65]	10
VernierLUT_Cnt_T_s16[66]	22
VernierLUT_Cnt_T_s16[67]	2
VernierLUT_Cnt_T_s16[68]	4
VernierLUT_Cnt_T_s16[69]	6
VernierLUT_Cnt_T_s16[70]	8
VernierLUT_Cnt_T_s16[71]	10
VernierLUT_Cnt_T_s16[72]	12
VernierLUT_Cnt_T_s16[73]	14
VernierLUT_Cnt_T_s16[74]	16
VernierLUT_Cnt_T_s16[75]	18
VernierLUT_Cnt_T_s16[76]	20
VernierLUT_Cnt_T_s16[77]	1
VernierLUT_Cnt_T_s16[78]	3
VernierLUT_Cnt_T_s16[79]	5
VernierLUT_Cnt_T_s16[80]	7
VernierLUT_Cnt_T_s16[81]	9
VernierLUT_Cnt_T_s16[82]	11
VernierLUT_Cnt_T_s16[83]	13
VernierLUT_Cnt_T_s16[84]	15
VernierLUT_Cnt_T_s16[85]	17

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	10	10	✓
tgt_VernierLevelNo_Cnt_T_u08	21	21	✓

Test Step 2.3 (Repeat Count = 1) ✓

Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	2	2	✓

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08		
Level_Deg_T_f32	245.2		
LookupTableXSize_Cnt_T_u08	22		
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08		
VernierLUT_Cnt_T_s16[0]	-396		
VernierLUT_Cnt_T_s16[1]	-360		
VernierLUT_Cnt_T_s16[2]	-324		
VernierLUT_Cnt_T_s16[3]	-288		
VernierLUT_Cnt_T_s16[4]	-252		
VernierLUT_Cnt_T_s16[5]	-216		
VernierLUT_Cnt_T_s16[6]	-180		
VernierLUT_Cnt_T_s16[7]	-144		
VernierLUT_Cnt_T_s16[8]	-108		
VernierLUT_Cnt_T_s16[9]	-72		
VernierLUT_Cnt_T_s16[10]	-36		
VernierLUT_Cnt_T_s16[11]	0		
VernierLUT_Cnt_T_s16[12]	36		
VernierLUT_Cnt_T_s16[13]	72		
VernierLUT_Cnt_T_s16[14]	108		
VernierLUT_Cnt_T_s16[15]	144		
VernierLUT_Cnt_T_s16[16]	180		
VernierLUT_Cnt_T_s16[17]	216		
VernierLUT_Cnt_T_s16[18]	252		
VernierLUT_Cnt_T_s16[19]	288		
VernierLUT_Cnt_T_s16[20]	324		
VernierLUT_Cnt_T_s16[21]	360		
VernierLUT_Cnt_T_s16[22]	9		
VernierLUT_Cnt_T_s16[23]	0		
VernierLUT_Cnt_T_s16[24]	1		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	3		
VernierLUT_Cnt_T_s16[27]	4		

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[28]	5		
VernierLUT_Cnt_T_s16[29]	6		
VernierLUT_Cnt_T_s16[30]	7		
VernierLUT_Cnt_T_s16[31]	8		
VernierLUT_Cnt_T_s16[32]	9		
VernierLUT_Cnt_T_s16[33]	0		
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	2		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	7	7	✔
tgt_SpurRevPtr_Cnt_T_u08	7	7	✔
tgt_VernierLevelNo_Cnt_T_u08	15	15	✔

Test Step 2.5 (Repeat Count = 1)		✓
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	-456.8	
LookupTableXSize_Cnt_T_u08	17	

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VernierLookup

Name	Input Value
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2
VernierLUT_Cnt_T_s16[57]	15
VernierLUT_Cnt_T_s16[58]	12
VernierLUT_Cnt_T_s16[59]	9
VernierLUT_Cnt_T_s16[60]	6
VernierLUT_Cnt_T_s16[61]	3
VernierLUT_Cnt_T_s16[62]	16
VernierLUT_Cnt_T_s16[63]	13
VernierLUT_Cnt_T_s16[64]	10
VernierLUT_Cnt_T_s16[65]	7
VernierLUT_Cnt_T_s16[66]	4
VernierLUT_Cnt_T_s16[67]	17
VernierLUT_Cnt_T_s16[68]	-163
VernierLUT_Cnt_T_s16[69]	-131
VernierLUT_Cnt_T_s16[70]	-99
VernierLUT_Cnt_T_s16[71]	-66

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	1	1	✓

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08		
Level_Deg_T_f32	0		
LookupTableXSize_Cnt_T_u08	17		
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08		
VernierLUT_Cnt_T_s16[0]	-163		
VernierLUT_Cnt_T_s16[1]	-131		
VernierLUT_Cnt_T_s16[2]	-99		
VernierLUT_Cnt_T_s16[3]	-66		
VernierLUT_Cnt_T_s16[4]	-33		
VernierLUT_Cnt_T_s16[5]	0		
VernierLUT_Cnt_T_s16[6]	32		
VernierLUT_Cnt_T_s16[7]	65		
VernierLUT_Cnt_T_s16[8]	98		
VernierLUT_Cnt_T_s16[9]	130		
VernierLUT_Cnt_T_s16[10]	163		
VernierLUT_Cnt_T_s16[11]	196		
VernierLUT_Cnt_T_s16[12]	229		
VernierLUT_Cnt_T_s16[13]	261		
VernierLUT_Cnt_T_s16[14]	294		
VernierLUT_Cnt_T_s16[15]	327		
VernierLUT_Cnt_T_s16[16]	359		
VernierLUT_Cnt_T_s16[17]	0		
VernierLUT_Cnt_T_s16[18]	4		
VernierLUT_Cnt_T_s16[19]	3		
VernierLUT_Cnt_T_s16[20]	2		
VernierLUT_Cnt_T_s16[21]	1		
VernierLUT_Cnt_T_s16[22]	0		
VernierLUT_Cnt_T_s16[23]	4		
VernierLUT_Cnt_T_s16[24]	3		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	1		
VernierLUT_Cnt_T_s16[27]	0		
VernierLUT_Cnt_T_s16[28]	4		
VernierLUT_Cnt_T_s16[29]	3		
VernierLUT_Cnt_T_s16[30]	2		
VernierLUT_Cnt_T_s16[31]	1		
VernierLUT_Cnt_T_s16[32]	0		
VernierLUT_Cnt_T_s16[33]	4		
VernierLUT_Cnt_T_s16[34]	0		
VernierLUT_Cnt_T_s16[35]	8		
VernierLUT_Cnt_T_s16[36]	6		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	2		
VernierLUT_Cnt_T_s16[39]	0		
VernierLUT_Cnt_T_s16[40]	9		
VernierLUT_Cnt_T_s16[41]	7		
VernierLUT_Cnt_T_s16[42]	5		

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	2	2	✓

Test Step 2.7 (Repeat Count = 1)		✓
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	-7	
LookupTableXSize_Cnt_T_u08	22	
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
VernierLUT_Cnt_T_s16[0]	-396	
VernierLUT_Cnt_T_s16[1]	-360	
VernierLUT_Cnt_T_s16[2]	-324	
VernierLUT_Cnt_T_s16[3]	-288	
VernierLUT_Cnt_T_s16[4]	-252	
VernierLUT_Cnt_T_s16[5]	-216	
VernierLUT_Cnt_T_s16[6]	-180	
VernierLUT_Cnt_T_s16[7]	-144	
VernierLUT_Cnt_T_s16[8]	-108	
VernierLUT_Cnt_T_s16[9]	-72	
VernierLUT_Cnt_T_s16[10]	-36	
VernierLUT_Cnt_T_s16[11]	0	
VernierLUT_Cnt_T_s16[12]	36	
VernierLUT_Cnt_T_s16[13]	72	

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VernierLookup

Name	Input Value
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2
VernierLUT_Cnt_T_s16[36]	3
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[42]	9
VernierLUT_Cnt_T_s16[43]	0
VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	5
VernierLUT_Cnt_T_s16[50]	6
VernierLUT_Cnt_T_s16[51]	7
VernierLUT_Cnt_T_s16[52]	8
VernierLUT_Cnt_T_s16[53]	9
VernierLUT_Cnt_T_s16[54]	10
VernierLUT_Cnt_T_s16[55]	0
VernierLUT_Cnt_T_s16[56]	1
VernierLUT_Cnt_T_s16[57]	2
VernierLUT_Cnt_T_s16[58]	3
VernierLUT_Cnt_T_s16[59]	4
VernierLUT_Cnt_T_s16[60]	5
VernierLUT_Cnt_T_s16[61]	6
VernierLUT_Cnt_T_s16[62]	7
VernierLUT_Cnt_T_s16[63]	8
VernierLUT_Cnt_T_s16[64]	9
VernierLUT_Cnt_T_s16[65]	10
VernierLUT_Cnt_T_s16[66]	22
VernierLUT_Cnt_T_s16[67]	2
VernierLUT_Cnt_T_s16[68]	4
VernierLUT_Cnt_T_s16[69]	6
VernierLUT_Cnt_T_s16[70]	8
VernierLUT_Cnt_T_s16[71]	10
VernierLUT_Cnt_T_s16[72]	12
VernierLUT_Cnt_T_s16[73]	14
VernierLUT_Cnt_T_s16[74]	16
VernierLUT_Cnt_T_s16[75]	18
VernierLUT_Cnt_T_s16[76]	20
VernierLUT_Cnt_T_s16[77]	1
VernierLUT_Cnt_T_s16[78]	3
VernierLUT_Cnt_T_s16[79]	5
VernierLUT_Cnt_T_s16[80]	7
VernierLUT_Cnt_T_s16[81]	9
VernierLUT_Cnt_T_s16[82]	11
VernierLUT_Cnt_T_s16[83]	13
VernierLUT_Cnt_T_s16[84]	15
VernierLUT_Cnt_T_s16[85]	17
VernierLUT_Cnt_T_s16[86]	19

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VernierLookup

Name	Input Value	Expected Value	Result
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	1	1	✓

Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2
VernierLUT_Cnt_T_s16[57]	15

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	1	1	✓

Test Step 2.9 (Repeat Count = 1)		✓
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	360	
LookupTableXSize_Cnt_T_u08	22	
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
VernierLUT_Cnt_T_s16[0]	-396	
VernierLUT_Cnt_T_s16[1]	-360	
VernierLUT_Cnt_T_s16[2]	-324	
VernierLUT_Cnt_T_s16[3]	-288	
VernierLUT_Cnt_T_s16[4]	-252	
VernierLUT_Cnt_T_s16[5]	-216	
VernierLUT_Cnt_T_s16[6]	-180	
VernierLUT_Cnt_T_s16[7]	-144	
VernierLUT_Cnt_T_s16[8]	-108	
VernierLUT_Cnt_T_s16[9]	-72	
VernierLUT_Cnt_T_s16[10]	-36	
VernierLUT_Cnt_T_s16[11]	0	
VernierLUT_Cnt_T_s16[12]	36	
VernierLUT_Cnt_T_s16[13]	72	
VernierLUT_Cnt_T_s16[14]	108	
VernierLUT_Cnt_T_s16[15]	144	
VernierLUT_Cnt_T_s16[16]	180	
VernierLUT_Cnt_T_s16[17]	216	
VernierLUT_Cnt_T_s16[18]	252	
VernierLUT_Cnt_T_s16[19]	288	
VernierLUT_Cnt_T_s16[20]	324	
VernierLUT_Cnt_T_s16[21]	360	
VernierLUT_Cnt_T_s16[22]	9	
VernierLUT_Cnt_T_s16[23]	0	
VernierLUT_Cnt_T_s16[24]	1	
VernierLUT_Cnt_T_s16[25]	2	
VernierLUT_Cnt_T_s16[26]	3	
VernierLUT_Cnt_T_s16[27]	4	
VernierLUT_Cnt_T_s16[28]	5	

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[29]	6		
VernierLUT_Cnt_T_s16[30]	7		
VernierLUT_Cnt_T_s16[31]	8		
VernierLUT_Cnt_T_s16[32]	9		
VernierLUT_Cnt_T_s16[33]	0		
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	2		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✔
tgt_SpurRevPtr_Cnt_T_u08	10	10	✔
tgt_VernierLevelNo_Cnt_T_u08	21	21	✔

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VernierLookup



Test Case 3: Path Test

Specification

Performance Metrics:
(With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 16.00 Cycles
TS3.2 378.00 Cycles
TS3.3 203.00 Cycles
TS3.4 358.00 Cycles

Description

VECTOR DESCRIPTION:

TS3.1 If (Level_Deg_T_f32 <= T2_VernierLUT0_Cnt_u16(0)) =>True
TS3.2 "If (Level_Deg_T_f32 <= T2_VernierLUT0_Cnt_u16(0)) =>False
For Search_Cnt_T_u08 = 0 To (TableSize_Cnt_T_u08 - 1)=>True
If (MatchFound_Cnt_T_lgc = False)=>True
If (Level_Deg_T_f32 < T2_VernierLUT0_Cnt_u16(Search_Cnt_T_u08 + 1))=>False
If (MatchFound_Cnt_T_lgc = False)=>True"
TS3.3 "If (Level_Deg_T_f32 < T2_VernierLUT0_Cnt_u16(Search_Cnt_T_u08 + 1))=>True
If (Level_Deg_T_f32 < Middle_Cnt_T_f32)=>True"
TS3.4 If (Level_Deg_T_f32 < Middle_Cnt_T_f32)=>False

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✔
tgt_SpurRevPtr_Cnt_T_u08	0	0	✔
tgt_VernierLevelNo_Cnt_T_u08	1	1	✔

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	360
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[20]	324		
VernierLUT_Cnt_T_s16[21]	360		
VernierLUT_Cnt_T_s16[22]	9		
VernierLUT_Cnt_T_s16[23]	0		
VernierLUT_Cnt_T_s16[24]	1		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	3		
VernierLUT_Cnt_T_s16[27]	4		
VernierLUT_Cnt_T_s16[28]	5		
VernierLUT_Cnt_T_s16[29]	6		
VernierLUT_Cnt_T_s16[30]	7		
VernierLUT_Cnt_T_s16[31]	8		
VernierLUT_Cnt_T_s16[32]	9		
VernierLUT_Cnt_T_s16[33]	0		
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	2		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	10	10	✓
tgt_VernierLevelNo_Cnt_T_u08	21	21	✓

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VernierLookup

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2
VernierLUT_Cnt_T_s16[57]	15
VernierLUT_Cnt_T_s16[58]	12
VernierLUT_Cnt_T_s16[59]	9
VernierLUT_Cnt_T_s16[60]	6
VernierLUT_Cnt_T_s16[61]	3
VernierLUT_Cnt_T_s16[62]	16
VernierLUT_Cnt_T_s16[63]	13
VernierLUT_Cnt_T_s16[64]	10

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	2	2	✓

Test Step 3.4 (Repeat Count = 1)				✓
Name	Input Value			
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08			
Level_Deg_T_f32	245.2			
LookupTableXSize_Cnt_T_u08	22			
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08			
VernierLUT_Cnt_T_s16[0]	-396			
VernierLUT_Cnt_T_s16[1]	-360			
VernierLUT_Cnt_T_s16[2]	-324			
VernierLUT_Cnt_T_s16[3]	-288			
VernierLUT_Cnt_T_s16[4]	-252			
VernierLUT_Cnt_T_s16[5]	-216			
VernierLUT_Cnt_T_s16[6]	-180			
VernierLUT_Cnt_T_s16[7]	-144			
VernierLUT_Cnt_T_s16[8]	-108			
VernierLUT_Cnt_T_s16[9]	-72			
VernierLUT_Cnt_T_s16[10]	-36			
VernierLUT_Cnt_T_s16[11]	0			
VernierLUT_Cnt_T_s16[12]	36			
VernierLUT_Cnt_T_s16[13]	72			
VernierLUT_Cnt_T_s16[14]	108			
VernierLUT_Cnt_T_s16[15]	144			
VernierLUT_Cnt_T_s16[16]	180			
VernierLUT_Cnt_T_s16[17]	216			
VernierLUT_Cnt_T_s16[18]	252			
VernierLUT_Cnt_T_s16[19]	288			
VernierLUT_Cnt_T_s16[20]	324			
VernierLUT_Cnt_T_s16[21]	360			
VernierLUT_Cnt_T_s16[22]	9			
VernierLUT_Cnt_T_s16[23]	0			
VernierLUT_Cnt_T_s16[24]	1			
VernierLUT_Cnt_T_s16[25]	2			
VernierLUT_Cnt_T_s16[26]	3			
VernierLUT_Cnt_T_s16[27]	4			
VernierLUT_Cnt_T_s16[28]	5			
VernierLUT_Cnt_T_s16[29]	6			
VernierLUT_Cnt_T_s16[30]	7			
VernierLUT_Cnt_T_s16[31]	8			
VernierLUT_Cnt_T_s16[32]	9			
VernierLUT_Cnt_T_s16[33]	0			
VernierLUT_Cnt_T_s16[34]	1			
VernierLUT_Cnt_T_s16[35]	2			

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	7	7	✓
tgt_SpurRevPtr_Cnt_T_u08	7	7	✓
tgt_VernierLevelNo_Cnt_T_u08	15	15	✓