

### **Summary**

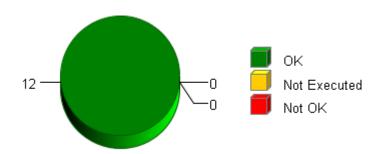
## **Overall Test Object Results (including Coverage)**

**Total Test Objects:** 12

> Successful: 12 0 Failed: 0 Not Executed:

2014-10-14 Date:

Time: 17:39:58+0530



### **Selected Project Items**

Test Object "CBD UnitTest/DigColPs/ComputeRoughTurns"

Test Object "CBD UnitTest/DigColPs/ConstrainOneRev"

Test Object "CBD\_UnitTest/DigColPs/DiagnosticThreshold"

Test Object "CBD UnitTest/DigColPs/DigColPs Init1"

Test Object "CBD\_UnitTest/DigColPs/DigColPs\_Per1"

Test Object "CBD UnitTest/DigColPs/DigColPs Per2"

Test Object "CBD\_UnitTest/DigColPs/DigColPs\_SCom\_CustClrTrim"

Test Object "CBD\_UnitTest/DigColPs/DigColPs\_SCom\_CustSetTrim"

Test Object "CBD UnitTest/DigColPs/DigColPs SCom NxtClrTrim"

Test Object "CBD\_UnitTest/DigColPs/DigColPs\_SCom\_NxtSetTrim"

Test Object "CBD\_UnitTest/DigColPs/OddParityFault"

Test Object "CBD\_UnitTest/DigColPs/VernierLookup"

### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

### **Batch Operation Settings**

**Check Interface:** No **Generate Driver:** Yes **Execute Test:** Yes **Create New Test Run:** No

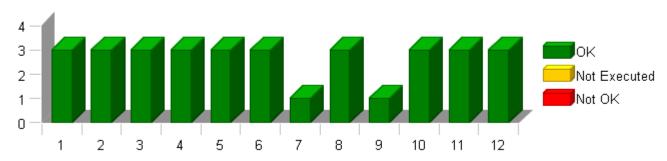
**Test Object Only** Instrumentation:

Coverage: Statement Coverage, Branch Coverage, Modified Condition / Decision Coverage,

Multiple Condition Coverage

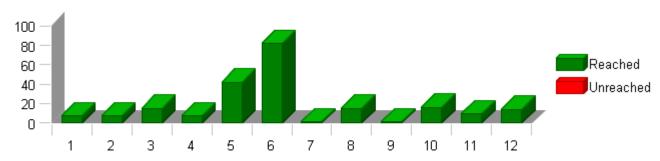


### Test Case Results for Each Test Object (without Coverage)



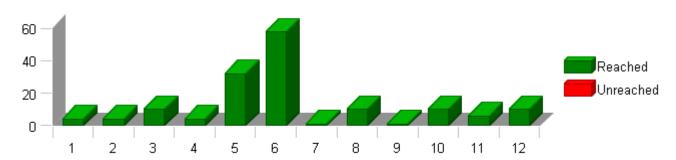
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

### Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

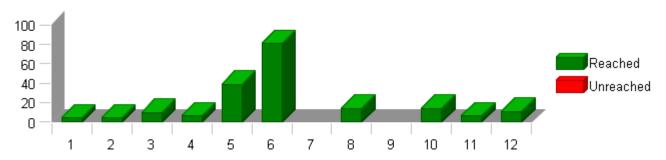
### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.



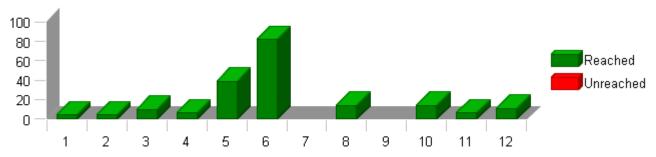
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## **TEST OVERVIEW REPORT**

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## **Test Object List**

Project DigColPs

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	MC/DC	MCC	Test Cases Result
	DigColPs	100 %	100 %	100 %	100 %	32 of 32 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	32 of 32 passed
	DigColPs	100 %	100 %	100 %	100 %	32 of 32 passed
1	<u>ComputeRoughTurns</u>	100 %	100 %	100 %	100 %	3 of 3 passed
2	<u>ConstrainOneRev</u>	100 %	100 %	100 %	100 %	3 of 3 passed ✓
3	<u>DiagnosticThreshold</u>	100 %	100 %	100 %	100 %	3 of 3 passed ✓
4	DigColPs Init1	100 %	100 %	100 %	100 %	3 of 3 passed
5	DigColPs Per1	100 %	100 %	100 %	100 %	3 of 3 passed
6	<u>DigColPs_Per2</u>	100 %	100 %	100 %	100 %	3 of 3 passed
7	DigColPs SCom CustClrTrim	100 %	100 %	-	-	1 of 1 passed
8	<u>DigColPs_SCom_CustSetTrim</u>	100 %	100 %	100 %	100 %	3 of 3 passed
9	DigColPs SCom NxtClrTrim	100 %	100 %	-	-	1 of 1 passed
10	<u>DigColPs SCom NxtSetTrim</u>	100 %	100 %	100 %	100 %	3 of 3 passed
11	<u>OddParityFault</u>	100 %	100 %	100 %	100 %	3 of 3 passed
12	<u>VernierLookup</u>	100 %	100 %	100 %	100 %	3 of 3 passed ✓

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DigColPs\_SCom\_CustClrTrim

Project DigColPs
Module DigColPs

Test Object DigColPs\_SCom\_CustClrTrim

### Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX		
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml		
Target Environment	TI TMS 570 PLS UDE (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c		
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include (Compiler Install Path)\tinclude		
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c		
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigCoIPs\utp\contract -I\$(PROJECTROOT)\DigCoIPs\utp\contractS_DigCoIPs -I\$(PROJECTROOT)\DigCoIPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path\)include		

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Name Nodule 'DigColPs'	Text

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	

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DigColPs\_SCom\_CustClrTrim



Attributes			
Name	Value		
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>		
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>		
Time Unit	Cycles		
Timer Enabled	false		
Timer Prescale	0		
Timer Resolution	1		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>		



# Test Case 1: Boundary Test

DigColPs\_SCom\_CustClrTrim

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 515.00 Cycles

Description Vector Description:

TS1.1 Clear all the Trim variables

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•

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DigColPs\_SCom\_NxtSetTrim

Project DigColPs

Module DigColPs

Test Object DigColPs\_SCom\_NxtSetTrim

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX	
Configuration File D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config		
Target Environment	TI TMS 570 PLS UDE (Default)	
Kind of Test Unit Test		
Linker Options		
Source File(s)		
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c	
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include (Compiler Install Path)\include	
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c	
Compiler Options  -Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -l\$(PROJECTROOT)\DigColPs\utp\contract -l\$(PROJECTROOT)\DigColPs\utp\contract -l\$(PROJECTROOT)\St (Compiler Install Path)\underside -l\$(PROJECTROOT)\DigColPs\underside -l\$(PROJECTROOT)\NxtrLib\underside -l\$(PROJECTROOT)\St (Compiler Install Path)\underside -l\$(PROJECTROOT)\DigColPs\underside -l\$(PROJECTROOT)\NxtrLib\underside -l\$(PROJECTROOT)\St (Compiler Install Path)\underside -l\$(PROJECTROOT)\DigColPs\underside -l\$(PROJECTROOT)\NxtrLib\underside -l\$(PROJECTROOT)\DigColPs\underside -l\$(PROJECTROOT)\		

ame	Text
indule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro "Redundant_Format_1_m" to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac_" "

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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DigColPs\_SCom\_NxtSetTrim



Attributes					
Name	Value				
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj				
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src				
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>				
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl				
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2				
Time Unit	Cycles				
Timer Enabled	false				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>				



### **Test Case 1: Metrics Test** Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 765.00 Cycles TS1.2 1429.00 Cycles

#### Description Vector Description:

(I2CColSensorFault\_Cnt\_T\_lgc = False) And (I2CSpurSensorFault\_Cnt\_T\_lgc = False) \_ And (DigColPs\_I2CHwCustData\_Uls\_M\_u16 <> D\_I2CHWCUSTDATAUNKWN\_CNT\_U16) \_ And (DigColPs\_TrimCompStatic\_Cnt\_M\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16) \_ And (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)) ==> TRUE;
(BitwiseAND(DigColPs\_I2CHwCustData\_Uls\_M\_u16, D\_I2CHWTRIMINSENSOR\_CNT\_U16) <> D\_I2CHWTRIMINSENSOR\_CNT\_U16) ==> FALSE:

(I2CHwTrimColOffset\_Deg\_T\_f32 >= 0) ==> TRUE; (DigColPs\_TrimCompStatic\_Cnt\_M\_u16 = D\_TRIMCOMPLETE\_CNT\_U16) ==> TRUE"

Test Step 1.1 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_ColTrimStatic_Deg_M_f32	360		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	360		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	<b>✓</b>
DigColPs TrimCompStatic Cnt M u16	4488	4488	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	~

Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	89.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	41		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	-
DigColPs I2CHwTrimTransCnts Uls M u08	6	6	•

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DigColPs\_SCom\_NxtSetTrim

Name	Actual Value	Expected Value	Result
DigColPs_SCom_NxtSetTrim()	0	0	~
DigColPs_SpurTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~

### **Test Case 2: Boundary Test**

### Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

### CPU Cycles:

1812.00 Cycles 765.00 Cycles 812.00 Cycles 812.00 Cycles 812.00 Cycles 812.00 Cycles 812.00 Cycles 765.00 Cycles 765.00 Cycles 765.00 Cycles 767.00 Cycles 765.00 Cycles 767.00 Cycles 765.00 Cycles 767.00 Cycles 765.00 Cycles 765.00 Cycles 765.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11 TS2.12 TS2.12 TS2.13 TS2.14 TS2.15 TS2.16 TS2.17 TS2.18 TS2.19 TS2.20 TS2.21 765.00 Cycles 767.00 Cycles

#### Description Vector Description:

TS2.1 All Min

TS2.2 All Max

TS2.2 All Max
TS2.3 DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc=>Min
TS2.4 DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc=>Max
TS2.5 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc=>Min
TS2.6 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc=>Max
TS2.7 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc=>Max
TS2.8 DigColPs\_I2CHwColAngle\_Deg\_M\_f32=>Min
TS2.8 DigColPs\_I2CHwColAngle\_Deg\_M\_f32=>Max
TS2.9 DigColPs\_I2CHwColAngle\_Deg\_M\_f32=>Pos
TS2.10 DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Min
TS2.11 DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Max
TS2.12 DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Pos
TS2.13 DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32=>Max
TS2.14 DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32=>Max
TS2.15 DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32=>Max TS2.15 DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32=>Pos TS2.16 Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=>Min TS2.17 Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=>Max TS2.18 Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=>Pos TS2.19 DigColPsInt\_GetCustData=>Min
TS2.20 DigColPsInt\_GetCustData=>Max

TS2.21 DigColPsInt\_GetCustData=>Pos

rest Step 2.1 (Repeat Count = 1)			<u> </u>
Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_ColTrimStatic_Deg_M_f32	-360		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	-360		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	<b>~</b>

0

0

DigColPs SCom NxtSetTrim()





Name	Actual Value	Expected Value	Result
DigColPs_SpurTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>~</b>
ReleaseResource	1	ReleaseResource	1	~

Test Step 2.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_ColTrimStatic_Deg_M_f32	360		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	360		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	

Name	Input Value		
DigColPsInt_GetCustData()	12		
DigColPs_ColTrimStatic_Deg_M_f32	12		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	156		
DigColPs_I2CHwColAngle_Deg_M_f32	24.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32	64.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	54		
DigColPs_TrimCompStatic_Cnt_M_u16	100		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1854		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	312	312.0002 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	•
DigColPs_SCom_NxtSetTrim()	0	0	•
DigColPs_SpurTrimStatic_Deg_M_f32	163.199951	163.199456 ± 0.00048828125	•
DigColPs TrimCompStatic Cnt M u16	1	1	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~

DigColPs\_SCom\_NxtSetTrim



Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	44		
DigColPs_ColTrimStatic_Deg_M_f32	24		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	199		
DigColPs_I2CHwColAngle_Deg_M_f32	68.6		
DigColPs_I2CHwSpurAngle_Deg_M_f32	63.7		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	65		
DigColPs_TrimCompStatic_Cnt_M_u16	200		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2456		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	24	24 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	65	65 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	200	200	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	<b>✓</b>

Test Step 2.5 (Repeat Count = 1)			•
Name	Input Value		
DigColPsInt_GetCustData()	76		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	242		
DigColPs_I2CHwColAngle_Deg_M_f32	89.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	41		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	124	124.0002 ± 0.00048828125	-
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	•
DigColPs_SCom_NxtSetTrim()	0	0	
DigColPs_SpurTrimStatic_Deg_M_f32	352.400024	352.4000062 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	<b>-</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	•

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	108
DigColPs_ColTrimStatic_Deg_M_f32	96
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	285
DigColPs_I2CHwColAngle_Deg_M_f32	154

DigColPs\_SCom\_NxtSetTrim

DigColPs\_TrimCompStatic\_Cnt\_M\_u16

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Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	182.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	121		
DigColPs_TrimCompStatic_Cnt_M_u16	451		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3660		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	96	96 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	121	121 ± 0.00048828125	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~

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Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	140		
DigColPs_ColTrimStatic_Deg_M_f32	192		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	328		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	1.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	204		
DigColPs_TrimCompStatic_Cnt_M_u16	620		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4262		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	296	296.00004 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	0	0	~
DigColPs_SpurTrimStatic_Deg_M_f32	181.599976	181.6004 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	<b>✓</b>

DigColPs\_SCom\_NxtSetTrim

DigColPs\_TrimCompStatic\_Cnt\_M\_u16



Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetCustData()	172		
DigColPs_ColTrimStatic_Deg_M_f32	218		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	244.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	14		
DigColPs_TrimCompStatic_Cnt_M_u16	780		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4464		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	218	218 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	14	14 ± 0.00048828125	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	_

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Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	204		
DigColPs_ColTrimStatic_Deg_M_f32	8		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	414		
DigColPs_I2CHwColAngle_Deg_M_f32	180.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	117		
DigColPs_TrimCompStatic_Cnt_M_u16	54		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4466		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	8	8 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	117	117 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	54	54	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	

Test Step 2.10 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	236	
DigColPs_ColTrimStatic_Deg_M_f32	16	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	457	
DigColPs_I2CHwColAngle_Deg_M_f32	296.5	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	

DigColPs\_SCom\_NxtSetTrim



Name	Input Value		
DigColPs_SpurTrimStatic_Deg_M_f32	46		
DigColPs_TrimCompStatic_Cnt_M_u16	946		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4068		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	16	16 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	46	46 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	946	946	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	268		
DigColPs_ColTrimStatic_Deg_M_f32	57		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	500		
DigColPs_I2CHwColAngle_Deg_M_f32	176.3		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	94		
DigColPs_TrimCompStatic_Cnt_M_u16	1122		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3670		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	57	57 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	94	94 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1122	1122	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>	
ReleaseResource	1	ReleaseResource	1	_	

Test Step 2.12 (Repeat Count = 1)			V
Name	Input Value		
DigColPsInt_GetCustData()	300		
DigColPs_ColTrimStatic_Deg_M_f32	39		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	543		
DigColPs_I2CHwColAngle_Deg_M_f32	184.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32	180.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	23		
DigColPs_TrimCompStatic_Cnt_M_u16	846		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4272		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	39	39 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	23	23 ± 0.00048828125	<b>✓</b>

DigColPs\_SCom\_NxtSetTrim



Name	Actual Value	Expected Value	Result
DigColPs_TrimCompStatic_Cnt_M_u16	846	846	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~

Test Step 2.13 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	332		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	192.5		
DigColPs_I2CHwSpurAngle_Deg_M_f32	101.1		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	46		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3874		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	48	48 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	46	46 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	300	300	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>~</b>	
ReleaseResource	1	ReleaseResource	1	~	

Test Step 2.14 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	364		
DigColPs_ColTrimStatic_Deg_M_f32	96		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	200.6		
DigColPs_I2CHwSpurAngle_Deg_M_f32	119.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	94		
DigColPs_TrimCompStatic_Cnt_M_u16	451		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4476		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	96	96 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	•
DigColPs_SCom_NxtSetTrim()	34	34	•
DigColPs_SpurTrimStatic_Deg_M_f32	94	94 ± 0.00048828125	•
DigColPs TrimCompStatic Cnt M u16	451	451	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>	
ReleaseResource	1	ReleaseResource	1	~	

DigColPs\_SCom\_NxtSetTrim

DigColPs\_SpurTrimStatic\_Deg\_M\_f32

DigColPs\_TrimCompStatic\_Cnt\_M\_u16



142 ± 0.00048828125

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Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	396		
DigColPs_ColTrimStatic_Deg_M_f32	144		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	900		
DigColPs_I2CHwColAngle_Deg_M_f32	208.7		
DigColPs_I2CHwSpurAngle_Deg_M_f32	138.5		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	142		
DigColPs_TrimCompStatic_Cnt_M_u16	602		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4078		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	144	144 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_SCom_NxtSetTrim()	34	34	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
GetResource	1	GetResource	1	~		
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>		
ReleaseResource	1	ReleaseResource	1	_		

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Test Step 2.16 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	428		
DigColPs_ColTrimStatic_Deg_M_f32	192		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	586		
DigColPs_I2CHwColAngle_Deg_M_f32	216.8		
DigColPs_I2CHwSpurAngle_Deg_M_f32	157.2		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	190		
DigColPs_TrimCompStatic_Cnt_M_u16	753		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	192	192 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	190	190 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	753	753	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	

Test Step 2.17 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	460	
DigColPs_ColTrimStatic_Deg_M_f32	240	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	629	
DigColPs_I2CHwColAngle_Deg_M_f32	224.9	
DigColPs_I2CHwSpurAngle_Deg_M_f32	175.9	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	

DigColPs\_SCom\_NxtSetTrim



Name	Input Value		
DigColPs_SpurTrimStatic_Deg_M_f32	238		
DigColPs_TrimCompStatic_Cnt_M_u16	904		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	240	240 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	238	238 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	904	904	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	-

Test Step 2.18 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	492		
DigColPs_ColTrimStatic_Deg_M_f32	288		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	672		
DigColPs_I2CHwColAngle_Deg_M_f32	233		
DigColPs_I2CHwSpurAngle_Deg_M_f32	194.6		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	286		
DigColPs_TrimCompStatic_Cnt_M_u16	1055		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	288	288 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	286	286 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1055	1055	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	

Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_ColTrimStatic_Deg_M_f32	336		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	715		
DigColPs_I2CHwColAngle_Deg_M_f32	241.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	213.3		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	334		
DigColPs_TrimCompStatic_Cnt_M_u16	1206		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3680		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	336	336 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	•
DigColPs_SCom_NxtSetTrim()	34	34	•
DigColPs SpurTrimStatic Deg M f32	334	334 ± 0.00048828125	•

DigColPs\_SCom\_NxtSetTrim



Name	Actual Value	Expected Value	Result
DigColPs_TrimCompStatic_Cnt_M_u16	1206	1206	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
GetResource	1	GetResource	1	~		
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~		
ReleaseResource	1	ReleaseResource	1	~		

Test Step 2.20 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_ColTrimStatic_Deg_M_f32	192		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	758		
DigColPs_I2CHwColAngle_Deg_M_f32	249.2		
DigColPs_I2CHwSpurAngle_Deg_M_f32	232		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	121		
DigColPs_TrimCompStatic_Cnt_M_u16	1357		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1028		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	192	192 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_SCom_NxtSetTrim()	34	34	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	121	121 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	1357	1357	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	

Test Step 2.21 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetCustData()	365		
DigColPs_ColTrimStatic_Deg_M_f32	218		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	801		
DigColPs_I2CHwColAngle_Deg_M_f32	257.3		
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.7		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_SpurTrimStatic_Deg_M_f32	204		
DigColPs_TrimCompStatic_Cnt_M_u16	1508		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1088		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	218	218 ± 0.00048828125	-
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	204	204 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	1508	1508	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•	
ReleaseResource	1	ReleaseResource	1	•	

**Test Case 3: Path Test** 



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&&((CligColles-Tilliconlestate\_Clini\_M\_Uni := D\_TRIMCOMPLETE\_CNT\_U16)=-FALSE) "

TS3.5 " ((I2CColSensorFault\_Cnt\_T\_lgc == FALSE)==> TRUE) && ((I2CSpurSensorFault\_Cnt\_T\_lgc == FALSE)==>TRUE)

&&((DigColPs\_I2CHwCustData\_Uls\_M\_u16 != D\_I2CHWCUSTDATAUNKWN\_CNT\_U16)==>FRUE)

&&((DigColPs\_TrimCompStatic\_Cnt\_M\_u16 != D\_TRIMCOMPLETE\_CNT\_U16)==>FALSE)"

TS3.6 " ((I2CColSensorFault\_Cnt\_T\_lgc == FALSE)==> TRUE) && ((I2CSpurSensorFault\_Cnt\_T\_lgc == FALSE)==>TRUE)

&&((DigColPs\_I2CHwCustData\_Uls\_M\_u16 != D\_I2CHWCUSTDATAUNKWN\_CNT\_U16)==>FALSE)" FS3.7 "((I2CColSensorFault\_Cnt\_T\_lgc == FALSE)==> TRUE) && ((I2CSpurSensorFault\_Cnt\_T\_lgc == FALSE)==>FALSE) && ((DigColPs\_I2CHwCustData\_Uls\_M\_u16 != D\_I2CHWCUSTDATAUNKWN\_CNT\_U16)==>FALSE) && ((DigColPs\_TrimCompStatic\_Cnt\_M\_u16 != D\_TRIMCOMPLETE\_CNT\_U16)==>FALSE) &&((CligColPs\_TrintcompStatic\_Cri\_M\_U16 != D\_TRIMCOMPLETE\_CNT\_U16)==>FALSE) "

&&((Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16 != D\_TRIMCOMPLETE\_CNT\_U16)==>FALSE) "

TS3.8 " ((I2CColSensorFault\_Cnt\_T\_lgc == FALSE)==> FALSE) && ((I2CSpurSensorFault\_Cnt\_T\_lgc == FALSE)==>FALSE)

&&((DigColPs\_I2CHwCustData\_Uls\_M\_u16 != D\_I2CHWCUSTDATAUNKWN\_CNT\_U16)==>FALSE)

&&((DigColPs\_TrimCompStatic\_Cnt\_M\_u16 != D\_TRIMCOMPLETE\_CNT\_U16)==>FALSE)

&&((Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16 != D\_TRIMCOMPLETE\_CNT\_U16)==>FALSE) " Test Step 3.1 (Repeat Count = 1) Input Value Name DigColPsInt\_GetCustData() 12 DigColPs\_ColTrimStatic\_Deg\_M\_f32 12 DigColPs\_I2CColSensorFault\_Cnt\_M\_Igc 0 DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32 156 DigColPs\_I2CHwColAngle\_Deg\_M\_f32 24.4 64.5  $DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32$ DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08  ${\tt DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc}$ 0 DigColPs\_SpurTrimStatic\_Deg\_M\_f32 54

tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1854		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	312	312 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	~
DigColPs_SCom_NxtSetTrim()	0	0	~
DigColPs_SpurTrimStatic_Deg_M_f32	163.199951	163.2 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	~

tgt\_Rte\_Inst\_Sa\_DigColPs

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Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>~</b>	
ReleaseResource	1	ReleaseResource	1	•	
ConstrainOneRev	2	ConstrainOneRev	2	<b>✓</b>	

DigColPs TrimCompStatic Cnt M u16

Rte\_Inst\_Sa\_DigColPs

DigColPs\_SCom\_NxtSetTrim



Test Step 3.2 (Repeat Count = 1)			<b>√</b>
Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1000		
DigColPs_I2CHwColAngle_Deg_M_f32	89.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	41		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-260	-260 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	~
DigColPs_SCom_NxtSetTrim()	0	0	~
DigColPs_SpurTrimStatic_Deg_M_f32	-140	-140 ± 0.00048828125	~
DigColPs TrimCompStatic Cnt M u16	1	1	<b>✓</b>

Test Step Call Trace   ✓						
Actual Function	Count	Expected Function	Count	Result		
GetResource	1	GetResource	1	~		
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~		
ReleaseResource	1	ReleaseResource	1	~		
ConstrainOneRev	2	ConstrainOneRev	2	~		

Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_ColTrimStatic_Deg_M_f32	48		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	500		
DigColPs_I2CHwColAngle_Deg_M_f32	89.1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.4		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	41		
DigColPs_TrimCompStatic_Cnt_M_u16	300		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3058		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	320	320 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	✓
DigColPs_SCom_NxtSetTrim()	0	0	~
DigColPs_SpurTrimStatic_Deg_M_f32	200	200 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	-
ConstrainOneRev	2	ConstrainOneRev	2	<b>✓</b>

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	120
DigColPs_ColTrimStatic_Deg_M_f32	8
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	414
DigColPs_I2CHwColAngle_Deg_M_f32	180.4



DigColPs_SCom	_NxtSetTrim

Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.8		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	117		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	8	8 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	117	117 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	2	2	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~

Name	Input Value		
DigColPsInt_GetCustData()	120		
DigColPs_ColTrimStatic_Deg_M_f32	8		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	414		
DigColPs_I2CHwColAngle_Deg_M_f32	180.4		
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	117		
DigColPs_TrimCompStatic_Cnt_M_u16	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4466		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_ColTrimStatic_Deg_M_f32	8	8 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	•
DigColPs_SCom_NxtSetTrim()	34	34	
DigColPs_SpurTrimStatic_Deg_M_f32	117	117 ± 0.00048828125	•
DigColPs TrimCompStatic Cnt M u16	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>

Test Step 3.6 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_ColTrimStatic_Deg_M_f32	218		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	244.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	14		
DigColPs_TrimCompStatic_Cnt_M_u16	780		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4464		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	218	218 ± 0.00048828125	<b>✓</b>



DiaColPs	SCom	NxtSetTrim
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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_SCom_NxtSetTrim()	34	34	~
DigColPs_SpurTrimStatic_Deg_M_f32	14	14 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	780	780	~

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~

Test Step 3.7 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
DigColPsInt_GetCustData()	511			
DigColPs_ColTrimStatic_Deg_M_f32	360	360		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800			
DigColPs_I2CHwColAngle_Deg_M_f32	360			
DigColPs_I2CHwSpurAngle_Deg_M_f32	360			
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6			
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1			
DigColPs_SpurTrimStatic_Deg_M_f32	360			
DigColPs_TrimCompStatic_Cnt_M_u16	1			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_ColTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	~	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	<b>✓</b>	
DigColPs_SCom_NxtSetTrim()	34	34	~	
DigColPs_SpurTrimStatic_Deg_M_f32	360	360 ± 0.00048828125	<b>✓</b>	
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	-

Test Step 3.8 (Repeat Count = 1)			<b>√</b>
Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_ColTrimStatic_Deg_M_f32	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_SpurTrimStatic_Deg_M_f32	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	-
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	<b>✓</b>
DigColPs_SCom_NxtSetTrim()	0	0	-
DigColPs_SpurTrimStatic_Deg_M_f32	-180	-180 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	1	1	~

DigColPs\_SCom\_NxtSetTrim

ReleaseResource

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1



 Test Step Call Trace

 Actual Function
 Count GetResource
 Expected Function
 Count GetResource
 Result of the second of

ReleaseResource

DigColPs\_Per1

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Project DigColPs Module DigColPs Test Object DigColPs\_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>✓</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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DigColPs\_Per1



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>

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DigColPs\_Per1





### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cvcles:

TS1.1 4792.00 Cycles Longest Execution Path TS1.2 4457.00 Cycles Shortest Execution Path

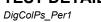
#### VECTOR DESCRIPTION: Description

```
TS1.1 "Longest Execution Path =>
 if (I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>False
if (I2CSensCommFits_Cnt_T_u08 != 0U)=>False
 If (I2CSensCommFits_Cnt_T_u08 != 0U)=>False
if (I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if (I0IgColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>True
if (ICOlSensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if (ICOlSensorFault_Cnt_T_lgc == TRUE) ||
if ((DigColPs_SpurSensorDiagFailed_Cnt_M_Igc == TRUE) || (SpurParityOrCommErr_Cnt_T_Igc == TRUE))=>False
if ((ColSensorFault_Cnt_T_Igc == TRUE) ||
(SpurSensorFault_Cnt_T_Igc == TRUE) ||
(SpurSensorFault_Cnt_T_Igc == TRUE) ||
(SpurParityErrorEvt_Cnt_T_Igc == TRUE) ||
(SpurParityErrorEvt_Cnt_T_Igc == TRUE) >>False
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True
if (SensorsSampleOK_Cnt_T_Igc == TRUE) >> True
if (DigColPs_ColLPFinitDone_Cnt_M_Igc == FALSE)=> True
if (DigColPs_SpurLPFInitDone_Cnt_M_Igc == FALSE)=> True
if ((ColParityOrCommErr_Cnt_T_Igc == TRUE) || (SpurParityOrCommErr_Cnt_T_Igc == TRUE))=> False
if ((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=> True"
TS1.2 "Shortest Execution_Path =>
 If ((DIgCoIP's_CoIsensorFaultAcc_Cnt_M_u16 == 00) && (DIgCoIP's_St
TS1.2 "Shortest Execution Path =>
if (I2CHwDataType_Cnt_T_u08!= D_ANGLEDATA_CNT_U08)=>False
if (I2CHwColAngle_Cnt_T_u16 & 0x40000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x40000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000!)!= 00!)=>False
 if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000) != 0U)=>False
if ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((ColSensorFault_Cnt_T_lgc == TRUE) ||
(SpurSensorFault_Cnt_T_lgc == TRUE) ||
(ColParityErrorEvt_Cnt_T_lgc == TRUE) => True
if (SensorSampleOK_Cnt_T_lgc == TRUE) => False
if ((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) => False
if ((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) => False
if ((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=> False"
```

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	250		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	512		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	122		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	920		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	210.6		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	<b>✓</b>
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~

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Name	Actual Value	Expected Value	Result
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	502	502	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	<b>V</b>

Name	Input Value	
DigColPsInt_GetData()	7	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-160	
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.324	
DigColPs ColLPFInitDone Cnt M lgc	0	
DigColPs ColParityErrorAcc Cnt M u16	256	
DigColPs ColRoughTurns Cnt M s16	-4	
DigColPs ColSensorDiagFailed Cnt M Igc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156	
DigColPs I2CHwColAngle Cnt M u16	0	
DigColPs I2CHwDataType Cnt M u08	1	
DigColPs I2CHwSpurAngle Cnt M u16	0	
DigColPs I2CSensCommFlts Cnt M u08	12	
DigColPs PrevI2CHwColAngle Cnt M u16	1561	
DigColPs_PrevI2CHwColAngle_Ont_M_010	190	
DigColPs PrevI2CHwSpurAngle Cnt M u16	3500	
DigColPs PrevI2CHwSpurAngle Deg M f32	225	
DigColPs RegI2CSnsrDataType Cnt M u08	0	
DigColPs SpurAngleLPFKSV Cnt M str.SV Uls f32	406	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.344	
DigColPs SpurLPFInitDone Cnt M lgc	1	
DigColPs SpurParityErrorAcc Cnt M u16	254	
DigColPs SpurRoughTurns Cnt M s16	-4	
DigColPs SpurSensorDiagFailed Cnt M lgc	0	
DigCoIPS_SpurSensorFaultAcc Cnt_M_igc DigCoIPs_SpurSensorFaultAcc Cnt_M_u16	168	
SenseDetErrDiag Cnt str.Threshold	80	
<del> </del>	38	
<_SenseDetErrDiag_Cnt_str.PStep < SenseDetErrDiag_Cnt_str.NStep	13	

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DigColPs\_Per1

Name	Input Value		
k_SenseParityErrDiag_Cnt_str.Threshold	710		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-413.628082	-413.6280859 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	281	281	•
DigColPs_ColParityError_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143	143	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	306.371918	306.3719141 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	112.956299	112.9563125 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1561	1561	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	137.197266	137.1972656 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3500	3500	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	307.617188	307.6171875 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-247.043701	-247.0436875 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	279	279	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-5	-5	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155	155	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	<b>~</b>

DigColPs\_Per1

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Test Case 2: Boundary Test

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DigColPs\_Per1



Specification

```
Performance Metrics: (With "None" instrumentation and WithPS
 Environment)
CPU Cycles:
                                5109.00 Cycles
5174.00 Cycles
4830.00 Cycles
5043.00 Cycles
TS2 1
 TS2.2
 TS2.3
TS2.4
                                5043.00 Cycles
5026.00 Cycles
5010.00 Cycles
 TS2.5
TS2.6
TS2.7
                                5004.00 Cvcles
 TS2.8
                                4994.00 Cycles
4997.00 Cycles
5003.00 Cycles
 TS2.9
TS2.10
TS2.11
                                   5016.00 Cycles
4890.00 Cycles
5026.00 Cycles
5036.00 Cycles
 TS2.12
TS2.13
TS2.14
TS2.15
                                    5016.00 Cycles
 TS2.16
TS2.17
TS2.18
                                    4994.00 Cycles
4881.00 Cycles
4982.00 Cycles
 TS2.19
                                 4982.00 Cycles
5001.00 Cycles
5015.00 Cycles
5028.00 Cycles
5028.00 Cycles
5015.00 Cycles
5015.00 Cycles
5016.00 Cycles
5016.00 Cycles
5009.00 Cycles
5014.00 Cycles
5014.00 Cycles
5004.00 Cycles
5042.00 Cycles
TS2.20
TS2.21
TS2.22
TS2.23
 TS2.24
TS2.25
TS2.26
TS2.27
TS2.28
TS2.29
TS2.30
TS2.31
TS2.32
TS2.33
TS2.34
TS2.35
                                   5042.00 Cycles
5010.00 Cycles
5044.00 Cycles
5039.00 Cycles
                                   5018.00 Cycles
5018.00 Cycles
5027.00 Cycles
4951.00 Cycles
5002.00 Cycles
4986.00 Cycles
4983.00 Cycles
4975.00 Cycles
TS2.36
TS2.37
TS2.38
TS2.39
TS2.40
TS2.41
TS2.42
                                  4975.00 Cycles
4885.00 Cycles
5004.00 Cycles
5015.00 Cycles
5015.00 Cycles
5030.00 Cycles
5030.00 Cycles
4980.00 Cycles
5028.00 Cycles
 TS2.43
TS2.44
TS2.45
TS2.46
TS2.46
TS2.47
TS2.48
TS2.49
TS2.50
TS2.51
                                   5028.00 Cycles
4898.00 Cycles
5021.00 Cycles
5018.00 Cycles
5014.00 Cycles
5034.00 Cycles
4976.00 Cycles
5018.00 Cycles
TS2.52
TS2.53
TS2.54
TS2.55
TS2.56
TS2.57
TS2.58
                                  5018.00 Cycles
4977.00 Cycles
5021.00 Cycles
5042.00 Cycles
5082.00 Cycles
5086.00 Cycles
5096.00 Cycles
5096.00 Cycles
5037.00 Cycles
4987.00 Cycles
5040.00 Cycles
TS2.59
TS2.60
TS2.61
TS2.62
TS2.62
TS2.63
TS2.64
TS2.65
TS2.66
TS2.67
                                   4987.00 Cycles
5040.00 Cycles
5009.00 Cycles
5014.00 Cycles
4999.00 Cycles
4988.00 Cycles
5041.00 Cycles
TS2.68
TS2.69
TS2.70
TS2.71
TS2.72
TS2.73
TS2.74
TS2.75
                                   4988.00 Cycles
5043.00 Cycles
4988.00 Cycles
4991.00 Cycles
4996.00 Cycles
TS2.76
TS2.77
TS2.78
                                   4992.00 Cycles
4930.00 Cycles
4746.00 Cycles
4729.00 Cycles
TS2.79
TS2.80
TS2.81
TS2.82
                                   4729.00 Cycles
4729.00 Cycles
4987.00 Cycles
5015.00 Cycles
5014.00 Cycles
4999.00 Cycles
5040.00 Cycles
5040.00 Cycles
TS2.83
TS2.84
TS2.85
 TS2.86
TS2.87
 TS2.88
TS2.89
                                   4988.00 Cycles
5014.00 Cycles
4988.00 Cycles
5028.00 Cycles
5014.00 Cycles
 TS2.90
TS2.91
TS2.92
TS2.93
TS2.94
TS2.95
TS2.96
TS2.97
                                    4988.00 Cycles
5009.00 Cycles
4988.00 Cycles
 TS2.98
                                    4988.00 Cycles
                                    4988.00 Cycles
4973.00 Cycles
5028.00 Cycles
4988.00 Cycles
4746.00 Cycles
4726.00 Cycles
4750.00 Cycles
4773.00 Cycles
 TS2.99
 TS2.100
TS2.101
```

TS2.102 TS2 103 TS2.103 TS2.104 TS2.105



## DigColPs\_Per1

Description

### VECTOR DESCRIPTION:

TS2.1 All Min TS2.2 All Max TS2.3 DigColPs\_I2CHwColAngle\_Cnt\_M\_u16=Min TS2.4 DigColPs\_I2CHwColAngle\_Cnt\_M\_u16=Max TS2.5 DigColPs\_I2CHwColAngle\_Cnt\_M\_u16=Pos TS2.6 DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Min TS2.7 DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Max TS2.8 DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Pos TS2.9 DigColPs\_I2CHwDataType\_Cnt\_M\_u08=Min TS2.10 DigColPs\_I2CHwDataType\_Cnt\_M\_u08=Max TS2.11 DigColPs\_I2CHwDataType\_Cnt\_M\_u08=Pos DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08=Min DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08=Max DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08=Pos TS2.13 TS2.14 TS2.15 DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc=Min DigCoIPs\_ColSensorDiagFailed\_Cnt\_M\_igc=Min DigCoIPs\_ColSensorDiagFailed\_Cnt\_M\_igc=Max k\_SenseDetErrDiag\_Cnt\_str.Threshold=Min k\_SenseDetErrDiag\_Cnt\_str.Threshold=Max k\_SenseDetErrDiag\_Cnt\_str.Threshold=Pos k\_SenseDetErrDiag\_Cnt\_str.Pstep=Min k\_SenseDetErrDiag\_Cnt\_str.Pstep=Max k\_SenseDetErrDiag\_Cnt\_str.Pstep=Pos k\_SenseDetErrDiag\_Cnt\_str.Pstep=Min TS2.16 TS2.17 TS2.18 TS2.19 TS2 20 TS2.21 TS2.22 k\_SenseDetErrDiag\_Cnt\_str.Nstep=Min
k\_SenseDetErrDiag\_Cnt\_str.Nstep=Mix
k\_SenseDetErrDiag\_Cnt\_str.Nstep=Max
k\_SenseDetErrDiag\_Cnt\_str.Nstep=Pos
DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16=Min
DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16=Max TS2 23 TS2.24 TS2.25 TS2 26 TS2.27 DigCoIPs\_ColParityErrorAcc\_Cnt\_M\_u16=Pos
DigCoIPs\_SpurSensorFaultAcc\_Cnt\_M\_u16=Min
DigCoIPs\_SpurSensorFaultAcc\_Cnt\_M\_u16=Max
DigCoIPs\_SpurSensorFaultAcc\_Cnt\_M\_u16=Pos TS2.28 TS2.29 TS2.30 TS2.31 DigCoIPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc=Min DigCoIPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc=Max DigCoIPs\_SpurParityErrorAcc\_Cnt\_M\_u16=Min TS2.32 TS2.33 TS2.34 DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16=Max DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16=Pos DigColPs\_PrevI2CHwColAngle\_Cnt\_M\_u16=Min TS2.35 TS2.36 TS2.37 DigCoIPs PrevI2CHwCoIAngle Cnt M u16=Max DigCoIPs PrevI2CHwCoIAngle Cnt M u16=Pos DigCoIPs CoISensorFaultAcc Cnt M u16=Min TS2.38 TS2 39 TS2.40 DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16=Max DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16=Pos DigColPs\_ColRoughTurns\_Cnt\_M\_s08=Min TS2.41 TS2 42 TS2.43 TS2.44 DigColPs\_ColRoughTurns\_Cnt\_M\_s08=Max DigColPs\_ColRoughTurns\_Cnt\_M\_s08=Zero DigColPs\_ColRoughTurns\_Cnt\_M\_s08=Pos TS2.45 TS2.46 DigCoIPS\_CoIROughTurns\_Cnt\_M\_s08=Pos DigCoIPs\_CoIRoughTurns\_Cnt\_M\_s08=Neg DigCoIPs\_SpurRoughTurns\_Cnt\_M\_s08=Min DigCoIPs\_SpurRoughTurns\_Cnt\_M\_s08=Zero TS2.48 TS2.49 DigCoIPs\_SpurRoughTurns\_Cnt\_M\_s08=Pos
DigCoIPs\_SpurRoughTurns\_Cnt\_M\_s08=Neg
DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.K=Min
DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.K=Max
DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.K=Pos
DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV=Min TS2.51 TS2.52 TS2.53 TS2.54 TS2 55 TS2.56 DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV=Min DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV=Max DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV=Pos DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV=Zero DigCoIPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV=Neg DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.K=Min DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.K=Pos DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Min DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Min DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Max DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Pos DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Zero DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Zero DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Zero DigCoIPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV=Reg Rte\_Pim\_DigCoIPsEOL.CoITrim\_Deg\_f32=Min Rte\_Pim\_DigCoIPsEOL.CoITrim\_Deg\_f32=Max TS2.57 TS2 58 TS2.59 TS2.60 TS2 61 TS2.62 TS2.63 TS2.64 TS2.65 TS2.66 TS2.67 TS2.68 TS2.69 TS2.70 Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Max Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Pos Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32=Min TS2 71 TS2.72 TS2.73 Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32=Max Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32=Pos DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16=Min TS2 74 TS2.75 DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16=Max DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16=Pos k\_SenseParityErrDiag\_Cnt\_str.Threshold=Min TS2.76 TS2 77 TS2.78 k\_SenseParityErrDiag\_Cnt\_str.Threshold=Max TS2.79 k\_SenseParityErrDiag\_Cnt\_str.Threshold=Pos k\_StepDetect\_Deg\_f32=Min k\_StepDetect\_Deg\_f32=Max TS2.80 TS2.81 TS2.82 k\_StepDetect\_Deg\_f32=Pos DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32=Min DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32=Max TS2.83 TS2.84 TS2.85 DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32=Pos DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32=Min DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32=Max TS2.86 TS2.87 TS2.88 DigCoIPs PrevI2CHwSpurAngle Deg M f32=Pos DigCoIPs ReqI2CSnsrDataType\_Cnt\_M\_u08=Min DigCoIPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08=Max TS2.89 TS2 90 TS2 91 TS2.92 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08=Pos k\_SenseParityErrDiag\_Cnt\_str.Pstep=Min k\_SenseParityErrDiag\_Cnt\_str.Pstep=Max TS2 93 TS2.94

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TS2.95 k\_SenseParityErrDiag\_Cnt\_str.Pstep=Pos
TS2.96 k\_SenseParityErrDiag\_Cnt\_str.Nstep=Min
TS2.97 k\_SenseParityErrDiag\_Cnt\_str.Nstep=Max
TS2.98 k\_SenseParityErrDiag\_Cnt\_str.Nstep=Pos
TS2.99 DigColPsInt\_GetData=Min
TS2.100 DigColPsInt\_GetData=Max
TS2.101 DigColPsInt\_GetData=Pos
TS2.102 DigColPs\_ColLPFInitDone\_Cnt\_M\_lgc=Min
TS2.103 DigColPs\_ColLPFInitDone\_Cnt\_M\_lgc=Max
TS2.104 DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc=Min
TS2.105 DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc=Min

Test Step 2.1 (Repeat Count = 1)			•
Name	Input Value		
DigColPsInt GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs ColLPFInitDone Cnt M Igc	0		
DigColPs ColParityErrorAcc Cnt M u16	0		
DigColPs ColRoughTurns Cnt M s16	-5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	1		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	0		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	
DigColPs ColLPFInitDone Cnt M Igc	0	0	
DigColPs ColParityErrorAcc Cnt M u16	0	0	
DigColPs ColParityError Cnt M Igc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	
DigColPs ColSensorDiagFailed Cnt M lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	
<del></del>			

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	<b>✓</b>
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	0	0	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960	-3960 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>



Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1) Name	Input Value		
	16		
DigColPsInt_GetData()			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000		
DigColPs_ColRoughTurns_Cnt_M_s16	5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255		
DigColPs_I2CHwColAngle_Cnt_M_u16	65535		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08	31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4320		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000		
DigColPs_SpurRoughTurns_Cnt_M_s16	11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	255		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	50		
k_SenseParityErrDiag_Cnt_str.Threshold	1000		
k_SenseParityErrDiag_Cnt_str.PStep	50		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	340		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2159.91211	2159.912109 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000	1000	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	205	205	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4319.91211	4319.912109 ± 0.00048828125	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DisColDe CourDerityFrantee Cot M v46	1000	1000	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000		

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205	205	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.3 (Repeat Count = 1)			· ·
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	999		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	124		
DigColPs_I2CSensCommFlts_Cnt_M_u08	1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30		
k_SenseDetErrDiag_Cnt_str.Threshold	2		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	2		
k_SenseParityErrDiag_Cnt_str.Threshold	15		
k_SenseParityErrDiag_Cnt_str.PStep	1		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	22.3		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-612.747253	-612.7472656 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	15	15	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	122	122	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	107.252747	107.2527344 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	105.221069	105.2210938 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5	5	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.439453125	0.439453125 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12	12	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.0546875	1.0546875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1694.77893	-1694.778906 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	15	15	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	28	28	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.16		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	153		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128		
DigColPs_I2CHwColAngle_Cnt_M_u16	65535		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	524		
DigColPs_I2CSensCommFlts_Cnt_M_u08	2		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	15		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	126		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50		
k_SenseDetErrDiag_Cnt_str.Threshold	4		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	3		
k_SenseParityErrDiag_Cnt_str.Threshold	10		
k_SenseParityErrDiag_Cnt_str.PStep	3		
k_SenseParityErrDiag_Cnt_str.NStep	2		
k_StepDetect_Deg_f32	24		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-566.189087	-566.1890625 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColDo, ColDorityErrorAco, Cot. M. u16	10	10	

10

-4

1

125

10

-4 1

125

DigCoIPs\_CoIParityErrorAcc\_Cnt\_M\_u16
DigCoIPs\_CoIParityError\_Cnt\_M\_Igc
DigCoIPs\_CoIRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

DigColPs\_Per1

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Actual Value **Expected Value** DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc DigColPs\_I2CHwColAngle\_Deg\_M\_f32 153.810913  $153.8109375 \pm 0.0001220703125$ DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32 212.807007 212.8070313 ± 0.0001220703125  ${\tt DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08}$ 2 2 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_Igc 1 DigColPs\_PrevI2CHwColAngle\_Cnt\_M\_u16 15 15 DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 1.31835938 1.318359375 ± 0.0001220703125  $DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16$ DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32 0.087890625 0.087890625 ± 0.0001220703125 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08 -1587.192969 ± 0.00048828125 DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32 -1587.19299 DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc n 0 DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16 10 10 DigColPs\_SpurParityError\_Cnt\_M\_lgc n 0 DigColPs\_SpurRoughTurns\_Cnt\_M\_s16 -4 -4 DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc 1 47 47 DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16 Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) 109 109  $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)$ 2 2

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	-
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	-
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	-

1





Test Step 2.5 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-300		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	168		
DigColPs_I2CHwColAngle_Cnt_M_u16	2048		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs I2CHwSpurAngle Cnt M u16	924		
DigColPs_I2CSensCommFlts_Cnt_M_u08	3		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	16		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_ReqizeShsiDataType_Cht_w_uoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.14		
	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	142		
DigColPs_SpurParityErrorAcc_Cnt_M_u16			
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101		
k_SenseDetErrDiag_Cnt_str.Threshold	6		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	4		
k_SenseParityErrDiag_Cnt_str.Threshold	20		
k_SenseParityErrDiag_Cnt_str.PStep	5		
k_SenseParityErrDiag_Cnt_str.NStep	3		
k_StepDetect_Deg_f32	26		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-455.982422	-455.9824219 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	20	20	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	164	164	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	264.017578	264.0175781 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	358.996826	358.996875 ± 0.0001220703125	•
DigCoIPs I2CSensCommFlts Cnt M u08	3	3	•
DigOoii 3_12006118001111111 118_OTIL_M_000			
	1	1	
DigColPs_12CSensConfiniTits_Crit_w_uoo  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1 1	1	٠,
DigColPs_l2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16			•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	1	1	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1 0.087890625	1 0.087890625 ± 0.0001220703125	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1 0.087890625 16	1 0.087890625 ± 0.0001220703125 16	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1 0.087890625 16 1.40625	1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1 0.087890625 16 1.40625 3	1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_Igc	1 0.087890625 16 1.40625 3 -1441.00317	1 0.087890625 $\pm$ 0.0001220703125 16 1.40625 $\pm$ 0.0001220703125 3 -1441.003125 $\pm$ 0.00048828125	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16	1 0.087890625 16 1.40625 3 -1441.00317	1 0.087890625 $\pm$ 0.0001220703125 16 1.40625 $\pm$ 0.0001220703125 3 -1441.003125 $\pm$ 0.00048828125 1	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16	1 0.087890625 16 1.40625 3 -1441.00317 1	1 0.087890625 $\pm$ 0.0001220703125 16 1.40625 $\pm$ 0.0001220703125 3 -1441.003125 $\pm$ 0.00048828125 1 20	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_st6	1 0.087890625 16 1.40625 3 -1441.00317 1 20 0		
DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_gc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	1 0.087890625 16 1.40625 3 -1441.00317 1 20 0 -3 0	1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3 0	
DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	1 0.087890625 16 1.40625 3 -1441.00317 1 20 0 -3 0 97	1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3 0 97	
DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_Req12CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1 0.087890625 16 1.40625 3 -1441.00317 1 20 0 -3 0	1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146		
DigColPs_I2CHwColAngle_Cnt_M_u16	124		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	25		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	35		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	20		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	12		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	8		
k_SenseDetErrDiag_Cnt_str.PStep	20		
k_SenseDetErrDiag_Cnt_str.NStep	5		
k_SenseParityErrDiag_Cnt_str.Threshold	30		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	4		
k_StepDetect_Deg_f32	28.5		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-237.87265	-237.8726563 ± 0.00048828125	<b>✓</b>
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	30	30	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141	141	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	122.12735	122.1273438 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	176.351563	176.3515625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	25	25	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	2.19726563	2.197265625 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	20	20	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.7578125	1.7578125 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1263.64844	-1263.648438 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	30	30	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	139	139	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.28		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123		
DigColPs_I2CHwColAngle_Cnt_M_u16	628		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08	5		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	15.5		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	856		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	40		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	30		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-71.1386719	-71.13867188 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	40	40	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	117	117	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	288.861328	288.8613281 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.9484863	24.9484375 ± 0.0001220703125	~
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35	35	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.07617188	3.076171875 ± 0.0001220703125	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24	24	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.109375	2.109375 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1055.05151	-1055.051563 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	40	40	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_Igc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	10	10	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.32		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	245		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146		
DigColPs_I2CHwColAngle_Cnt_M_u16	1132		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2124		
DigColPs_I2CSensCommFlts_Cnt_M_u08	6		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	45		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	55		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	28		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	18		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.32		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	146		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106		
k_SenseDetErrDiag_Cnt_str.Threshold	12		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	7		
k_SenseParityErrDiag_Cnt_str.Threshold	50		
k_SenseParityErrDiag_Cnt_str.PStep	11		
k_SenseParityErrDiag_Cnt_str.NStep	6		
k_StepDetect_Deg_f32	32		
Name	Actual Value	Expected Value	Resul
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	116.465622	116.465625 ± 0.00048828125	
DigColPs ColLPFInitDone Cnt M lgc	1	1	
DigColPs ColParityErrorAcc Cnt M u16	50	50	٠,
Di O ID O ID II E O I MAI			

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DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	116.465622	116.465625 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	264.787476	264.7875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	45	45	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.95507813	3.955078125 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	28	28	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.4609375	2.4609375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-815.212524	-815.2125 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	50	50	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	99	99	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>~</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.36		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158		
DigColPs_I2CHwColAngle_Cnt_M_u16	1636		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2524		
ligColPs_I2CSensCommFlts_Cnt_M_u08	7		
igColPs_PrevI2CHwColAngle_Cnt_M_u16	55		
igColPs_PrevI2CHwColAngle_Deg_M_f32	65		
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	32		
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	21		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1100		
ligColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.38		
ligColPs_SpurLPFInitDone_Cnt_M_lgc	1		
igColPs_SpurParityErrorAcc_Cnt_M_u16	756		
igColPs_SpurRoughTurns_Cnt_M_s16	1		
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
igColPs_SpurSensorFaultAcc_Cnt_M_u16	103		
_SenseDetErrDiag_Cnt_str.Threshold	14		
_SenseDetErrDiag_Cnt_str.PStep	35		
_SenseDetErrDiag_Cnt_str.NStep	8		
_SenseParityErrDiag_Cnt_str.Threshold	60		
_SenseParityErrDiag_Cnt_str.PStep	13		
_SenseParityErrDiag_Cnt_str.NStep	7		
	34		
_StepDetect_Deg_f32	34 Actual Value	Expected Value	Res
_StepDetect_Deg_f32 lame	Actual Value	Expected Value 324,9402344 ± 0.00048828125	Res
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	Actual Value 324.940247	324.9402344 ± 0.00048828125	Res
_StepDetect_Deg_f32  ame  igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igCoIPs_ColLPFInitDone_Cnt_M_lgc	Actual Value 324.940247 0	324.9402344 ± 0.00048828125 0	Res
_StepDetect_Deg_f32  ame igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16	Actual Value 324.940247 0 60	324.9402344 ± 0.00048828125 0 60	Res
_StepDetect_Deg_f32  lame  igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igCoIPs_ColLPFIritDone_Cnt_M_lgc  igCoIPs_ColParityErrorAcc_Cnt_M_u16  igCoIPs_ColParityError_Cnt_M_lgc	Actual Value 324.940247 0 60 0	324.9402344 ± 0.00048828125 0 60 0	Res
StepDetect_Deg_f32 ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16	Actual Value 324.940247 0 60 0 2	324,9402344 ± 0.00048828125 0 60 0 2	Res
StepDetect_Deg_f32  imme  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc	Actual Value 324.940247 0 60 0 2	324,9402344 ± 0.00048828125 0 60 0 2	Res
StepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16	Actual Value 324.940247 0 60 0 2 0 150	324.9402344 ± 0.00048828125 0 60 0 2 0 150	Res
StepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc	Actual Value 324.940247 0 60 0 2 0 150	324.9402344 ± 0.00048828125 0 60 0 2 0 150	Res
StepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CCHwColAngle_Deg_M_f32	Actual Value 324.940247 0 60 0 2 0 150 1 324.940247	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125	Res
_StepDetect_Deg_f32  lame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CCHS_I2CCHS_M_glc  igColPs_I2CHWColAngle_Deg_M_f32  igColPs_I2CHwSpurAngle_Deg_M_f32	Actual Value 324.940247 0 60 0 2 0 150 1 324.940247 175.868713	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125	Res
_StepDetect_Deg_f32  lame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_12CColSensorFault_Cnt_M_lgc  igColPs_12CCHyColAngle_Deg_M_f32  igColPs_12CHySpurAngle_Deg_M_f32  igColPs_12CSensCommFlts_Cnt_M_u08	Actual Value 324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125	Res
LigcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  ligColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  ligColPs_ColLPFInitDone_Cnt_M_lgc  ligColPs_ColParityErrorAcc_Cnt_M_u16  ligColPs_ColParityError_Cnt_M_lgc  ligColPs_ColRoughTurns_Cnt_M_s16  ligColPs_ColSensorDiagFailed_Cnt_M_lgc  ligColPs_ColSensorFaultAcc_Cnt_M_u16  ligColPs_12CColSensorFault_Cnt_M_lgc  ligColPs_12CHwColAngle_Deg_M_f32  ligColPs_12CHwSpurAngle_Deg_M_f32  ligColPs_12CSensCommFlts_Cnt_M_u08  ligColPs_12CSpurSensorFault_Cnt_M_lgc	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7	Res
LigcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_12CColSensorFault_Cnt_M_lgc  igColPs_12CHwColAngle_Deg_M_f32  igColPs_12CHwSpurAngle_Deg_M_f32  igColPs_12CSensCommFlts_Cnt_M_u08  igColPs_12CSpurSensorFault_Cnt_M_lgc  igColPs_12CSpurSensorFault_Cnt_M_u08  igColPs_12CSpurSensorFault_Cnt_M_lgc  igColPs_Prevl2CHwColAngle_Cnt_M_u16	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55	Res
stepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CHwColAngle_Deg_M_f32  igColPs_I2CHwSpurAngle_Deg_M_f32  igColPs_I2CSensCommFlts_Cnt_M_u08  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Deg_M_f32	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125	Res
stepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CHwColAngle_Deg_M_f32  igColPs_I2CHwSpurAngle_Deg_M_f32  igColPs_I2CSensCommFlts_Cnt_M_u08  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_I2CSpurSensorFault_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32	Actual Value  324,940247 0 60 0 2 0 150 1 324,940247 175,868713 7 1 55 4,83398438 32	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125	Res
stepDetect_Deg_f32ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_l2CColSensorFaultAcc_Cnt_M_u16 igColPs_l2CHwColAngle_Deg_M_f32 igColPs_l2CHwSpurAngle_Deg_M_f32 igColPs_l2CSensCommFlts_Cnt_M_u08 igColPs_l2CSpurSensorFault_Cnt_M_lgc igColPs_l2CSpurSensorFault_Cnt_M_lgc igColPs_l2CSpurSensorFault_Cnt_M_u18 igColPs_l2CSpurSensorFault_Cnt_M_u16 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32	Actual Value  324,940247 0 60 0 2 0 150 1 324,940247 175,868713 7 1 55 4,83398438 32 2,8125	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125	Res
ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColAngleLPFKSV_Cnt_M_gtr.SV_Uls_f32  igColPs_ColPrinitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_gc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CHwColAngle_Deg_M_f32  igColPs_I2CHwSpurAngle_Deg_M_f32  igColPs_I2CSensCommFlts_Cnt_M_u08  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_ReqI2CSnsrDataType_Cnt_M_u08	Actual Value  324,940247 0 60 0 2 0 150 1 324,940247 175,868713 7 1 55 4,83398438 32 2,8125 2	$324.9402344 \pm 0.00048828125$ 0 60 0 2 0 150 1 324.9402344 $\pm$ 0.0001220703125 175.86875 $\pm$ 0.0001220703125 7 1 55 4.833984375 $\pm$ 0.0001220703125 32 2.8125 $\pm$ 0.0001220703125	Res
stepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_l2CColSensorFault_Cnt_M_lgc igColPs_l2CHwColAngle_Deg_M_f32 igColPs_l2CHwSpurAngle_Deg_M_f32 igColPs_l2CSensCommFits_Cnt_M_u08 igColPs_l2CSensCommFits_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	Actual Value  324,940247 0 60 0 2 0 150 1 324,940247 175,868713 7 1 55 4,83398438 32 2,8125 2 -544,131287	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125	Res
stepDetect_Deg_f32  ame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_l2CColSensorFault_Cnt_M_lgc  igColPs_l2CHwColAngle_Deg_M_f32  igColPs_l2CHwSpurAngle_Deg_M_f32  igColPs_l2CSensCommFlts_Cnt_M_u08  igColPs_l2CSensCommFlts_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Ont_M_u16  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_SpurLPFInitDone_Cnt_M_lgc	Actual Value  324,940247 0 60 0 2 0 150 1 324,940247 175,868713 7 1 55 4,83398438 32 2,8125 2 -544,131287 1	$324.9402344 \pm 0.00048828125$ 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1	Res
StepDetect_Deg_f32  Imme  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CHwColAngle_Deg_M_f32  igColPs_I2CHwSpurAngle_Deg_M_f32  igColPs_I2CSpurSensorFault_Cnt_M_u08  igColPs_I2CSpurSensorFault_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_SpurLPFInitDone_Cnt_M_lgc  igColPs_SpurParityErrorAcc_Cnt_M_u16	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60	Res
StepDetect_Deg_f32  iame  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_lgc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_12CHwColAngle_Deg_M_f32  igColPs_12CHwSpurAngle_Deg_M_f32  igColPs_12CSpurSensorFault_Cnt_M_u08  igColPs_12CSpurSensorFault_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwColAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Cnt_M_u16  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_SpurLPFInitDone_Cnt_M_lgc  igColPs_SpurParityErrorAcc_Cnt_M_u16  igColPs_SpurParityErrorAcc_Cnt_M_u16	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0	324,9402344 ± 0.00048828125 0 60 0 2 0 150 1 324,9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0	Res
LigcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_ColLPFInitDone_Cnt_M_lgc  igColPs_ColParityErrorAcc_Cnt_M_u16  igColPs_ColParityError_Cnt_M_gc  igColPs_ColRoughTurns_Cnt_M_s16  igColPs_ColSensorDiagFailed_Cnt_M_lgc  igColPs_ColSensorFaultAcc_Cnt_M_u16  igColPs_I2CColSensorFault_Cnt_M_lgc  igColPs_I2CHwColAngle_Deg_M_f32  igColPs_I2CSensCommFlts_Cnt_M_u08  igColPs_I2CSensCommFlts_Cnt_M_u08  igColPs_I2CSpurSensorFault_Cnt_M_lgc  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwColAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_PrevI2CHwSpurAngle_Deg_M_f32  igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  igColPs_SpurAngleLPFKSV_Cnt_M_gc  igColPs_SpurParityError_Cnt_M_lgc  igColPs_SpurParityError_Cnt_M_lgc  igColPs_SpurParityError_Cnt_M_lgc  igColPs_SpurRoughTurns_Cnt_M_s16	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1	Res
Jame  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_L2CColSensorFault_Cnt_M_lgc  DigColPs_I2CCHwColAngle_Deg_M_f32  DigColPs_I2CCHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurAngleTurns_Cnt_M_s16  DigColPs_SpurSpurSpurDiagFailed_Cnt_M_lgc	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0 1	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1	Res
StepDetect_Deg_f32  Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFinitDone_Cnt_M_igc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_igc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_u16  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CCHwColAngle_Deg_M_f32  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_u6  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0 1 0 95	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1 0 95	Res
Jame  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_L2CColSensorFault_Cnt_M_lgc  DigColPs_I2CCHwColAngle_Deg_M_f32  DigColPs_I2CCHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurAngleTurns_Cnt_M_s16  DigColPs_SpurSpurSpurDiagFailed_Cnt_M_lgc	Actual Value  324.940247 0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0 1	324.9402344 ± 0.00048828125 0 60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1	Res

Test Step 2.10 (Repeat Count = 1)



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	263		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	2140		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2924		
DigColPs_I2CSensCommFlts_Cnt_M_u08	8		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	65		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75.8		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	24		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.44		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	964		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151		
k_SenseDetErrDiag_Cnt_str.Threshold	16		
k_SenseDetErrDiag_Cnt_str.PStep	40		
k_SenseDetErrDiag_Cnt_str.NStep	9		
k_SenseParityErrDiag_Cnt_str.Threshold	70		
k_SenseParityErrDiag_Cnt_str.PStep	15		
k_SenseParityErrDiag_Cnt_str.NStep	8		
k_StepDetect_Deg_f32	36.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	554.285156	554.2851563 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	70	70	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	177	177	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	194.285156	194.2851563 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	118.1922	118.1921875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	65	65	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5.71289063	5.712890625 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36	36	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.1640625	3.1640625 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-241.8078	-241.8078125 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	70	70	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142	142	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.44		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	254		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152		
DigColPs_I2CHwColAngle_Cnt_M_u16	2644		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	3324		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	75		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	85		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	40		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	27.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	746		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	18		
k_SenseDetErrDiag_Cnt_str.PStep	45		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	80		
k_SenseParityErrDiag_Cnt_str.PStep	17		
k_SenseParityErrDiag_Cnt_str.NStep	9		
k_StepDetect_Deg_f32	38		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	804.500366	804.5003906 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	80	80	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142	142	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	84.5003662	84.50039063 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	91.7578125	91.7578125 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	75	75	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	6.59179688	6.591796875 ± 0.0001220703125	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	40	40	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.515625	3.515625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	91.7578125	91.7578125 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	80	80	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155	155	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 2.12 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.48		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	214		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
DigColPs_I2CHwColAngle_Cnt_M_u16	3148		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	3724		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	85		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	95		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	44		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	30		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.56		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175		
k_SenseDetErrDiag_Cnt_str.Threshold	20		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	11		
k_SenseParityErrDiag_Cnt_str.Threshold	90		
k_SenseParityErrDiag_Cnt_str.PStep	19		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	40		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1075.58594	1075.585938 ± 0.00048828125	-
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	90	90	•

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DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	355.585938	355.5859375 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	96.5656738	96.565625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	85	85	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	7.47070313	7.470703125 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	44	44	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.8671875	3.8671875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	456.565674	456.565625 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	90	90	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	164	164	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

Test Step Call Trace   ✓						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
OddParityFault	2	OddParityFault	2	<b>~</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>		
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>~</b>		
ConstrainOneRev	2	ConstrainOneRev	2	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		





Test Step 2.13 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	3652		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2		
DigColPs_I2CSensCommFlts_Cnt_M_u08	31		
DigCoIPs PrevI2CHwColAngle Cnt M u16	95		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	105		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	48		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	33		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs SpurAngleLPFKSV Cnt M str.SV Uls f32	-700		
	0.62		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	523		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	185		
k_SenseDetErrDiag_Cnt_str.Threshold	22		
k_SenseDetErrDiag_Cnt_str.PStep	2		
k_SenseDetErrDiag_Cnt_str.NStep	12		
k_SenseParityErrDiag_Cnt_str.Threshold	100		
k_SenseParityErrDiag_Cnt_str.PStep	21		
k_SenseParityErrDiag_Cnt_str.NStep	11		
k_StepDetect_Deg_f32	42		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1180.3418	1180.341797 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	100	100	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigCoIPs CoIRoughTurns Cnt M s16	5	5	٠,
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs ColSensorFaultAcc Cnt M u16	174	174	٠,
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	100.341797	100.3417969 ± 0.0001220703125	
DigColPs I2CHwSpurAngle Deg M f32	269.415649	269.415625 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	
DigColPs PrevI2CHwColAngle Cnt M u16	95	95	
0 - 0		8.349609375 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	8.34960938		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	48	48	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.21875	4.21875 ± 0.0001220703125	` \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	629.415649	629.415625 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	100	100	•
DigColPs SpurParityError Cnt M Igc	0	0	•
· - · · · ·	4	4	•
· - · · · ·		0	
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	
DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 173	173	
DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	173	173	•

Test Step 2.14 (Repeat Count = 1)



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.56		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	865		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142		
DigColPs_I2CHwColAngle_Cnt_M_u16	88		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	105		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	115		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	52		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	36		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	195		
k_SenseDetErrDiag_Cnt_str.Threshold	24		
k_SenseDetErrDiag_Cnt_str.PStep	4		
k_SenseDetErrDiag_Cnt_str.NStep	13		
k_SenseParityErrDiag_Cnt_str.Threshold	110		
k SenseParityErrDiag Cnt str.PStep	23		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	44.2		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-335.632019	-335.6320313 ± 0.00048828125	/
DigColPs_ColLPFInitDone_Cnt_M_igc	1	1	<b>V</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	110	110	
DigColPs_ColParityError_Cnt_M_lgc	0	0	_
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	_
DigColPs_ColSensorFaultAcc_Cnt_M_u16	129	129	
DigColPs_I2CColSensorFault_Cnt_M_gc	1	1	<b>V</b>
DigColPs_I2CHwColAngle_Deg_M_f32	24.367981	24.36796875 ± 0.0001220703125	
DigColPs I2CHwSpurAngle Deg M f32	271.907837	271.9078125 ± 0.0001220703125	<b>V</b>
DigColPs I2CSensCommFlts Cnt M u08	12	12	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	_
DigColPs PrevI2CHwColAngle Cnt M u16	105	105	
DigColPs PrevI2CHwColAngle Deg M f32	9.22851563	9.228515625 ± 0.0001220703125	-
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	52	52	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.5703125	4.5703125 ± 0.0001220703125	•
DigColPs Reql2CSnsrDataType Cnt M u08	2	4.5703125 ± 0.0001220703125	
DigColPs_ReqizCSristDataType_Cnt_M_utoo  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1168.09216	-1168.092188 ± 0.00048828125	~
DigColPs SpurLPFInitDone Cnt M Igc	0	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	110	110	•
	110	110	
DigColPs SpurParityError Cnt M lgc	0	0	<b>✓</b>





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	182	182	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enable12CInterrupt	1	EnableI2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	<b>✓</b>	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	13		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	568		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	244		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	39		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.74		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	235		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142		
k_SenseDetErrDiag_Cnt_str.Threshold	26		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	120		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	13		
k_StepDetect_Deg_f32	46		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-361.935547	-361.9355469 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	120	120	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	172	172	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	358.064453	358.0644531 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	248.042236	248.0421875 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115	115	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.1074219	10.10742188 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56	56	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.921875	4.921875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1191.95776	-1191.957813 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120	120	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128	128	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	<b>~</b>
Enablel2CInterrupt	1	Enable12CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.16 (Repeat Count = 1)  Name	Input Value		
DigColPsInt GetData()	14		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	800		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.64		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	965		
DigColPs ColRoughTurns Cnt M s16	-3		
DigColPs ColSensorDiagFailed Cnt M Igc	1		
DigColPs ColSensorFaultAcc Cnt M u16	184		
DigColPs I2CHwColAngle Cnt M u16	400		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs I2CHwSpurAngle Cnt M u16	62		
DigColPs I2CSensCommFlts Cnt M u08	11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	125		
DigColPs PrevI2CHwColAngle Deg M f32	135		
DigColPs PrevI2CHwSpurAngle Cnt M u16	60		
DigColPs PrevI2CHwSpurAngle Deg M f32	42		
DigColPs Reql2CSnsrDataType Cnt M u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
DigColPs SpurLPFInitDone Cnt M Igc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs SpurRoughTurns Cnt M s16	-3		
DigColPs SpurSensorDiagFailed Cnt M Igc	0		
DigColPs SpurSensorFaultAcc Cnt M u16	152		
k SenseDetErrDiag Cnt str.Threshold	28		
k SenseDetErrDiag Cnt str.PStep	8		
k SenseDetErrDiag Cnt str.NStep	15		
k SenseParityErrDiag Cnt str.Threshold	130		
k SenseParityErrDiag Cnt str.PStep	27		
k_SenseParityErrDiag_Cnt_str.NStep	14		
k StepDetect Deg f32	48		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-165.768738	-165.76875 ± 0.00048828125	Result
DigColPs ColLPFInitDone Cnt M Igc	1	1	
DigColPs ColParityErrorAcc Cnt M u16	130	130	
Digoon o_con unityEnorAcc_ont_w_u10	100	100	

0

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DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	194.231262	194.23125 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	140.21875	140.21875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	14	14	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	125	125	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.9863281	10.98632813 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	60	60	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	5.2734375	5.2734375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-939.78125	-939.78125 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130	130	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	137	137	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	14	14	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
OddParityFault	2	OddParityFault	2	<b>~</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>		
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>~</b>		
ConstrainOneRev	2	ConstrainOneRev	2	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		





Test Step 2.17 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.68		
DigColPs ColLPFInitDone Cnt M Igc	0		
	456		
DigColPs_ColParityErrorAcc_Cnt_M_u16	-2		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	186		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	556		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08 DigColPs_I2CHwSpurAngle_Cnt_M_u16	82		
	12		
DigColPs_I2CSensCommFlts_Cnt_M_u08	135		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145		
DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	64		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	45.5		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	-300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.86		
DigColPs_SpurLPFInitDone_Cnt_M_lgc			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	142		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	163		
k_SenseDetErrDiag_Cnt_str.Threshold	1		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	140		
k_SenseParityErrDiag_Cnt_str.PStep	29		
k_SenseParityErrDiag_Cnt_str.NStep	15		
k_StepDetect_Deg_f32	50.5		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	51.2683716	51.26835937 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	140	140	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	170	170	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	51.2683716	51.26835937 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	63.6374512	63.6375 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	15	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
	135	135	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133	44 00500400 + 0 0004000700405	
	11.8652344	11.86523438 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Deg_M_f32		11.80523438 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	11.8652344		
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.8652344 64	64	•
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	11.8652344 64 5.625	64 5.625 ± 0.0001220703125	•
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32	11.8652344 64 5.625 0	64 5.625 ± 0.0001220703125 0	•
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	11.8652344 64 5.625 0 -656.362549	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125	
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	11.8652344 64 5.625 0 -656.362549	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125 1	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	11.8652344 64 5.625 0 -656.362549 1	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125 1 140	
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16	11.8652344 64 5.625 0 -656.362549 1 140	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125 1 140 0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_gc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16	11.8652344 64 5.625 0 -656.362549 1 140 0	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125 1 140 0 -2	
DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16	11.8652344 64 5.625 0 -656.362549 1 140 0 -2	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125 1 140 0 -2	
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	11.8652344 64 5.625 0 -656.362549 1 140 0 -2 1 147	64 5.625 ± 0.0001220703125 0 -656.3625 ± 0.00048828125 1 140 0 -2 1 147	



Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~			
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•			
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~			
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~			
DiagnosticThreshold	2	DiagnosticThreshold	2	~			
OddParityFault	2	OddParityFault	2	•			
DiagnosticThreshold	2	DiagnosticThreshold	2	•			
ComputeRoughTurns	2	ComputeRoughTurns	2	•			
ConstrainOneRev	2	ConstrainOneRev	2	~			
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~			
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~			
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•			

Test Step 2.18 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	16		
	1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0.72		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.72		
DigColPs_ColLPFInitDone_Cnt_M_lgc			
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156		
DigColPs_I2CHwColAngle_Cnt_M_u16	712		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	102		
DigColPs_I2CSensCommFlts_Cnt_M_u08	13		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	68		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	48		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142		
k_SenseDetErrDiag_Cnt_str.Threshold	255		
k_SenseDetErrDiag_Cnt_str.PStep	12		
k_SenseDetErrDiag_Cnt_str.NStep	17		
k_SenseParityErrDiag_Cnt_str.Threshold	150		
k_SenseParityErrDiag_Cnt_str.PStep	31		
k_SenseParityErrDiag_Cnt_str.NStep	16		
k_StepDetect_Deg_f32	52		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	289.175781	289.1757813 ± 0.00048828125	,
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	150	150	
DigColPs_ColParityError_Cnt_M_lgc	1	1	
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	139	139	
DigColPs 12CColSensorFault Cnt M lgc	1	1	
DigColPs I2CHwColAngle Deg M f32	289.175781	289.1757813 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	18.2984314	18.2984375 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145	145	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	12.7441406	12.74414063 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	68	68	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	5.9765625	5.9765625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	,
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-341.701569	-341.7015625 ± 0.00048828125	,
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	150	150	•
	1	1	

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.76		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	526		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	868		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	122		
DigColPs_I2CSensCommFlts_Cnt_M_u08	14		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	155		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	165		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	72		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	51		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.125		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	50		
k_SenseDetErrDiag_Cnt_str.PStep	14		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	160		
k_SenseParityErrDiag_Cnt_str.PStep	33		
k_SenseParityErrDiag_Cnt_str.NStep	17		
k_StepDetect_Deg_f32	54		
Name	Actual Value	Expected Value	Result

K_Otopbeted_beg_to2	0 1		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	537.600037	537.6 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	509	509	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	177.600037	177.6 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	272.5	272.5 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	•

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DigColPs\_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-87.5	-87.5 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	608	608	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	Enable12CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	8.0		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	286		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135		
DigColPs_I2CHwColAngle_Cnt_M_u16	1024		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	142		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	54		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.135		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	170		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	18		
k_StepDetect_Deg_f32	56		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	827.601563	827.6015625 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~

170

0

2

1

116

170

2

1

116

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	107.601563	107.6015625 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	49.5017586	49.50175781 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165	165	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	14.5019531	14.50195313 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76	76	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	49.5017586	49.50175781 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	170	170	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106	106	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 2.21 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.84		
DigColPs ColLPFInitDone Cnt M Igc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	245		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs ColSensorFaultAcc Cnt M u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	1180		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs I2CHwSpurAngle Cnt M u16	162		
DigColPs_I2CSensCommFlts_Cnt_M_u08	16		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	175		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	185		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	80		
DigColPs PrevI2CHwSpurAngle Deg M f32	57		
DigColPs_Previ2CnwSpurArigie_Deg_in_i32 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_ReqizeShsiDataType_Cht_M_uoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.145		
	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	532		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	2		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	20		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	20		
k_SenseParityErrDiag_Cnt_str.Threshold	180		
k_SenseParityErrDiag_Cnt_str.PStep	37		
k_SenseParityErrDiag_Cnt_str.NStep	19		
k_StepDetect_Deg_f32	58		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1128.11987	1128.119922 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	180	180	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
	-		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	10	10	•
		10	
DigColPs_I2CColSensorFault_Cnt_M_Igc	10		
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32	10	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	10 1 48.119873	1 48.11992188 ± 0.0001220703125	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08	10 1 48.119873 190.919525	1 48.11992188 ± 0.0001220703125 190.9195313 ± 0.0001220703125	
DigCoIPs_CoISensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CCoISensorFault_Cnt_M_lgc  DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	10 1 48.119873 190.919525 2	1 48.11992188 ± 0.0001220703125 190.9195313 ± 0.0001220703125 2	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	10 1 48.119873 190.919525 2 1	1	,
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	10 1 48.119873 190.919525 2 1 1775	1	,
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	10 1 48.119873 190.919525 2 1 1775 15.3808594	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10 1 48.119873 190.919525 2 1 175 15.3808594	1 48.11992188 ± 0.0001220703125 190.9195313 ± 0.0001220703125 2 1 175 15.38085938 ± 0.0001220703125 80	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125	$1\\48.11992188 \pm 0.0001220703125\\190.9195313 \pm 0.0001220703125\\2\\11\\175\\15.38085938 \pm 0.0001220703125\\80\\7.03125 \pm 0.0001220703125$	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4	$ 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ 1 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525 1 180	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ 1 \\ 180 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525 1 180 0	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ 1 \\ 180 \\ 0 \\ \end{array} $	
DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525 1 180 0 2	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ 1 \\ 180 \\ 0 \\ 2 \\ 1 \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525 1 180 0 2 1 145	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ 1 \\ 180 \\ 0 \\ 2 \\ 1 \\ 145 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	10 1 48.119873 190.919525 2 1 175 15.3808594 80 7.03125 4 190.919525 1 180 0 2	$ \begin{array}{c} 1 \\ 48.11992188 \pm 0.0001220703125 \\ 190.9195313 \pm 0.0001220703125 \\ 2 \\ 1 \\ 175 \\ 15.38085938 \pm 0.0001220703125 \\ 80 \\ 7.03125 \pm 0.0001220703125 \\ 4 \\ 190.9195313 \pm 0.00048828125 \\ 1 \\ 180 \\ 0 \\ 2 \\ 1 \end{array} $	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	<b>✓</b>	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 2.22 (Repeat Count = 1)			✓	
Name	Input Value			
DigColPsInt_GetData()	3			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.88			
DigColPs_ColLPFInitDone_Cnt_M_lgc	1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	863			
DigColPs_ColRoughTurns_Cnt_M_s16	3			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50			
DigColPs_I2CHwColAngle_Cnt_M_u16	1336			
DigColPs_I2CHwDataType_Cnt_M_u08	0			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	182			
DigColPs_I2CSensCommFlts_Cnt_M_u08	17			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	185			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	195			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	84			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	60			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.155			
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652			
DigColPs_SpurRoughTurns_Cnt_M_s16	3			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144			
k_SenseDetErrDiag_Cnt_str.Threshold	30			
k_SenseDetErrDiag_Cnt_str.PStep	25			
k_SenseDetErrDiag_Cnt_str.NStep	21			
k_SenseParityErrDiag_Cnt_str.Threshold	190			
k_SenseParityErrDiag_Cnt_str.PStep	39			
k_SenseParityErrDiag_Cnt_str.NStep	20			
k_StepDetect_Deg_f32	60.8			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1449.50854	1449.508594 ± 0.00048828125	~	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>	
DigColPs_ColParityErrorAcc_Cnt_M_u16	190	190	<b>✓</b>	
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>	
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	29	29	~	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓	
DigColPs_I2CHwColAngle_Deg_M_f32	9.50854492	9.50859375 ± 0.0001220703125	~	
DigColPs_I2CHwSpurAngle_Deg_M_f32	337.544342	337.5443359 ± 0.0001220703125	~	
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	~	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	185	185	~	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	16.2597656	16.25976563 ± 0.0001220703125	<b>✓</b>	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	84	84	~	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.3828125	7.3828125 ± 0.0001220703125	~	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	337.544342	337.5443359 ± 0.00048828125	~	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	190	190	~	
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~	

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123	123	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.23 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	865		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101		
DigColPs_I2CHwColAngle_Cnt_M_u16	1492		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	202		
DigColPs_I2CSensCommFlts_Cnt_M_u08	18		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	205		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	63		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.165		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	351		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124		
k_SenseDetErrDiag_Cnt_str.Threshold	40		
k_SenseDetErrDiag_Cnt_str.PStep	3		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	200		
k_SenseParityErrDiag_Cnt_str.PStep	41		
k_SenseParityErrDiag_Cnt_str.NStep	21		
k_StepDetect_Deg_f32	62		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1791.76758	1791.767578 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	200	200	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	~
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101	101	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	351.767578	351.7675781 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	129.37616	129.3761719 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195	195	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	17.1386719	17.13867188 ± 0.0001220703125	<b>~</b>

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88	88	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.734375	7.734375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	489.37616	489.3761719 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	200	200	•
DigColPs_SpurParityError_Cnt_M_lgc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124	124	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	456		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144		
DigColPs_I2CHwColAngle_Cnt_M_u16	1648		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	222		
DigColPs_I2CSensCommFlts_Cnt_M_u08	19		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	205		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	215		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	92		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	66.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.175		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	143		
k_SenseDetErrDiag_Cnt_str.Threshold	50		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	50		
k_SenseParityErrDiag_Cnt_str.Threshold	210		
k_SenseParityErrDiag_Cnt_str.PStep	43		
k_SenseParityErrDiag_Cnt_str.NStep	22		
k_StepDetect_Deg_f32	64		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1621.80176	1621.801758 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	210	210	•
DigColPs ColParityError Cnt M Igc	0	0	

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DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	181.801758	181.8017578 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	223.415039	223.4150391 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	205	205	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.0175781	18.01757813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	92	92	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.0859375	8.0859375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	583.415039	583.4150391 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	210	210	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	93	93	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 2.25 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	235		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs ColSensorFaultAcc Cnt M u16	105		
DigColPs_I2CHwColAngle_Cnt_M_u16	1804		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	242		
DigColPs_I2CSensCommFlts_Cnt_M_u08	20		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	225		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96		
DigColPs PrevI2CHwSpurAngle Deg M f32	69		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_ReqizOshsiDataType_Cht_M_u00 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	500		
	0.185		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.185		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	321		
DigColPs_SpurParityErrorAcc_Cnt_M_u16			
DigColPs_SpurRoughTurns_Cnt_M_s16	-4 1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	131		
k_SenseDetErrDiag_Cnt_str.Threshold	60		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	220		
k_SenseParityErrDiag_Cnt_str.PStep	45		
k_SenseParityErrDiag_Cnt_str.NStep	23		
k_StepDetect_Deg_f32	66.6		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1147.7793	1147.779297 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	220	220	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	_   ·
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
		00	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	80	80	
	80	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc			
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32	1	1	•
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	1 67.7792969	1 67.77929688 ± 0.0001220703125	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08	1 67.7792969 142.660919	1 67.77929688 ± 0.0001220703125 142.6609375 ± 0.0001220703125	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc	1 67.7792969 142.660919 6	1 67.77929688 ± 0.0001220703125 142.6609375 ± 0.0001220703125 6	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	1 67.7792969 142.660919 6	1 67.77929688 ± 0.0001220703125 142.6609375 ± 0.0001220703125 6 1	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	1 67.7792969 142.660919 6 1 215	1 67.77929688 ± 0.0001220703125 142.6609375 ± 0.0001220703125 6 1 215	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	1 67.7792969 142.660919 6 1 215 18.8964844	1 67.77929688 ± 0.0001220703125 142.6609375 ± 0.0001220703125 6 1 215 18.89648438 ± 0.0001220703125	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1 67.7792969 142.660919 6 1 215 18.8964844	1 67.77929688 ± 0.0001220703125 142.6609375 ± 0.0001220703125 6 1 215 18.89648438 ± 0.0001220703125 96	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375	$ 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919 1	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ 1 \\ 220 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919 1 220	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ 1 \\ 220 \\ 0 \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_Igc DigCoIPs_SpurRoughTurns_Cnt_M_Igc DigCoIPs_SpurRoughTurns_Cnt_M_st6	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919 1 220 0	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ 1 \\ 220 \\ 0 \\ -4 \\ \end{array} $	
DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919 1 220 0	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ 1 \\ 220 \\ 0 \\ -4 \\ 1 \end{array} $	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_Igc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_Igc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16  Rte_Call_Sa_DicCoIPs_NxtrDiagNarc_SetNTCStatus(NTC_Cnt_T_enum)	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919 1 220 0	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ 1 \\ 220 \\ 0 \\ -4 \\ 1 \\ 106 \\ \end{array} $	
DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1 67.7792969 142.660919 6 1 215 18.8964844 96 8.4375 3 142.660919 1 220 0	$ \begin{array}{c} 1 \\ 67.77929688 \pm 0.0001220703125 \\ 142.6609375 \pm 0.0001220703125 \\ 6 \\ 1 \\ 215 \\ 18.89648438 \pm 0.0001220703125 \\ 96 \\ 8.4375 \pm 0.0001220703125 \\ 3 \\ 142.6609375 \pm 0.00048828125 \\ 1 \\ 220 \\ 0 \\ -4 \\ 1 \end{array} $	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.26 (Repeat Count = 1)			✓		
Name	Input Value				
DigColPsInt_GetData()	7				
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1700				
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.3				
DigColPs_ColLPFInitDone_Cnt_M_lgc		1			
DigColPs_ColParityErrorAcc_Cnt_M_u16	0				
DigColPs_ColRoughTurns_Cnt_M_s16	-4	· ·			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1				
DigColPs ColSensorFaultAcc Cnt M u16	106				
DigColPs_I2CHwColAngle_Cnt_M_u16	1960				
DigColPs_I2CHwDataType_Cnt_M_u08	4				
DigColPs I2CHwSpurAngle Cnt M u16	262				
DigColPs I2CSensCommFlts Cnt M u08	21				
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	225				
DigColPs_PrevI2CHwColAngle_Deg_M_f32	235				
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100				
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	72				
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4				
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	600				
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.195				
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0				
DigColPs_SpurParityErrorAcc_Cnt_M_u16	314				
DigColPs_SpurRoughTurns_Cnt_M_s16	-4				
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0				
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	100				
k SenseDetErrDiag Cnt str.Threshold	70				
k_SenseDetErrDiag_Ont_str.PStep	12				
k SenseDetErrDiag_Ont_str.r Step	2				
k_SenseParityErrDiag_Cnt_str.Threshold	230				
k_SenseParityErrDiag_Cnt_str.PStep	47				
k_SenseParityErrDiag_Cnt_str.NStep	24				
	68				
k_StepDetect_Deg_f32			n   n		
Name	Actual Value	Expected Value	Result		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1508.06738	-1508.067383 ± 0.00048828125	~		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>V</b>		
DigColPs_ColParityErrorAcc_Cnt_M_u16	47	47	~		
DigColPs_ColParityError_Cnt_M_lgc	0	0	~		
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	104	104	<b>*</b>		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>~</b>		
DigColPs_I2CHwColAngle_Deg_M_f32	291.932617	291.9326172 ± 0.0001220703125	~		
DigColPs_I2CHwSpurAngle_Deg_M_f32	203.913879	203.9138672 ± 0.0001220703125	~		
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	225	225	~		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	19.7753906	19.77539063 ± 0.0001220703125	~		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100	100	~		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.7890625	8.7890625 ± 0.0001220703125	~		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	~		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	203.913879	203.9138672 ± 0.00048828125	~		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	230	230	~		
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~		

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	98	98	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.27 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103		
DigColPs_I2CHwColAngle_Cnt_M_u16	2116		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	282		
DigColPs_I2CSensCommFlts_Cnt_M_u08	22		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	235		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	245		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	104		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	75		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.205		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	568		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	182		
k_SenseDetErrDiag_Cnt_str.Threshold	80		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	3		
k_SenseParityErrDiag_Cnt_str.Threshold	240		
k_SenseParityErrDiag_Cnt_str.PStep	49		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	70		
Name	Actual Value	Expected Value	Result

K_0tcpb6tcot_beg_to2	10		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1239.73828	-1239.738281 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100	100	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	200.261719	200.2617188 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	336.973846	336.9738281 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	235	235	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	20.6542969	20.65429688 ± 0.0001220703125	•

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	104	104	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.140625	9.140625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336.973846	336.9738281 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240	240	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	179	179	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	9		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	500		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	151		
DigColPs I2CHwColAngle Cnt M u16	2272		
DigColPs I2CHwDataType Cnt M u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	302		
DigColPs I2CSensCommFlts Cnt M u08	23		
DigColPs PrevI2CHwColAngle Cnt M u16	245		
DigColPs PrevI2CHwColAngle Deg M f32	255		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	108		
DigColPs PrevI2CHwSpurAngle Deg M f32	78		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.215		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	425		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	150		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	18		
k_SenseDetErrDiag_Cnt_str.NStep	4		
k_SenseParityErrDiag_Cnt_str.Threshold	250		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	26		
k_StepDetect_Deg_f32	72.2		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-919.233398	-919.2333984 ± 0.00048828125	
DigColPs ColLPFInitDone Cnt M Igc	1	1	

250

1

-1

1

147

250

1

-1

1

147

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	160.766602	160.7666016 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	115.240814	115.2408203 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	245	245	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	21.5332031	21.53320313 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	108	108	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.4921875	9.4921875 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	475.240814	475.2408203 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	250	250	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146	146	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.29 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	253		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165		
DigColPs_I2CHwColAngle_Cnt_M_u16	2428		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
0igColPs_I2CHwSpurAngle_Cnt_M_u16	322		
ligColPs_I2CSensCommFlts_Cnt_M_u08	24		
igColPs_PrevI2CHwColAngle_Cnt_M_u16	255		
igColPs_PrevI2CHwColAngle_Deg_M_f32	265		
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112		
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	81		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
ligColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.225		
ligColPs_SpurLPFInitDone_Cnt_M_lgc	1		
igColPs_SpurParityErrorAcc_Cnt_M_u16	965		
ligColPs_SpurRoughTurns_Cnt_M_s16	-1		
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
igColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
_SenseDetErrDiag_Cnt_str.Threshold	100		
_SenseDetErrDiag_Cnt_str.PStep	21		
_SenseDetErrDiag_Cnt_str.NStep	5		
_SenseParityErrDiag_Cnt_str.Threshold	260		
_SenseParityErrDiag_Cnt_str.PStep	2		
_SenseParityErrDiag_Cnt_str.NStep	27		
_StepDetect_Deg_f32	74		
lame	Actual Value	Expected Value	Res
higColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-546.552673	-546.5527344 ± 0.00048828125	
igColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
igColPs_ColParityErrorAcc_Cnt_M_u16	255	255	
igColPs_ColParityError_Cnt_M_lgc	0	0	
igColPs_ColRoughTurns_Cnt_M_s16	0	0	
igColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
igColPs ColSensorFaultAcc Cnt M u16	160	160	
igColPs_I2CColSensorFault_Cnt_M_lgc	0	0	
igColPs_I2CHwColAngle_Deg_M_f32	173.447327	173.4472656 ± 0.0001220703125	
igColPs I2CHwSpurAngle Deg M f32	258.714844	258.7148438 ± 0.0001220703125	
igcoir s_izor iwopurArigie_beg_ivi_ioz		10	
igColDo 12CConoCommElto Cot M u09			
	10		
ligColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16	1 255	1 255	
igColPs_I2CSpurSensorFault_Cnt_M_Igc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32	1 255 22.4121094	1 255 22.41210938 ± 0.0001220703125	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1 255 22.4121094 112	1 255 22.41210938 ± 0.0001220703125 112	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	1 255 22.4121094 112 9.84375	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	1 255 22.4121094 112 9.84375 2	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	1 255 22.4121094 112 9.84375 2 618.714844	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16	1 255 22.4121094 112 9.84375 2 618.714844 1	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc igCoIPs_SpurParityErrorAcc_Cnt_M_u16 igCoIPs_SpurParityErrorCnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844 1 260	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityErrorCnt_M_lgc igColPs_SpurParityErrorCnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0	
bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Deg_M_f32 bigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 bigColPs_PrevI2CHwSpurAngle_Deg_M_f32 bigColPs_ReqI2CSnsrDataType_Cnt_M_u08 bigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 bigColPs_SpurLPFInitDone_Cnt_M_lgc bigColPs_SpurParityErrorAcc_Cnt_M_u16 bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0 -1 1	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0 -1	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0 -1 1 1 0	
DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorCnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigCoIPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Sa_DigCoIPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0 -1 1	

Test Step 2.30 (Repeat Count = 1)



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

rest stop 2.00 (Repeat Sount 1)			
Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	126		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
DigColPs_I2CHwColAngle_Cnt_M_u16	2584		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs I2CHwSpurAngle Cnt M u16	342		
DigColPs I2CSensCommFlts Cnt M u08	25		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235		
DigColPs SpurLPFInitDone Cnt M Igc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	110		
k_SenseDetErrDiag_Cnt_str.PStep	24		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k SenseParityErrDiag Cnt str.Threshold	270		
k_SenseParityErrDiag_Cnt_str.PStep	4		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	76		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-121.696289	-121.6962891 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	238.303711	238.3037109 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	47.395874	47.39589844 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>V</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	<b>~</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	116	
DiqColPs PrevI2CHwSpurAngle Deg M f32	10.1953125	10.1953125 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	767.395874	767.3958984 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	
,			

 $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte Call Sa DigColPs NytrDiagMgr SetNTCStatus(Param Cnt T u08)	11	11	<b>•</b>

Test Step Call Trace   ✓							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~			
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•			
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•			
EnableI2CInterrupt	1	Enablel2CInterrupt	1	•			
DiagnosticThreshold	2	DiagnosticThreshold	2	~			
OddParityFault	2	OddParityFault	2	•			
DiagnosticThreshold	2	DiagnosticThreshold	2	~			
ComputeRoughTurns	2	ComputeRoughTurns	2	•			
ConstrainOneRev	2	ConstrainOneRev	2	•			
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•			
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•			
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>			

Test Step 2.31 (Repeat Count = 1)	1 (1)	
Name	Input Value	
DigColPsInt_GetData()	12	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1200	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	142	
DigColPs_ColRoughTurns_Cnt_M_s16	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185	
DigColPs_I2CHwColAngle_Cnt_M_u16	2740	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	362	
DigColPs_I2CSensCommFlts_Cnt_M_u08	26	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	275	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	285	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	120	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	87.7	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.245	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	523	
DigColPs SpurRoughTurns Cnt M s16	1	
DigColPs SpurSensorDiagFailed Cnt M lgc	1	
DigColPs SpurSensorFaultAcc Cnt M u16	155	
k SenseDetErrDiag Cnt str.Threshold	120	
k SenseDetErrDiag Cnt str.PStep	27	
k SenseDetErrDiag Cnt str.NStep	7	
k SenseParityErrDiag Cnt str.Threshold	280	
k SenseParityErrDiag Cnt str.PStep	6	
k_SenseParityErrDiag_Cnt_str.NStep	29	
k StepDetect Deg f32	78	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	355.335938	355.3359375 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	148	148	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	178	178	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	355.335938	355.3359375 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	201.283997	201.2839844 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	275	275	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	24.1699219	24.16992188 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	120	120	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.546875	10.546875 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	921.283997	921.2839844 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	280	280	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	148	148	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	13		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1100		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.9		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195		
DigColPs_I2CHwColAngle_Cnt_M_u16	2896		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	27		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	285		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	295		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	124		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	90		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.255		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	654		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124		
k_SenseDetErrDiag_Cnt_str.Threshold	130		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	8		
k_SenseParityErrDiag_Cnt_str.Threshold	290		
k_SenseParityErrDiag_Cnt_str.PStep	8		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	80		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	884.543945	884.5439453 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DiaColPs ColParityErrorAcc Cnt M u16	290	200	

290

0

3

1

187

290

3

1

187

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	164.543945	164.5439453 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.379150391	0.379101562 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	285	285	-
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.0488281	25.04882813 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	124	124	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.8984375	10.8984375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.37915	1080.379102 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	290	290	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	-
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	116	116	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.33 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	14		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	856		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142		
)igColPs_I2CHwColAngle_Cnt_M_u16	3052		
ligColPs_I2CHwDataType_Cnt_M_u08	1		
ligColPs_I2CHwSpurAngle_Cnt_M_u16	402		
igColPs_I2CSensCommFlts_Cnt_M_u08	28		
igColPs_PrevI2CHwColAngle_Cnt_M_u16	295		
igColPs_PrevI2CHwColAngle_Deg_M_f32	305		
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	128		
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	93		
igColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300		
ligColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.265		
igColPs_SpurLPFInitDone_Cnt_M_lgc	1		
igColPs_SpurParityErrorAcc_Cnt_M_u16	258		
igColPs_SpurRoughTurns_Cnt_M_s16	3		
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
igColPs_SpurSensorFaultAcc_Cnt_M_u16	128		
_SenseDetErrDiag_Cnt_str.Threshold	140		
_SenseDetErrDiag_Cnt_str.PStep	33		
_SenseDetErrDiag_Cnt_str.NStep	9		
_SenseParityErrDiag_Cnt_str.Threshold	300		
_SenseParityErrDiag_Cnt_str.PStep	10		
_SenseParityErrDiag_Cnt_str.NStep	31		
_StepDetect_Deg_f32	82		
lame	Actual Value	Expected Value	Res
		1465.927734 ± 0.00048828125	
igColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1465.92773		
		0	
igColPs_ColLPFInitDone_Cnt_M_lgc	0		
igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16	0 300	300	
igColPs_ColLPFInitDone_Cnt_M_Igc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_Igc	0		
igCoIPs_CoILPFInitDone_Cnt_M_Igc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_Igc igCoIPs_CoIRoughTurns_Cnt_M_s16	0 300 1 4	300 1 4	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc	0 300 1 4 0	300 1 4 0	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc	0 300 1 4 0	300 1 4 0 133	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_CoISensorFaultAcc_Cnt_M_u16	0 300 1 4 0 133	300 1 4 0 133	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32	0 300 1 4 0 133 1 25.9277344	300 1 4 0 133 1 25.92773438 ± 0.0001220703125	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32	0 300 1 4 0 133 1 25.9277344 164.681274	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_u16 igCoIPs_I2CCoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08	0 300 1 4 0 133 1 25.9277344 164.681274	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc	0 300 1 4 0 133 1 25.9277344 164.681274 14	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	0 300 1 4 0 133 1 25.9277344 164.681274 14 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 295 25.9277344	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 295 25.9277344 128	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128	
gCoIPs_CoILPFInitDone_Cnt_M_lgc gCoIPs_CoIParityErrorAcc_Cnt_M_u16 gCoIPs_CoIParityError_Cnt_M_lgc gCoIPs_CoIParityError_Cnt_M_lgc gCoIPs_CoISensorDiagFailed_Cnt_M_lgc gCoIPs_CoISensorFaultAcc_Cnt_M_u16 gCoIPs_I2CCoISensorFault_Cnt_M_lgc gCoIPs_I2CHwCoIAngle_Deg_M_f32 gCoIPs_I2CHwSpurAngle_Deg_M_f32 gCoIPs_I2CSensCommFlts_Cnt_M_u08 gCoIPs_I2CSpurSensorFault_Cnt_M_lgc gCoIPs_I2CSpurSensorFault_Cnt_M_u16 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 295 25.9277344 128 11.25	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125	
gCoIPs_CoILPFInitDone_Cnt_M_lgc gCoIPs_CoIParityErrorAcc_Cnt_M_u16 gCoIPs_CoIParityError_Cnt_M_lgc gCoIPs_CoIParityError_Cnt_M_lgc gCoIPs_CoIRoughTurns_Cnt_M_s16 gCoIPs_CoISensorDiagFailed_Cnt_M_lgc gCoIPs_CoISensorFaultAcc_Cnt_M_u16 gCoIPs_I2CCoISensorFault_Cnt_M_lgc gCoIPs_I2CHwCoIAngle_Deg_M_f32 gCoIPs_I2CHwSpurAngle_Deg_M_f32 gCoIPs_I2CSensCommFlts_Cnt_M_u08 gCoIPs_I2CSpurSensorFault_Cnt_M_lgc gCoIPs_I2CSpurSensorFault_Cnt_M_u16 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PRevI2CHwSpurAngle_Deg_M_f32 gCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 295 25.9277344 128 11.25 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSensCommFlts_Cnt_M_u16 igCoIPs_I2CSensCommFlts_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Ont_M_u16 igCoIPs_PrevI2CHwSpurAngle_Ont_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 295 25.9277344 128 11.25 1 1244.68127	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSensCommFlts_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_On_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 295 25.9277344 128 11.25 1 1244.68127 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFits_Cnt_M_u08 igCoIPs_I2CSensCommFits_Cnt_M_u6 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurAprityErrorAcc_Cnt_M_u16	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 1 295 25.9277344 128 11.25 1 1244.68127 1 1268	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1 268	
igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSensCommFlts_Cnt_M_u6 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurParityErrorAcc_Cnt_M_u16 igCoIPs_SpurParityErrorAcc_Cnt_M_u16 igCoIPs_SpurParityError_Cnt_M_lgc	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 1 295 25.9277344 128 11.25 1 1244.68127 1 268 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1 268 1	
igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityError_Cnt_M_lgc igColPs_SpurParityError_Cnt_M_lgc igColPs_SpurRoughTurns_Cnt_M_s16	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 1 295 25.9277344 128 11.25 1 1244.68127 1 268 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1 268 1 3	
DigCoIPs_CoILPFInitDone_Cnt_M_lgc DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u16 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 1295 25.9277344 128 11.25 1 1244.68127 1 268 1 3 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1 268 1 3 1	
DigCoIPs_CoIAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_CoILPFInitDone_Cnt_M_Igc DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIParityError_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 1 295 25.9277344 128 11.25 1 1244.68127 1 268 1 3 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1 268 1 3 1 119	
DigCoIPs_CoILPFInitDone_Cnt_M_lgc DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u16 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 300 1 4 0 133 1 25.9277344 164.681274 14 1 1295 25.9277344 128 11.25 1 1244.68127 1 268 1 3 1	300 1 4 0 133 1 25.92773438 ± 0.0001220703125 164.68125 ± 0.0001220703125 14 1 295 25.92773438 ± 0.0001220703125 128 11.25 ± 0.0001220703125 1 1244.68125 ± 0.00048828125 1 268 1 3 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.34 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.224		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	146		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	3208		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	422		
DigColPs_I2CSensCommFlts_Cnt_M_u08	29		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	305		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	315		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	132		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	96		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.275		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	150		
k_SenseDetErrDiag_Cnt_str.PStep	36		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	310		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	32		
k_StepDetect_Deg_f32	84		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-289.195313	-289.1953125 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	158	158	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	70.8046875	70.8046875 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	73.1904297	73.19042969 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	15	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	305	305	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	26.8066406	26.80664063 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	132	132	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.6015625	11.6015625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1513.19043	1513.19043 ± 0.00048828125	<b>~</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>V</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	12	12	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	16
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-800
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.226
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	756
DigColPs_ColRoughTurns_Cnt_M_s16	4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	163
DigColPs_I2CHwColAngle_Cnt_M_u16	3364
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	442
DigColPs_I2CSensCommFlts_Cnt_M_u08	30
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	315
DigColPs_PrevI2CHwColAngle_Deg_M_f32	325
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	136
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	99.1
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.285
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000
DigColPs_SpurRoughTurns_Cnt_M_s16	4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146
k_SenseDetErrDiag_Cnt_str.Threshold	160
k_SenseDetErrDiag_Cnt_str.PStep	39
k_SenseDetErrDiag_Cnt_str.NStep	11
k_SenseParityErrDiag_Cnt_str.Threshold	320
k_SenseParityErrDiag_Cnt_str.PStep	14
k_SenseParityErrDiag_Cnt_str.NStep	33
k_StepDetect_Deg_f32	86

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-206.143066	-206.1430664 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	320	320	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152	152	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	153.856934	153.8569336 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	148.906616	148.9066406 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	315	315	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	27.6855469	27.68554688 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	136	136	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.953125	11.953125 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1588.90662	1588.906641 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	320	320	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.36 (Repeat Count = 1)	Inmut Value		
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.228		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	964		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	3520		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	462		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	325		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	335		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	140		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	102		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.295		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	501		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	170		
k_SenseDetErrDiag_Cnt_str.PStep	42		
k_SenseDetErrDiag_Cnt_str.NStep	12		
k_SenseParityErrDiag_Cnt_str.Threshold	330		
k_SenseParityErrDiag_Cnt_str.PStep	16		
k_SenseParityErrDiag_Cnt_str.NStep	34		
k_StepDetect_Deg_f32	88.5		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-786.640015	-786.64 ± 0.00048828125	~
DigColPs ColLPFInitDone Cnt M Igc	1	1	•

930

0

-3

1

0

930

-3

1

0

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	293.359985	293.36 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	89.4000244	89.4 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	809.400024	809.4 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	467	467	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•





Test Step 2.37 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.23		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	746		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158		
DigColPs_I2CHwColAngle_Cnt_M_u16	3676		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	482		
DigColPs_I2CSensCommFlts_Cnt_M_u08	1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	345		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	144		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	105		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.305		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	235		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	180		
k_SenseDetErrDiag_Cnt_str.PStep	45		
k_SenseDetErrDiag_Cnt_str.NStep	13		
k_SenseParityErrDiag_Cnt_str.Threshold	340		
k_SenseParityErrDiag_Cnt_str.PStep	18		
k_SenseParityErrDiag_Cnt_str.NStep	35		
k_StepDetect_Deg_f32	90		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-710.400024	-710.4 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	340	340	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	145	145	
DigColPs I2CColSensorFault Cnt M Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	9.59997559	9.6 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	135.960144	135.9601563 ± 0.0001220703125	
DigColPs I2CSensCommFlts Cnt M u08	1	1	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
·		0 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	144	144	
		12.65625 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	12.65625	0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0	0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0 855.960144	855.9601563 ± 0.00048828125	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	0 855.960144 1	855.9601563 ± 0.00048828125	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	0 855.960144 1 253	855.9601563 ± 0.00048828125 1 253	
DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_igc	0 855.960144 1 253 0	855.9601563 ± 0.00048828125 1 253 0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	0 855.960144 1 253 0	855.9601563 ± 0.00048828125 1 253 0 -3	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 855.960144 1 253 0 -3	855.9601563 ± 0.00048828125 1 253 0 -3 0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 855.960144 1 253 0 -3 0	855.9601563 ± 0.00048828125 1 253 0 -3 0 133	0
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 855.960144 1 253 0 -3 0 133	855.9601563 ± 0.00048828125 1 253 0 -3 0 133 109	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 855.960144 1 253 0 -3 0	855.9601563 ± 0.00048828125 1 253 0 -3 0 133	0



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.38 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.232		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	865		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125		
DigColPs_I2CHwColAngle_Cnt_M_u16	3832		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	2		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	355		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	148		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	108		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.315		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158		
k_SenseDetErrDiag_Cnt_str.Threshold	190		
k_SenseDetErrDiag_Cnt_str.PStep	48		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	350		
k_SenseParityErrDiag_Cnt_str.PStep	20		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	92		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-551.060364	-551.0603906 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	350	350	•
DigColPs_ColParityError_Cnt_M_lgc	1	1	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	111	111	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	168.939636	168.9396094 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	137.982468	137.9824609 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	148	148	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	13.0078125	13.0078125 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-222.017532	-222.0175391 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	165	165	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144	144	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.39 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.234		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165		
DigColPs_I2CHwColAngle_Cnt_M_u16	3988		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	522		
DigColPs_I2CSensCommFlts_Cnt_M_u08	3		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2047		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	152		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	111		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	10		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.325		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	568		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	200		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	15		
k_SenseParityErrDiag_Cnt_str.Threshold	360		
k_SenseParityErrDiag_Cnt_str.PStep	22		
k_SenseParityErrDiag_Cnt_str.NStep	37		
k_StepDetect_Deg_f32	94		
Name	Actual Value	Expected Value	Result

7	
Actual Value Expected Value Result	Name
-517.020569 -517.0205664 ± 0.00048828125	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32
0	DigColPs_ColLPFInitDone_Cnt_M_lgc
360	DigColPs_ColParityErrorAcc_Cnt_M_u16
0 0	DigColPs_ColParityError_Cnt_M_lgc
-3	DigColPs_ColRoughTurns_Cnt_M_s16
0	DigColPs_ColSensorDiagFailed_Cnt_M_lgc
150	DigColPs_ColSensorFaultAcc_Cnt_M_u16
1 1	DigColPs_I2CColSensorFault_Cnt_M_Igc
202.979431 202.9794336 ± 0.0001220703125	DigColPs_I2CHwColAngle_Deg_M_f32
254.091797 254.0917969 ± 0.0001220703125	DigColPs_I2CHwSpurAngle_Deg_M_f32
3	DigColPs_I2CSensCommFlts_Cnt_M_u08
1 1	DigColPs_I2CSpurSensorFault_Cnt_M_lgc
2047	DigColPs_PrevI2CHwColAngle_Cnt_M_u16
179.912109 179.9121094 ± 0.0001220703125	DigColPs_PrevI2CHwColAngle_Deg_M_f32
	·

DigColPs\_Per1

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	152	152	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	13.359375	13.359375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-105.908195	-105.9082031 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	360	360	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	171	171	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt GetData()	4		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.236		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	542		
DigColPs_I2CSensCommFlts_Cnt_M_u08	4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	7		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	114		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	20		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.335		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	624		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152		
k_SenseDetErrDiag_Cnt_str.Threshold	210		
k_SenseDetErrDiag_Cnt_str.PStep	1		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	370		
k_SenseParityErrDiag_Cnt_str.PStep	24		
k_SenseParityErrDiag_Cnt_str.NStep	38		
k_StepDetect_Deg_f32	96		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-314.014801	-314.0148047 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColDo, ColDorityErrorAco, Cot. M. u16	100	160	

169

-1

1

0

169

-1

1

0

DigCoIPs\_CoIParityErrorAcc\_Cnt\_M\_u16
DigCoIPs\_CoIParityError\_Cnt\_M\_Igc
DigCoIPs\_CoIRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	45.985199	45.98519531 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	13.2999992	13.3 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	7	7	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.615234375	0.615234375 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	13.2999992	13.3 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	370	370	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	136	136	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>





Name	Input Value	
DigColPsInt_GetData()	5	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.238	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs ColParityErrorAcc Cnt M u16	235	
DigColPs_ColRoughTurns_Cnt_M_s16	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255	
DigColPs_I2CHwColAngle_Cnt_M_u16	8	
DigColPs I2CHwDataType Cnt M u08	4	
DigColPs I2CHwSpurAngle Cnt M u16	562	
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	49	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	117	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	30	
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.345	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
DigColPs_SpurP=rintDorie_Crit_ivi_igc DigColPs_SpurParityErrorAcc_Cnt_M_u16	354	
DigColPs SpurRoughTurns Cnt M s16	0	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_spurSensorFaultAcc Cnt M u16	175	
k_SenseDetErrDiag_Cnt_str.Threshold	220	
k SenseDetErrDiag Cnt_str.PStep	2	
	17	
k_SenseDetErrDiag_Cnt_str.NStep	380	
k_SenseParityErrDiag_Cnt_str.Threshold	26	
k_SenseParityErrDiag_Cnt_str.PStep	39	
k_SenseParityErrDiag_Cnt_str.NStep	98	
k_StepDetect_Deg_f32		- In
Name	Actual Value Expected Val	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		0.00048828125
DidColPs Coll Pelniflone Cnt M. Idc		
	0 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	261 261	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc	261 0 261 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	261 261 0 0 0 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16	261 261 0 0 0 0 0 0 0 0 238 238	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_12CCoISensorFault_Cnt_M_lgc	261 261 0 0 0 0 0 0 0 0 0 0 0 238 238 0 0 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc  DigCoIPs_CoISensorFaultAcc_Cnt_M_u16  DigCoIPs_12CCoISensorFault_Cnt_M_lgc  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_DFKSV_Cnt_M_str.SV_Uls_f32	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFlts_Cnt_M_u08  DigCoIPs_12CSensCommFlts_Cnt_M_u08  DigCoIPs_Prevl2CHwColAngle_Deg_M_f32  DigCoIPs_Prevl2CHwColAngle_Deg_M_f32  DigCoIPs_Prevl2CHwColAngle_Deg_M_f32  DigCoIPs_Prevl2CHwColAngle_Deg_M_f32  DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32  DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32  DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngle_Def_M_f32  DigCoIPs_SpurAngle_DefKSV_Cnt_M_u08  DigCoIPs_SpurAngle_DefKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_ColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_Prevl2CHwColAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwColAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_u68 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHsSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHsSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_lgc	261	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16  DigCoIPs_CoIParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CCHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_Prev12CHwColAngle_Cnt_M_u16  DigCoIPs_Prev12CHwColAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHsypurAngle_Deg_M_f32  DigCoIPs_Prev12CHsypurAngle_Deg_M_f32  DigCoIPs_Prev12CHsypurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	261	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_CoILPFInitDone_Cnt_M_Igc DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	261       261         0       0         0       0         0       0         238       238         0       0         208.624985       208.6249805 ±         19.6196785       19.61967773 ±         5       5         1       1         49       49         4.30664063       4.306640625 ±         4095       359.912109         359.912109       359.9121094 ±         4       4         19.6196785       19.61967773 ±         1       1         380       380         0       0         -1       -1         0       0         158       158	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CCHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_Prev12CHwColAngle_Cnt_M_u16  DigCoIPs_Prev12CHwColAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32  DigCoIPs_Prev12CHsypurAngle_Deg_M_f32  DigCoIPs_Prev12CHsypurAngle_Deg_M_f32  DigCoIPs_Prev12CHsypurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	261	0.0001220703125 0.0001220703125 0.0001220703125



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.42 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156		
DigColPs_I2CHwColAngle_Cnt_M_u16	15		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs I2CHwSpurAngle Cnt M u16	582		
DigColPs I2CSensCommFlts Cnt M u08	6		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	91		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.7		
DigColPs PrevI2CHwSpurAngle Cnt M u16	2047		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	120		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	40		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.355		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs SpurParityErrorAcc Cnt M u16	536		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs SpurSensorFaultAcc Cnt M u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	230		
k_SenseDetErrDiag_Cnt_str.PStep	3		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	390		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	40		
k_StepDetect_Deg_f32	100		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	12.3195267	12.31953125 ± 0.00048828125	Result
	1	1	
DigColPs_ColLPFInitDone_Cnt_M_lgc	284	284	
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	-
DigColPs_ColParityError_Cnt_M_lgc	1	1	
DigColPs_ColRoughTurns_Cnt_M_s16	1		-
DigColPs_ColSensorDiagFailed_Cnt_M_lgc		1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	138	138	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	12.3195267	12.31953125 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	217.468796	217.4687988 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	91	91	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	7.99804688	7.998046875 ± 0.0001220703125	<b>V</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047	2047	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	179.912109	179.9121094 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	<b>V</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	217.468796	217.4687988 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	· ·
DigColPs_SpurParityErrorAcc_Cnt_M_u16	390	390	<b>V</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.43 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.242		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	142		
DigColPs_ColRoughTurns_Cnt_M_s16	-5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131		
DigColPs_I2CHwColAngle_Cnt_M_u16	22		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	602		
DigColPs_I2CSensCommFlts_Cnt_M_u08	7		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	20		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	123		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	50		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.365		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142		
k_SenseDetErrDiag_Cnt_str.Threshold	240		
k_SenseDetErrDiag_Cnt_str.PStep	4		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	400		
k_SenseParityErrDiag_Cnt_str.PStep	30		
k_SenseParityErrDiag_Cnt_str.NStep	41		
k_StepDetect_Deg_f32	102		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-432.771149	-432.7711523 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	172	172	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	112	112	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	287.228851	287.2288477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.757995605	0.758007813 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133	133	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	11.6894531	11.68945313 ± 0.0001220703125	<b>✓</b>

DigColPs\_Per1

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100	100	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.7890625	8.7890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-359.242004	-359.2419922 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	400	400	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123	123	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.44 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt GetData()	8		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	100		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.244		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	253		
DigColPs ColRoughTurns Cnt M s16	5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	100		
DigColPs I2CHwColAngle Cnt M u16	29		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	622		
DigColPs I2CSensCommFlts Cnt M u08	8		
DigColPs PrevI2CHwColAngle Cnt M u16	175		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.6		
DigColPs PrevI2CHwSpurAngle Cnt M u16	200		
DigColPs PrevI2CHwSpurAngle Deg M f32	126		
DigColPs RegI2CSnsrDataType Cnt M u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	60		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.375		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	586		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	250		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	20		
k_SenseParityErrDiag_Cnt_str.Threshold	410		
k_SenseParityErrDiag_Cnt_str.PStep	32		
k_SenseParityErrDiag_Cnt_str.NStep	42		
k_StepDetect_Deg_f32	104		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	518.552979	518.5529297 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	285	285	•
DI O ID O ID II E O I M I			

0

5

1

80

5

1

80

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	158.552979	158.5529297 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.091797	359.0917969 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	175	175	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.3808594	15.38085938 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	200	200	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	17.578125	17.578125 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	719.091797	719.0917969 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	410	410	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	166	166	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

Test Step Call Trace	lest Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.45 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	625		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	182		
DigColPs I2CHwColAngle Cnt M u16	2140		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs I2CHwSpurAngle Cnt M u16	2924		
DigColPs_I2CSensCommFlts_Cnt_M_u08	8		
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	65		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	24.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.44		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
bigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs SpurRoughTurns Cnt M s16	0		
· - · · ·	1		
ligColPs_SpurSensorDiagFailed_Cnt_M_lgc ligColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
	16		
_SenseDetErrDiag_Cnt_str.Threshold			
_SenseDetErrDiag_Cnt_str.PStep	40		
_SenseDetErrDiag_Cnt_str.NStep	9		
SenseParityErrDiag_Cnt_str.Threshold	70		
x_SenseParityErrDiag_Cnt_str.PStep	15		
x_SenseParityErrDiag_Cnt_str.NStep	8		
_StepDetect_Deg_f32	36.3		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	266.285156	266.2851563 ± 0.00048828125	
ligColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
ligColPs_ColParityErrorAcc_Cnt_M_u16	70	70	
igColPs_ColParityError_Cnt_M_lgc	0	0	
ligColPs_ColRoughTurns_Cnt_M_s16	1	1	
igColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
	1 173	1 173	
ligColPs_ColSensorFaultAcc_Cnt_M_u16			
ligColPs_ColSensorFaultAcc_Cnt_M_u16 ligColPs_I2CColSensorFault_Cnt_M_lgc	173	173	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32	173 1	173 1	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32	173 1 266.285156	173 1 266.2851563 ± 0.0001220703125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08	173 1 266.285156 161.392212	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	173 1 266.285156 161.392212 8	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8	
igColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFlts_Cnt_M_u08 bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_Previ2CHwColAngle_Cnt_M_u16	173 1 266.285156 161.392212 8	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1	
igColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFlts_Cnt_M_u08 bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Deg_M_f32	173 1 266.285156 161.392212 8 1 65	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	173 1 266.285156 161.392212 8 1 65 5.71289063	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16 igColPs_PrevI2CHwSpurAngle_Deg_M_f32	173 1 266.285156 161.392212 8 1 65 5.71289063 36	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1	$173$ $1$ $266.2851563 \pm 0.0001220703125$ $161.3921875 \pm 0.0001220703125$ $8$ $1$ $65$ $5.712890625 \pm 0.0001220703125$ $36$ $3.1640625 \pm 0.0001220703125$ $3$ $-558.6078125 \pm 0.00048828125$ $1$	
igCoIPs_CoISensorFaultAcc_Cnt_M_u16  digCoIPs_I2CCoISensorFault_Cnt_M_lgc  digCoIPs_I2CHwCoIAngle_Deg_M_f32  digCoIPs_I2CHwSpurAngle_Deg_M_f32  digCoIPs_I2CSensCommFlts_Cnt_M_u08  digCoIPs_I2CSpurSensorFault_Cnt_M_lgc  digCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  digCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  digCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  digCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  digCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  digCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  digCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  digCoIPs_SpurLPFInitDone_Cnt_M_lgc  digCoIPs_SpurParityErrorAcc_Cnt_M_u16  digCoIPs_SpurParityError_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1 70	$173$ $1$ $266.2851563 \pm 0.0001220703125$ $161.3921875 \pm 0.0001220703125$ $8$ $1$ $65$ $5.712890625 \pm 0.0001220703125$ $36$ $3.1640625 \pm 0.0001220703125$ $3$ $-558.6078125 \pm 0.00048828125$ $1$ $70$	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1 70 0	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125 1 70 0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3558.607788 1 70 0 0 1	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125 1 70 0 0 1	
DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CCOISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurPrinitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigC	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1 70 0 0 1 175	$173$ $1$ $266.2851563 \pm 0.0001220703125$ $161.3921875 \pm 0.0001220703125$ $8$ $1$ $65$ $5.712890625 \pm 0.0001220703125$ $3$ $3.1640625 \pm 0.0001220703125$ $3$ $-558.6078125 \pm 0.00048828125$ $1$ $70$ $0$ $0$ $1$ $175$	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3558.607788 1 70 0 0 1	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125 1 70 0 0 1	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.46 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150		
DigColPs_I2CHwColAngle_Cnt_M_u16	1024		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	142		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	54		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.135		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	170		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	18		
k_StepDetect_Deg_f32	56		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1403.60156	1403.601563 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131	131	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	323.601563	323.6015625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	195.301773	195.3017578 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165	165	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	14.5019531	14.50195313 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76	76	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	195.301773	195.3017578 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	170	170	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	167	167	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.47 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.246		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	532		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141		
DigColPs_I2CHwColAngle_Cnt_M_u16	36		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	642		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	217		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	30		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	129		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	70		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.385		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	286		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156		
k_SenseDetErrDiag_Cnt_str.Threshold	12		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	21		
k_SenseParityErrDiag_Cnt_str.Threshold	420		
k_SenseParityErrDiag_Cnt_str.PStep	34		
k_SenseParityErrDiag_Cnt_str.NStep	43		
k_StepDetect_Deg_f32	106		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-110.188232	-110.1882227 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	420	420	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	249.811768	249.8117773 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.801392	218.8013672 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	217	217	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	19.0722656	19.07226563 ± 0.0001220703125	<b>~</b>

DigColPs\_Per1

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	300	300	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	26.3671875	26.3671875 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-501.198608	-501.1986328 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	320	320	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	<b>~</b>
Enablel2CInterrupt	1	Enable12CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.248		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	652		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142		
DigColPs_I2CHwColAngle_Cnt_M_u16	43		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	662		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	259		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	35.2		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	132		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	80		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	134		
k_SenseDetErrDiag_Cnt_str.Threshold	14		
k_SenseDetErrDiag_Cnt_str.PStep	7		
k_SenseDetErrDiag_Cnt_str.NStep	22		
k_SenseParityErrDiag_Cnt_str.Threshold	430		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	44		
k_StepDetect_Deg_f32	108		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	141.965393	141.9653906 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	·
DigColPs_ColParityErrorAcc_Cnt_M_u16	430	430	-
Di O ID O ID II E O LAN L	_	_	

0

-1

1

120

0

-1

1

120

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	141.965393	141.9653906 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.08667	298.0867188 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	259	259	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	22.7636719	22.76367188 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	400	400	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	35.15625	35.15625 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1501.91333	-1501.913281 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	289	289	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	112	112	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>



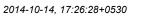


Test Step 2.49 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.25		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	351		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	50		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	682		
DigColPs_I2CSensCommFlts_Cnt_M_u08	11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	301		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	40		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	500		
DigColPs PrevI2CHwSpurAngle Deg M f32	135		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	90		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.405		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurPrintDorle_Cnt_in_igc  DigColPs_SpurParityErrorAcc_Cnt_M_u16	236		
DigColPs_SpurRoughTurns_Cnt_M_s16	11		
	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
	16		
k_SenseDetErrDiag_Cnt_str.Threshold	8		
k_SenseDetErrDiag_Cnt_str.PStep	23		
k_SenseDetErrDiag_Cnt_str.NStep	440		
k_SenseParityErrDiag_Cnt_str.Threshold			
k_SenseParityErrDiag_Cnt_str.PStep	38		
k_SenseParityErrDiag_Cnt_str.NStep	45		
k_StepDetect_Deg_f32	110	1	1_
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	36.6137695	36.61376953 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	389	389	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs 12CColSensorFault Cnt M Igo	0	0	
	0		
DigColPs_I2CHwColAngle_Deg_M_f32	36.6137695	36.61376953 ± 0.0001220703125	
DigColPs_I2CHwColAngle_Deg_M_f32		36.61376953 ± 0.0001220703125 235.1478516 ± 0.0001220703125	
DigColPs_I2CHwColAngle_Deg_M_f32	36.6137695		
DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08	36.6137695 235.147827 11 0	235.1478516 ± 0.0001220703125 11 0	
DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08	36.6137695 235.147827 11	235.1478516 ± 0.0001220703125 11	
DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	36.6137695 235.147827 11 0	235.1478516 ± 0.0001220703125 11 0	
DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36.6137695 235.147827 11 0 301	235.1478516 ± 0.0001220703125 11 0 301	
DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36.6137695 235.147827 11 0 301 26.4550781	235.1478516 ± 0.0001220703125 11 0 301 26.45507813 ± 0.0001220703125	
DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	36.6137695 235.147827 11 0 301 26.4550781	235.1478516 ± 0.0001220703125 11 0 301 26.45507813 ± 0.0001220703125 500	
DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_Req12CSnsrDataType_Cnt_M_u08	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$	
DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_DFKSV_Cnt_M_str.SV_UIs_f32	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$	
DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$	
DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$ $1$	
DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$ $1$ $274$	
DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_st6	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$ $1$ $274$ $0$	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0 11	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$ $1$ $274$ $0$ $11$	
DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_Req12CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0 11	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$ $1$ $274$ $0$ $11$ $0$	
DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurRoughTurns_Cnt_M_st6  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0 11	$235.1478516 \pm 0.0001220703125$ $11$ $0$ $301$ $26.45507813 \pm 0.0001220703125$ $500$ $43.9453125 \pm 0.0001220703125$ $0$ $1675.147852 \pm 0.00048828125$ $1$ $274$ $0$ $11$ $0$ $0$	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.50 (Repeat Count = 1)			•
Name	Input Value		
DigColPsInt_GetData()	13		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124		
DigColPs_I2CHwColAngle_Cnt_M_u16	244		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	39		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.74		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	452		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30		
k_SenseDetErrDiag_Cnt_str.Threshold	18		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	120		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	13		
k_StepDetect_Deg_f32	46.5		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	70.0644531	70.06445313 ± 0.00048828125	11000
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16		l '	
	120	120	
	120	120	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	0 -1	0 -1	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0 -1 0	0 -1 0	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0 -1 0 110	0 -1 0 110	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc	0 -1 0 110 1	0 -1 0 110 1	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32	0 -1 0 110 1 70.0644531	0 -1 0 110 1 70.06445313 ± 0.0001220703125	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32	0 -1 0 110 1 70.0644531 233.642181	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08	0 -1 0 110 1 70.0644531 233.642181	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc	0 -1 0 110 1 70.0644531 233.642181 13	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prevl2CHwColAngle_Cnt_M_u16	0 -1 0 110 1 70.0644531 233.642181 13 1	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 -1 0 110 1 70.0644531 233.642181 13 1 115	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 -1 0 110 1 70.0644531 233.642181 13 1 115 10.1074219	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125 56	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSensCommFlts_Cnt_M_lgc DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0 -1 0 110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125 56 4.921875 ± 0.0001220703125	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSensCommFlts_Cnt_M_lgc DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prevl2CHwColAngle_Cnt_M_u16 DigColPs_Prevl2CHwColAngle_Deg_M_f32 DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigColPs_Prevl2CHwSpurAngle_Deg_M_f32 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0 -1 0 110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875 3	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125 56 4.921875 ± 0.0001220703125 3	
DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	0 -1 0 110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875 3 -126.357819	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 15 10.10742188 ± 0.0001220703125 56 4.921875 ± 0.0001220703125 3 -126.3578125 ± 0.00048828125	
DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_lgc DigCoIPs_I2CSensCommFits_Cnt_M_Igc DigCoIPs_I2CSensCommFits_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 -1 0 110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875 3	0 -1 0 110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125 56 4.921875 ± 0.0001220703125 3	





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	16	16	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	•
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.51 (Repeat Count = 1)	In and Malian	
Name	Input Value	
DigColPsInt_GetData()	6	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	321	
DigColPs_ColRoughTurns_Cnt_M_s16	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128	
DigColPs_I2CHwColAngle_Cnt_M_u16	1804	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	242	
DigColPs_I2CSensCommFlts_Cnt_M_u08	20	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	225	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	69.2	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.185	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	635	
DigColPs_SpurRoughTurns_Cnt_M_s16	3	
DigColPs SpurSensorDiagFailed Cnt M lgc	1	
DigColPs SpurSensorFaultAcc Cnt M u16	50	
k_SenseDetErrDiag_Cnt_str.Threshold	20	
k SenseDetErrDiag Cnt str.PStep	9	
k SenseDetErrDiag Cnt str.NStep	25	
k SenseParityErrDiag Cnt str.Threshold	220	
k SenseParityErrDiag Cnt str.PStep	45	
k_SenseParityErrDiag_Cnt_str.NStep	23	
k StepDetect Deg f32	66	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1363.7793	1363.779297 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	220	220	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103	103	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	283.779297	283.7792969 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	248.860962	248.8609375 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215	215	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.8964844	18.89648438 ± 0.0001220703125	~

DigColPs\_Per1

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96	96	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.4375	8.4375 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	608.860962	608.8609375 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220	220	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	25	25	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.52 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	314		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	57		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	702		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	343		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	138		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.415		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	241		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	22		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	450		
k_SenseParityErrDiag_Cnt_str.PStep	40		
k_SenseParityErrDiag_Cnt_str.NStep	46		
k_StepDetect_Deg_f32	112		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	381.596924	381.5969141 ± 0.00048828125	•

1

354

1

0

1

0

1

354

1

0

1

0

DigColPs\_ColLPFInitDone\_Cnt\_M\_lgc

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	21.5969238	21.59691406 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	352.184784	352.1847656 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	343	343	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	30.1464844	30.14648438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	600	600	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	52.734375	52.734375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-367.815216	-367.8152344 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	281	281	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 2.53 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	13		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	568		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146		
DigColPs_I2CHwColAngle_Cnt_M_u16	64		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	722		
DigColPs_I2CSensCommFlts_Cnt_M_u08	13		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	385		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	50		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	141		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	110		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.425		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	523		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	24		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	460		
k_SenseParityErrDiag_Cnt_str.PStep	42		
k_SenseParityErrDiag_Cnt_str.NStep	47		
k_StepDetect_Deg_f32	114		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	460	460	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	121	121	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	242.397461	242.3974609 ± 0.0001220703125	
DigColPs I2CSensCommFlts Cnt M u08	13	13	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	385	385	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32			
0 - 0 -	33.8378906	33.83789063 ± 0.0001220703125 700	
	700		
	04 500 4075	61.5234375 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	61.5234375	0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2 242.397461	242.3974609 ± 0.00048828125	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	2 242.397461 1	242.3974609 ± 0.00048828125	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	2 242.397461 1 460	242.3974609 ± 0.00048828125 1 460	
DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_igc	2 242.397461 1 460 0	242.3974609 ± 0.00048828125 1 460 0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16	2 242.397461 1 460 0	242.3974609 ± 0.00048828125 1 460 0	•
DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	2 242.397461 1 460 0 1	242.3974609 ± 0.00048828125 1 460 0 1	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16	2 242.397461 1 460 0 1 0	242.3974609 ± 0.00048828125 1 460 0 1 0 119	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2 242.397461 1 460 0 1 0 119 109	242.3974609 ± 0.00048828125 1 460 0 1 0 119 109	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	2 242.397461 1 460 0 1 0	242.3974609 ± 0.00048828125 1 460 0 1 0 119	•

Test Step 2.54 (Repeat Count = 1)



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	14		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	425		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	71		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	742		
DigColPs_I2CSensCommFlts_Cnt_M_u08	14		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	427		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	55		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	144		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	120		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.435		
DigColPs SpurLPFInitDone Cnt M Igc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	365		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	26		
k_SenseDetErrDiag_Cnt_str.PStep	11		
k_SenseDetErrDiag_Cnt_str.NStep	26		
k_SenseParityErrDiag_Cnt_str.Threshold	470		
k_SenseParityErrDiag_Cnt_str.PStep	44		
k_SenseParityErrDiag_Cnt_str.NStep	48		
k_StepDetect_Deg_f32	116		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	757.529297	757.5292969 ± 0.00048828125	/
DigColPs ColLPFInitDone Cnt M Igc	1	1	<b>~</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	469	469	
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>V</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs I2CColSensorFault Cnt M Igc	1	1	<b>V</b>
DigColPs_I2CHwColAngle_Deg_M_f32	37.5292969	37.52929688 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	51.5859375	51.5859375 ± 0.0001220703125	<b>~</b>
DigColPs I2CSensCommFlts Cnt M u08	14	14	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	<b>~</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	427	427	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	37.5292969	37.52929688 ± 0.0001220703125	_
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	800	800	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	70.3125	70.3125 ± 0.0001220703125	_
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	411.585938	411.5859375 ± 0.00048828125	_
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	
DigColPs SpurParityErrorAcc Cnt M u16	409	409	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.55 (Repeat Count = 1) Name	Input Value	
DigColPsInt GetData()	15	
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	900	
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.258	
DigColPs ColLPFInitDone Cnt M Igc	0.230	
DigColPs ColParityErrorAcc Cnt M u16	965	
DigColPs ColRoughTurns Cnt M s16	3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs ColSensorFaultAcc Cnt M u16	146	
DigColPs_12CHwColAngle_Cnt_M_u16	78	
DigColPs I2CHwDataType Cnt M u08	4	
DigColPs 12CHwSpurAngle Cnt M u16	762	
	15	
DigCoIPs_I2CSensCommFlts_Cnt_M_u08		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	469	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	60	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	900	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	147	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	130	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.445	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256	
DigColPs_SpurRoughTurns_Cnt_M_s16	3	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106	
k_SenseDetErrDiag_Cnt_str.Threshold	28	
k_SenseDetErrDiag_Cnt_str.PStep	12	
k_SenseDetErrDiag_Cnt_str.NStep	27	
k_SenseParityErrDiag_Cnt_str.Threshold	480	
k_SenseParityErrDiag_Cnt_str.PStep	46	
k_SenseParityErrDiag_Cnt_str.NStep	49	
k_StepDetect_Deg_f32	118	
Massa	A stud Value	

Actual Value	Expected Value	Result
957.074951	957.0749414 ± 0.00048828125	~
0	0	<b>✓</b>
480	480	~
0	0	<b>✓</b>
3	3	~
0	0	<b>✓</b>
119	119	~
1	1	<b>✓</b>
237.074951	237.0749414 ± 0.0001220703125	~
227.950195	227.9501953 ± 0.0001220703125	<b>✓</b>
15	15	~
0	0	<b>✓</b>
469	469	~
41.2207031	41.22070313 ± 0.0001220703125	•
	957.074951 0 480 0 3 0 119 1 237.074951 227.950195 15 0 469	957.074951 957.0749414 ± 0.00048828125 0 0 0 480 480 0 0 3 3 3 0 0 0 119 119 119 1 237.074951 237.0749414 ± 0.0001220703125 227.950195 227.9501953 ± 0.0001220703125 15 0 0 469 469

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	900	900	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	79.1015625	79.1015625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	587.950195	587.9501953 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	302	302	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	79	79	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	15	15	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	16		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1800		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.26		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	85		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	782		
DigColPs_I2CSensCommFlts_Cnt_M_u08	16		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	511		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	65		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	150		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	140		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.455		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	365		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	30		
k_SenseDetErrDiag_Cnt_str.PStep	13		
k_SenseDetErrDiag_Cnt_str.NStep	28		
k_SenseParityErrDiag_Cnt_str.Threshold	490		
k_SenseParityErrDiag_Cnt_str.PStep	48		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	120		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-945.922913	-945.9228516 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DiaColPs ColParityErrorAcc Cnt M u16	460	460	

460

0

4

1

0

460

4

1

0

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	134.077087	134.0771484 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	51.4902344	51.49023438 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	511	511	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	44.9121094	44.91210938 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1000	1000	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	87.890625	87.890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	771.490234	771.4902344 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	413	413	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.57 (Repeat Count = 1) Name	Input Value		
DigColPsInt GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.262		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs ColParityErrorAcc Cnt M u16	523		
· - ·	4		
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	92		
DigColPs I2CHwDataType Cnt M u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	802		
	17		
DigColPs_I2CSensCommFlts_Cnt_M_u08			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	553 70		
DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1100		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	153		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	150		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.465		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	251		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151		
k_SenseDetErrDiag_Cnt_str.Threshold	32		
k_SenseDetErrDiag_Cnt_str.PStep	14		
k_SenseDetErrDiag_Cnt_str.NStep	29		
k_SenseParityErrDiag_Cnt_str.Threshold	500		
k_SenseParityErrDiag_Cnt_str.PStep	1		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	122		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1984.09412	1984.094121 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	500	500	- I
DigColPs_ColParityError_Cnt_M_lgc	1	1	٠ ا
DigColPs_ColRoughTurns_Cnt_M_s16	<u> </u>	4	
	4	7	٠ ا
	0	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	0 157	0 157	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	0 157 1	0 157 1	•
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32	0 157 1 184.094116	0 157 1 184.0941211 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08	0 157 1 184.094116 74.8060303	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc	0 157 1 184.094116 74.8060303	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	0 157 1 184.094116 74.8060303 0	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	0 157 1 184.094116 74.8060303 0 0 553	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	0 157 1 184.094116 74.8060303 0 0 553 48.6035156	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875	0 157 1 184.0941211 $\pm$ 0.0001220703125 74.80605469 $\pm$ 0.0001220703125 0 0 553 48.60351563 $\pm$ 0.0001220703125 1100 96.6796875 $\pm$ 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875	0 157 1 184.0941211 $\pm$ 0.0001220703125 74.80605469 $\pm$ 0.0001220703125 0 0 553 48.60351563 $\pm$ 0.0001220703125 1100 96.6796875 $\pm$ 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603	0 157 1 184.0941211 $\pm$ 0.0001220703125 74.80605469 $\pm$ 0.0001220703125 0 0 553 48.60351563 $\pm$ 0.0001220703125 1100 96.6796875 $\pm$ 0.0001220703125 1 794.8060547 $\pm$ 0.00048828125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1	0 157 1 184.0941211 $\pm$ 0.0001220703125 74.80605469 $\pm$ 0.0001220703125 0 0 553 48.60351563 $\pm$ 0.0001220703125 1100 96.6796875 $\pm$ 0.0001220703125 1 794.8060547 $\pm$ 0.00048828125 1 252	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252	0 157 1 184.0941211 $\pm$ 0.0001220703125 74.80605469 $\pm$ 0.0001220703125 0 0 553 48.60351563 $\pm$ 0.0001220703125 1100 96.6796875 $\pm$ 0.0001220703125 1 794.8060547 $\pm$ 0.00048828125 1 252	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252	0 157 1 184.0941211 $\pm$ 0.0001220703125 74.80605469 $\pm$ 0.0001220703125 0 0 553 48.60351563 $\pm$ 0.0001220703125 1100 96.6796875 $\pm$ 0.0001220703125 1 794.8060547 $\pm$ 0.00048828125 1 252 1 4	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252 1 4	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125 1100 96.6796875 ± 0.0001220703125 1 794.8060547 ± 0.00048828125 1 252 1 4 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252 1 4 0	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125 1100 96.6796875 ± 0.0001220703125 1 794.8060547 ± 0.00048828125 1 252 1 4 0 122	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252 1 4	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125 1100 96.6796875 ± 0.0001220703125 1 794.8060547 ± 0.00048828125 1 252 1 4 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.58 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	654		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152		
DigColPs_I2CHwColAngle_Cnt_M_u16	99		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	822		
DigColPs_I2CSensCommFlts_Cnt_M_u08	18		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	595		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	156		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	160		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.475		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	362		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	34		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	30		
k_SenseParityErrDiag_Cnt_str.Threshold	510		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	2		
k_StepDetect_Deg_f32	124		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900	900 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	510	510	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	122	122	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	180	180 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	170.097656	170.0976563 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	595	595	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	52.2949219	52.29492188 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1200	1200	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	105.46875	105.46875 ± 0.0001220703125	<b>✓</b>
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-549.902344	-549.9023438 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>V</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	364	364	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>~</b>

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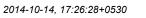
Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.59 (Repeat Count = 1)		V
Name	Input Value	
DigColPsInt_GetData()	2	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	126	
DigColPs_ColRoughTurns_Cnt_M_s16	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175	
DigColPs_I2CHwColAngle_Cnt_M_u16	2584	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342	
DigColPs_I2CSensCommFlts_Cnt_M_u08	25	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412	
DigColPs_SpurRoughTurns_Cnt_M_s16	0	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255	
k_SenseDetErrDiag_Cnt_str.Threshold	110	
k_SenseDetErrDiag_Cnt_str.PStep	24	
k_SenseDetErrDiag_Cnt_str.NStep	6	
k_SenseParityErrDiag_Cnt_str.Threshold	270	
k_SenseParityErrDiag_Cnt_str.PStep	4	
k_SenseParityErrDiag_Cnt_str.NStep	28	
k_StepDetect_Deg_f32	76	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	268.303711	268.3037109 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	~
DigColPs_ColParityError_Cnt_M_Igc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	268.303711	268.3037109 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	2.39589834	2.395898438 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	~

DigColPs\_Per1





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	116	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2.39589834	2.395898438 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•		
DiagnosticThreshold	2	DiagnosticThreshold	2	•		
OddParityFault	2	OddParityFault	2	<b>✓</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>		
ConstrainOneRev	2	ConstrainOneRev	2	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		

Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	632		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	113		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	862		
DigColPs_I2CSensCommFlts_Cnt_M_u08	20		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	679		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	85		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	162		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	180		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.495		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	624		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	185		
k_SenseDetErrDiag_Cnt_str.Threshold	38		
k_SenseDetErrDiag_Cnt_str.PStep	17		
k_SenseDetErrDiag_Cnt_str.NStep	32		
k_SenseParityErrDiag_Cnt_str.Threshold	530		
k_SenseParityErrDiag_Cnt_str.PStep	4		
k_SenseParityErrDiag_Cnt_str.NStep	4		
k_StepDetect_Deg_f32	128		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-960.161133	-960.1611328 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	530	530	•
DI O ID O ID II E O I M I			

0

-3

1

154

0

-3

1

154

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	119.838867	119.8388672 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	337.208191	337.2082031 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	679	679	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	59.6777344	59.67773438 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1400	1400	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	123.046875	123.046875 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>~</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-382.791809	-382.7917969 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	530	530	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	153	153	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>~</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.61 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs ColSensorFaultAcc Cnt M u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	120		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	882		
DigColPs_I2CSensCommFlts_Cnt_M_u08	21		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	721		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	90		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1500		
DigColPs PrevI2CHwSpurAngle Deg M f32	165		
DigColPs_Previ2CHwSpurArigie_Deg_M_i32 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_ReqizeShsiDataType_Cht_w_uoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16	125		
	-2		
DigColPs_SpurRoughTurns_Cnt_M_s16			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	40		
k_SenseDetErrDiag_Cnt_str.PStep	18		
k_SenseDetErrDiag_Cnt_str.NStep	33		
k_SenseParityErrDiag_Cnt_str.Threshold	540		
k_SenseParityErrDiag_Cnt_str.PStep	5		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	130.9		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	588.673828	588.6738281 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	417	417	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	•
	-2 0	-2 0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	-2 0	0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	-2 0 0	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	-2 0 0 0	0 0 0	
DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32	-2 0 0 0 228.673828	0 0 0 228.6738281 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08	-2 0 0 0 0 228.673828	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_l2CColSensorFault_Cnt_M_lgc DigColPs_l2CHwColAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CSensCommFlts_Cnt_M_u08 DigColPs_l2CSpurSensorFault_Cnt_M_lgc	-2 0 0 0 228.673828 0 4	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	-2 0 0 0 228.673828 0 4	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	-2 0 0 0 228.673828 0 4 0 721	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	-2 0 0 0 228.673828 0 4 0 721 63.3691406	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721 63.36914063 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721 63.36914063 ± 0.0001220703125 1500	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721 63.36914063 ± 0.0001220703125 1500 131.8359375 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0	0 0 0 0 0 228.6738281 $\pm$ 0.0001220703125 0 $\pm$ 0.0001220703125 4 0 0 721 63.36914063 $\pm$ 0.0001220703125 1500 131.8359375 $\pm$ 0.0001220703125 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800	0 0 0 0 0 228.6738281 $\pm$ 0.0001220703125 0 $\pm$ 0.0001220703125 4 0 0 721 63.36914063 $\pm$ 0.0001220703125 1500 131.8359375 $\pm$ 0.0001220703125 0 -1800 $\pm$ 0.00048828125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130	$ \begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ \end{array} $	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0	$ \begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ \end{array} $	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0 -2	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ 0 \end{array}$	
DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc  DigCoIPs_CoISensorFaultAcc_Cnt_M_u16  DigCoIPs_12CCoISensorFault_Cnt_M_lgc  DigCoIPs_12CCHoCoIAngle_Deg_M_f32  DigCoIPs_12CHwCoIAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFits_Cnt_M_u08  DigCoIPs_12CSensCommFits_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurParityError_Cnt_M_s16  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16  Rte_Call_Sa_DiaCoIPs_NxtrDiagMarc_SetNTCStatus(NTC_Cnt_T_enum)	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0 -2 0	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ 0 \\ 0 \end{array}$	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0 -2	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ 0 \end{array}$	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
OddParityFault	2	OddParityFault	2	•		
DiagnosticThreshold	2	DiagnosticThreshold	2	•		
ComputeRoughTurns	2	ComputeRoughTurns	2	•		
ConstrainOneRev	2	ConstrainOneRev	2	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		

Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.25		
DigColPs_ColLPFInitDone_Cnt_M_gc	1		
DigColPs ColParityErrorAcc Cnt M u16	256		
DigColPs ColRoughTurns Cnt M s16	-1		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	127		
DigColPs I2CHwDataType Cnt M u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	902		
DigColPs I2CSensCommFlts Cnt M u08	22		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	763		
DigColPs PrevI2CHwColAngle Deg M f32	95		
DigColPs PrevI2CHwSpurAngle Cnt M u16	1600		
DigColPs PrevI2CHwSpurAngle Deg M f32	168.5		
DigColPs Reql2CSnsrDataType Cnt M u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs SpurLPFInitDone Cnt M lgc	0		
DigColPs SpurParityErrorAcc Cnt M u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs SpurSensorFaultAcc Cnt M u16	0		
k SenseDetErrDiag Cnt str.Threshold	42		
k SenseDetErrDiag Cnt str.PStep	19		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	550		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	6		
k_StepDetect_Deg_f32	132		
Name	Actual Value	Expected Value	Resul
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	676.765137	676.7651367 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	262	262	•
DigColPs ColParityError Cnt M Igc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	316.765137	316.7651367 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	140.625	140.625 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	763	763	•

67.0605469

1600

140.625

-219.375

259

0

DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16

DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32

DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08

DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc

 ${\sf DigColPs\_SpurParityError\_Cnt\_M\_lgc}$ 

DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16

 ${\tt DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32}$ 

67.06054688 ± 0.0001220703125

140.625 ± 0.0001220703125

-219.375 ± 0.00048828125

0

0

259

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

DigColPs\_Per1

2014-10-14, 17:26:28+0530

0



 Name
 Actual Value
 Expected Value
 Result

 DigColPs\_SpurRoughTurns\_Cnt\_M\_s16
 -1
 -1
 -1

 DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc
 1
 1
 1

 DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16
 0
 0
 0

 Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)
 109
 109
 109

 Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)
 0
 0
 0

0

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 2.63 (Repeat Count = 1) Name	Input Value		
DigColPsInt GetData()	6		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs ColLPFInitDone Cnt M Igc	0		
DigColPs ColParityErrorAcc Cnt M u16	235		
DigColPs ColRoughTurns Cnt M s16	0		
DigColPs ColSensorDiagFailed Cnt M lqc	0		
DigColPs ColSensorFaultAcc Cnt M u16	184		
DigColPs I2CHwColAngle Cnt M u16	134		
DigColPs I2CHwDataType Cnt M u08	2		
DigColPs I2CHwSpurAngle Cnt M u16	922		
DigColPs I2CSensCommFlts Cnt M u08	23		
DigColPs PrevI2CHwColAngle Cnt M u16	805		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	100.9		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	171		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152		
k_SenseDetErrDiag_Cnt_str.Threshold	44		
k_SenseDetErrDiag_Cnt_str.PStep	20		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	560		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	7		
k_StepDetect_Deg_f32	134		
Name	Actual Value	Expected Value	Result
DiaColPs ColAnglet PEKSV Cnt M str SV Ltls f32	791 225586	791 2255859 + 0 00048828125	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	791.225586	791.2255859 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	242	242	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	149	149	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	71.2255859	71.22558594 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	164.707031	164.7070313 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	805	805	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	70.7519531	70.75195313 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1700	1700	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	149.414063	149.4140625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	524.707031	524.7070313 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	560	560	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	117	117	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	~

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.64 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	253		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165		
DigColPs_I2CHwColAngle_Cnt_M_u16	2428		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	322		
DigColPs_I2CSensCommFlts_Cnt_M_u08	24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	255		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	265		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	81		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.225		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	100		
k_SenseDetErrDiag_Cnt_str.PStep	21		
k_SenseDetErrDiag_Cnt_str.NStep	5		
k_SenseParityErrDiag_Cnt_str.Threshold	260		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	27		
k_StepDetect_Deg_f32	74		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-546.552673	-546.5527344 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>

255

0

0

0

160

255

0

0

160

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	173.447327	173.4472656 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	92.2148438	92.21484375 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	255	255	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	22.4121094	22.41210938 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112	112	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.84375	9.84375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3147.78516	-3147.785156 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	260	260	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.65 (Repeat Count = 1) Name	Input Value		
	8		
DigColPsInt_GetData() DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.4		
DigCoIPS_COIATIGIELPFKSV_CITI_M_SIT.K_OIS_I32  DigCoIPS COILPFInitDone Cnt M Igc	0.4		
DigColPs_ColParityErrorAcc_Cnt_M_u16	568		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16			
DigColPs_I2CHwColAngle_Cnt_M_u16	148		
DigColPs_I2CHwDataType_Cnt_M_u08	962		
DigCoIPs_I2CHwSpurAngle_Cnt_M_u16	25		
DigColPs_I2CSensCommFlts_Cnt_M_u08			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	889		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	110		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1900		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	177		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4320		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.545		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	241		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	48		
k_SenseDetErrDiag_Cnt_str.PStep	22		
k_SenseDetErrDiag_Cnt_str.NStep	37		
k_SenseParityErrDiag_Cnt_str.Threshold	580		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	9		
k_StepDetect_Deg_f32	138		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1099.25391	1099.253906 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	577	577	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	
DigColPs_I2CHwColAngle_Deg_M_f32	19.2539063	19.25390625 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	289.010742	289.0107422 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	889	889	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	78.1347656	78.13476563 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1900	1900	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	166.992188	166.9921875 ± 0.0001220703125	
DigColPs RegI2CSnsrDataType Cnt M u08	4	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2449.01074	2449.010742 ± 0.00048828125	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	250	250	
DigColPs SpurParityError Cnt M Igc	0	0	
DigCoIPs SpurRoughTurns Cnt M s16	2	2	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	
	,		
	109	109	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	109	109	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 2.66 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	134		
DigColPs_I2CHwColAngle_Cnt_M_u16	155		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	982		
DigColPs_I2CSensCommFlts_Cnt_M_u08	26		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	931		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	115		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158		
k_SenseDetErrDiag_Cnt_str.Threshold	50		
k_SenseDetErrDiag_Cnt_str.PStep	23		
k_SenseDetErrDiag_Cnt_str.NStep	38		
k_SenseParityErrDiag_Cnt_str.Threshold	590		
k_SenseParityErrDiag_Cnt_str.PStep	10		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	140		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1292.82178	1292.821777 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	590	590	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	96	96	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	212.821777	212.8217773 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	180	180 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	931	931	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	81.8261719	81.82617188 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2000	2000	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	175.78125	175.78125 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900	900 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	155	155	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

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DigColPs\_Per1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	120	120	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.67 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	126		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
DigColPs_I2CHwColAngle_Cnt_M_u16	2584		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	25		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	110		
k_SenseDetErrDiag_Cnt_str.PStep	24		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	270		
k_SenseParityErrDiag_Cnt_str.PStep	4		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	76		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-121.696289	-121.6962891 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	238.303711	238.3037109 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	2.39589834	2.395898438 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	<b>✓</b>

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DigColPs\_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	116	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2.39589834	2.395898438 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.68 (Repeat Count = 1)			_
Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	536		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	169		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022		
DigColPs_I2CSensCommFlts_Cnt_M_u08	28		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	186		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	54		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	40		
k_SenseParityErrDiag_Cnt_str.Threshold	610		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	144		
Name	Actual Value	Expected Value	Resul
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1561.06494	1561.064941 ± 0.00048828125	
DigColPs ColLPFInitDone Cnt M Igc	1	1	
DigColPs ColParityErrorAcc Cnt M u16	548	548	
DisColDs. ColDs:it:Error Cot M. Iss	0	2	

0

4

1

144

0

4

1

144

DigColPs\_ColParityError\_Cnt\_M\_lgc DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

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DigColPs\_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200	2200	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	193.359375	193.359375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	366.679688	366.6796875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 2.69 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	176		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1042		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	130.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	189		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0.153		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.152		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	56		
k_SenseDetErrDiag_Cnt_str.PStep	26		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	620		
k_SenseParityErrDiag_Cnt_str.PStep	13		
k_SenseParityErrDiag_Cnt_str.NStep	13		
k_StepDetect_Deg_f32	146		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	932.130859	932.1308984 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	576	576	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
			•
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	•
	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	0	0 186	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0 186 0	0 186 0	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0 186 0 212.130859	0 186	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32	0 186 0 212.130859 218.486572	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08	0 186 0 212.130859 218.486572	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFlts_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc	0 186 0 212.130859 218.486572 12	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0 186 0 212.130859 218.486572 12 1 1057	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12 1 1057	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 186 0 212.130859 218.486572 12 1 1057 92.9003906	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12 1 1057 92.90039063 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFlts_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12 1 1057 92.90039063 ± 0.0001220703125 2300	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12 1 1057 92.90039063 ± 0.0001220703125	
DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438	0 186 0 212.1308984 $\pm$ 0.0001220703125 218.4865625 $\pm$ 0.0001220703125 12 1 1057 92.90039063 $\pm$ 0.0001220703125 2300 202.1484375 $\pm$ 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_Reqi2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428	0 186 0 212.1308984 $\pm$ 0.0001220703125 218.4865625 $\pm$ 0.0001220703125 12 1 1057 92.90039063 $\pm$ 0.0001220703125 2300 202.1484375 $\pm$ 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_Req12CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428	0 186 0 212.1308984 $\pm$ 0.0001220703125 218.4865625 $\pm$ 0.0001220703125 12 1 1057 92.90039063 $\pm$ 0.0001220703125 2300 202.1484375 $\pm$ 0.0001220703125 3 -141.5134375 $\pm$ 0.00048828125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620	$\begin{array}{c} 0 \\ 186 \\ 0 \\ 212.1308984 \pm 0.0001220703125 \\ 218.4865625 \pm 0.0001220703125 \\ 12 \\ 1 \\ 1057 \\ 92.90039063 \pm 0.0001220703125 \\ 2300 \\ 202.1484375 \pm 0.0001220703125 \\ 3 \\ -141.5134375 \pm 0.00048828125 \\ 1 \\ 620 \end{array}$	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620 0	$\begin{array}{c} 0 \\ 186 \\ 0 \\ 212.1308984 \pm 0.0001220703125 \\ 218.4865625 \pm 0.0001220703125 \\ 12 \\ 1 \\ 1057 \\ 92.90039063 \pm 0.0001220703125 \\ 2300 \\ 202.1484375 \pm 0.0001220703125 \\ 3 \\ -141.5134375 \pm 0.00048828125 \\ 1 \\ 620 \\ 0 \\ \end{array}$	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620	$\begin{array}{c} 0 \\ 186 \\ 0 \\ 212.1308984 \pm 0.0001220703125 \\ 218.4865625 \pm 0.0001220703125 \\ 12 \\ 1 \\ 1057 \\ 92.90039063 \pm 0.0001220703125 \\ 2300 \\ 202.1484375 \pm 0.0001220703125 \\ 3 \\ -141.5134375 \pm 0.00048828125 \\ 1 \\ 620 \\ 0 \\ -4 \end{array}$	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CCHwColAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4 0	$\begin{array}{c} 0 \\ 186 \\ 0 \\ 212.1308984 \pm 0.0001220703125 \\ 218.4865625 \pm 0.0001220703125 \\ 12 \\ 1 \\ 1057 \\ 92.90039063 \pm 0.0001220703125 \\ 2300 \\ 202.1484375 \pm 0.0001220703125 \\ 3 \\ -141.5134375 \pm 0.00048828125 \\ 1 \\ 620 \\ 0 \\ -4 \\ 0 \end{array}$	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_l2CColSensorFault_Cnt_M_lgc  DigColPs_l2CHwColAngle_Deg_M_f32  DigColPs_l2CHwSpurAngle_Deg_M_f32  DigColPs_l2CSensCommFlts_Cnt_M_u08  DigColPs_l2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4	$\begin{array}{c} 0 \\ 186 \\ 0 \\ 212.1308984 \pm 0.0001220703125 \\ 218.4865625 \pm 0.0001220703125 \\ 12 \\ 1 \\ 1057 \\ 92.90039063 \pm 0.0001220703125 \\ 2300 \\ 202.1484375 \pm 0.0001220703125 \\ 3 \\ -141.5134375 \pm 0.00048828125 \\ 1 \\ 620 \\ 0 \\ -4 \end{array}$	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4 0	$\begin{array}{c} 0 \\ 186 \\ 0 \\ 212.1308984 \pm 0.0001220703125 \\ 218.4865625 \pm 0.0001220703125 \\ 12 \\ 1 \\ 1057 \\ 92.90039063 \pm 0.0001220703125 \\ 2300 \\ 202.1484375 \pm 0.0001220703125 \\ 3 \\ -141.5134375 \pm 0.00048828125 \\ 1 \\ 620 \\ 0 \\ -4 \\ 0 \end{array}$	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4 0 144	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12 1 1057 92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620 0 -4 0 144	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Dneg_M_f32  DigColPs_PrevI2CHwSpurAngle_Dneg_M_f32  DigColPs_PrevI2CHwSpurAngle_Dneg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 186 0 212.130859 218.486572 12 1 1057 92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4 0 144 109	0 186 0 212.1308984 ± 0.0001220703125 218.4865625 ± 0.0001220703125 12 1 1057 92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620 0 -4 0 144 109	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
OddParityFault	2	OddParityFault	2	<b>✓</b>		
DiagnosticThreshold	2	DiagnosticThreshold	2	•		
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>		
ConstrainOneRev	2	ConstrainOneRev	2	•		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		

Test Step 2.70 (Repeat Count = 1)	Immed Males		
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101		
DigColPs_I2CHwColAngle_Cnt_M_u16	294		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
<pre>s_SenseDetErrDiag_Cnt_str.Threshold</pre>	90		
SenseDetErrDiag_Cnt_str.PStep	43		
_SenseDetErrDiag_Cnt_str.NStep	18		
_SenseParityErrDiag_Cnt_str.Threshold	760		
SenseParityErrDiag_Cnt_str.PStep	30		
_SenseParityErrDiag_Cnt_str.NStep	30		
s_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Res
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	654	654	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	
DisColDo Dos ICCCoorDataTura Cat. M. v00		0	

852.358521

760

0

DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08
DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32

DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc

 ${\sf DigColPs\_SpurParityError\_Cnt\_M\_lgc}$ 

DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16

852.3585 ± 0.00048828125

0

0

760

DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
EnableI2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Name	Input Value		
DiqColPsInt GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.354		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	624		
DigColPs ColRoughTurns Cnt M s16	1		
DigColPs_ColRoughTuffs_Cft_M_sto	1		
DigColPs ColSensorFaultAcc Cnt M u16	101		
DigCoIPs_CoiSellsorPaultAcc_Cft_M_u16  DigCoIPs I2CHwColAngle Cnt M u16	295		
· - · · · · · · · · · · · · · · · · · ·	295		
DigColPs_I2CHwDataType_Cnt_M_u08	1382		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Name	Input Value		
DigColPsInt GetData()	1		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	320		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.342		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	362		
DigColPs ColRoughTurns Cnt M s16	-1		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DiaColPs ColParityErrorAcc Cnt M u16	300	300	

390

0

-1

1

14

390

-1

1

14

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

2014-10-14, 17:26:28+0530



DigColPs\_Per1

Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 2.73 (Repeat Count = 1)			<b>9</b>
	Innut Value		
Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1440		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.276		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	286		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	204		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1122		
DigColPs_I2CSensCommFlts_Cnt_M_u08	4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1225		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	150		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	201.4		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1275		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.216		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	999		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	100		
k_SenseDetErrDiag_Cnt_str.Threshold	64		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	5		
k_SenseParityErrDiag_Cnt_str.Threshold	660		
k_SenseParityErrDiag_Cnt_str.PStep	17		
k_SenseParityErrDiag_Cnt_str.NStep	17		
k_StepDetect_Deg_f32	154		
k_StepDetect_Deg_f32  Name	154 Actual Value	Expected Value	Result
Name		Expected Value -1112.20418 ± 0.00048828125	Result
Name DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1112.20422	·	
Name DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc	Actual Value	-1112.20418 ± 0.00048828125	~
Name DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16	Actual Value -1112.20422 0	-1112.20418 ± 0.00048828125	~
Name DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc	Actual Value -1112.20422 0 303	-1112.20418 ± 0.00048828125 0 303 0	<b>*</b>
Name DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	Actual Value -1112.20422 0 303 0 -1	-1112.20418 ± 0.00048828125 0 303 0 -1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc	Actual Value -1112.20422 0 303 0 -1 0	-1112.20418 ± 0.00048828125 0 303 0 -1	· · · · · · · · · · · · · · · · · · ·
Name DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	Actual Value -1112.20422 0 303 0 -1 0 181	-1112.20418 ± 0.00048828125 0 303 0 -1 0	***
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc	Actual Value -1112.20422 0 303 0 -1 0 181	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181	***
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_ColLPFInitDone_Cnt_M_lgc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CCH_ColAngle_Deg_M_f32	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125	***
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_ColLPFInitDone_Cnt_M_Igc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_Igc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125	***************************************
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125	***
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16	***************************************
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 11225	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1	***************************************
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_ColLPFInitDone_Cnt_M_lgc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125	***************************************
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_ColLPFInitDone_Cnt_M_lgc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700	***************************************
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_ColLPFInitDone_Cnt_M_lgc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_I2CSpurSensorFault_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125	***************************************
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_I2CSpurSensorFault_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2	***************************************
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_ColLPFInitDone_Cnt_M_lgc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125	***************************************
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CSensCommFlts_Cnt_M_u08  DigColPs_12CSensCommFlts_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensOfFault_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1	***************************************
Name  DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_ColLPFInitDone_Cnt_M_lgc  DigCoIPs_ColParityErrorAcc_Cnt_M_u16  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColParityError_Cnt_M_lgc  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_12CColSensorFault_Cnt_M_lgc  DigCoIPs_12CHwColAngle_Deg_M_f32  DigCoIPs_12CHwSpurAngle_Deg_M_f32  DigCoIPs_12CSensCommFlts_Cnt_M_u08  DigCoIPs_12CSensCommFlts_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurAngleLPFINIDOne_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 11225 107.666016 2700 237.304688 2 973.097778 1 660	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660	***************************************
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CCHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSensCommFits_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Det_M_f32  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Det_M_f32  DigColPs_PrevI2CHwColAngle_Det_M_f32  DigColPs_PrevI2CHwColAngle_Det_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Det_M_f32  DigColPs_PrevI2CHwSpurAngle_Det_M_f32  DigColPs_PrevI2CHwSpurAngle_Det_M_u16  DigColPs_PseqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0	>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFINIDOne_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0 -1	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSensCommFlts_Cnt_M_u6c  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0 -1	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1	
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_u16	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0 -1 0 95	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1 0 95	
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_u16  Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 1660 0 -1 0 95	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1 0 95 109	> > > > > > > > > > > > > > > > > > >
Name  DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_ColLPFInitDone_Cnt_M_lgc  DigColPs_ColParityErrorAcc_Cnt_M_u16  DigColPs_ColParityError_Cnt_M_lgc  DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorDiagFailed_Cnt_M_u16	Actual Value -1112.20422 0 303 0 -1 0 181 0 327.795776 253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0 -1 0 95	-1112.20418 ± 0.00048828125 0 303 0 -1 0 181 0 327.7958203 ± 0.0001220703125 253.0978125 ± 0.0001220703125 16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1 0 95	



Test Step Call Trace   ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.74 (Repeat Count = 1)			V
Name	Input Value		
DigColPsInt GetData()	10		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	320		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.342		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
	30		
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k SenseDetErrDiag Cnt str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	rtoodii
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
	390	390	j
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	
DigColPs_ColParityError_Cnt_M_lgc			
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	-
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	-
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	-
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	-

DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.75 (Repeat Count = 1)	Invest Value		
Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	536		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	169		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022		
DigColPs_I2CSensCommFlts_Cnt_M_u08	28		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	54		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	40		
k_SenseParityErrDiag_Cnt_str.Threshold	610		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	144		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	144	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	90	90 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	~

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DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	450	450 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace   ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	•
DigColPs ColParityError Cnt M Igc	0	0	

-1

1

14

-1

1

14

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

 ${\tt DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16}$ 

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

 $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)$ 

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

DigColPs\_Per1

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Actual Value **Expected Value** DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc DigColPs\_I2CHwColAngle\_Deg\_M\_f32 138.148849  $138.1488477 \pm 0.0001220703125$ DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32 87.4535522 87.45354688 ± 0.0001220703125  ${\tt DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08}$ 16 16 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc 1 DigColPs\_PrevI2CHwColAngle\_Cnt\_M\_u16 1687 1687 DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 148.271484 148.2714844 ± 0.0001220703125  $DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16$ 4095 4095 DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32 359.912109 359.9121094 ± 0.0001220703125 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08 447.453552 DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32 447.4535469 ± 0.00048828125 DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc n DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16 740 740 DigColPs\_SpurParityError\_Cnt\_M\_lgc n 0 DigColPs\_SpurRoughTurns\_Cnt\_M\_s16 -1 DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc

130

109

16

130

109

16

1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	-
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	-
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	-
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	<b>✓</b>





Test Step 2.77 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetData()	4	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	362	
DigColPs_ColRoughTurns_Cnt_M_s16	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
)igColPs_I2CHwColAngle_Cnt_M_u16	281	
ligColPs_I2CHwDataType_Cnt_M_u08	0	
ligColPs_I2CHwSpurAngle_Cnt_M_u16	1342	
igColPs_I2CSensCommFlts_Cnt_M_u08	15	
igColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	
igColPs_PrevI2CHwColAngle_Deg_M_f32	0	
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047	
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	360	
igColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	
igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736	
igColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392	
igColPs_SpurLPFInitDone_Cnt_M_lgc	0	
igColPs_SpurParityErrorAcc_Cnt_M_u16	865	
igColPs_SpurRoughTurns_Cnt_M_s16	-1	
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	
igColPs_SpurSensorFaultAcc_Cnt_M_u16	146	
_SenseDetErrDiag_Cnt_str.Threshold	86	
_SenseDetErrDiag_Cnt_str.PStep	41	
_SenseDetErrDiag_Cnt_str.NStep	16	
_SenseParityErrDiag_Cnt_str.Threshold	740	
_SenseParityErrDiag_Cnt_str.PStep	28	
_SenseParityErrDiag_Cnt_str.NStep	25	
_StepDetect_Deg_f32	170.7	
lame	Actual Value Expected Value	Res
unic	Actual value	
	138.148849 138.1488477 ± 0.00048828125	1100
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849 138.1488477 ± 0.00048828125	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc	138.148849 138.1488477 ± 0.00048828125 1 1	
igCoIPs_CoIAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_CoILPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16	138.148849 138.1488477 ± 0.00048828125 1 1 1 390 390	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc	138.148849 138.1488477 ± 0.00048828125 1 1	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16	138.148849	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc	138.148849	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 gCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc	138.148849	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColRoughTurns_Cnt_M_s16 igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_ColSensorFaultAcc_Cnt_M_u16	138.148849	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColRoughTurns_Cnt_M_s16 igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CCH_ColAngle_Deg_M_f32	138.148849	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32	138.148849     138.1488477 ± 0.00048828125       1     1       390     390       0     0       -1     -1       1     1       14     14       1     1       138.148849     138.1488477 ± 0.0001220703125       158.01355     158.0135469 ± 0.0001220703125	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08	138.148849     138.1488477 ± 0.00048828125       1     1       390     390       0     0       -1     -1       1     1       14     14       1     1       138.148849     138.1488477 ± 0.0001220703125       158.01355     158.0135469 ± 0.0001220703125       4     4	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_12CColSensorFault_Cnt_M_lgc igColPs_12CHwColAngle_Deg_M_f32 igColPs_12CHwSpurAngle_Deg_M_f32 igColPs_12CSensCommFlts_Cnt_M_u08 igColPs_12CSpurSensorFault_Cnt_M_lgc	138.148849  138.1488477 ± 0.00048828125  1  390  0  -1  1  1  1  1  1  1  1  1  14  1  1  138.148849  138.148847 ± 0.0001220703125  4  4  1  1  1  1  1  1  1  1  1  1  1	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_I2CSpurSensorFault_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16	138.148849     138.1488477 ± 0.00048828125       1     1       390     390       0     0       -1     -1       1     1       14     14       1     1       138.148849     138.1488477 ± 0.0001220703125       158.01355     158.0135469 ± 0.0001220703125       4     4       1     1       1687     1687	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_I2CSpurSensorFault_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32	138.148849  138.1488477 ± 0.00048828125  1  390  0  -1  1  1  1  1  1  1  1  1  1  1  1  1	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CHwColAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	138.148849  138.1488477 ± 0.00048828125  1  390  0  0  -1  1  1  1  1  1  1  1  1  1  1  1  1	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CHwColAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  1  1  1  1  1  1  1  1  1  1  1  1	
gCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 gCoIPs_ColLPFInitDone_Cnt_M_lgc gCoIPs_ColParityErrorAcc_Cnt_M_u16 gCoIPs_ColParityError_Cnt_M_lgc gCoIPs_ColParityError_Cnt_M_lgc gCoIPs_ColSensorDiagFailed_Cnt_M_lgc gCoIPs_ColSensorFaultAcc_Cnt_M_u16 gCoIPs_I2CColSensorFault_Cnt_M_lgc gCoIPs_I2CHwColAngle_Deg_M_f32 gCoIPs_I2CHwSpurAngle_Deg_M_f32 gCoIPs_I2CSensCommFlts_Cnt_M_u08 gCoIPs_I2CSpurSensorFault_Cnt_M_lgc gCoIPs_I2CSpurSensorFault_Cnt_M_u16 gCoIPs_I2CSpurSensorFault_Cnt_M_u16 gCoIPs_PrevI2CHwColAngle_Cnt_M_u16 gCoIPs_PrevI2CHwColAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 gCoIPs_PRevI2CHwSpurAngle_Deg_M_f32	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  1  1  1  1  1  1  1  1  1  1  1  1	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CHwColAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  1  1  1  1  1  1  1  1  1  1  1  1	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CHwColAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  1  1  1  1  1  1  1  1  1  1  1  1	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColRoughTurns_Cnt_M_s16 igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CHwColAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFits_Cnt_M_u08 igCoIPs_I2CSensCommFits_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_SpurAprityErrorAcc_Cnt_M_u16	138.148849  1 18.1488477 ± 0.00048828125  1 1  390  0 390  0 0  -1 -1  1 1  14 14  1 1  138.148849  138.1488477 ± 0.0001220703125  158.01355  158.01355  158.01355  1687  148.271484  2047  179.912109  179.9121094 ± 0.0001220703125  3 3  518.01355  518.0135469 ± 0.00048828125  0 0  740  740	
igCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_ColLPFInitDone_Cnt_M_lgc igCoIPs_ColParityErrorAcc_Cnt_M_u16 igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColParityError_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ColSensorDiagFailed_Cnt_M_lgc igCoIPs_ICColSensorFaultAcc_Cnt_M_u16 igCoIPs_I2CColSensorFault_Cnt_M_lgc igCoIPs_I2CHwColAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFits_Cnt_M_u08 igCoIPs_I2CSensCommFits_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Cnt_M_u16 igCoIPs_PrevI2CHwColAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igCoIPs_SpurParityErrorAcc_Cnt_M_lgc igCoIPs_SpurParityError_Cnt_M_lgc	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  -1  1  14  1  14  1  138.148849  138.1488477 ± 0.0001220703125  158.01355  158.01355  158.01355  1687  148.271484  148.271484  2047  179.912109  3  3  518.01355  518.0135469 ± 0.0001220703125  2047  179.912109  3  3  518.01355  518.0135469 ± 0.0001220703125  0  740  0  740  0	
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ICCOlSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurAngleLPFKSV_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityError_Cnt_M_lgc igColPs_SpurRoughTurns_Cnt_M_s16	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  -1  1  14  14  1  138.148849  138.1488477 ± 0.0001220703125  158.01355  158.01355  4  4  1  1  1687  1687  148.271484  2047  2048  3  518.01355  518.0135469 ± 0.0001220703125  3  518.01355  0  0  740  0  0  0  0	
bigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 bigCoIPs_ColLPFInitDone_Cnt_M_lgc bigCoIPs_ColParityErrorAcc_Cnt_M_u16 bigCoIPs_ColParityError_Cnt_M_lgc bigCoIPs_ColParityError_Cnt_M_lgc bigCoIPs_ColRoughTurns_Cnt_M_s16 bigCoIPs_ColSensorDiagFailed_Cnt_M_lgc bigCoIPs_ColSensorFaultAcc_Cnt_M_u16 bigCoIPs_I2CColSensorFault_Cnt_M_lgc bigCoIPs_I2CHwColAngle_Deg_M_f32 bigCoIPs_I2CHwSpurAngle_Deg_M_f32 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSpurSensorFault_Cnt_M_lgc bigCoIPs_PrevI2CHwColAngle_Deg_M_f32 bigCoIPs_PrevI2CHwColAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_SpurLPFInitDone_Cnt_M_u08 bigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 bigCoIPs_SpurParityError_Cnt_M_lgc bigCoIPs_SpurParityError_Cnt_M_lgc bigCoIPs_SpurRoughTurns_Cnt_M_s16 bigCoIPs_SpurSensorDiagFailed_Cnt_M_gc	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  -1  1  14  14  1  138.148849  138.1488477 ± 0.0001220703125  158.01355  158.01355  158.01355  4  4  1  1  1687  1687  148.271484  2047  179.912109  179.912109  3  518.01355  518.0135469 ± 0.0001220703125  2047  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048	
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_ColLPFInitDone_Cnt_M_lgc DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Det_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  -1  1  14  14  1  138.148849  138.1488477 ± 0.0001220703125  158.01355  158.01355  4  4  4  1  1687  1687  148.271484  2047  179.912109  179.912109  3  518.01355  518.0135469 ± 0.0001220703125  2047  179.912109  179.912109  179.9121094 ± 0.0001220703125  0  0  740  0  0  0  0  1  1  130	
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_ColLPFInitDone_Cnt_M_lgc DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensorFault_Cnt_M_lgc DigCoIPs_PevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_u08 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	138.148849  138.1488477 ± 0.00048828125  1  390  390  0  -1  -1  1  14  14  1  138.148849  138.1488477 ± 0.0001220703125  158.01355  158.01355  158.01355  4  4  1  1  1687  1687  148.271484  2047  179.912109  179.912109  3  518.01355  518.0135469 ± 0.0001220703125  2047  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048  2048	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.78 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.306		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	241		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	239		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1222		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1435		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	216		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	76		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.296		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	245		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	145		
k_SenseDetErrDiag_Cnt_str.Threshold	74		
k_SenseDetErrDiag_Cnt_str.PStep	35		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	22		
k_SenseParityErrDiag_Cnt_str.NStep	22		
k_StepDetect_Deg_f32	164		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	35.0736694	35.07365234 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	1	1	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	176	176	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	35.0736694	35.07365234 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	202.994019	202.994 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1435	1435	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	126.123047	126.1230469 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3200	3200	<b>~</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	281.25	281.25 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	<b>V</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	562.994019	562.994 ± 0.00048828125	<b>V</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1	1	<b>V</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~



Dig	ColF	Ps_	Pe	r1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.79 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-480		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	246		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1242		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1477		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	219		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	186		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	76		
k_SenseDetErrDiag_Cnt_str.PStep	36		
k_SenseDetErrDiag_Cnt_str.NStep	11		
k_SenseParityErrDiag_Cnt_str.Threshold	1000		
k_SenseParityErrDiag_Cnt_str.PStep	23		
k_SenseParityErrDiag_Cnt_str.NStep	23		
k_StepDetect_Deg_f32	166		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	159.542114	159.5421094 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	546	546	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	159.542114	159.5421094 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	307.740234	307.7401875 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1477	1477	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	129.814453	129.8144531 ± 0.0001220703125	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3300	3300	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	290.039063	290.0390625 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	667.740234	667.7401875 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	279	279	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt GetData()	6		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.318		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	365		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	253		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1262		
DigColPs_I2CSensCommFlts_Cnt_M_u08	11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1519		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	185		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	222		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	296		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.328		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	263		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	78		
k_SenseDetErrDiag_Cnt_str.PStep	37		
k_SenseDetErrDiag_Cnt_str.NStep	12		
k_SenseParityErrDiag_Cnt_str.Threshold	500		
k_SenseParityErrDiag_Cnt_str.PStep	24		
k_SenseParityErrDiag_Cnt_str.NStep	24		
k_StepDetect_Deg_f32	168		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-633.705139	-633.7051367 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DisColDe ColDevity/ErrorAce Cot M v.46	200	200	

389

0

-4

1

0

389

-4 1

0

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	86.2948608	86.29486328 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	184.607605	184.607625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1519	1519	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	133.505859	133.5058594 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3400	3400	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	298.828125	298.828125 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-175.392395	-175.392375 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	287	287	~
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>~</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>~</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.81 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	7		
	-160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.324		
	0.324		
DigCoIPs_CoILPFInitDone_Cnt_M_Igc	256		
DigColPs_ColParityErrorAcc_Cnt_M_u16			
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0 156		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	260		
DigColPs_I2CHwColAngle_Cnt_M_u16	2		
DigCoIPs_I2CHwDataType_Cnt_M_u08			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1282 12		
DigColPs_I2CSensCommFlts_Cnt_M_u08			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1561 190		
DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3500 225		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	406		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.344		
DigColPs_SpurLPFInitDone_Cnt_M_lgc			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	254		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168		
k_SenseDetErrDiag_Cnt_str.Threshold	80		
k_SenseDetErrDiag_Cnt_str.PStep	38		
k_SenseDetErrDiag_Cnt_str.NStep	13		
k_SenseParityErrDiag_Cnt_str.Threshold	710		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	20	1	1
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-413.628082	-413.6280859 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	281	281	•
	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0 143	0 143	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	0 143 0	0 143 0	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32	0 143 0 306.371918	0 143 0 306.3719141 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32	0 143 0 306.371918 112.956299	0 143 0 306.3719141 $\pm$ 0.0001220703125 112.9563125 $\pm$ 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFlts_Cnt_M_u08	0 143 0 306.371918 112.956299 7	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFlts_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc	0 143 0 306.371918 112.956299 7	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0 143 0 306.371918 112.956299 7 0	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 143 0 306.371918 112.956299 7 0 1561 137.197266	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFlts_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0	0 143 0 306.3719141 $\pm$ 0.0001220703125 112.9563125 $\pm$ 0.0001220703125 7 0 1561 137.1972656 $\pm$ 0.0001220703125 3500 307.6171875 $\pm$ 0.0001220703125 0	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_Reqi2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701	0 143 0 306.3719141 $\pm$ 0.0001220703125 112.9563125 $\pm$ 0.0001220703125 7 0 1561 137.1972656 $\pm$ 0.0001220703125 3500 307.6171875 $\pm$ 0.0001220703125 0 -247.0436875 $\pm$ 0.00048828125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_Req12CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1	0 143 0 306.3719141 $\pm$ 0.0001220703125 112.9563125 $\pm$ 0.0001220703125 7 0 1561 137.1972656 $\pm$ 0.0001220703125 3500 307.6171875 $\pm$ 0.0001220703125 0 -247.0436875 $\pm$ 0.00048828125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279	0 143 0 306.3719141 $\pm$ 0.0001220703125 112.9563125 $\pm$ 0.0001220703125 7 0 1561 137.1972656 $\pm$ 0.0001220703125 3500 307.6171875 $\pm$ 0.0001220703125 0 -247.0436875 $\pm$ 0.00048828125 1 279	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0 -5	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0 -5	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0 -5	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_L2CColSensorFault_Cnt_M_lgc  DigColPs_L2CColSensorFault_Cnt_M_lgc  DigColPs_L2CHwColAngle_Deg_M_f32  DigColPs_L2CShensCommFlts_Cnt_M_u08  DigColPs_L2CSpurSensorFault_Cnt_M_lgc  DigColPs_Prev12CHwColAngle_Cnt_M_u16  DigColPs_Prev12CHwColAngle_Deg_M_f32  DigColPs_Prev12CHwSpurAngle_Deg_M_f32  DigColPs_Prev12CHwSpurAngle_Deg_M_f32  DigColPs_Prev12CHwSpurAngle_Deg_M_f32  DigColPs_Prev12CHwSpurAngle_Deg_M_f32  DigColPs_Prev12CHwSpurAngle_Deg_M_f32  DigColPs_Prev12CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurParityErrorCnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0 -5	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0 -5	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFlts_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Deg_M_f32  DigColPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityErrorAcc_Cnt_M_u16  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0 -5 0	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0 -5 0 155	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_PrevI2CHwColAngle_Cnt_M_u16  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwColAngle_Deg_M_f32  DigColPs_PrevI2CHwSpurAngle_Dneg_M_f32  DigColPs_PrevI2CHwSpurAngle_Dneg_M_f32  DigColPs_PrevI2CHwSpurAngle_Dneg_M_f32  DigColPs_ReqI2CSnsrDataType_Cnt_M_u08  DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32  DigColPs_SpurLPFInitDone_Cnt_M_lgc  DigColPs_SpurParityError_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurRoughTurns_Cnt_M_s16  DigColPs_SpurSensorDiagFailed_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 143 0 306.371918 112.956299 7 0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0 -5 0 155	0 143 0 306.3719141 ± 0.0001220703125 112.9563125 ± 0.0001220703125 7 0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0 -247.0436875 ± 0.00048828125 1 279 0 -5 0 155 *none*	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
EnableI2CInterrupt	1	Enablel2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 2.82 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.33		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	365		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	134		
DigColPs I2CHwColAngle Cnt M u16	267		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1302		
DigColPs I2CSensCommFlts Cnt M u08	13		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1603		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	195		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	228		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	516		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.36		
DigColPs SpurLPFInitDone Cnt M Igc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	214		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs SpurSensorDiagFailed Cnt M Igc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	82		
	39		
k_SenseDetErrDiag_Cnt_str.PStep	14		
k_SenseDetErrDiag_Cnt_str.NStep	720		
k_SenseParityErrDiag_Cnt_str.Threshold	26		
k_SenseParityErrDiag_Cnt_str.PStep	26		
k_SenseParityErrDiag_Cnt_str.NStep	340		
k_StepDetect_Deg_f32		I=	1
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-309.906738	-309.9067383 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	391	391	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	50.0932617	50.09326172 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.3462219	55.34625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>~</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1603	1603	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	140.888672	140.8886719 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3600	3600	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	316.40625	316.40625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55.3462219	55.34625 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240	240	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>





Name	Actual Value	Expected Value	Result
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	132	132	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	*none*	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	<b>~</b>
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	<b>✓</b>
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.83 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetData()	9
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	160
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.336
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	251
DigColPs_ColRoughTurns_Cnt_M_s16	-2
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135
DigColPs_I2CHwColAngle_Cnt_M_u16	274
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1322
DigColPs_I2CSensCommFlts_Cnt_M_u08	14
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1645
DigColPs_PrevI2CHwColAngle_Deg_M_f32	200
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3700
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	231
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	626
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.376
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256
DigColPs_SpurRoughTurns_Cnt_M_s16	-2
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
k_SenseDetErrDiag_Cnt_str.Threshold	84
k_SenseDetErrDiag_Cnt_str.PStep	40
k_SenseDetErrDiag_Cnt_str.NStep	15
k_SenseParityErrDiag_Cnt_str.Threshold	730
k_SenseParityErrDiag_Cnt_str.PStep	27
k_SenseParityErrDiag_Cnt_str.NStep	27
k_StepDetect_Deg_f32	180

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Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-87.1010895	-87.10109375 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	278	278	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	272.898926	272.8989063 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	242.17746	242.1774375 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1645	1645	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	144.580078	144.5800781 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3700	3700	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	325.195313	325.1953125 ± 0.0001220703125	~





Name	Actual Value	Expected Value	Result
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	242.17746	242.1774375 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	283	283	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	108	108	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	10		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	362		
DigColPs ColRoughTurns Cnt M s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColDo Coll DEInitDone Cot M Igo	1	1	

Name	Actual Value	Expected value	Itosuit
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	<b>~</b>

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Name	Actual Value	Expected Value	Result
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	<b>✓</b>
Enablel2CInterrupt	1	Enablel2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>V</b>

Test Step 2.85 (Repeat Count = 1) Name	Input Value		
DigColPsInt GetData()	7		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354		
DigColPs ColLPFInitDone Cnt M lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	624		
DigColPs ColRoughTurns Cnt M s16	1		
DigColPs ColSensorDiagFailed Cnt M Igc	1		
DigColPs ColSensorFaultAcc Cnt M u16	101		
DigColPs I2CHwColAngle Cnt M u16	295		
DigColPs_12CHwDataType_Cnt_M_u08	295		
DigColPs I2CHwSpurAngle Cnt M u16	1382		
DigColPs 12CSensCommFlts Cnt M u08	17		
DigColPs PrevI2CHwColAngle Cnt M u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360		
DigColPs_PrevI2CHwColAligle_Deg_in_i32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs PrevI2CHwSpurAngle Deg M f32	0		
DigColPs Reql2CSnsrDataType Cnt M u08	4		
	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0.424		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	965		
DigColPs_SpurParityErrorAcc_Cnt_M_u16			
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	723.421631	723.4216211 ± 0.00048828125	•
Discoults Call DElaitDana Cat M Inc	4		

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DigColPs\_ColLPFInitDone\_Cnt\_M\_lgc DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16





Name	Actual Value	Expected Value	Result
DigColPs_ColParityError_Cnt_M_Igc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	~
DigColPs_ColSensorDiagFailed_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	3.42163086	3.421621094 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>-</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.86 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	294
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
k_SenseDetErrDiag_Cnt_str.Threshold	90
k_SenseDetErrDiag_Cnt_str.PStep	43
k_SenseDetErrDiag_Cnt_str.NStep	18
k_SenseParityErrDiag_Cnt_str.Threshold	760

DigColPs\_SpurRoughTurns\_Cnt\_M\_s16

DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16

 $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

 $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

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DigColPs\_Per1 Input Value k\_SenseParityErrDiag\_Cnt\_str.PStep 30 k\_SenseParityErrDiag\_Cnt\_str.NStep 30 k\_StepDetect\_Deg\_f32 174 Name **Actual Value Expected Value** Result DigColPs\_ColAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32 595.981628  $595.9816211 \pm 0.00048828125$ DigColPs\_ColLPFInitDone\_Cnt\_M\_lgc 1 DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 654 654 DigColPs\_ColParityError\_Cnt\_M\_lgc 0 0  $DigColPs\_ColRoughTurns\_Cnt\_M\_s16$ 1 DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc 1  ${\sf DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16}$ 83 83 ソソソソソソ DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc 1 DigColPs\_I2CHwColAngle\_Deg\_M\_f32 235.981628 235.9816211 ± 0.0001220703125 DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32 132.358521 132.3585 ± 0.0001220703125 DigColPs I2CSensCommFlts Cnt M u08 12 12 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc DigColPs\_PrevI2CHwColAngle\_Cnt\_M\_u16 1771 1771 DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 155.654297 155.6542969 ± 0.0001220703125 DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16 4000  ${\tt DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32}$  $351.5625 \pm 0.0001220703125$ 351.5625 DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08 0 ソソソソソ DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32 852.358521  $852.3585 \pm 0.00048828125$ DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc 0 0 DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16 760 760 DigColPs\_SpurParityError\_Cnt\_M\_lgc 0 0

est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

1

168

109

12

1

1

168

109

12

1

Test Step 2.87 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	295
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965



DigColPs\_Per1

Name	Input Value		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Namo	Actual Value	Expected Value	Posult

K_0tep50test_5cg_1c2	11.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	Ī	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>~</b>
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	<b>~</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>~</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	<b>~</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Nama	Input Value	
Name	Input Value	
DigColPsInt_GetData()	10	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	362	
DigColPs_ColRoughTurns_Cnt_M_s16	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_I2CHwColAngle_Cnt_M_u16	281	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342	
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	
DigColPs PrevI2CHwColAngle Deg M f32	0	



DigColPs_Per1	2014-10-14, 17.20.20+0330	Raz	Orcat
Name	Input Value		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>~</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1.	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	~
DigColPs 12CColSensorFault Cnt M Igo	1	1	<b>4</b>

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	<b>✓</b>
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	~
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	-
DisableI2CInterrupt	1	Disablel2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	<b>-</b>
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	-
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	-
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.89 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetData()	11
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	536
DigColPs_ColRoughTurns_Cnt_M_s16	4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1

DiaColPs Per1

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89.20898438 ± 0.0001220703125

193.359375 ± 0.0001220703125

366.6796875 ± 0.00048828125

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DigColPs_Per1		TO ACC	MACILAL	
Name	Input Value			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184			
DigColPs_I2CHwColAngle_Cnt_M_u16	169			
DigColPs_I2CHwDataType_Cnt_M_u08	2			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022			
DigColPs_I2CSensCommFlts_Cnt_M_u08	28			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015			
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180.4			
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5			
DigColPs SpurLPFInitDone Cnt M lgc	0			
DigColPs SpurParityErrorAcc Cnt M u16	625			
DigColPs SpurRoughTurns Cnt M s16	4			
DigColPs SpurSensorDiagFailed Cnt M lgc	1			
DigColPs SpurSensorFaultAcc Cnt M u16	165			
k_SenseDetErrDiag_Cnt_str.Threshold	54			
k SenseDetErrDiag Cnt str.PStep	25			
k SenseDetErrDiag Cnt str.NStep	40			
k SenseParityErrDiag Cnt str.Threshold	610			
k SenseParityErrDiag Cnt str.PStep	12			
k SenseParityErrDiag Cnt str.NStep	12			
k StepDetect Deg f32	144			
Name	Actual Value	Expected Value	Resul	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	•	
DigColPs ColLPFInitDone Cnt M lgc	1	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	•	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•	
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	•	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•	
DigColPs ColSensorFaultAcc Cnt M u16	144	144	•	
DigColPs I2CColSensorFault Cnt M Igc	1	1		
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	•	
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	•	
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	•	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	•	
· - · ·				

Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cn	t_1_enum)	1  1		
Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	-
Disablel2CInterrupt	1	Disablel2CInterrupt	1	-
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	-
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	-
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	-

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193.359375

366.679688

2200

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DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32

DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16

DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32

DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08
DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32

 ${\sf DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc}$ 

 ${\sf DigColPs\_SpurParityError\_Cnt\_M\_Igc}$ 

DigColPs\_SpurRoughTurns\_Cnt\_M\_s16

DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16





Test Step 2.90 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	30		
DigColPs I2CHwColAngle Cnt M u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs PrevI2CHwSpurAngle Deg M f32	234		
DigColPs_PrevizenwspurArigie_Deg_w_isz DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_ReqizeStisiDataType_Crit_ivi_u06  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
	0.392		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	865		
	-1		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	_   ·
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
<del></del>	'		
	14	14	
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14 1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	14		
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	14	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	14 1 138.148849	1 138.1488477 ± 0.0001220703125	
DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08	14 1 138.148849 77.2898865	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	14 1 138.148849 77.2898865 16	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16	
DigCoIPs_CoISensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CCoISensorFault_Cnt_M_lgc  DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	14 1 138.148849 77.2898865 16	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	14 1 138.148849 77.2898865 16 1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensorFault_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_st6	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740 0 -1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0 -1 1	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_UIs_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740 0 -1 1 1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0 -1 1 130	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08  DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740 0 -1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0 -1 1	

Test Step 2.91 (Repeat Count = 1)



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	640		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.354		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	101		
DigColPs I2CHwColAngle Cnt M u16	295		
DigColPs I2CHwDataType Cnt M u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs I2CSensCommFlts Cnt M u08	17		
DigColPs PrevI2CHwColAngle Cnt M u16	1771		
DigColPs PrevI2CHwColAngle Deg M f32	280		
DigColPs PrevI2CHwSpurAngle Cnt M u16	4000		
DigColPs PrevI2CHwSpurAngle Deg M f32	240		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.424		
DigColPs SpurLPFInitDone Cnt M Igc	0		
DigColPs SpurParityErrorAcc Cnt M u16	965		
DigColPs SpurRoughTurns Cnt M s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs SpurSensorFaultAcc Cnt M u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k SenseDetErrDiag_Ont_str. Micshold	43		
k SenseDetErrDiag_Ont_str. Otep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Francisco d Value	Daguilé
	595.981628	Expected Value 595.9816211 ± 0.00048828125	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1	595.9816211 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc			
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	<b>Y</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	<b>Y</b>
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	· ·
DigColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>V</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	~

155.654297

852.358521

4000 351.5625

760

0

 ${\tt DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32}$ 

DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16

DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08

DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc

 ${\sf DigColPs\_SpurParityError\_Cnt\_M\_lgc}$ 

DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16

155.6542969 ± 0.0001220703125

351.5625 ± 0.0001220703125

852.3585 ± 0.00048828125

0

0

760

2014-10-14, 17:26:28+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.92 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result

	1.44		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	181	181	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	•
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.93 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt GetData()	2		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1760		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.396		
DigColPs ColLPFInitDone Cnt M Igc	0		
DigColPs ColParityErrorAcc Cnt M u16	523		
DigColPs ColRoughTurns Cnt M s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs ColSensorFaultAcc Cnt M u16	184		
DigColPs I2CHwColAngle Cnt M u16	344		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1522		
DigColPs_I2CSensCommFlts_Cnt_M_u08	24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	270		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	245		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1780		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.536		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	104		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	830		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	37		
k_StepDetect_Deg_f32	188		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	707.231689	707.2316797 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs ColParityErrorAcc Cnt M u16	523	523	٠,
Di O ID O ID II E O I M I			

0

-3

0

159

0

-3

0

159

DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	347.231689	347.2316797 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.2365723	80.2365625 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065	2065	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	181.494141	181.4941406 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956	956	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84.0234375	84.0234375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1359.76343	-1359.763438 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	830	830	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	161	161	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





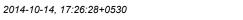
Test Step 2.94 (Repeat Count = 1) Name	Input Value		
DigColPsInt GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
	0.354		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc			
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1 101		
DigColPs_ColSensorFaultAcc_Cnt_M_u16			
DigColPs_I2CHwColAngle_Cnt_M_u16	295		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
C_SenseDetErrDiag_Cnt_str.Threshold	90		
z_SenseDetErrDiag_Cnt_str.PStep	43		
SenseDetErrDiag_Cnt_str.NStep	18		
<_SenseParityErrDiag_Cnt_str.Threshold	760		
<_SenseParityErrDiag_Cnt_str.PStep	50		
k_SenseParityErrDiag_Cnt_str.NStep	30		
<_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Res
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	674	674	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	
DigColPs PrevI2CHwColAngle Deg M f32	155.654297	155.6542969 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	
DigColPs PrevI2CHwSpurAngle Deg M f32	351.5625	351.5625 ± 0.0001220703125	
DigColPs RegI2CSnsrDataType Cnt M u08	0	0	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	852.358521	852.3585 ± 0.00048828125	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	
DigColPs SpurParityError Cnt M Igc	0	0	
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	
ZINGOUL O ODULOUISULI BUILDOU OLIL IVI UTU	100		
	109	109	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	109	109	

Test Step 2.95 (Repeat Count = 1)



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	<b>✓</b>	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs SpurSensorFaultAcc Cnt M u16	184		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	/ /
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	_
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	
DigColPs PrevI2CHwColAngle Deg M f32	177.802734	177.8027344 ± 0.0001220703125	-
DigColPs_PrevI2CHwColAfigle_Deg_M_132 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	
DigColPs_Req12CSristData1ype_Cnt_M_u06  DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	•
DigColPs SpurLPFInitDone Cnt M Igc	0	06.97237612 ± 0.00046626123	
DigColPs SpurParityErrorAcc Cnt M u16		1	
	820	820	
DigColPs_SpurParityError_Cnt_M_lgc	820 0	820	~





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.96 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	176		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1042		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	130.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	189		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.152		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	56		
k_SenseDetErrDiag_Cnt_str.PStep	26		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	620		
k_SenseParityErrDiag_Cnt_str.PStep	13		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	146		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	932.130859	932.1308984 ± 0.00048828125	<b>✓</b>
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	576	576	<b>✓</b>
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	186	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	212.130859	212.1308984 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.486572	218.4865625 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057	1057	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	92.9003906	92.90039063 ± 0.0001220703125	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2300	2300	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	202.148438	202.1484375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-141.513428	-141.5134375 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	620	620	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144	144	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt GetData()	10		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	320		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.342		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DiaColPs ColParityFrrorAcc Cnt M u16	300	300	

390

0

-1

1

14

390

-1

1

14

DigColPs\_ColParityErrorAcc\_Cnt\_M\_u16 DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•





Test Step 2.98 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	30		
DigColPs I2CHwColAngle Cnt M u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs PrevI2CHwSpurAngle Deg M f32	234		
DigColPs_PtevizonwSpurArigie_Deg_wi_isz DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_Reqi2C3181Data1ype_Cht_M_u00 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
	0.392		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	865		
	-1		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
		4	
	-1	-1	_   ·
DigColPs_ColRoughTurns_Cnt_M_s16	-1 1	1	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc			
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	1	1	•
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	1 14	1 14	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	1 14 1	1 14 1	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32	1 14 1 138.148849	1 14 1 138.1488477 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08	1 14 1 138.148849 77.2898865	1 14 1 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_12CColSensorFault_Cnt_M_lgc  DigColPs_12CHwColAngle_Deg_M_f32  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc	1 14 1 138.148849 77.2898865	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10	
DigColPs_ColRoughTurns_Cnt_M_s16  DigColPs_ColSensorDiagFailed_Cnt_M_lgc  DigColPs_ColSensorFaultAcc_Cnt_M_u16  DigColPs_I2CColSensorFault_Cnt_M_lgc  DigColPs_I2CHwColAngle_Deg_M_f32  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1 14 1 138.148849 77.2898865 10	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10	
DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	1 14 1 138.148849 77.2898865 10 1	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	1 14 1 138.148849 77.2898865 10 1 1687 148.271484	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125	
DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3	
DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125	
DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CCHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886 0 740	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740	
DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886 0 740 0	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0	
DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886 0 740 0 -1	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1	
DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityError_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886 0 740 0 -1	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1 1	
DigCoIPs_CoIRoughTurns_Cnt_M_s16  DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc  DigCoIPs_CoISensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CCoISensorFault_Cnt_M_lgc  DigCoIPs_I2CCoISensorFault_Cnt_M_lgc  DigCoIPs_I2CHwCoIAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFits_Cnt_M_u08  DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc  DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_lgc  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc  DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886 0 740 0 -1 1 1 130	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1 1 130	
DigCoIPs_ColRoughTurns_Cnt_M_s16  DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc  DigCoIPs_ColSensorFaultAcc_Cnt_M_u16  DigCoIPs_I2CColSensorFault_Cnt_M_lgc  DigCoIPs_I2CHwColAngle_Deg_M_f32  DigCoIPs_I2CHwSpurAngle_Deg_M_f32  DigCoIPs_I2CSensCommFlts_Cnt_M_u08  DigCoIPs_I2CSensCommFlts_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwColAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32  DigCoIPs_SpurLPFInitDone_Cnt_M_u08  DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurParityErrorAcc_Cnt_M_u16  DigCoIPs_SpurRoughTurns_Cnt_M_lgc  DigCoIPs_SpurRoughTurns_Cnt_M_s16  DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	1 14 1 138.148849 77.2898865 10 1 1687 148.271484 3800 333.984375 3 437.289886 0 740 0 -1	1 14 1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 10 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1 1	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.99 (Repeat Count = 1)			V
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	337		
DigColPs_I2CHwColAngle_Cnt_M_u16			
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k SenseDetErrDiag Cnt str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	554.800049	554.8 ± 0.00048828125	Result
	1	1	
DigColPs_ColLPFInitDone_Cnt_M_lgc			
DigColPs_ColParityErrorAcc_Cnt_M_u16	109	109	-
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	<b>*</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	_
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	194.800049	194.8 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	214.080078	214.08 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	-
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	-
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	214.080078	214.08 ± 0.00048828125	-
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	827	827	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	

DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.100 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1760		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.396		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	344		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1522		
DigColPs_I2CSensCommFlts_Cnt_M_u08	24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	270		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	245		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1780		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.536		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	104		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	830		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	37		
k_StepDetect_Deg_f32	188		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	707.231689	707.2316797 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	523	523	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	159	159	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	347.231689	347.2316797 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.2365723	80.2365625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>~</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065	2065	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	181.494141	181.4941406 ± 0.0001220703125	~

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956	956	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84.0234375	84.0234375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1359.76343	-1359.763438 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	830	830	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	161	161	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	181	181	٠,
DigColPs ColParityError Cnt M Igc	0	0	

-4

1

162

-4 1

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DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.102 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs ColLPFInitDone Cnt M Igc	0.45		
	250		
DigColPs_ColParityErrorAcc_Cnt_M_u16	-3		
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs ColSensorDiagFailed Cnt M Igc	1		
DigColPs ColSensorFaultAcc Cnt M u16	0		
	1		
DigColPs_I2CHwColAngle_Cnt_M_u16 DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	1		
0 0	12		
DigCoIPs_I2CSensCommFlts_Cnt_M_u08			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	512		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	122		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	920		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	210.6		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-719.912109	-719.9121094 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	360.087891	360.0878906 ± 0.00048828125	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	502	502	
DigColPs_SpurParityError_Cnt_M_lgc	0	0	
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	
	0	0	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorDiagFailed_Cnt_M_Igc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	
	0 109	0 109	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16			

Test Step 2.103 (Repeat Count = 1)

Name



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Input Value

Name	iliput value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	378		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.456		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	345		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2485		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1306		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	285.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1020		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.696		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	324		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	124		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	930		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	321.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-122.647919	-122.6479219 ± 0.00048828125	- Ttoouit
DigColPs ColLPFInitDone Cnt M lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	333	333	
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs ColRoughTurns Cnt M s16	-2	-2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>~</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs I2CColSensorFault Cnt M lgc	0	0	<b>V</b>
DigColPs_I2CHwColAngle_Deg_M_f32	237.352081	237.3520781 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	200.701172	200.7011719 ± 0.0001220703125	<b>*</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	_
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	<b>*</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	-
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	-
	0.087890625	0.087890625 ± 0.0001220703125	-
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1	1	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	560.701172	560.7011719 ± 0.00048828125	
	1	1	
DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAco_Cnt_M_u16	312	312	
DigColDs ShurDarityError Cat M Isa	312	U12	· ·

0

0

DigColPs\_SpurParityError\_Cnt\_M\_lgc

DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable12CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.104 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	420		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.462		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	625		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	19		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2527		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	250.9		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1341		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	310.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.712		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	645		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	126		
k_SenseDetErrDiag_Cnt_str.PStep	11		
k_SenseDetErrDiag_Cnt_str.NStep	36		
k_SenseParityErrDiag_Cnt_str.Threshold	940		
k_SenseParityErrDiag_Cnt_str.PStep	8		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	105.8		
Name	Actual Value	Expected Value	Result

K_Otopbeteet_beg_toz	100.0		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	226.000595	226.0006055 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	600	600	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	226.000595	226.0006055 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	<b>✓</b>

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DigColPs\_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	620	620	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.105 (Repeat Count = 1)			_
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	462		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.468		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	21		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2569		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	312.8		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1376		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	127.1		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1580		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.728		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	741		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	128		
k_SenseDetErrDiag_Cnt_str.PStep	12		
k_SenseDetErrDiag_Cnt_str.NStep	37		
k_SenseParityErrDiag_Cnt_str.Threshold	950		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	120.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	360.087891	360.0878906 ± 0.00048828125	-
DigColPs ColLPFInitDone Cnt M Igc	1	1	•
DigColPs ColParityErrorAcc Cnt M u16	382	382	-
Discolles Callesia Essay Cat M. Inc	0	0	

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DigColPs\_ColParityError\_Cnt\_M\_lgc

DigColPs\_ColRoughTurns\_Cnt\_M\_s16

 ${\tt DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc}$ 

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	38.144043	38.14398437 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	<b>✓</b>
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1478.14404	1478.143984 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	711	711	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	<b>✓</b>

DigColPs\_Per1

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Test Case 3: Path Test

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DigColPs\_Per1



#### Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 5049.00 Cycles
TS3.2 5174.00 Cycles
TS3.3 4828.00 Cycles
TS3.4 5030.00 Cycles
TS3.5 4671.00 Cycles
TS3.6 5045.00 Cycles
TS3.7 5082.00 Cycles
TS3.8 4914.00 Cycles
TS3.9 4722.00 Cycles
TS3.10 4791.00 Cycles
TS3.11 4742.00 Cycles
TS3.11 5002.00 Cycles



#### **Description** VECTOR DESCRIPTION:

```
TS3.1 "(I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>TRUE (I2CSensCommFlts_Cnt_T_u08 != 0U)=>FALSE ((I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE ((ColSensorEault_Cnt_T_lgc == TRUE))=>FALSE
   ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)|| (ColParityOrCommErr_Cnt_T_lgc == TRUE)|=>FALSE ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)|| (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE ((ColSensorFault_Cnt_T_lgc == TRUE)|| (SpurSensorFault_Cnt_T_lgc == TRUE)|| (SpurSensorFault_Cnt_T_u08 |= D_ANGLEDATA_CNT_U08)|| (SpurSensorFault_Cnt_T_u16 & 0x4000U) |= 0U)|| ((SCHwSpurAngle_Cnt_T_u16 & 0x4000U) |= 0U)|| ((SCHwSpur
((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) = 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)F || (ColParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((ColSensorFault_Cnt_T_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((ColParityGrorEvt_Cnt_T_lgc == TRUE)F || (ColParityGrorEvt_Cnt_T_lgc == TRUE)F || (SpurParityGrorEvt_Cnt_T_lgc == TRUE)F || (Sp
         ((DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoISensorFault_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurParityErrorEvt_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoIParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoIParityOrCommErr_Cnt_T_ugc == TRUE))=>TRUE ((I2CSensCommFlts_Cnt_T_uge == D_ANGLEDATA_CNT_Uge)F ((I2CHwDataType_Cnt_T_uge) == D_ANGLEDATA_CNT_Uge)F ((I2CHwDataType_Cnt_T_uge) == D_ANGLEDATA_CNT_Uge)F ((I2CHwCoIAngle_Cnt_T_ufe) & 0x4000U) != 0U)F ((I2CHwCoIAngle_Cnt_T_ufe) & 0x4000U) != 0U)F ((I2CHwCoIAngle_Cnt_T_ufe) & 0x8000U) != 0U)=>TRUE ((I2CHwSpurAngle_Cnt_T_ufe) & 0x8000U) != 0U)=>TRUE ((I2CHwSpurAngle_Cnt_T_ufe) & 0x8000U) != 0U)=>TRUE ((DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc == TRUE) || (CoIParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoISensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrComm
                      ( (ColSensorFault_Cnt_T_lgc == TRUE) ||
(SpurSensorFault_Cnt_T_lgc == TRUE) ||
```



```
(ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
(SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurSensorSampleOK_Cnt_T_lgc == TRUE
```

Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	0		
DigColPs_ColRoughTurns_Cnt_M_s16	-5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	1		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	20		
Nama	Actual Value	Expected Value	Pocult

Actual Value	Expected Value	Result
-1800	-1800 ± 0.00048828125	~
1	1	•
0	0	~
0	0	~
-5	-5	~
0	0	<b>✓</b>
0	0	~
0	0	<b>✓</b>
0	0 ± 0.0001220703125	~
0	0 ± 0.0001220703125	•
0	0	~
0	0	~
0	0	~
0	0 ± 0.0001220703125	•
0	0	~
	-1800 1 0 0 -5	-1800

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Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enable12CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>~</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.2 (Repeat Count = 1)			_
Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000		
DigColPs_ColRoughTurns_Cnt_M_s16	5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255		
DigColPs_I2CHwColAngle_Cnt_M_u16	65535		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08	31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000		
DigColPs_SpurRoughTurns_Cnt_M_s16	11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	255		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	50		
k_SenseParityErrDiag_Cnt_str.Threshold	1000		
k_SenseParityErrDiag_Cnt_str.PStep	50		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	340		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2159.91211	2159.912109 ± 0.00048828125	-
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	-
DigColPs ColParityErrorAcc Cnt M u16	1000	1000	-
DigColPs ColParityError Cnt M Igc	0	0	-
Bis Oslina oslin	-	-	

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DigColPs\_ColRoughTurns\_Cnt\_M\_s16
DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc

DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16

DigColPs\_I2CColSensorFault\_Cnt\_M\_Igc





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4319.91211	4319.912109 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000	1000	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205	205	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	100		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	124		
DigColPs_I2CSensCommFlts_Cnt_M_u08	1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	110		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	110		
k_SenseDetErrDiag_Cnt_str.Threshold	2		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	2		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	1		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	22		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-612.747253	-612.7472656 ± 0.00048828125	





Name	Actual Value	Expected Value	Result
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	1	1	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	107.252747	107.2527344 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	105.221069	105.2210938 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5	5	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.439453125	0.439453125 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12	12	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.0546875	1.0546875 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1694.77893	-1694.778906 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	108	108	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.28
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	110
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1
DigColPs_I2CHwColAngle_Cnt_M_u16	628
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	15
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	120
k_SenseDetErrDiag_Cnt_str.Threshold	10
k_SenseDetErrDiag_Cnt_str.PStep	25



Name	Input Value		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	40		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	30		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-373.538666	-373.5386719 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	40	40	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	346.461334	346.4613281 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	104.148438	104.1484375 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35	35	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.07617188	3.076171875 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24	24	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.109375	2.109375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1335.85156	-1335.851563 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	40	40	~
DigColPs_SpurParityError_Cnt_M_lgc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	10	10	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetData()	4
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	120
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1
DigColPs_I2CHwColAngle_Cnt_M_u16	1492
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	202
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195
DigColPs_PrevI2CHwColAngle_Deg_M_f32	205
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	63
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.165



Name	Input Value		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130		
k_SenseDetErrDiag_Cnt_str.Threshold	40		
k_SenseDetErrDiag_Cnt_str.PStep	3		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	200		
k_SenseParityErrDiag_Cnt_str.PStep	41		
k_SenseParityErrDiag_Cnt_str.NStep	21		
k_StepDetect_Deg_f32	62		
Name	Actual Value	Expected Value	Result

(_0.0p2-0.00(_2-0g102			
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-857.83252	-857.8324219 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	161	161	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1	1	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	222.16748	222.1675781 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.1761475	14.17617188 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195	195	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	17.1386719	17.13867188 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88	88	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.734375	7.734375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	14.1761475	14.17617188 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	171	171	~
DigColPs_SpurParityError_Cnt_M_lgc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	*none*	<b>~</b>
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.6 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetData()	9	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	300	
DigColPs_ColRoughTurns_Cnt_M_s16	-2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	3	
DigColPs_I2CHwColAngle_Cnt_M_u16	32767	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	32767	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	

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67.58789063 ± 0.0001220703125

320.592832 ± 0.00048828125

3

28

0

-1

150

109

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1

DigColPs_Per1	2014-10-14, 17.20.20+0000	Razo	orcat
Name	Input Value		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	132		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	296		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.658		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	200		
k_SenseDetErrDiag_Cnt_str.Threshold	150		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	20		
k_SenseParityErrDiag_Cnt_str.Threshold	28		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	39		
k_StepDetect_Deg_f32	169		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-630.636353	-630.6363281 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	28	28	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	33	33	<b>✓</b>
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	89.3636475	89.36367188 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	320.592896	320.592832 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	695	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	61.0839844	61.08398438 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	769	~

67.5878906

320.592896

28

0

-1

150

109

9

1

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	-
Disablel2CInterrupt	1	Disablel2CInterrupt	1	<b> </b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	-
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	-
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	-
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	-

Test Step 3.7 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetData()	0
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	310
DigColPs_ColRoughTurns_Cnt_M_s16	-1

DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08

DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16

DigColPs\_SpurParityError\_Cnt\_M\_lgc

DigColPs\_SpurRoughTurns\_Cnt\_M\_s16

DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc

DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16

DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

 $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

DigColPs\_Per1

2014-10-14, 17:26:28+0530



Input Value DigColPs\_ColSensorDiagFailed\_Cnt\_M\_lgc DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16 3 DigColPs\_I2CHwColAngle\_Cnt\_M\_u16 32768 DigColPs\_I2CHwDataType\_Cnt\_M\_u08 32768 DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16 DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08 0 DigColPs\_PrevI2CHwColAngle\_Cnt\_M\_u16 695 132 DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16 769 DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32 296 DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08 3 1500  ${\tt DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32}$ DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.K\_Uls\_f32 0.658 DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc 0 DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16 230 DigColPs\_SpurRoughTurns\_Cnt\_M\_s16 -1  ${\tt DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc}$ 0 DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16 210  $k\_SenseDetErrDiag\_Cnt\_str.Threshold$ 150 k\_SenseDetErrDiag\_Cnt\_str.PStep 30  $k\_SenseDetErrDiag\_Cnt\_str.NStep$ 20  $k\_SenseParityErrDiag\_Cnt\_str.Threshold$ 28  $k\_SenseParityErrDiag\_Cnt\_str.PStep$ 36 k\_SenseParityErrDiag\_Cnt\_str.NStep 39 k\_StepDetect\_Deg\_f32 169

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-443.43634	-443.4363281 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	28	28	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	276.56366	276.5636719 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	197.472839	197.472832 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	695	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	61.0839844	61.08398438 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	769	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	67.5878906	67.58789063 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	557.472839	557.472832 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	28	28	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	190	190	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	65	65	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	<b>✓</b>
DigColPsInt_GetData	1	DigColPsInt_GetData	1	<b>~</b>
Enablel2CInterrupt	1	Enablel2CInterrupt	1	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	<b>✓</b>
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>✓</b>
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	<b>✓</b>





Name	Input Value	
DigColPsInt_GetData()	0	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600	
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.52	
DigColPs ColLPFInitDone Cnt M Igc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	320	
DigColPs_ColRoughTurns_Cnt_M_s16	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	3	
DigColPs_I2CHwColAngle_Cnt_M_u16	256	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	255	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	132	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	296	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.658	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
bigColPs_SpurParityErrorAcc_Cnt_M_u16	240	
DigColPs SpurRoughTurns Cnt M s16	0	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
	220	
ligColPs_SpurSensorFaultAcc_Cnt_M_u16	150	
_SenseDetErrDiag_Cnt_str.Threshold SenseDetErrDiag_Cnt_str.PStep	30	
SenseDetErrDiag_Cnt_str.NStep	20	
- '- '-	28	
_SenseParityErrDiag_Cnt_str.Threshold	36	
x_SenseParityErrDiag_Cnt_str.PStep	39	
<pre>c_SenseParityErrDiag_Cnt_str.NStep</pre>	169	
x_StepDetect_Deg_f32		
Name	Actual Value Expected Value	Resi
bigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-256.236328 -256.2363281 ± 0.00048828125	
ligColPs_ColLPFInitDone_Cnt_M_lgc	0 0	
ligCoIPs_CoIParityErrorAcc_Cnt_M_u16	281 281 0	
	0 0	
	0	
igColPs_ColRoughTurns_Cnt_M_s16	0 0	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc	1 1	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16	1 0 0	
bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc	1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
oigColPs_ColRoughTurns_Cnt_M_s16  oigColPs_ColSensorDiagFailed_Cnt_M_lgc  oigColPs_ColSensorFaultAcc_Cnt_M_u16  oigColPs_I2CColSensorFault_Cnt_M_lgc  oigColPs_I2CHwColAngle_Deg_M_f32	1 1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	1 1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
oigColPs_ColRoughTurns_Cnt_M_s16  oigColPs_ColSensorDiagFailed_Cnt_M_lgc  oigColPs_ColSensorFaultAcc_Cnt_M_u16  oigColPs_I2CColSensorFault_Cnt_M_lgc  oigColPs_I2CHwColAngle_Deg_M_f32  oigColPs_I2CHwSpurAngle_Deg_M_f32	1	
bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_l2CColSensorFault_Cnt_M_lgc bigColPs_l2CHwColAngle_Deg_M_f32 bigColPs_l2CHwSpurAngle_Deg_M_f32 bigColPs_l2CSensCommFlts_Cnt_M_u08 bigColPs_l2CSensCommFlts_Cnt_M_u08	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_Previ2CHwColAngle_Cnt_M_u16	1	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	
bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFlts_Cnt_M_u08 bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Deg_M_f32 bigColPs_PrevI2CHwColAngle_Deg_M_f32 bigColPs_PrevI2CHwColAngle_Deg_M_f32 bigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CCHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CCHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_u08 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityErrorCnt_M_lgc	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u6 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_Prevl2CHwColAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwColAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	1       1         0       0         1       1         103.763672       103.7636719 ± 0.0001220703125         74.3528442       74.35283203 ± 0.0001220703125         0       0         1       1         695       695         61.0839844       61.08398438 ± 0.0001220703125         769       769         67.5878906       67.58789063 ± 0.0001220703125         3       3         794.352844       794.352832 ± 0.00048828125         1       1         28       28         1       1         0       0	
DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_lgc	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_Prevl2CHwCoIAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwCoIAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	1       1         0       0         1       1         103.763672       103.7636719 ± 0.0001220703125         74.3528442       74.35283203 ± 0.0001220703125         0       0         1       1         695       695         61.0839844       61.08398438 ± 0.0001220703125         769       769         67.5878906       67.58789063 ± 0.0001220703125         3       3         794.352844       794.352832 ± 0.00048828125         1       1         28       28         1       1         0       0	





Test Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
EnableI2CInterrupt	1	Enable12CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 3.9 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	168		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.426		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	379		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs I2CHwSpurAngle Cnt M u16	1622		
DigColPs_I2CSensCommFlts_Cnt_M_u08	29		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2275		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	320		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1131		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	250		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-380		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.616		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs SpurSensorDiagFailed Cnt M Igc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	114		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	30		
k SenseParityErrDiag Cnt str.Threshold	880		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	198		
Name	Actual Value	Expected Value	Popult
		Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	334.971191	334.9711992 ± 0.00048828125	_
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	258	258	· ·
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	<b>~</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	<b>~</b>
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	334.971191	334.9711992 ± 0.0001220703125	<b>V</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	137.073059	137.0730469 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2275	2275	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	199.951172	199.9511719 ± 0.0001220703125	<b>~</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1131	1131	<b>V</b>
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	99.4042969	99.40429688 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>V</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	137.073059	137.0730469 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	255	255	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>



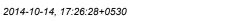


Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156	156	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	64	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-	
EnableI2CInterrupt	1	Enablel2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	<b>~</b>	
OddParityFault	2	OddParityFault	2	<b>✓</b>	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

Test Step 3.10 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	250		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	512		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	122		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	920		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	210.6		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	502	502	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
EnableI2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	-	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	-	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	378		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.456		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	345		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2485		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1306		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	285.4		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1020		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.696		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	324		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	124		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	930		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	321.4		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	533.992065	533.9920781 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs ColParityErrorAcc Cnt M u16	333	333	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	533.992065	533.9920781 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	333	333	<b>✓</b>
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	<b>✓</b>
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	<b>✓</b>

 $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

DigColPs\_Per1

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Actual Value **Expected Value** DigColPs\_I2CHwColAngle\_Deg\_M\_f32 173.992065 173.9920781 ± 0.0001220703125 DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32 91.2611694 91.26117188 ± 0.0001220703125 DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08 0 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc DigColPs\_PrevI2CHwColAngle\_Cnt\_M\_u16 0.087890625 0.087890625 ± 0.0001220703125 DigColPs\_PrevI2CHwColAngle\_Deg\_M\_f32 DigColPs\_PrevI2CHwSpurAngle\_Cnt\_M\_u16 0.087890625 0.087890625 ± 0.0001220703125 DigColPs\_PrevI2CHwSpurAngle\_Deg\_M\_f32 DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08 811.261169 811.2611719 ± 0.00048828125 DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.SV\_Uls\_f32 DigColPs\_SpurLPFInitDone\_Cnt\_M\_lgc DigColPs\_SpurParityErrorAcc\_Cnt\_M\_u16 312 312 DigColPs\_SpurParityError\_Cnt\_M\_lgc 0 0 DigColPs\_SpurRoughTurns\_Cnt\_M\_s16 2 2 DigColPs\_SpurSensorDiagFailed\_Cnt\_M\_lgc DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16 0 0  $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 109 109 Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) 0 0

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	

0

0

Test Step 3.12 (Repeat Count = 1) Name	Input Value		
	· ·		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	384.388855	384.3888477 ± 0.00048828125	

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Name	Actual Value	Expected Value	Result
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngle_Deg_M_f32	24.388855	24.38884766 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.529846	359.529875 ± 0.0001220703125	<b>✓</b>
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	<b>✓</b>
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	719.529846	719.529875 ± 0.00048828125	<b>✓</b>
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	<b>✓</b>
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	<b>✓</b>
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	<b>✓</b>	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	<b>✓</b>	

OddParityFault

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Project	DigColPs
Module	DigColPs
Test Object	OddParityFault

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date: 10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes		
Name	Value	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>	



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 168.00 Cycles Longest Execution Path TS1.2 10.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Longest Execution Path => while (Input\_Cnt\_T\_u16 > 0U)=>TRUE if ((Input\_Cnt\_T\_u16 & 1U)!= 0U)=>TRUE if ((Parity\_Cnt\_T\_u08 & 1U) == 0U)=>TRUE" TS1.2 "Shortest Execution Path => while (Input\_Cnt\_T\_u08 & 1U) == 0U)=>FALSE if ((Parity\_Cnt\_T\_u08 & 1U) == 0U)=>TRUE"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 1.2 (Repeat Count = 1)			~
Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

### Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 10.00 Cycles TS2.2 168.00 Cycles TS2.3 58.00 Cycles

Description

VECTOR DESCRIPTION:

TS2.1 Input\_Cnt\_T\_u16=Min TS2.2 Input\_Cnt\_T\_u16=Max TS2.3 Input\_Cnt\_T\_u16=Pos

Test Step 2.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 2.2 (Repeat Count = 1)			~
Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 2.3 (Repeat Count = 1)			~
Name	Input Value		
Input_Cnt_T_u16	44		
Name	Actual Value	Expected Value	Result
OddParityFault()	0	0	~



### Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 10.00 Cycles TS3.2 168.00 Cycles TS3.3 33.00 Cycles TS3.4 1.00 Cycles

#### VECTOR DESCRIPTION: Description

TS3.1 "while (Input\_Cnt\_T\_u16 > 0U)=>FALSE if ((Parity\_Cnt\_T\_u08 & 1U) == 0U)=>TRUE"
TS3.2 "while (Input\_Cnt\_T\_u16 > 0U)=>TRUE if ((Input\_Cnt\_T\_u16 & 1U) != 0U)=>TRUE"
TS3.3 if ((Input\_Cnt\_T\_u16 & 1U) != 0U)=>FALSE
TS3.4 if ((Parity\_Cnt\_T\_u08 & 1U) != 0U)=>FALSE

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	<b>✓</b>

Test Step 3.2 (Repeat Count = 1)			~
Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	10		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 3.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Input_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
OddParityFault()	0	0	<b>✓</b>

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ComputeRoughTurns

 Project
 DigColPs

 Module
 DigColPs

 Test Object
 ComputeRoughTurns

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

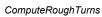
### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs\utp\contr

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg</pre>
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>



#### Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 TS1.2 5.00 Cycles Longest Execution Path 3.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path => if (Delta\_Deg\_T\_f32 > k\_StepDetect\_Deg\_f32)=FALSE else if (Delta\_Deg\_T\_f32 < -k\_StepDetect\_Deg\_f32)=FALSE" TS1.2 "Shortest Execution Path => if (Delta\_Deg\_T\_f32 > k\_StepDetect\_Deg\_f32)=TRUE"

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	30		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	tgt_RoughTurnAccPtr_Cnt_T_s16	
k_StepDetect_Deg_f32	30		
tgt_RoughTurnAccPtr_Cnt_T_s16	-3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1080	-1080 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-3	-3	<b>✓</b>

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1080	1080 ± 0.00048828125	<b>✓</b>
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3	<b>✓</b>



#### **Test Case 2: Boundary Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 8.00 Cycles
TS2.2 3.00 Cycles
TS2.3 8.00 Cycles
TS2.4 3.00 Cycles
TS2.5 5.00 Cycles
TS2.6 3.00 Cycles
TS2.7 8.00 Cycles
TS2.7 8.00 Cycles
TS2.9 5.00 Cycles
TS2.10 5.00 Cycles
TS2.11 5.00 Cycles
TS2.12 5.00 Cycles
TS2.13 5.00 Cycles
TS2.13 5.00 Cycles
TS2.15 5.00 Cycles
TS2.15 5.00 Cycles

#### Description V

#### VECTOR DESCRIPTION:

TS2.1 All Min

 TS2.2
 All Max

 TS2.3
 Delta\_Deg\_T\_f32=Min

 TS2.4
 Delta\_Deg\_T\_f32=Max

 TS2.5
 Delta\_Deg\_T\_f32=Max

 TS2.6
 Delta\_Deg\_T\_f32=Pos

 TS2.7
 Delta\_Deg\_T\_f32=Neg

 TS2.8
 k\_StepDetect\_Deg\_f32=Min

 TS2.9
 k\_StepDetect\_Deg\_f32=Max

 TS2.10
 k\_StepDetect\_Deg\_f32=Pos

 TS2.11
 RoughTurnAccPtr\_Cnt\_T\_s08=Min

 TS2.12
 RoughTurnAccPtr\_Cnt\_T\_s08=Max

 TS2.13
 RoughTurnAccPtr\_Cnt\_T\_s08=Pos

 TS2.14
 RoughTurnAccPtr\_Cnt\_T\_s08=Neg

 TS2.15
 RoughTurnAccPtr\_Cnt\_T\_s08=Neg

#### Test Step 2.1 (Repeat Count = 1) Name Input Value Delta\_Deg\_T\_f32 -360 $RoughTurnAccPtr\_Cnt\_T\_s16$ tgt\_RoughTurnAccPtr\_Cnt\_T\_s16 k\_StepDetect\_Deg\_f32 20 -5 $tgt\_RoughTurnAccPtr\_Cnt\_T\_s16$ Name **Actual Value Expected Value** Result ComputeRoughTurns() -1440 -1440 ± 0.00048828125 tgt\_RoughTurnAccPtr\_Cnt\_T\_s16 -4

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1440	1440 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	4	4	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)			V
Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	24.5		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	•
tqt RoughTurnAccPtr Cnt T s16	1	1	<b>₩</b>

Test Step 2.4 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Delta_Deg_T_f32	360
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16
k_StepDetect_Deg_f32	28

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ComputeRoughTurns

Name	Input Value		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	0	0 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	0	0	~

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	0		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	tgt_RoughTurnAccPtr_Cnt_T_s16	
k_StepDetect_Deg_f32	44.4	44.4	
tgt_RoughTurnAccPtr_Cnt_T_s16	2		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	180.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	tgt_RoughTurnAccPtr_Cnt_T_s16	
k_StepDetect_Deg_f32	68		
tgt_RoughTurnAccPtr_Cnt_T_s16	3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	<b>✓</b>
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	✓

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	-150.6		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	tgt_RoughTurnAccPtr_Cnt_T_s16	
k_StepDetect_Deg_f32	90.2		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1800	1800 ± 0.00048828125	~
tgt RoughTurnAccPtr Cnt T s16	5	5	<b>✓</b>

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	50		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	20		
tgt_RoughTurnAccPtr_Cnt_T_s16	3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	<b>✓</b>

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-30.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	16	
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓



ComputeRoughTurns

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	0		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	100.8		
tgt_RoughTurnAccPtr_Cnt_T_s16	2		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	<b>✓</b>

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	154.2		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	s16	
k_StepDetect_Deg_f32	155		
tgt_RoughTurnAccPtr_Cnt_T_s16	-5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1800	-1800 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-5	-5	✓

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	40		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	25.4		
tgt_RoughTurnAccPtr_Cnt_T_s16	5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1440	1440 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	4	4	•

Test Step 2.13 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	-300		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	6	
k_StepDetect_Deg_f32	300		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	0	0 ± 0.00048828125	<b>*</b>
tgt_RoughTurnAccPtr_Cnt_T_s16	0	0	<b>~</b>

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	10.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	16	
k_StepDetect_Deg_f32	150.1		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓

Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	150		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	200		
tgt_RoughTurnAccPtr_Cnt_T_s16	-2		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-720	-720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-2	-2	~



#### **Test Case 3: Path Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 3.00 Cycles TS3.2 8.00 Cycles TS3.3 5.00 Cycles

VECTOR DESCRIPTION: Description

 $\begin{tabular}{ll} TS3.1 & if (Delta\_Deg\_T\_f32 > k\_StepDetect\_Deg\_f32) = TRUE \\ TS3.2 & "if (Delta\_Deg\_T\_f32 > k\_StepDetect\_Deg\_f32) = FALSE \\ else & if (Delta\_Deg\_T\_f32 < -k\_StepDetect\_Deg\_f32) = TRUE" \\ TS3.3 & "if (Delta\_Deg\_T\_f32 > k\_StepDetect\_Deg\_f32) = FALSE \\ else & if (Delta\_Deg\_T\_f32 < -k\_StepDetect\_Deg\_f32) = FALSE" \\ \end{tabular}$ 

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1080	1080 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3	~

Test Step 3.2 (Repeat Count = 1)			<b>~</b>
Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	24		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓

Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Delta_Deg_T_f32	30		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	s16	
k_StepDetect_Deg_f32	30		
tgt_RoughTurnAccPtr_Cnt_T_s16	-3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1080	-1080 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-3	-3	<b>✓</b>

DigColPs\_Per2

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Project DigColPs Module DigColPs **Test Object** DigColPs\_Per2

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\S_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path\)include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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DigColPs\_Per2



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>



```
Test Case 1: Metrics Test
Specification
                                                              Performance Metrics:
(With "None" instrumentation and WithPS
Environment)
                                                                                           8951.00 Cycles Longest Execution Path 12143.00 Cycles Shortest Execution Path
                                                               TS1.2
Description
                                                              VECTOR DESCRIPTION:
                                                              TS1.1 "Longest Execution Path =>
                                                            TS1.1 "Longest Execution Path =>
if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08) &&
(I2CColSensorFault_Cnt_T_lgc == FALSE) &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE) &&
(TrimCompleteEOL_Cnt_T_lgc == TRUE) )=>True
if (HwAVernCorrFault_Cnt_T_lgc == FALSE)=>True
if (HwAVernCorrFault_Cnt_T_lgc == FALSE)=>True
                                                             (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>False
if ((DigColPs_VernierAngleOORange_Cnt_M_u16 == 0U) && (DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 == 0U))=>False
if ((2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>False
if ((ColParityError_Cnt_T_lgc == TRUE) ||
(SpurParityError_Cnt_T_lgc == TRUE) ||
(I2CSensCommFlts_Cnt_T_u08 != 0U) )=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE) >>False
else if ((I2CColSensorFault_Cnt_T_lgc == TRUE) )=>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == TRUE) >>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) >>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) >>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) >>False
if (DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(I2CColSensorFaultAcc_Cnt_M_u16 == 0U)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
                                                             if ( (ErrorDataReady_Cnt_T_lgc == TRUE) && (ParityOrCommFault_Cnt_T_lgc == FALSE) && (12CSpurSensorFault_Cnt_T_lgc == TRUE) )=>False
                                                           (i2CSpurSensorFault_Cnt_T_igc == TRUE) )=>False
if (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U)=>True"
TS1.2 "Shortest Execution Path =>
if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if ((12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08) &&
(i2CColSensorFault_Cnt_T_igc == FALSE) &&
(i2CSpurSensorFault_Cnt_T_igc == FALSE) &&
(i2CSpurSensorFault_Cnt_T_igc == FALSE) &&
(i2CSpurSensorFault_Cnt_T_igc == TRUE) )=>False
if (k_SelectFromColumn_Cnt_igc == TRUE) )=>False
if ((AbsVernDiagError_Deg_T_f32) *_VernCorrerrorThresh_Deg_f32) && (AngleDataAvailable_Cnt_T_igc == TRUE))=>False
if ((AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
(AngleDataAvailable_Cnt_T_igc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_igc == TRUE) )=>False
if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) ==
TRUE)=>>True
                                                           (SpurParityError_Cht_I_gc == IRUE) ||
(I2CSensCommFits_Cnt_T_u08 != 0U) )=>False
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) &&
(I2CColSensorFault_Cnt_T_lgc == TRUE) =>>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == TRUE) &&
(I2CSpurSensorFault_Cnt_T_lgc == TRUE) =>>True
```

Test Step 1.1 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	255	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_ColTrimStatic_Deg_M_f32	259.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	46069	
DigColPs_I2CHwColAngle_Deg_M_f32	360	





Name  DigColPs_I2CHwDataType_Cnt_M_u08  DigColPs_I2CHwSpurAngle_Cnt_M_u16  DigColPs_I2CHwSpurAngle_Deg_M_f32  DigColPs_I2CHwTrimTransCnts_Uls_M_u08  DigColPs_I2CSensCommFits_Cnt_M_u08  DigColPs_I2CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_SpurParityError_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurTrimStatic_Deg_M_f32	Input Value  1
DigColPs_12CHwSpurAngle_Cnt_M_u16  DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CHwTrimTransCnts_Uls_M_u08  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevColPos_Deg_M_f32  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_SkipStepFitDetectAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurTrimStatic_Deg_M_f32	29552 33.3 2 9 0 0 224.1625181
DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CHwTrimTransCnts_Uls_M_u08  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevColPos_Deg_M_f32  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_SkipStepFitDetectAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurTrimStatic_Deg_M_f32	33.3 2 9 0 0 224.1625181
DigColPs_12CHwSpurAngle_Deg_M_f32  DigColPs_12CHwTrimTransCnts_Uls_M_u08  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevColPos_Deg_M_f32  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_SkipStepFitDetectAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurTrimStatic_Deg_M_f32	2 9 0 0 224.1625181 7
DigColPs_12CHwTrimTransCnts_Uls_M_u08  DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevColPos_Deg_M_f32  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_SkipStepFitDetectAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurTrimStatic_Deg_M_f32	2 9 0 0 224.1625181 7
DigColPs_12CSensCommFits_Cnt_M_u08  DigColPs_12CSpurSensorFault_Cnt_M_lgc  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc  DigColPs_PrevColPos_Deg_M_f32  DigColPs_PrevVernierLevelNo_Cnt_M_u08  DigColPs_SkipStepFltDetectAcc_Cnt_M_u16  DigColPs_SpurParityError_Cnt_M_lgc  DigColPs_SpurSensorFaultAcc_Cnt_M_u16  DigColPs_SpurTrimStatic_Deg_M_f32	9 0 0 224.1625181 7
DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	0 0 224.1625181 7
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	0 224.1625181 7
DigCoIPs_PrevCoIPos_Deg_M_f32 DigCoIPs_PrevVernierLeveINo_Cnt_M_u08 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_SpurTrimStatic_Deg_M_f32	224.1625181 7
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	
DigColPs_SpurParityError_Cnt_M_Igc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	4
DigColPs_SpurParityError_Cnt_M_Igc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2 ColSpurVernierLUT Cnt s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][15]	
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2 DualSpurVernierLUT Cnt s16[0][8]	-108
T2 DualSpurVernierLUT Cnt s16[0][9]	-72
	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][10] T3_DualSpurVernierLUT_Cst_s16[0][11]	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][3]	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T3_DualSpurVernierLUT_Cst_s16[1][18]	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[2][9]	
T2_DualSpurVernierLUT_Cnt_s16[2][10] T0_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

DigColPs\_Per2

2014-10-14, 17:31:16+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpur/crierLUT_Cnt_s16[3][10] T3_DualSpur/crierLUT_Cnt_s16[3][41]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt Igc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	55		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	3.54		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	sPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState	_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp	o_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371.030273	371.0302938 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4	4	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	4	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-528.969727	-528.9697062 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	
NTC	0x6E	0x6E	•
Param	0x00	0x00	,
Statue	0.00	0,400	

0x00

0x6F

0x00

0x00

0x00

0x6F

0x00

0x00

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Status

Param

Status

NTC

6



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

T2_ColSpurVernierLUT_Cnt_s16[0][9] 130  T2_ColSpurVernierLUT_Cnt_s16[0][10] 163  T2_ColSpurVernierLUT_Cnt_s16[0][11] 196  T2_ColSpurVernierLUT_Cnt_s16[0][12] 229  T2_ColSpurVernierLUT_Cnt_s16[0][13] 261  T2_ColSpurVernierLUT_Cnt_s16[0][14] 294  T2_ColSpurVernierLUT_Cnt_s16[0][15] 327  T2_ColSpurVernierLUT_Cnt_s16[0][16] 359  T2_ColSpurVernierLUT_Cnt_s16[0][16] 359  T2_ColSpurVernierLUT_Cnt_s16[1][0] 0  T2_ColSpurVernierLUT_Cnt_s16[1][0] 4  T2_ColSpurVernierLUT_Cnt_s16[1][1] 4  T2_ColSpurVernierLUT_Cnt_s16[1][2] 3  T2_ColSpurVernierLUT_Cnt_s16[1][3] 2  T2_ColSpurVernierLUT_Cnt_s16[1][4] 1  T2_ColSpurVernierLUT_Cnt_s16[1][5] 0  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 0		
Popt ComPanie Constitutation   44	Test Step 1.2 (Repeat Count = 1)	v
Digital Part   Digi		Input Value
Depoche   Defamery Finer, Crift, M. 196   Depoche   Defamery Crift M. 196   Depoche   Defamery Crift M. 196   Depoche   Defamery Crift M. 196   Depoche		·
DipColley Colfembro Paul Muse De Muse DipColley Have Working De Muse DipColley Colfembro De Muse DipColley Have Working DipColley H		
DigicoRep. NotWorkProfile (Crit M.) Igc		
Digicolles    ECCRISTORTIAL C. IT. M. Igo		
DigicalPs_IDCOASMORPS_CLTU_M_ISS   DigicalPs_IDCOASMORS_CRTU_M_ISS   Dig		
Digicoling		
DigicalPs   Inches   December		
DigicalPay   2CH-Moran Type: Crit   Must		
DepCoRest   20CH-MayunAge  DepCoRest   20CH   20C	· · · · · · · · · · · · · · · · · · ·	
DigCoPe_12COM-SpurAnge_Deg_M_S2  DigCoPe_12COSpurSensoramifas_Cnt_M_U88  DigCoPe_12COSpurSensoramifas_Cnt_M_U88  DigCoPe_12COSpurSensoramifas_Cnt_M_U89  DigCoSpurSensoramifas_Cnt_M_U89  DigCoSpurSensoramifas_Cnt_M_U89  DigCoSpurSensoramifas_Cnt_M_U89  DigCoSpurSensoramifas_Cnt_M_U89  DigCoSpurSensoramifas_Cnt_M_U89  DigCoSpu		
DepColleg.   I2CHM/TrimTransCrist. U.B. M. Ju08   0     DepColleg.   I2CSenscommist. Crit. M. Use   1     DepColleg.   I2CSenscommist. Crit. M. Use   1     DepColleg.   I2CSenscommist. Crit. M. Use   1     DepColleg.   Prev. Magle Data Available. Crit. M. Use   1     DepColleg.   Prev. Magle Data Available. Crit. M. Use   1     DepColleg.   Prev. Magle Data Available. Crit. M. Use   1     DepColleg.   Subject Diebeckor. Crit. M. Use   2     DepColleg.   Vernice Angle Crit. M. Us		
DigCoPs_   ZCSansCommils_Cnt_M_U08		
DigCoPs_ I2CSpurSensorFault_Crit_M_lgc		
DigColPs_PrevAnglebatavalable_Crt_M_gc   DigColPs_PrevColPs_Deg_M_f32   1800   DigColPs_PrevColPs_Deg_M_f32   1800   DigColPs_PrevColPs_Deg_M_f32   1800   DigColPs_SpishplebetAbc_Crt_M_u16   21   DigColPs_SpishplebetAbc_Crt_M_u16   25   DigColPs_SpishplebetAbc_Crt_M_u16   25   DigColPs_SpishplebetAbc_Crt_M_u16   25   DigColPs_TrimCompStatic_Crt_M_u16   26   DigColPs_TrimCompStatic_Crt_M_u16   20   Title_TrimCompStatic_Crt_M_u16   20   Title_TrimCompStati		·
DigCoRPs_PrevCoRPos_Deg_M_152		
DigCoPle_PrevVeriniet.nevRol_Cnt_M_u08   1   DigCoPle_SpirSeinSeinDetectAcc_Cnt_M_u16   21   DigCoPle_SpirSeinSeinDetectAcc_Cnt_M_u16   25   DigCoPle_SpirSeinSeinDetectAcc_Cnt_M_u16   255   DigCoPle_SpirSeinSeinDetectAcc_Cnt_M_u16   26   DigCoPle_SpirSeinSeinDetectAcc_Cnt_M_u16   26   DigCoPle_PrevTorDetectAcc_Cnt_M_u16   20   DigCoPle_PrevTorDetectAcc_Cnt_M_u16   21   DigCoPle_PrevTorDetectAcc_Cnt_M_u16   20   DigCoPle_PrevTorDetectAcc_Cnt_M_u16   21   DigCoPle_PrevTorDetectAcc_Cnt_M_u16		
DigCoIPs_SkipStepFIIDtectAcc_Cnt_M_u16		
DigCoPs_SpurPensorFaulAcc_Cnt_M_u16   255     DigCoPs_SpurPensorFaulAcc_Cnt_M_u16   255     DigCoPs_SpurPensorFaulAcc_Cnt_M_u16   255     DigCoPs_SpurPensorFaulAcc_Cnt_M_u16   2488     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   2488     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   20     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   20     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   21     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   21     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   21     DigCoPs_VermCorrObetecAcc_Cnt_M_u16   21     DigCoPs_Verminet_UT_Cnt_st6[0]0   1-163     T2_ColSpurVermet_UT_Cnt_st6[0]1   -131     T2_ColSpurVermet_UT_Cnt_st6[0]1   -131     T2_ColSpurVermet_UT_Cnt_st6[0]1   -33     T2_ColSpurVermet_UT_Cnt_st6[0]1   -33     T2_ColSpurVermet_UT_Cnt_st6[0]1   -33     T2_ColSpurVermet_UT_Cnt_st6[0]6   32     T2_ColSpurVermet_UT_Cnt_st6[0]6   32     T2_ColSpurVermet_UT_Cnt_st6[0]6   32     T2_ColSpurVermet_UT_Cnt_st6[0]7   -65     T2_ColSpurVermet_UT_Cnt_st6[0]8   98     T2_ColSpurVermet_UT_Cnt_st6[0]8   98     T2_ColSpurVermet_UT_Cnt_st6[0]9   130     T2_ColSpurVermet_UT_Cnt_st6[0]10   163     T2_ColSpurVermet_UT_Cnt_st6[0]11   196     T2_ColSpurVermet_UT_Cnt_st6[0]11   196     T2_ColSpurVermet_UT_Cnt_st6[0]12   229     T2_ColSpurVermet_UT_Cnt_st6[0]13   261     T2_ColSpurVermet_UT_Cnt_st6[0]13   261     T2_ColSpurVermet_UT_Cnt_st6[0]13   261     T2_ColSpurVermet_UT_Cnt_st6[0]13   261     T2_ColSpurVermet_UT_Cnt_st6[0]13   27     T2_ColSpurVermet_UT_Cnt_st6[0]14   1     T2_ColSpurVermet_UT_Cnt_st6[0]15   0     T2_ColSpurVermet_UT_Cnt_st6[0]16   0     T2_ColSpurVermet_UT_Cnt_st6[0]17   0     T2_ColSpurVermet_UT_Cnt_st6		
DigCoPs_SpurTmiStatic_Deg_M_T32   360     DigCoPs_SpurTmiStatic_Deg_M_T32   360     DigCoPs_TmiCrompStatic_Cnt_M_uri6   4488     DigCoPs_VernCornDetectAcc_Cnt_M_uri6   20     DigCoPs_VernCornDetectAcc_Cnt_M_uri6   20     DigCoPs_VernCornDetectAcc_Cnt_M_uri6   21     Rie_Inst_Sa_DigColPs   tgt_Rie_Inst_Sa_DigColPs     T2_ColSpurVerniert.UT_Cnt_s160[0]   -163     T2_ColSpurVerniert.UT_Cnt_s160[1]   -131     T2_ColSpurVerniert.UT_Cnt_s160[1]   -131     T2_ColSpurVerniert.UT_Cnt_s160[1]   -33     T2_ColSpurVerniert.UT_Cnt_s160[1]   -33     T2_ColSpurVerniert.UT_Cnt_s160[1]   -33     T2_ColSpurVerniert.UT_Cnt_s160[1]   -32     T2_Col		
DigCoIPs   SpurTrimStatic_Deg_M_132   380     DigCoIPs   TrimCompStatic_Cnt_M_u16   4488     DigCoIPs   TrimCompStatic_Cnt_M_u16   20     DigCoIPs   SpurTrimStatic_Deg_M_132   20     DigCoIPs   DigCoIPs   20     DigCoIPs   SpurTrimStatic_Deg_M_132   20     DigCoIPs   DigCoIPs		
DigCoIPs_TrimCompStatic_Cnt_M_u16   20		
DigCoIPs   VernicAngleCORange   Cnt_M_igc   1		
DigColPs   VernierAngleOORange_Cnt_M_lgc   1   163   163   163   172   163   172   163   173   163   173   163   173		
Rte_inst_Sa_DigColPs		
12_ColSpurVernierLUT_Cnt_s16[0][0]   -163   -131   -131   -131   -132   -132   -132   -132   -132   -132   -133   -132   -133   -133   -134		
12_ColSpurVernierLUT_Cnt_s16[0][1] -131 12_ColSpurVernierLUT_Cnt_s16[0][2] -99 12_ColSpurVernierLUT_Cnt_s16[0][3] -66 12_ColSpurVernierLUT_Cnt_s16[0][4] -33 12_ColSpurVernierLUT_Cnt_s16[0][6] 0 12_ColSpurVernierLUT_Cnt_s16[0][6] 32 12_ColSpurVernierLUT_Cnt_s16[0][6] 32 12_ColSpurVernierLUT_Cnt_s16[0][8] 98 12_ColSpurVernierLUT_Cnt_s16[0][9] 130 12_ColSpurVernierLUT_Cnt_s16[0][9] 130 12_ColSpurVernierLUT_Cnt_s16[0][9] 130 12_ColSpurVernierLUT_Cnt_s16[0][1] 163 12_ColSpurVernierLUT_Cnt_s16[0][1] 229 12_ColSpurVernierLUT_Cnt_s16[0][1] 229 12_ColSpurVernierLUT_Cnt_s16[0][1] 241 12_ColSpurVernierLUT_Cnt_s16[0][1] 294 12_ColSpurVernierLUT_Cnt_s16[0][1] 399 12_ColSpurVernierLUT_Cnt_s16[0][1] 399 12_ColSpurVernierLUT_Cnt_s16[0][1] 399 12_ColSpurVernierLUT_Cnt_s16[0][1] 399 12_ColSpurVernierLUT_Cnt_s16[0][1] 4 12_ColSpurVernierLUT_Cnt_s16[0][1] 4 12_ColSpurVernierLUT_Cnt_s16[0][1] 4 12_ColSpurVernierLUT_Cnt_s16[1][1] 4 12_ColSpurVernierLUT_Cnt_s16[1][1] 4 12_ColSpurVernierLUT_Cnt_s16[1][1] 1 13_ColSpurVernierLUT_Cnt_s16[1][1] 1 14_ColSpurVernierLUT_Cnt_s16[1][1] 1 15_ColSpurVernierLUT_Cnt_s16[1][1] 1 16_ColSpurVe		
T2_ColSpurVernierLUT_Cnt_st6[0][2] -99  T2_ColSpurVernierLUT_Cnt_st6[0][3] -66  T2_ColSpurVernierLUT_Cnt_st6[0][4] -33  T2_ColSpurVernierLUT_Cnt_st6[0][6] -32  T2_ColSpurVernierLUT_Cnt_st6[0][7] -65  T2_ColSpurVernierLUT_Cnt_st6[0][8] -32  T2_ColSpurVernierLUT_Cnt_st6[0][8] -98  T2_ColSpurVernierLUT_Cnt_st6[0][9] -130  T2_ColSpurVernierLUT_Cnt_st6[0][9] -130  T2_ColSpurVernierLUT_Cnt_st6[0][10] -163  T2_ColSpurVernierLUT_Cnt_st6[0][11] -166  T2_ColSpurVernierLUT_Cnt_st6[0][12] -229  T2_ColSpurVernierLUT_Cnt_st6[0][13] -261  T2_ColSpurVernierLUT_Cnt_st6[0][14] -294  T2_ColSpurVernierLUT_Cnt_st6[0][15] -327  T2_ColSpurVernierLUT_Cnt_st6[0][16] -359  T2_ColSpurVernierLUT_Cnt_st6[0][16] -359  T2_ColSpurVernierLUT_Cnt_st6[0][16] -359  T2_ColSpurVernierLUT_Cnt_st6[0][16] -359  T2_ColSpurVernierLUT_Cnt_st6[0][16] -370  T2_ColSpurVernierLUT_Cnt_st6[0][16] -40  T2_ColSpurVernierLUT_Cnt_st6[0][1		
T2_ColSpurVernierLUT_Cnt_s16[0][3] -66 T2_ColSpurVernierLUT_Cnt_s16[0][4] -33 T2_ColSpurVernierLUT_Cnt_s16[0][6] -20 T2_ColSpurVernierLUT_Cnt_s16[0][6] -32 T2_ColSpurVernierLUT_Cnt_s16[0][7] -65 T2_ColSpurVernierLUT_Cnt_s16[0][9] -88 T2_ColSpurVernierLUT_Cnt_s16[0][9] -130 T2_ColSpurVernierLUT_Cnt_s16[0][9] -130 T2_ColSpurVernierLUT_Cnt_s16[0][10] -163 T2_ColSpurVernierLUT_Cnt_s16[0][11] -196 T2_ColSpurVernierLUT_Cnt_s16[0][11] -196 T2_ColSpurVernierLUT_Cnt_s16[0][13] -261 T2_ColSpurVernierLUT_Cnt_s16[0][13] -261 T2_ColSpurVernierLUT_Cnt_s16[0][14] -294 T2_ColSpurVernierLUT_Cnt_s16[0][16] -359 T2_ColSpurVernierLUT_Cnt_s16[0][16] -359 T2_ColSpurVernierLUT_Cnt_s16[0][16] -359 T2_ColSpurVernierLUT_Cnt_s16[1][0] -0 T2_ColSpurVernierLUT_Cnt_s16[1][1] -4 T2_ColSpurVernierLUT_Cnt_s16[1][1] -1 T2_ColSpurVernierLUT_Cnt_s16[1][1		
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T2_ColSpurVernierLUT_Cnt_s16[0][6]       32         T2_ColSpurVernierLUT_Cnt_s16[0][8]       98         T2_ColSpurVernierLUT_Cnt_s16[0][9]       130         T2_ColSpurVernierLUT_Cnt_s16[0][10]       163         T2_ColSpurVernierLUT_Cnt_s16[0][11]       196         T2_ColSpurVernierLUT_Cnt_s16[0][12]       229         T2_ColSpurVernierLUT_Cnt_s16[0][13]       261         T2_ColSpurVernierLUT_Cnt_s16[0][14]       294         T2_ColSpurVernierLUT_Cnt_s16[0][16]       327         T2_ColSpurVernierLUT_Cnt_s16[0][16]       359         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVernierLUT_Cnt_s16[1][1]       3         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVernierLUT_Cnt_s16[1][1]       1         T2_ColSpurVe		0
T2_ColSpurVernierLUT_Cnt_s16[0][7]		32
T2_ColSpurVernierLUT_Cnt_s16[0][8] 98 T2_ColSpurVernierLUT_Cnt_s16[0][9] 130 T2_ColSpurVernierLUT_Cnt_s16[0][10] 163 T2_ColSpurVernierLUT_Cnt_s16[0][11] 196 T2_ColSpurVernierLUT_Cnt_s16[0][12] 229 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 1 T2_ColSpurVernierLUT_Cnt_s16[1][6] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 1		65
T2_ColSpurVernierLUT_Cnt_s16[0][10]  163  T2_ColSpurVernierLUT_Cnt_s16[0][11]  196  T2_ColSpurVernierLUT_Cnt_s16[0][12]  229  T2_ColSpurVernierLUT_Cnt_s16[0][13]  261  T2_ColSpurVernierLUT_Cnt_s16[0][14]  294  T2_ColSpurVernierLUT_Cnt_s16[0][15]  327  T2_ColSpurVernierLUT_Cnt_s16[0][16]  359  T2_ColSpurVernierLUT_Cnt_s16[1][0]  0  T2_ColSpurVernierLUT_Cnt_s16[1][0]  4  T2_ColSpurVernierLUT_Cnt_s16[1][1]  4  T2_ColSpurVernierLUT_Cnt_s16[1][3]  2  T2_ColSpurVernierLUT_Cnt_s16[1][3]  2  T2_ColSpurVernierLUT_Cnt_s16[1][4]  1  T2_ColSpurVernierLUT_Cnt_s16[1][6]  4  T2_ColSpurVernierLUT_Cnt_s16[1][6]  4  T2_ColSpurVernierLUT_Cnt_s16[1][6]  4  T2_ColSpurVernierLUT_Cnt_s16[1][7]  3  T2_ColSpurVernierLUT_Cnt_s16[1][8]  2  T2_ColSpurVernierLUT_Cnt_s16[1][8]  2  T2_ColSpurVernierLUT_Cnt_s16[1][9]  1  T2_ColSpurVernierLUT_Cnt_s16[1][10]  3	T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][11] 196 T2_ColSpurVernierLUT_Cnt_s16[0][12] 229 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][11] 3	T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][12] 229  T2_ColSpurVernierLUT_Cnt_s16[0][13] 261  T2_ColSpurVernierLUT_Cnt_s16[0][14] 294  T2_ColSpurVernierLUT_Cnt_s16[0][15] 327  T2_ColSpurVernierLUT_Cnt_s16[0][16] 359  T2_ColSpurVernierLUT_Cnt_s16[1][0] 0  T2_ColSpurVernierLUT_Cnt_s16[1][0] 4  T2_ColSpurVernierLUT_Cnt_s16[1][1] 4  T2_ColSpurVernierLUT_Cnt_s16[1][2] 3  T2_ColSpurVernierLUT_Cnt_s16[1][2] 3  T2_ColSpurVernierLUT_Cnt_s16[1][3] 2  T2_ColSpurVernierLUT_Cnt_s16[1][4] 1  T2_ColSpurVernierLUT_Cnt_s16[1][5] 0  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][7] 3  T2_ColSpurVernierLUT_Cnt_s16[1][8] 2  T2_ColSpurVernierLUT_Cnt_s16[1][8] 2  T2_ColSpurVernierLUT_Cnt_s16[1][9] 1  T2_ColSpurVernierLUT_Cnt_s16[1][1] 4  T2_ColSpurVernierLUT_Cnt_s16[1][1] 3	T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][13] 261  T2_ColSpurVernierLUT_Cnt_s16[0][14] 294  T2_ColSpurVernierLUT_Cnt_s16[0][15] 327  T2_ColSpurVernierLUT_Cnt_s16[0][16] 359  T2_ColSpurVernierLUT_Cnt_s16[1][0] 0  T2_ColSpurVernierLUT_Cnt_s16[1][1] 4  T2_ColSpurVernierLUT_Cnt_s16[1][2] 3  T2_ColSpurVernierLUT_Cnt_s16[1][3] 2  T2_ColSpurVernierLUT_Cnt_s16[1][4] 1  T2_ColSpurVernierLUT_Cnt_s16[1][5] 0  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 2  T2_ColSpurVernierLUT_Cnt_s16[1][6] 2  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 5  T2_ColSpurVernierLUT_Cnt_s16[1][6] 6  T2_ColSpurVernierLUT_Cnt_s16[1][6] 7  T2_ColSpurVernierLUT_Cnt_s16[1][6] 7  T2_ColSpurVernierLUT_Cnt_s16[1][6] 7  T2_ColSpurVernierLUT_Cnt_s16[1][6] 7  T2_ColSpurVernierLUT_Cnt_s16[1][6] 7  T2_ColSpurVernierLUT_Cnt_s16[1][6] 7  T2_ColSpurVernierLUT_Cnt_s16[1][10] 7  T2_ColSpurVernierLUT_Cnt_s16[1][	T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][13]       261         T2_ColSpurVernierLUT_Cnt_s16[0][14]       294         T2_ColSpurVernierLUT_Cnt_s16[0][15]       327         T2_ColSpurVernierLUT_Cnt_s16[0][16]       359         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][6]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       2         T2_ColSpurVernierLUT_Cnt_s16[1][6]       2         T2_ColSpurVernierLUT_Cnt_s16[1][6]       1         T2_ColSpurVernierLUT_Cnt_s16[1][6]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0	T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][15] 327  T2_ColSpurVernierLUT_Cnt_s16[0][16] 359  T2_ColSpurVernierLUT_Cnt_s16[1][0] 0  T2_ColSpurVernierLUT_Cnt_s16[1][1] 4  T2_ColSpurVernierLUT_Cnt_s16[1][2] 3  T2_ColSpurVernierLUT_Cnt_s16[1][3] 2  T2_ColSpurVernierLUT_Cnt_s16[1][4] 1  T2_ColSpurVernierLUT_Cnt_s16[1][5] 0  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][6] 4  T2_ColSpurVernierLUT_Cnt_s16[1][8] 2  T2_ColSpurVernierLUT_Cnt_s16[1][9] 1  T2_ColSpurVernierLUT_Cnt_s16[1][9] 1  T2_ColSpurVernierLUT_Cnt_s16[1][10] 0  T2_ColSpurVernierLUT_Cnt_s16[1][10] 1  T2_ColSpurVernierLUT_Cnt_s16[1][10] 1  T2_ColSpurVernierLUT_Cnt_s16[1][10] 3  T2_ColSpurVernierLUT_Cnt_s16[1][10] 3	T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][16]       359         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][5]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4	T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][7] 3 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][9] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][12] 3	T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[1][1]	T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][1]	T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][7] 3 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][9] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][12] 3	T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][4]	T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][7] 3 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][8] 1 T2_ColSpurVernierLUT_Cnt_s16[1][9] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][12] 3	T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][5]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3	T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3	T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3	T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3	T2_ColSpurVernierLUT_Cnt_s16[1][7]	
T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3	T2_ColSpurVernierLUT_Cnt_s16[1][8]	
T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][12] 3	T2_ColSpurVernierLUT_Cnt_s16[1][9]	1.
T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][12] 3	T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
	T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][13] 2	T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
	T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Input Value  1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2 DualSpurVernierLUT Cnt s16[1][14]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2 DualSpurVernierLUT Cnt s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	t Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
	Actual Value	Expected Value	Result
	Actual Value	Expedied value	- INCSUIT
Name DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	.4

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DigColPs\_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x04	0x04	<b>✓</b>
Status	0x01	0x01	~
NTC	0x6E	0x6E	~
Param	0x7F	0x7F	~
Status	0x01	0x01	~
NTC	0x6F	0x6F	<b>✓</b>
Param	0x7F	0x7F	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>~</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

DigColPs\_Per2

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Test Case 2: Boundary Test

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#### Specification

DigColPs\_Per2

Performance Metrics: (With "None" instrumentation and WithPS Environment) CPU Cycles: 12132.00 Cycles 5985.00 Cycles 5849.00 Cycles 5935.00 Cycles TS2 1 TS2.2 TS2.3 TS2.4 5906.00 Cycles 5921.00 Cycles 2806.00 Cycles TS2.5 TS2.6 TS2.7 2658.00 Cvcles TS2.8 5843.00 Cycles 5773.00 Cycles 8997.00 Cycles TS2.9 TS2.10 TS2.11 2696.00 Cycles 8863.00 Cycles 8857.00 Cycles 8868.00 Cycles TS2.12 TS2.13 TS2.14 TS2.15 8856.00 Cycles 8849.00 Cycles 8854.00 Cycles 5822.00 Cycles TS2.16 TS2.17 TS2.18 TS2.19 5822.00 Cycles
12189.00 Cycles
12185.00 Cycles
5712.00 Cycles
3039.00 Cycles
3080.00 Cycles
2696.00 Cycles
8857.00 Cycles
8857.00 Cycles
8857.00 Cycles
8856.00 Cycles
8854.00 Cycles
8854.00 Cycles
8854.00 Cycles TS2.20 TS2.21 TS2.22 TS2.23 TS2.24 TS2.25 TS2.26 TS2.27 TS2.28 TS2.29 TS2.30 TS2.31 TS2.32 TS2.33 TS2.34 TS2.35 5822.00 Cycles 12189.00 Cycles 12185.00 Cycles 5712.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles 2696.00 Cycles 8863.00 Cycles 8857.00 Cycles 8856.00 Cycles 8856.00 Cycles TS2.36 TS2.37 TS2.38 TS2.39 TS2.40 TS2.41 TS2.42 8856.00 Cycles 8849.00 Cycles 8854.00 Cycles 5822.00 Cycles 12189.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles TS2.43 TS2.44 TS2.45 TS2.46 TS2.46 TS2.47 TS2.48 TS2.49 TS2.50 8951.00 Cycles 12143.00 Cycles 12132.00 Cycles 5985.00 Cycles 5945.00 Cycles 5935.00 Cycles 5906.00 Cycles 5921.00 Cycles TS2.52 TS2.53 TS2.54 TS2.55 TS2.56 TS2.57 TS2.58 5921.00 Cycles 2806.00 Cycles 2658.00 Cycles 5843.00 Cycles 5773.00 Cycles 2696.00 Cycles 8863.00 Cycles 8865.00 Cycles 8865.00 Cycles 8856.00 Cycles 8854.00 Cycles 8854.00 Cycles 8854.00 Cycles 2822.00 Cycles 12189.00 Cycles TS2.59 TS2.60 TS2.61 TS2.62 TS2.63 TS2.64 TS2.65 TS2.66 TS2.66 TS2.68 TS2.69 TS2.70 TS2.71 TS2.72 TS2.73 TS2.74 TS2.75 12189.00 Cycles 12185.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles 2696.00 Cycles 8863.00 Cycles TS2.76 TS2.77 TS2.78 8857.00 Cycles 8868.00 Cycles 8856.00 Cycles 8849.00 Cycles TS2.79 TS2.80 TS2.81 TS2.82 TS2.83 TS2.84 TS2.85 8854.00 Cycles 5822.00 Cycles 12189.00 Cycles 12185.00 Cycles TS2.86 TS2.87 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles TS2.88 TS2.89 3080.00 Cycles 2696.00 Cycles 8863.00 Cycles 8857.00 Cycles 8868.00 Cycles 8856.00 Cycles TS2.90 TS2.91 TS2.92 TS2.93 TS2.94 TS2.95 TS2.96 TS2.97 8849.00 Cycles 8854.00 Cycles 5822.00 Cycles 5822.00 Cycles
12189.00 Cycles
12185.00 Cycles
5712.00 Cycles
3039.00 Cycles
3080.00 Cycles
3039.00 Cycles
3039.00 Cycles
3039.00 Cycles
3039.00 Cycles TS2.98 TS2.99 TS2.100 TS2.101 TS2.102 TS2 103 TS2.103 TS2.104 TS2.105

TS2.108 © Report created by TESSY V3.1.9, report template V2.1

TS2.106

TS2 107

3080.00 Cycles 5712.00 Cycles

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DigColPs\_Per2



TS2.109 3039.00 Cycles TS2.110 3080.00 Cycles





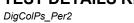
**Description** VECTOR DESCRIPTION:

TS2.1All Min TS2.2All Max TS2.3DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc=Min TS2.4DigColPs\_I2CColSensorFault\_Cnt\_M\_lgc=Max TS2.5DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc=Min TS2.6DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc=Max TS2.7DigColPs\_ColParityError\_Cnt\_M\_lgc=Min TS2.8DigColPs\_ColParityError\_Cnt\_M\_lgc=Max TS2.9DigColPs\_SpurParityError\_Cnt\_M\_lgc=Min TS2.10DigColPs\_SpurParityError\_Cnt\_M\_lgc=Max TS2.11DigColPs\_I2CHwColAngle\_Cnt\_M\_u16=Min TS2.12DigColPs\_I2CHwColAngle\_Cnt\_M\_u16=Max TS2.13DigColPs\_I2CHwColAngle\_Cnt\_M\_u16=Pos TS2.14DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Min TS2.15DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Max TS2.16DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Max
TS2.16DigColPs\_I2CHwSpurAngle\_Cnt\_M\_u16=Pos
TS2.17DigColPs\_I2CHwDataType\_Cnt\_M\_u08=Min
TS2.18DigColPs\_I2CHwDataType\_Cnt\_M\_u08=Max
TS2.19DigColPs\_I2CHwDataType\_Cnt\_M\_u08=Pos
TS2.20DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08=Min
TS2.21DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08=Max TS2.22DigColPs\_I2CSensCommFlts\_Cnt\_M\_u08=Pos TS2.23DigColPs\_I2CHwColAngleTrim\_Deg\_M\_f32=Min TS2.24DigColPs\_I2CHwColAngleTrim\_Deg\_M\_f32=Max TS2.25DigColPs | I2CHwColAngleTrim Deg M f32=Pos TS2.26DigColPs | I2CHwColAngleTrim Deg M f32=Neg TS2.27DigColPs | I2CHwColAngleTrim Deg M f32=Zero TS2.28DigColPs\_I2CHwSpurAngleTrim\_Deg\_M\_f32=Min
TS2.28DigColPs\_I2CHwSpurAngleTrim\_Deg\_M\_f32=Min
TS2.30DigColPs\_I2CHwSpurAngleTrim\_Deg\_M\_f32=Nax
TS2.30DigColPs\_I2CHwSpurAngleTrim\_Deg\_M\_f32=Pos
TS2.31DigColPs\_I2CHwSpurAngleTrim\_Deg\_M\_f32=Nax TS2.32DigCoIPs\_I2CHwSpurAngleTrim\_Deg\_M\_f32=Zero TS2.33Rte\_Pim\_DigCoIPsEOL.TrimComp\_Cnt\_u16=Min TS2.34Rte\_Pim\_DigCoIPsEOL.TrimComp\_Cnt\_u16=Max TS2.35Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=Pos TS2.36k\_SelectFromColumn\_Cnt\_lgc=Min TS2.37k\_SelectFromColumn\_Cnt\_lgc=Max TS2.38k\_VernCorrErrorThresh\_Deg\_f32=Min TS2.39k\_VernCorrErrorThresh\_Deg\_f32=Max TS2.40k\_VernCorrErrorThresh\_Deg\_f32=Pos TS2.41DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16=Min TS2.42DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16=Max TS2.43DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16=Pos TS2.44DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08=Min TS2.44DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08=Max
TS2.46DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08=Pos
TS2.47DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08=Pos
TS2.47DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc=Min
TS2.48DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc=Max
TS2.49DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16=Min
TS2.50DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16=Max TS2.51DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16=Pos TS2.52DigColPs\_PrevColPos\_Deg\_M\_f32=Min TS2.53DigColPs\_PrevColPos\_Deg\_M\_f32=Max TS2.54DigColPs\_PrevColPos\_Deg\_M\_f32=Pos TS2.55DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc=Min TS2.56DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc=Max TS2.57DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16=Min TS2.58DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16=Min TS2.59DigColPs\_ColSensorFaultAcc\_Cnt\_M\_u16=Pos TS2.60DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16=Min TS2.61DigCoIPs\_SpurSensorFaultAcc\_Cnt\_M\_u16=Max TS2.62DigCoIPs\_SpurSensorFaultAcc\_Cnt\_M\_u16=Pos TS2.63k\_VernCorrErrorDiag\_Cnt\_str.Pstep=Min TS2.64k\_VernCorrErrorDiag\_Cnt\_str.Pstep=Max TS2.65k\_VernCorrErrorDiag\_Cnt\_str.Pstep=Pos TS2.66k\_VernCorrErrorDiag\_Cnt\_str.Nstep=Min TS2.67k\_VernCorrErrorDiag\_Cnt\_str.Nstep=Max TS2.68k\_VernCorrErrorDiag\_Cnt\_str.Nstep=Pos TS2.69k\_VernCorrErrorDiag\_Cnt\_str.Threshold=Min TS2.70k\_VernCorrErrorDiag\_Cnt\_str.Threshold=Max TS2.71k\_VernCorrErrorDiag\_Cnt\_str.Threshold=pos TS2.72k\_SkipStepErrDiag\_Cnt\_str.Threshold=Min TS2.73k\_SkipStepErrDiag\_Cnt\_str.Threshold=Max TS2.73k\_SkipStepErrDiag\_Cnt\_str.Threshold=Pos TS2.75k\_SkipStepErrDiag\_Cnt\_str.Pstep=Min TS2.76k\_SkipStepErrDiag\_Cnt\_str.Pstep=Max TS2.77k\_SkipStepErrDiag\_Cnt\_str.Pstep=Pos TS2.78k\_SkipStepErrDiag\_Cnt\_str.Nstep=Min TS2.76k\_SkipStepErrDiag\_Cnt\_str.Nstep=Miax TS2.86k\_SkipStepErrDiag\_Cnt\_str.Nstep=Max TS2.86k\_SkipStepErrDiag\_Cnt\_str.Nstep=Pos TS2.81k\_VernOORangeThresh\_Deg\_f32=Miax TS2.82k\_VernOORangeThresh\_Deg\_f32=Max TS2.83K\_VernOORangeThresh\_Deg\_f32=Pos TS2.84MecState\_Cnt\_enum=>ProductionMode TS2.85MecState\_Cnt\_enum=>ManufacturingMode TS2.86MecState\_Cnt\_enum=>EngineeringMode
TS2.87DigCoIPs\_CoITrimStatic\_Deg\_M\_f32=>Min
TS2.88DigCoIPs\_CoITrimStatic\_Deg\_M\_f32=>Max TS2.89DigColPs\_ColTrimStatic\_Deg\_M\_f32=>Pos TS2.90DigColPs\_ColTrimStatic\_Deg\_M\_f32=>Neg TS2.91DigColPs\_ColTrimStatic\_Deg\_M\_f32=>Zero TS2.92DigColPs\_SpurTrimStatic\_Deg\_M\_f32=>Min TS2.93DigColPs\_SpurTrimStatic\_Deg\_M\_f32=>Max TS2.94DigColPs\_SpurTrimStatic\_Deg\_M\_f32=>Pos



TS2.95DigColPs\_SpurTrimStatic\_Deg\_M\_f32=>Neg
TS2.96DigColPs\_SpurTrimStatic\_Deg\_M\_f32=>Zero
TS2.97DigColPs\_TrimCompStatic\_Cnt\_M\_u16=>Min
TS2.98DigColPs\_TrimCompStatic\_Cnt\_M\_u16=>Max
TS2.99DigColPs\_TrimCompStatic\_Cnt\_M\_u16=>Pos
TS2.10DDigColPs\_I2CHwColAngle\_Deg\_M\_f32=>Min
TS2.101DigColPs\_I2CHwColAngle\_Deg\_M\_f32=>Max
TS2.102DigColPs\_I2CHwColAngle\_Deg\_M\_f32=>Pos
TS2.103DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Min
TS2.104DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Min
TS2.104DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Pos
TS2.105DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Pos
TS2.105DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32=>Pos
TS2.105DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc=>Min
TS2.107DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc=>Max
TS2.108DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08=>Min
TS2.109DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08=>Pos

Input Value
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
tgt_Rte_Inst_Sa_DigColPs
-163
-131
-99
-66
-33
0
32
65
98
130
163
196
229
261
294
327
359
0
4
3
2
1
0
4
3
2
1
0
4
3
2
_
1
1 0





Input Value			
12_CoSparVamentU_Cot_150[20]		Name	Input Value
12. ColSpar/went_U. Cot.; s16(1)] 12. ColSpar/went_U. Cot.; s16(1)] 12. ColSpar/went_U. Cot.; s16(1)] 12. ColSpar/went_U. Cot.; s16(1)] 13. ColSpar/went_U. Cot.; s16(1)] 14. ColSpar/went_U. Cot.; s16(1)] 15. ColSpar/went_U. Cot.; s16(1)] 16. ColSpar/went_U. Cot.; s16(1)] 17. ColSpar/went_U. Cot.; s16(1)] 18. ColSpar/went_U. Cot.; s16(1)] 19. Co	Asignative montal LT (Crt.   1402 1)		·
12 CoSsys/Yenet LT Cnt. s16(3)	Assignation   L.   Col.   1905 3		8
12, CoSpar/Vennicut U. Cnt.; s10[2]4   2   12, CoSpar/Vennicut U. Cnt.; s10[2]5   0   17, CoSpar/Vennicut U. Cnt.; s10[2]5   9   17, CoSpar/Vennicut U. Cnt.; s10[2]5   9   17, CoSpar/Vennicut U. Cnt.; s10[2]5   5   17, CoSpar/Vennicut U. Cnt.; s10[2]5   5   17, CoSpar/Vennicut U. Cnt.; s10[2]5   1   1   1   1   1   1   1   1   1			6
12, CoSpar/Vennicut U. Cnt.; s10[2]4   2   12, CoSpar/Vennicut U. Cnt.; s10[2]5   0   17, CoSpar/Vennicut U. Cnt.; s10[2]5   9   17, CoSpar/Vennicut U. Cnt.; s10[2]5   9   17, CoSpar/Vennicut U. Cnt.; s10[2]5   5   17, CoSpar/Vennicut U. Cnt.; s10[2]5   5   17, CoSpar/Vennicut U. Cnt.; s10[2]5   1   1   1   1   1   1   1   1   1		T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
12. CoSpavYemeLUT. Cnt. 190(2)17 12. CoSpavYemeLUT. Cnt. 190(2)17 12. CoSpavYemeLUT. Cnt. 190(2)19 13. CoSpavYemeLUT. Cnt. 190(2)19 14. CoSpavYemeLUT. Cnt. 190(2)19 15. CoSpavYemeLUT. Cnt. 190(2)19 16. CoSpavYemeLUT. Cnt. 190(2)19 17. CoSpavYemeLUT. Cnt. 190(2)19 18. CoSpavYemeLUT. Cnt. 190(2)19 19. CoSpavYemeLUT. Cnt. 190(2)19	ASSULVAMENT U. D. 1, 1907		2
12, coSpur/ement_UT_Cnt_stotPine	ASSUM/APPORTUT_CILSTORTED	T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
12. Colspar/wentLUT_Cnt_stigUpt   5 12. Colspar/wentLUT_Cnt_stigUpt   1 13. Colspar/wentLUT_Cnt_stigUpt   1 14. Colspar/wentLUT_Cnt_stigUpt   1 15. Colspar/wentLUT_Cnt_stigUpt   1 16. Colspar/wentLUT_Cnt_stigUpt   1 17. Colspar/wentLUT_Cnt_stigUpt   1 18. Colspar/wentLUT_Cnt_stigUpt   1 19. Colspar/wentLUT_Cnt_stigUp	Sepur/metaLUT_Cut_stopping	T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
12_CoSput/went_UT_Cnt_stop[2]	Separ/ment UT_Cnsto[1]		7
12. ColSput/venicUT_Cnt_18[2][11] 12. ColSput/venicUT_Cnt_18[2][11] 13. ColSput/venicUT_Cnt_18[2][11] 14. ColSput/venicUT_Cnt_18[2][11] 15. ColSput/venicUT_Cnt_18[2][11] 16. ColSput/venicUT_Cnt_18[2][11] 17. ColSput/venicUT_Cnt_18[2][11] 18. ColSput/venicUT_Cnt_18[2][11] 19. Co	Sepur/senut.UF_cn_stock_100   1		
12. CoSpurVernictUT, Cet. 1 sti2[11] 10 12. CoSpurVernictUT, Cet. 1 sti2[113] 8 12. CoSpurVernictUT, Cet. 1 sti2[113] 8 12. CoSpurVernictUT, Cet. 1 sti2[114] 4 17. CoSpurVernictUT, Cet. 1 sti2[115] 12 17. CoSpurVernictUT, Cet. 1 sti2[116] 10 17. CoSpurVernictUT, Cet. 1 sti2[116] 11 17. CoSpurVernictUT, Cet. 1 sti2[116] 11 17. CoSpurVernictUT, Cet. 1 sti2[116] 11 17. CoSpurVernictUT, Cet. 1 sti2[11] 12 17. CoSpurVernictUT, Cet. 1 sti2[11] 13 18. CoSpurVernictUT, Cet. 1 sti2[11] 13 19. CoSpurVernictUT, Cet. 1 sti2[11] 13 1			
12_ColSpur/emietUT_Cnt_15(2)[11]   8   12_ColSpur/emietUT_Cnt_15(2)[11]   6   12_ColSpur/emietUT_Cnt_15(2)[11]   6   12_ColSpur/emietUT_Cnt_15(2)[11]   2   12_ColSpur/emietUT_Cnt_15(2)[11]   1   1   1   1   1   1   1   1   1			
12_ColSput/emitUT_Cnt_18[2]118    6			
12_Colspur/emierLUT_Cnt_sto[2]14   4     12_Colspur/emierLUT_Cnt_sto[2]16   2     12_Colspur/emierLUT_Cnt_sto[2]10   1     12_Colspur/emierLUT_Cnt_sto[3]10   1     12_Colspur/emierLUT_Cnt_sto[3]10   1     12_Colspur/emierLUT_Cnt_sto[3]11   14     12_Colspur/emierLUT_Cnt_sto[3]11   14     12_Colspur/emierLUT_Cnt_sto[3]13   8     12_Colspur/emierLUT_Cnt_sto[3]13   8     12_Colspur/emierLUT_Cnt_sto[3]13   8     12_Colspur/emierLUT_Cnt_sto[3]14   5     12_Colspur/emierLUT_Cnt_sto[3]15   2     12_Colspur/emierLUT_Cnt_sto[3]16   15     12_Colspur/emierLUT_Cnt_sto[3]17   12     12_Colspur/emierLUT_Cnt_sto[3]19   16     12_Colspur/emierLUT_Cnt_sto[3]10   17     12_Colspur/emierLUT_Cnt_sto[3]10   19     12_Colspur/emierLUT_Cnt_sto[3]10   19	Acceptable   Acc		
12_ColSpurVermietUT_Cnt_st6Q116   10   1   1   1   1   1   1   1   1			
T2_ColSpurVemietUT_Cnt_st6[3][6]   10   1   1   1   1   1   1   1   1	usbput/memLUT_Cnt_1963[0]         1           usbput/memLUT_Cnt_1963[0]         1           usbput/memLUT_Cnt_1963[0]         1           usbput/memLUT_Cnt_1963[0]         1           usbput/memLUT_Cnt_1963[0]         1           usbput/memLUT_Cnt_1963[0]         5           usbput/memLUT_Cnt_1963[0]         15           usbput/memLUT_Cnt_1963[0]         15           usbput/memLUT_Cnt_1963[0]         16           usbput/memLUT_Cnt_1963[0]         6           usbput/memLUT_Cnt_1963[0]         6           usbput/memLUT_Cnt_1963[0]         18           usbput/memLUT_Cnt_1963[0]         18           usbput/memLUT_Cnt_1963[0]         18           usbput/memLUT_Cnt_1963[0]         18           usbput/memLUT_Cnt_1963[0]         10           usbput/memLUT_Cnt_1963[0]         4           usbput/memLUT_Cnt_1963[0]         36		
12, ColSpurVernietUT_Cnt_st63[0]   14   12, ColSpurVernietUT_Cnt_st63[1]   14   12, ColSpurVernietUT_Cnt_st63[1]   14   12, ColSpurVernietUT_Cnt_st63[1]   15   12, ColSpurVernietUT_Cnt_st63[1]   5   12, ColSpurVernietUT_Cnt_st63[1]   5   12, ColSpurVernietUT_Cnt_st63[1]   15   12, ColSpurVernietUT_Cnt_st63[1]   15   12, ColSpurVernietUT_Cnt_st63[1]   15   12, ColSpurVernietUT_Cnt_st63[1]   16   13, ColSpurVernietUT_Cnt_st63[1]   16   17, ColSpurVernietUT_Cnt_st63[1]   17   18, ColSpurVernietUT_Cnt_st63[1]   17   18, ColSpurVernietUT_Cnt_st63[1]   17   19, Co			
12, ColSpurVermiert, UT_Cnt_s   5 3 1    14   17, ColSpurVermiert, UT_Cnt_s   5 3 3    8   17, ColSpurVermiert, UT_Cnt_s   5 3 3    8   17, ColSpurVermiert, UT_Cnt_s   5 3 3    8   17, ColSpurVermiert, UT_Cnt_s   5 3 3    5   17, ColSpurVermiert, UT_Cnt_s   5 3 3    5   17, ColSpurVermiert, UT_Cnt_s   5 3 3    15   15   15   15   15   15	AsspurvementUT_Cnt_st69[3]   14		
12, Colsput/emiet.UT_Cnt_s16[3][2]   11     72, Colsput/emiet.UT_Cnt_s16[3][3]   8     72, Colsput/emiet.UT_Cnt_s16[3][4]   5     72, Colsput/emiet.UT_Cnt_s16[3][6]   15     72, Colsput/emiet.UT_Cnt_s16[3][6]   15     72, Colsput/emiet.UT_Cnt_s16[3][7]   12     72, Colsput/emiet.UT_Cnt_s16[3][8]   9     72, Colsput/emiet.UT_Cnt_s16[3][8]   9     72, Colsput/emiet.UT_Cnt_s16[3][9]   6     72, Colsput/emiet.UT_Cnt_s16[3][9]   6     72, Colsput/emiet.UT_Cnt_s16[3][1]   16     72, Colsput/emiet.UT_Cnt_s16[3][1]   16     72, Colsput/emiet.UT_Cnt_s16[3][1]   17     73, Colsput/emiet.UT_Cnt_s16[3][1]   17     74, Colsput/emiet.UT_Cnt_s16[3][1]   17     75, Colsput/emiet.UT_Cnt_s16[3][1]   18     76, Colsput/emiet.UT_Cnt_s16[3][1]   19     76, Colsput/emiet.UT_Cnt_s16[3][1]   18     77, Colsput/emiet.UT_Cnt_s16[3][1]   18     78, Colsput/emiet.UT_Cnt_s16[3][1]   18     79, Colsput/emiet.UT_Cnt_s16[3][1]   18     70, Louslsput/emiet.UT_Cnt_s16[3][1]   18     70, Louslsput/emiet.UT_Cnt_s16[3][1]   18     70, Louslsput/emiet.UT_Cnt_s16[3][1]   18     71, Louslsput/emiet.UT_Cnt_s16[3][1]   18     72, Louslsput/emiet.UT_Cnt_s16[3][1]   18     73, Louslsput/emiet.UT_Cnt_s16[3][1]   18     74, Louslsput/emiet.UT_Cnt_s16[3][1]   18     75, Louslsput/emiet.UT_Cnt_s16[3][1]   18     76, Louslsput/emiet.UT_Cnt_s16[3][1]   18     77, Louslsput/emiet.UT_Cnt_s16[3][1]   18     78, Louslsput/emiet.UT_Cnt_s16[3][1]   18     79, Louslsput/emiet.UT_Cnt_s16[3][1]   18     70, Louslsput/emiet.UT_Cnt_s16[3][1]   18     71, Louslsput/emiet.UT_Cnt_s16[3][1]   18     72, Louslsput/emiet.UT_Cnt_s16[3][1]   18     73, Louslsput/emiet.UT_Cnt_s16[3][1]   18     74, Louslsput/emiet.UT_Cnt_s16[3][1]   18     75, Louslsput/emiet.UT_Cnt_s16[3][1]   18     75, Louslsput/emiet.UT_Cnt_s16[3][1]   18     75, Louslsput/emiet.UT_Cnt_s16[3	oispur/enet UT_Cnt_st03 3          81           oispur/enet UT_Cnt_st03 4          8           oispur/enet UT_Cnt_st03 9          2           oispur/enet UT_Cnt_st03 9          15           oispur/enet UT_Cnt_st03 9          15           oispur/enet UT_Cnt_st03 9          0           oispur/enet UT_Cnt_st03 9          0           oispur/enet UT_Cnt_st03 10          3           oispur/enet UT_Cnt_st03 11          16           oispur/enet UT_Cnt_st03 11          16           oispur/enet UT_Cnt_st03 11          18           oispur/enet UT_Cnt_st03 11          18           oispur/enet UT_Cnt_st03 11          7           oispur/enet UT_Cnt_st03 15          4           oispur/enet UT_Cnt_st03 16          17           oispur/enet UT_Cnt_st03 16          17           oispur/enet UT_Cnt_st03 16          17           oispur/enet UT_Cnt_st03 16          17           oispur/enet UT_Cnt_st03 16          20           oispur/enet UT_Cnt_st03 16          20           oispur/enet UT_Cnt_st03 16          22           oispur/enet UT_Cnt_st03 16          22           oispur/enet UT_Cnt_st03 16          22           oispur/enet UT_Cnt_st04 16 16          22           oispur/enet UT_Cnt		
T2_ColSpurVernierLUT_Cnt_s16[3][3]   8   8   8   8   8   8   8   8   8	AsspervementUT_Cnt_str8[3]    Assp		
12, ColSputVerniert.UT_Cnt_s163  4    5     12, ColSputVerniert.UT_Cnt_s163  5    15     12, ColSputVerniert.UT_Cnt_s163  5    15     12, ColSputVerniert.UT_Cnt_s163  7    12     12, ColSputVerniert.UT_Cnt_s163  7    12     12, ColSputVerniert.UT_Cnt_s163  7    12     12, ColSputVerniert.UT_Cnt_s163  7    16     12, ColSputVerniert.UT_Cnt_s163  7    16     12, ColSputVerniert.UT_Cnt_s163  7    16     12, ColSputVerniert.UT_Cnt_s163  7    16     12, ColSputVerniert.UT_Cnt_s163  7    18     12, ColSputVerniert.UT_Cnt_s163  7    19     12, ColSputVerniert.UT_Cnt_s163  7    19     12, ColSputVerniert.UT_Cnt_s163  7    17     12, DualSputVerniert.UT_Cnt_s163  7    17     12, DualSputVerniert.UT_Cnt_s163  7    18     13, DualSputVerniert.UT_Cnt_s163  7    18     14, DualSputVerniert.UT_Cnt_s163  7    18     15, DualSputVerniert.UT_Cnt_s163  7    18     16, DualSputVerniert.UT_Cnt_s163  7    18     17, DualSputVerniert.UT_Cnt_s163  7    18     18, DualSputVerniert.UT_Cnt_s163  7    18     19, DualSputVerniert.UT_Cnt_s163  7    18     10, DualSputVerniert.UT_Cnt_s163  7    18     11, DualSputVerniert.UT_Cnt_s163  7    18     12, DualSputVerniert.UT_Cnt_s163  7    18     13, DualSputVerniert.UT_Cnt_s163  7    18     14, DualSputVerniert.UT_Cnt_s163  7    18     15, DualSputVerniert.UT_Cnt_s163  7    18     18, DualSputVerniert.UT_Cnt_s163  7    18     19, DualSputVerniert.UT_Cnt_s163  7    18     19, DualSputVerniert.UT_Cnt_s163  7	Signut/went_UT_Cnt_st(S)  4    5		
12_ColSpurVemiet.UT_Cnt_st6[3][5]   2   12_ColSpurVemiet.UT_Cnt_st6[3][7]   12   12_ColSpurVemiet.UT_Cnt_st6[3][7]   12   12_ColSpurVemiet.UT_Cnt_st6[3][8]   9   12_ColSpurVemiet.UT_Cnt_st6[3][9]   6   6   12_ColSpurVemiet.UT_Cnt_st6[3][10]   3   12_ColSpurVemiet.UT_Cnt_st6[3][10]   3   12_ColSpurVemiet.UT_Cnt_st6[3][11]   16   12_ColSpurVemiet.UT_Cnt_st6[3][12]   13   12_ColSpurVemiet.UT_Cnt_st6[3][12]   13   12_ColSpurVemiet.UT_Cnt_st6[3][12]   13   12_ColSpurVemiet.UT_Cnt_st6[3][13]   10   12_ColSpurVemiet.UT_Cnt_st6[3][14]   7   12_ColSpurVemiet.UT_Cnt_st6[3][15]   4   12_ColSpurVemiet.UT_Cnt_st6[3][16]   17   12_ColSpurVemiet.UT_Cnt_st6[3][16]   17   12_ColSpurVemiet.UT_Cnt_st6[3][16]   17   12_ColSpurVemiet.UT_Cnt_st6[3][16]   366   12_DualSpurVemiet.UT_Cnt_st6[3][16]   366   12_DualSpurVemiet.UT_Cnt_st6[3][16]   366   12_DualSpurVemiet.UT_Cnt_st6[3][16]   368   12_DualSpurVemiet.UT_Cnt_st6[3][16]   369   369   369   360	Asspur/SementUT_Cnt_st(3) S S S S S S S S S S S S S S S S S S S		
12   ColSpurVemietUT_Cnt_st[6]    15   12   12   12   13   12   13   13   14   14   14   14   14   14	Designation   Col.		
12			
12 ColSpurVemietLUT_Cnt_stS[3][8]   9     12 ColSpurVemietLUT_Cnt_stS[3][9]   6     13 ColSpurVemietLUT_Cnt_stS[3][10]   3     14 ColSpurVemietLUT_Cnt_stS[3][11]   16     15 ColSpurVemietLUT_Cnt_stS[3][11]   16     16 ColSpurVemietLUT_Cnt_stS[3][12]   13     17 ColSpurVemietLUT_Cnt_stS[3][13]   10     18 ColSpurVemietLUT_Cnt_stS[3][14]   7     19 ColSpurVemietLUT_Cnt_stS[3][16]   4     19 ColSpurVemietLUT_Cnt_stS[3][16]   4     10 ColSpurVemietLUT_Cnt_stS[3][16]   17     10 ColSpurVemietLUT_Cnt_stS[3][16]   17     11 ColSpurVemietLUT_Cnt_stS[3][16]   17     12 DualSpurVemietLUT_Cnt_stS[3][16]   3-96     12 DualSpurVemietLUT_Cnt_stS[3][16]   3-96     12 DualSpurVemietLUT_Cnt_stS[3][16]   3-96     12 DualSpurVemietLUT_Cnt_stS[3][16]   3-98     13 DualSpurVemietLUT_Cnt_stS[3][16]   3-98     14 DualSpurVemietLUT_Cnt_stS[3][16]   3-98     15 DualSpurVemietLUT_Cnt_stS[3][16]   3-98     17 DualSpurVemietLUT_Cnt_stS[3][16]   3-98     18 DualSpurVemietLUT_Cn			
12. ColSpurVemierLUT_Cnt_s16[3][19]   6     12. ColSpurVemierLUT_Cnt_s16[3][10]   3     13. ColSpurVemierLUT_Cnt_s16[3][11]   16     14. ColSpurVemierLUT_Cnt_s16[3][12]   13     15. ColSpurVemierLUT_Cnt_s16[3][12]   17     17. ColSpurVemierLUT_Cnt_s16[3][13]   7     17. ColSpurVemierLUT_Cnt_s16[3][14]   7     17. ColSpurVemierLUT_Cnt_s16[3][15]   4     17. ColSpurVemierLUT_Cnt_s16[3][16]   17     17. ColSpurVemierLUT_Cnt_s16[3][16]   17     17. ColSpurVemierLUT_Cnt_s16[3][16]   17     17. ColSpurVemierLUT_Cnt_s16[3][16]   18     18. ColSpurVemierLUT_Cnt_s16[3][16]   19     19. DualSpurVemierLUT_Cnt_s16[3][16]   360     19. DualSpurVemierLUT_Cnt_s16[3][16]   360     19. DualSpurVemierLUT_Cnt_s16[3][16]   324     17. DualSpurVemierLUT_Cnt_s16[3][16]   328     18. DualSpurVemierLUT_Cnt_s16[3][16]   328     19. DualSpurVemierLUT_Cnt_s16[3][16]   360     19. DualSpurVemierLUT_Cnt_s16[3][16]   360     19. DualSpurVemierLUT_Cnt_s16[3][17]   344     19. DualSpurVemierLUT_Cnt_s16[3][17]   344     19. DualSpurVemierLUT_Cnt_s16[3][17]   360     19. Dua	uiSpuVreinet LT, Cnt, s16(3)(9)     6       uiSpuVreinet LT, Cnt, s16(3)(11)     16       uiSpuVreinet LT, Cnt, s16(3)(11)     16       uiSpuVreinet LT, Cnt, s16(3)(12)     13       uiSpuVreinet LT, Cnt, s16(3)(13)     10       uiSpuVreinet LT, Cnt, s16(3)(14)     7       uiSpuVreinet LT, Cnt, s16(3)(14)     4       uiSpuVreinet LT, Cnt, s16(3)(16)     17       uispuVreinet LT, Cnt, s16(3)(16)     17       uispuVreinet LT, Cnt, s16(0)(1)     306       uispuVreinet LT, Cnt, s16(0)(1)     306       uispuVreinet LT, Cnt, s16(0)(1)     300       uispuVreinet LT, Cnt, s16(0)(1)     224       uispuVreinet LT, Cnt, s16(0)(1)     228       uispuVreinet LT, Cnt, s16(0)(1)     222       uispuVreinet LT, Cnt, s16(0)(1)     180       uispuVreinet LT, Cnt, s16(0)(17)     144       uispuVreinet LT, Cnt, s16(0)(17)     144       uispuVreinet LT, Cnt, s16(0)(11)     36       uispuVreinet LT, Cnt, s16(1)(		
T2_ColSpurVemierLUT_Cnt_s16[3][11]		T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVemierLUT_Cnt_st6[3][12]   13   10   17   17   17   17   17   17   17	13	T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_st6[3][13]	OSput/Permiert_UT_Cnt_st6[3][13]   10   10   10   10   10   10   10   1	T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVemierLUT_Cnt_s16[3][14]   7     T2_ColSpurVemierLUT_Cnt_s16[3][15]   4     T2_ColSpurVemierLUT_Cnt_s16[3][16]   17     T2_DualSpurVemierLUT_Cnt_s16[0][0]   -396     T2_DualSpurVemierLUT_Cnt_s16[0][1]   -366     T2_DualSpurVemierLUT_Cnt_s16[0][2]   -324     T2_DualSpurVemierLUT_Cnt_s16[0][3]   -288     T2_DualSpurVemierLUT_Cnt_s16[0][4]   -252     T2_DualSpurVemierLUT_Cnt_s16[0][6]   -180     T2_DualSpurVemierLUT_Cnt_s16[0][6]   -180     T2_DualSpurVemierLUT_Cnt_s16[0][7]   -144     T2_DualSpurVemierLUT_Cnt_s16[0][8]   -72     T2_DualSpurVemierLUT_Cnt_s16[0][9]   -72     T2_DualSpurVemierLUT_Cnt_s16[0][1]   -72     T2_DualSpurVemie		T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][15]	Accession	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16(3)[16]	17	T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_DualSpurVernierLUT_Cnt_s16[0][0]   396     T2_DualSpurVernierLUT_Cnt_s16[0][1]   360     T2_DualSpurVernierLUT_Cnt_s16[0][1]   324     T2_DualSpurVernierLUT_Cnt_s16[0][3]   -288     T2_DualSpurVernierLUT_Cnt_s16[0][4]   -252     T2_DualSpurVernierLUT_Cnt_s16[0][5]   -216     T2_DualSpurVernierLUT_Cnt_s16[0][7]   -144     T2_DualSpurVernierLUT_Cnt_s16[0][8]   -108     T2_DualSpurVernierLUT_Cnt_s16[0][9]   -72     T2_DualSpurVernierLUT_Cnt_s16[0][9]   -72     DualSpurVernierLUT_Cnt_s16[0][1]   -72     DualSpurVernierLUT_Cnt_s16[1][1]   -72     DualSpurVernierLUT_Cnt_s16[1][1	ualSpurVernierLUT_Cnt_s16[0][0]       -396         ualSpurVernierLUT_Cnt_s16[0][1]       -360         ualSpurVernierLUT_Cnt_s16[0][2]       -324         ualSpurVernierLUT_Cnt_s16[0][3]       -288         ualSpurVernierLUT_Cnt_s16[0][5]       -216         ualSpurVernierLUT_Cnt_s16[0][5]       -216         ualSpurVernierLUT_Cnt_s16[0][7]       -144         ualSpurVernierLUT_Cnt_s16[0][8]       -108         ualSpurVernierLUT_Cnt_s16[0][9]       -72         ualSpurVernierLUT_Cnt_s16[0][1]       0         ualSpurVernierLUT_Cnt_s16[0][1]       0         ualSpurVernierLUT_Cnt_s16[0][1]       0         ualSpurVernierLUT_Cnt_s16[0][1]       72         ualSpurVernierLUT_Cnt_s16[0][1]       108         ualSpurVernierLUT_Cnt_s16[0][1]       108         ualSpurVernierLUT_Cnt_s16[0][1]       108         ualSpurVernierLUT_Cnt_s16[0][1]       180         ualSpurVernierLUT_Cnt_s16[0][1]       216         ualSpurVernierLUT_Cnt_s16[0][1]       226         ualSpurVernierLUT_Cnt_s16[0][1]       324         ualSpurVernierLUT_Cnt_s16[0][1]       360         ualSpurVernierLUT_Cnt_s16[0][1]       0         ualSpurVernierLUT_Cnt_s16[1][1]       0         ualSpurVernierLUT_Cnt_s16[1][1]       0	T2_ColSpurVernierLUT_Cnt_s16[3][15]	
T2_DualSpurVernierLUT_Cnt_s16[0][1]   -360	UasiSpurVermierLUT_Cnt_s16(0)[1]   -360    -324   -324    -325   -328    -326   -328    -326   -328    -326   -328    -326   -326   -326    -327   -326   -326    -328   -326		
T2_DualSpurVernierLUT_Cnt_s16[0] 2  -324     T2_DualSpurVernierLUT_Cnt_s16[0] 3  -288     T2_DualSpurVernierLUT_Cnt_s16[0] 4  -252     T2_DualSpurVernierLUT_Cnt_s16[0] 6  -180     T2_DualSpurVernierLUT_Cnt_s16[0] 7  -144     T2_DualSpurVernierLUT_Cnt_s16[0] 7  -144     T2_DualSpurVernierLUT_Cnt_s16[0] 8  -108     T2_DualSpurVernierLUT_Cnt_s16[0] 9  -72     T2_DualSpurVernierLUT_Cnt_s16[0] 10  -36     T2_DualSpurVernierLUT_Cnt_s16[0] 11  0     T2_DualSpurVernierLUT_Cnt_s16[0] 12  36     T2_DualSpurVernierLUT_Cnt_s16[0] 13  72     T2_DualSpurVernierLUT_Cnt_s16[0] 14  108     T2_DualSpurVernierLUT_Cnt_s16[0] 15  144     T2_DualSpurVernierLUT_Cnt_s16[0] 16  180     T2_DualSpurVernierLUT_Cnt_s16[0] 16  180     T2_DualSpurVernierLUT_Cnt_s16[0] 19  288     T2_DualSpurVernierLUT_Cnt_s16[0] 19  288     T2_DualSpurVernierLUT_Cnt_s16[0] 21  360     T2_DualSpurVernierLUT_Cnt_s16[1] 3  9     T2_DualSpurVernierLUT_Cnt_s16[1] 3  2     T2_DualSpurVernierLUT_Cnt_s16[1] 3  2     T2_DualSpurVernierLUT_Cnt_s16[1] 3  2     T2_DualSpurVernierLUT_Cnt_s16[1] 4  3	UasSpurVermierLUT_Cnt_s16[0][2]   -324   UasSpurVermierLUT_Cnt_s16[0][3]   -288   UasSpurVermierLUT_Cnt_s16[0][4]   -252   UasSpurVermierLUT_Cnt_s16[0][5]   -216   UasSpurVermierLUT_Cnt_s16[0][7]   -114   UasSpurVermierLUT_Cnt_s16[0][8]   -108   UasSpurVermierLUT_Cnt_s16[0][9]   -72   UasSpurVermierLUT_Cnt_s16[0][10]   -36   UasSpurVermierLUT_Cnt_s16[0][11]   0   UasSpurVermierLUT_Cnt_s16[0][12]   36   UasSpurVermierLUT_Cnt_s16[0][13]   72   UasSpurVermierLUT_Cnt_s16[0][14]   108   UasSpurVermierLUT_Cnt_s16[0][15]   144   UasSpurVermierLUT_Cnt_s16[0][16]   180   UasSpurVermierLUT_Cnt_s16[0][17]   216   UasSpurVermierLUT_Cnt_s16[0][16]   180   UasSpurVermierLUT_Cnt_s16[0][16]   180   UasSpurVermierLUT_Cnt_s16[0][17]   216   UasSpurVermierLUT_Cnt_s16[0][18]   252   UasSpurVermierLUT_Cnt_s16[0][19]   288   UasSpurVermierLUT_Cnt_s16[0][19]   324   UasSpurVermierLUT_Cnt_s16[0][19]   360   UasSpurVermierLUT_Cnt_s16[0][19]   370		
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][8] -72 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][19] 324 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3			
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][6] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 1 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 1 T2_DualSpurVernierLUT_Cnt_s16[1][0] 1 T2_DualSpurVernierLUT_Cnt_s16[1][0] 1 T2_DualSpurVernierLUT_Cnt_s16[1][0] 2 T2_DualSpurVernierLUT_Cnt_s16[1][0] 3 T2_DualSpurVernierLUT_Cnt_s16[1][0] 3 T2_DualSpurVernierLUT_Cnt_s16[1][0] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0	LualSpurVerniert.UT_Cnt_s16[0][4]   -252     LualSpurVerniert.UT_Cnt_s16[0][5]   -216     LualSpurVerniert.UT_Cnt_s16[0][6]   -180     LualSpurVerniert.UT_Cnt_s16[0][7]   -144     LualSpurVerniert.UT_Cnt_s16[0][8]   -108     LualSpurVerniert.UT_Cnt_s16[0][9]   -72     LualSpurVerniert.UT_Cnt_s16[0][10]   -36     LualSpurVerniert.UT_Cnt_s16[0][11]   0     LualSpurVerniert.UT_Cnt_s16[0][12]   -36     LualSpurVerniert.UT_Cnt_s16[0][13]   72     LualSpurVerniert.UT_Cnt_s16[0][14]   -108     LualSpurVerniert.UT_Cnt_s16[0][14]   -108     LualSpurVerniert.UT_Cnt_s16[0][15]   -144     LualSpurVerniert.UT_Cnt_s16[0][16]   -180     LualSpurVerniert.UT_Cnt_s16[0][17]   -216     LualSpurVerniert.UT_Cnt_s16[0][17]   -216     LualSpurVerniert.UT_Cnt_s16[0][18]   -252     LualSpurVerniert.UT_Cnt_s16[0][19]   -288     LualSpurVerniert.UT_Cnt_s16[0][19]   -288     LualSpurVerniert.UT_Cnt_s16[0][19]   -300     LualSpurVerniert.UT_Cnt_s16[0][1]   -9     LualSpurVerniert.UT_Cnt_s16[0][1]   -9     LualSpurVerniert.UT_Cnt_s16[0][1]   -9     LualSpurVerniert.UT_Cnt_s16[1][1]   -10		
T2_DualSpurVerniert.UT_Cnt_s16[0][6] -216 T2_DualSpurVerniert.UT_Cnt_s16[0][6] -180 T2_DualSpurVerniert.UT_Cnt_s16[0][7] -144 T2_DualSpurVerniert.UT_Cnt_s16[0][8] -108 T2_DualSpurVerniert.UT_Cnt_s16[0][9] -72 T2_DualSpurVerniert.UT_Cnt_s16[0][10] -36 T2_DualSpurVerniert.UT_Cnt_s16[0][11] 0 T2_DualSpurVerniert.UT_Cnt_s16[0][11] 0 T2_DualSpurVerniert.UT_Cnt_s16[0][12] 36 T2_DualSpurVerniert.UT_Cnt_s16[0][13] 72 T2_DualSpurVerniert.UT_Cnt_s16[0][14] 108 T2_DualSpurVerniert.UT_Cnt_s16[0][15] 144 T2_DualSpurVerniert.UT_Cnt_s16[0][16] 180 T2_DualSpurVerniert.UT_Cnt_s16[0][16] 180 T2_DualSpurVerniert.UT_Cnt_s16[0][18] 252 T2_DualSpurVerniert.UT_Cnt_s16[0][19] 288 T2_DualSpurVerniert.UT_Cnt_s16[0][19] 324 T2_DualSpurVerniert.UT_Cnt_s16[0][20] 324 T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360 T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360 T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360 T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360 T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360 T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360 T2_DualSpurVerniert.UT_Cnt_s16[1][1] 0 T2_DualSpurVerniert.UT_Cnt_s16[1][1] 0 T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1 T2_DualSpurVerniert.UT_Cnt_s16[1][3] 2 T2_DualSpurVerniert.UT_Cnt_s16[1][3] 2 T2_DualSpurVerniert.UT_Cnt_s16[1][4] 3	tualSpurVernierLUT_Cnt_s16[0][6]         -216           ualSpurVernierLUT_Cnt_s16[0][6]         -180           ualSpurVernierLUT_Cnt_s16[0][7]         -144           ualSpurVernierLUT_Cnt_s16[0][8]         -108           ualSpurVernierLUT_Cnt_s16[0][9]         -72           ualSpurVernierLUT_Cnt_s16[0][11]         0           ualSpurVernierLUT_Cnt_s16[0][12]         36           ualSpurVernierLUT_Cnt_s16[0][13]         72           ualSpurVernierLUT_Cnt_s16[0][14]         108           ualSpurVernierLUT_Cnt_s16[0][15]         144           ualSpurVernierLUT_Cnt_s16[0][16]         180           ualSpurVernierLUT_Cnt_s16[0][17]         216           ualSpurVernierLUT_Cnt_s16[0][19]         252           ualSpurVernierLUT_Cnt_s16[0][19]         324           ualSpurVernierLUT_Cnt_s16[0][2]         324           ualSpurVernierLUT_Cnt_s16[0][2]         360           ualSpurVernierLUT_Cnt_s16[1][1]         9           ualSpurVernierLUT_Cnt_s16[1][1]         9           ualSpurVernierLUT_Cnt_s16[1][1]         1           ualSpurVernierLUT_Cnt_s16[1][1]         1           ualSpurVernierLUT_Cnt_s16[1][1]         5           ualSpurVernierLUT_Cnt_s16[1][1]         6           ualSpurVernierLUT_Cnt_s16[1][1]         6		
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T2_DualSpurVerniert.UT_Cnt_s16[0][7] -144  T2_DualSpurVerniert.UT_Cnt_s16[0][8] -108  T2_DualSpurVerniert.UT_Cnt_s16[0][9] -72  T2_DualSpurVerniert.UT_Cnt_s16[0][10] -36  T2_DualSpurVerniert.UT_Cnt_s16[0][11] 0  T2_DualSpurVerniert.UT_Cnt_s16[0][12] 36  T2_DualSpurVerniert.UT_Cnt_s16[0][13] 72  T2_DualSpurVerniert.UT_Cnt_s16[0][14] 108  T2_DualSpurVerniert.UT_Cnt_s16[0][15] 144  T2_DualSpurVerniert.UT_Cnt_s16[0][16] 180  T2_DualSpurVerniert.UT_Cnt_s16[0][17] 216  T2_DualSpurVerniert.UT_Cnt_s16[0][17] 216  T2_DualSpurVerniert.UT_Cnt_s16[0][18] 252  T2_DualSpurVerniert.UT_Cnt_s16[0][19] 288  T2_DualSpurVerniert.UT_Cnt_s16[0][20] 324  T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360  T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360  T2_DualSpurVerniert.UT_Cnt_s16[1][0] 9  T2_DualSpurVerniert.UT_Cnt_s16[1][1] 0  T2_DualSpurVerniert.UT_Cnt_s16[1][1] 0  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][3] 2  T2_DualSpurVerniert.UT_Cnt_s16[1][3] 2  T2_DualSpurVerniert.UT_Cnt_s16[1][4] 3	1444		
T2_DualSpurVerniert.UT_Cnt_s16[0][8] -108  T2_DualSpurVerniert.UT_Cnt_s16[0][9] -72  T2_DualSpurVerniert.UT_Cnt_s16[0][10] -36  T2_DualSpurVerniert.UT_Cnt_s16[0][11] 0  T2_DualSpurVerniert.UT_Cnt_s16[0][12] 36  T2_DualSpurVerniert.UT_Cnt_s16[0][13] 72  T2_DualSpurVerniert.UT_Cnt_s16[0][14] 108  T2_DualSpurVerniert.UT_Cnt_s16[0][15] 144  T2_DualSpurVerniert.UT_Cnt_s16[0][16] 180  T2_DualSpurVerniert.UT_Cnt_s16[0][17] 216  T2_DualSpurVerniert.UT_Cnt_s16[0][18] 252  T2_DualSpurVerniert.UT_Cnt_s16[0][19] 288  T2_DualSpurVerniert.UT_Cnt_s16[0][20] 324  T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360  T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360  T2_DualSpurVerniert.UT_Cnt_s16[0][21] 360  T2_DualSpurVerniert.UT_Cnt_s16[1][0] 9  T2_DualSpurVerniert.UT_Cnt_s16[1][1] 0  T2_DualSpurVerniert.UT_Cnt_s16[1][1] 0  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 1  T2_DualSpurVerniert.UT_Cnt_s16[1][2] 3  T2_DualSpurVerniert.UT_Cnt_s16[1][3] 2  T2_DualSpurVerniert.UT_Cnt_s16[1][4] 3	AualSpurVernierLUT_Cnt_s16[0][8]   -108		
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 0  T2_DualSpurVernierLUT_Cnt_s16[0][21] 10	Aua SpurVernierLUT_Cnt_s16[0][19]   -72		
T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216         T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	- 36  vualSpurVernierLUT_Cnt_s16[0][10]  vualSpurVernierLUT_Cnt_s16[0][11]  vualSpurVernierLUT_Cnt_s16[0][12]  vualSpurVernierLUT_Cnt_s16[0][13]  72  vualSpurVernierLUT_Cnt_s16[0][14]  vualSpurVernierLUT_Cnt_s16[0][15]  vualSpurVernierLUT_Cnt_s16[0][16]  vualSpurVernierLUT_Cnt_s16[0][17]  vualSpurVernierLUT_Cnt_s16[0][17]  vualSpurVernierLUT_Cnt_s16[0][17]  vualSpurVernierLUT_Cnt_s16[0][18]  252  vualSpurVernierLUT_Cnt_s16[0][19]  vualSpurVernierLUT_Cnt_s16[0][20]  vualSpurVernierLUT_Cnt_s16[0][21]  vualSpurVernierLUT_Cnt_s16[0][21]  vualSpurVernierLUT_Cnt_s16[1][0]  vualSpurVernierLUT_Cnt_s16[1][1]  vualSpurVernierLUT_Cnt_s16[1][1]  vualSpurVernierLUT_Cnt_s16[1][2]  vualSpurVernierLUT_Cnt_s16[1][3]  vualSpurVernierLUT_Cnt_s16[1][4]  vualSpurVernierLUT_Cnt_s16[1][6]  vualSpurVe		
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T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3	144   144		72
T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3	144   144		
T2_DualSpurVernierLUT_Cnt_s16[0][17]       216         T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	qualSpurVernierLUT_Cnt_s16[0][17]       216         qualSpurVernierLUT_Cnt_s16[0][18]       252         qualSpurVernierLUT_Cnt_s16[0][19]       288         qualSpurVernierLUT_Cnt_s16[0][20]       324         qualSpurVernierLUT_Cnt_s16[0][21]       360         qualSpurVernierLUT_Cnt_s16[1][0]       9         qualSpurVernierLUT_Cnt_s16[1][1]       0         qualSpurVernierLUT_Cnt_s16[1][2]       1         qualSpurVernierLUT_Cnt_s16[1][3]       2         qualSpurVernierLUT_Cnt_s16[1][4]       3         qualSpurVernierLUT_Cnt_s16[1][6]       4         qualSpurVernierLUT_Cnt_s16[1][6]       5         qualSpurVernierLUT_Cnt_s16[1][7]       6         qualSpurVernierLUT_Cnt_s16[1][8]       7         qualSpurVernierLUT_Cnt_s16[1][9]       8         qualSpurVernierLUT_Cnt_s16[1][10]       9         qualSpurVernierLUT_Cnt_s16[1][10]       9         qualSpurVernierLUT_Cnt_s16[1][10]       9         qualSpurVernierLUT_Cnt_s16[1][10]       9         qualSpurVernierLUT_Cnt_s16[1][10]       9	T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	252   252   253   253   254   255	T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	288     288	T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	SualSpurVernierLUT_Cnt_s16[0][20]   324   360		
T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	Section   Sect		
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	SualSpurVernierLUT_Cnt_s16[1][0]   9     SualSpurVernierLUT_Cnt_s16[1][1]   0     SualSpurVernierLUT_Cnt_s16[1][2]   1     SualSpurVernierLUT_Cnt_s16[1][3]   2     SualSpurVernierLUT_Cnt_s16[1][4]   3     SualSpurVernierLUT_Cnt_s16[1][5]   4     SualSpurVernierLUT_Cnt_s16[1][6]   5     SualSpurVernierLUT_Cnt_s16[1][7]   6     SualSpurVernierLUT_Cnt_s16[1][8]   7     SualSpurVernierLUT_Cnt_s16[1][9]   8     SualSpurVernierLUT_Cnt_s16[1][1]   9     SualSpurVernierLUT_Cnt_s16[1][1]   9     SualSpurVernierLUT_Cnt_s16[1][1]   9     SualSpurVernierLUT_Cnt_s16[1][1]   0     SualSpurVernierLUT_Cnt_s16[1][1]		
T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	Description		
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3			
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3	2   2   2   2   2   2   2   2   2   2		
T2_DualSpurVernierLUT_Cnt_s16[1][4] 3	SualSpurVernierLUT_Cnt_s16[1][4]   3   3   3   3   3   3   3   3   3		
	AualSpurVernierLUT_Cnt_s16[1][5]		
12 Duaiopui veniiei EUT OII STO[1][0] [4	bualSpurVernierLUT_Cnt_s16[1][6]       5         bualSpurVernierLUT_Cnt_s16[1][7]       6         bualSpurVernierLUT_Cnt_s16[1][8]       7         bualSpurVernierLUT_Cnt_s16[1][9]       8         bualSpurVernierLUT_Cnt_s16[1][10]       9         bualSpurVernierLUT_Cnt_s16[1][11]       0		
	SualSpurVernierLUT_Cnt_s16[1][7]   6     SualSpurVernierLUT_Cnt_s16[1][8]   7     SualSpurVernierLUT_Cnt_s16[1][9]   8     SualSpurVernierLUT_Cnt_s16[1][10]   9     SualSpurVernierLUT_Cnt_s16[1][11]   0		
	Vala SpurVernierLUT_Cnt_s16[1][8] 7   Vala SpurVernierLUT_Cnt_s16[1][9] 8   Vala SpurVernierLUT_Cnt_s16[1][10] 9   Vala SpurVernierLUT_Cnt_s16[1][11] 0   Vala SpurVernierLUT_Cnt_s16[1][11]   Val		
	aualSpurVernierLUT_Cnt_s16[1][9]       8         aualSpurVernierLUT_Cnt_s16[1][10]       9         aualSpurVernierLUT_Cnt_s16[1][11]       0		
	dualSpurVernierLUT_Cnt_s16[1][10]         9           dualSpurVernierLUT_Cnt_s16[1][11]         0		
	ualSpurVernierLUT_Cnt_s16[1][11] 0		
		T2_DualSpurVernierLUT_Cnt_s16[1][12]	2
	uaispurvernierLUT Cnt s16[1][14]	T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2 3
		T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13] T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
	nualSpurvernierLUT Cnt s16[1][14] 3	T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	
		T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13] T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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DigColPs\_Per2

Name -	In and Maline		
Name T2_DualSpurVernierLUT_Cnt_s16[1][17]	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2 DualSpurVernierLUT Cnt s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	0		
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	V. T. O. I. I.	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cn tgt_Pim_DigColPsEOL	ı_igc	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	Result
DigColPs_HwAverncorr-auit_Cnt_m_igc  DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	
DigColPs I2CHwTrimTransCnts Uls M u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	_
DigColPs PrevColPos Deg M f32	0	0 ± 0.0001220703125	

DigColPs\_PrevColPos\_Deg\_M\_f32

0 ± 0.0001220703125

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DigCoIPs\_Per2

Name	Actual Value	Expected Value	Result
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	✓
Param	0x00	0x00	<b>✓</b>
Status	0x00	0x00	✓
NTC	0x6E	0x6E	✓
Param	0x00	0x00	<b>✓</b>
Status	0x00	0x00	~
NTC	0x6F	0x6F	<b>✓</b>
Param	0x00	0x00	~
Status	0x00	0x00	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt GetCustData()	511
DigColPs ColParityError Cnt M Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs ColTrimStatic Deg M f32	360
DigColPs HwAVernCorrFault Cnt M Igc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs I2CHwColAngle Deg M f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T0_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2 ColSpurVernierLUT Cnt s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T3_DualSpurVernierLUT_Cnt_s16[0][2]	-360 334
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][4]	-258 -252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-252 -216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180

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DigColPs\_Per2

Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
Γ2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
Γ2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
Γ2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
Γ2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
Γ2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
Γ2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
Γ2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
Γ2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
Γ2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
Γ2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
Γ2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
Γ2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
Γ2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
Γ2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
Γ2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
Γ2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
Γ2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
Γ2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
Γ2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
T2 DualSpurVernierLUT Cnt s16[2][20]	9	
Γ2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
Γ2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
Γ2_DualSpurVernierLUT_Cnt_s16[3][2]	4	
[2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
Γ2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
Γ2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
T2 DualSpurVernierLUT Cnt s16[3][7]	14	
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16	
Γ2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20	
Γ2_DualSpurVernierLUT_Cnt_s16[3][11]	1	
T2_DualSpurVernierLUT_Cnt_s16[3][11]	3	
Γ2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
[2_DualSpurVernierLUT_Cnt_s16[3][14]	7	
[2_DualSpurVernierLUT_Cnt_s16[3][14]	9	
2_DualSpurVernierLUT_Cnt_s16[3][15]  72_DualSpurVernierLUT_Cnt_s16[3][16]	11	
	13	
[2_DualSpurVernierLUT_Cnt_s16[3][17]		
[2_DualSpurVernierLUT_Cnt_s16[3][18]	15	
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17	
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19	
	21	
T2_DualSpurVernierLUT_Cnt_s16[3][21]	1	
2_DualSpurvernierLU1_Cnt_s16[3][21]  <_SelectFromColumn_Cnt_lgc  <_SkipStepErrDiag_Cnt_str.Threshold	1 255	



DigColPs_Per2	, , , , , , , , , , , , , , , , , , , ,	Razorcat
Name	Input Value	
k_SkipStepErrDiag_Cnt_str.NStep	50	
k_VernCorrErrorDiag_Cnt_str.Threshold	100	
k_VernCorrErrorDiag_Cnt_str.PStep	50	
k_VernCorrErrorDiag_Cnt_str.NStep	50	
k_VernCorrErrorThresh_Deg_f32	100	
k_VernOORangeThresh_Deg_f32	1800	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	

tgt_Rte_Inst_3a_bigColes.Filit_bigColesEOL	[tgt_Fiii]_bigCoiFsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Test Step Call Trace	Test Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 2.3 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_ColTrimStatic_Deg_M_f32	4.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	58760	
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972	
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	421.9525396	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142	
DigColPs_SpurTrimStatic_Deg_M_f32	5.8	
DigColPs_TrimCompStatic_Cnt_M_u16	124	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	

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DigColPs\_Per2



DigColPs_VernierAngleOORange_Cnt_M_lgc 1	31 9 6 3 2 5 8 8 80 63 96 629 61 94
Rte_Inst_Sa_DigColPs         tgt_T2_ColSpurVernierLUT_Cnt_s16[0][0]         -16           T2_ColSpurVernierLUT_Cnt_s16[0][1]         -13           T2_ColSpurVernierLUT_Cnt_s16[0][2]         -99           T2_ColSpurVernierLUT_Cnt_s16[0][3]         -66           T2_ColSpurVernierLUT_Cnt_s16[0][4]         -33           T2_ColSpurVernierLUT_Cnt_s16[0][5]         0           T2_ColSpurVernierLUT_Cnt_s16[0][6]         32           T2_ColSpurVernierLUT_Cnt_s16[0][7]         65           T2_ColSpurVernierLUT_Cnt_s16[0][8]         98           T2_ColSpurVernierLUT_Cnt_s16[0][9]         130           T2_ColSpurVernierLUT_Cnt_s16[0][10]         163           T2_ColSpurVernierLUT_Cnt_s16[0][11]         196           T2_ColSpurVernierLUT_Cnt_s16[0][12]         225           T2_ColSpurVernierLUT_Cnt_s16[0][13]         261           T2_ColSpurVernierLUT_Cnt_s16[0][14]         294           T2_ColSpurVernierLUT_Cnt_s16[0][16]         355           T2_ColSpurVernierLUT_Cnt_s16[1][0]         0           T2_ColSpurVernierLUT_Cnt_s16[1][0]         0           T2_ColSpurVernierLUT_Cnt_s16[1][1]         4           T2_ColSpurVernierLUT_Cnt_s16[1][6]         4           T2_ColSpurVernierLUT_Cnt_s16[1][6]         4           T2_ColSpurVernierLUT_Cnt_s16[1][1]         1	63 31 9 6 6 3 2 5 8 8 8 9 6 9 6 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
T2_ColSpurVernierLUT_Cnt_s16[0][0] -16 T2_ColSpurVernierLUT_Cnt_s16[0][1] -13 T2_ColSpurVernierLUT_Cnt_s16[0][2] -99 T2_ColSpurVernierLUT_Cnt_s16[0][3] -66 T2_ColSpurVernierLUT_Cnt_s16[0][4] -33 T2_ColSpurVernierLUT_Cnt_s16[0][6] -65 T2_ColSpurVernierLUT_Cnt_s16[0][6] -65 T2_ColSpurVernierLUT_Cnt_s16[0][7] -65 T2_ColSpurVernierLUT_Cnt_s16[0][8] -98 T2_ColSpurVernierLUT_Cnt_s16[0][9] -130 T2_ColSpurVernierLUT_Cnt_s16[0][9] -130 T2_ColSpurVernierLUT_Cnt_s16[0][10] -165 T2_ColSpurVernierLUT_Cnt_s16[0][11] -196 T2_ColSpurVernierLUT_Cnt_s16[0][12] -225 T2_ColSpurVernierLUT_Cnt_s16[0][13] -261 T2_ColSpurVernierLUT_Cnt_s16[0][14] -294 T2_ColSpurVernierLUT_Cnt_s16[0][16] -355 T2_ColSpurVernierLUT_Cnt_s16[0][16] -355 T2_ColSpurVernierLUT_Cnt_s16[1][0] -72_ColSpurVernierLUT_Cnt_s16[1][0] -72_ColSpurVernierLUT_Cnt_s16[1][1] -72_ColSpurVernierLUT_Cnt_s16[1][	63 31 9 6 6 3 2 5 8 8 8 9 6 9 6 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
T2_ColSpurVernierLUT_Cnt_s16[0][1] -13 T2_ColSpurVernierLUT_Cnt_s16[0][2] -99 T2_ColSpurVernierLUT_Cnt_s16[0][3] -66 T2_ColSpurVernierLUT_Cnt_s16[0][4] -33 T2_ColSpurVernierLUT_Cnt_s16[0][5] 0 T2_ColSpurVernierLUT_Cnt_s16[0][6] 32 T2_ColSpurVernierLUT_Cnt_s16[0][7] 65 T2_ColSpurVernierLUT_Cnt_s16[0][8] 98 T2_ColSpurVernierLUT_Cnt_s16[0][9] 133 T2_ColSpurVernierLUT_Cnt_s16[0][10] 165 T2_ColSpurVernierLUT_Cnt_s16[0][11] 196 T2_ColSpurVernierLUT_Cnt_s16[0][12] 225 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 358 T2_ColSpurVernierLUT_Cnt_s16[0][16] 358 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 12 T2_ColSpurVernierLUT_Cnt_s16[1][10] 12	31 9 6 3 2 5 8 8 80 63 9 61 94 27
T2_ColSpurVernierLUT_Cnt_s16[0][2] T2_ColSpurVernierLUT_Cnt_s16[0][3] T2_ColSpurVernierLUT_Cnt_s16[0][4] T2_ColSpurVernierLUT_Cnt_s16[0][5] T2_ColSpurVernierLUT_Cnt_s16[0][6] T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8] T2_ColSpurVernierLUT_Cnt_s16[0][9] T2_ColSpurVernierLUT_Cnt_s16[0][9] T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14] T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][10]	9 6 3 2 5 8 8 8 8 8 8 9 6 6 29 6 1 1
T2_ColSpurVernierLUT_Cnt_s16[0][3] -66 T2_ColSpurVernierLUT_Cnt_s16[0][4] -33 T2_ColSpurVernierLUT_Cnt_s16[0][5] 0 T2_ColSpurVernierLUT_Cnt_s16[0][6] 32 T2_ColSpurVernierLUT_Cnt_s16[0][7] 65 T2_ColSpurVernierLUT_Cnt_s16[0][8] 98 T2_ColSpurVernierLUT_Cnt_s16[0][9] 136 T2_ColSpurVernierLUT_Cnt_s16[0][10] 163 T2_ColSpurVernierLUT_Cnt_s16[0][11] 199 T2_ColSpurVernierLUT_Cnt_s16[0][12] 229 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][10] 1 T2_ColSpurVernierLUT_Cnt_s16[1][11] 1	6 3 2 2 5 3 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8
T2_ColSpurVernierLUT_Cnt_s16[0][4]  T2_ColSpurVernierLUT_Cnt_s16[0][5]  T2_ColSpurVernierLUT_Cnt_s16[0][6]  T2_ColSpurVernierLUT_Cnt_s16[0][7]  T2_ColSpurVernierLUT_Cnt_s16[0][8]  T2_ColSpurVernierLUT_Cnt_s16[0][9]  T2_ColSpurVernierLUT_Cnt_s16[0][10]  T2_ColSpurVernierLUT_Cnt_s16[0][11]  T2_ColSpurVernierLUT_Cnt_s16[0][12]  T2_ColSpurVernierLUT_Cnt_s16[0][13]  T2_ColSpurVernierLUT_Cnt_s16[0][14]  T2_ColSpurVernierLUT_Cnt_s16[0][15]  T2_ColSpurVernierLUT_Cnt_s16[0][16]  T2_ColSpurVernierLUT_Cnt_s16[0][16]  T2_ColSpurVernierLUT_Cnt_s16[0][16]  T2_ColSpurVernierLUT_Cnt_s16[1][0]  T2_ColSpurVernierLUT_Cnt_s16[1][1]  T2_ColSpurVernierLUT_Cnt_s16[1][1]  T2_ColSpurVernierLUT_Cnt_s16[1][2]  T2_ColSpurVernierLUT_Cnt_s16[1][3]  T2_ColSpurVernierLUT_Cnt_s16[1][6]	3 2 2 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3
T2_ColSpurVernierLUT_Cnt_s16[0][5]	2 5 3 3 30 53 30 66 29 61 94
T2_ColSpurVernierLUT_Cnt_s16[0][6]       32         T2_ColSpurVernierLUT_Cnt_s16[0][7]       65         T2_ColSpurVernierLUT_Cnt_s16[0][8]       98         T2_ColSpurVernierLUT_Cnt_s16[0][9]       130         T2_ColSpurVernierLUT_Cnt_s16[0][10]       163         T2_ColSpurVernierLUT_Cnt_s16[0][11]       196         T2_ColSpurVernierLUT_Cnt_s16[0][12]       225         T2_ColSpurVernierLUT_Cnt_s16[0][13]       261         T2_ColSpurVernierLUT_Cnt_s16[0][14]       294         T2_ColSpurVernierLUT_Cnt_s16[0][16]       355         T2_ColSpurVernierLUT_Cnt_s16[0][16]       355         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       2         T2_ColSpurVernierLUT_Cnt_s16[1][6]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       1         T2_ColSpurVernierLUT_Cnt_s16[1][6]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpur	5 3 30 53 96 629 61 94
T2_ColSpurVernierLUT_Cnt_s16[0][7] 65 T2_ColSpurVernierLUT_Cnt_s16[0][8] 98 T2_ColSpurVernierLUT_Cnt_s16[0][9] 130 T2_ColSpurVernierLUT_Cnt_s16[0][10] 163 T2_ColSpurVernierLUT_Cnt_s16[0][11] 196 T2_ColSpurVernierLUT_Cnt_s16[0][12] 225 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 358 T2_ColSpurVernierLUT_Cnt_s16[0][16] 358 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 1 T2_ColSpurVernierLUT_Cnt_s16[1][11] 1 T2_ColSpurVernierLUT_Cnt_s16[1][11] 1 T2_ColSpurVernierLUT_Cnt_s16[1][11] 1 T2_ColSpurVernierLUT_Cnt_s16[1][11] 1	5 3 30 53 96 629 61 94
T2_ColSpurVernierLUT_Cnt_s16[0][8]       98         T2_ColSpurVernierLUT_Cnt_s16[0][9]       130         T2_ColSpurVernierLUT_Cnt_s16[0][10]       163         T2_ColSpurVernierLUT_Cnt_s16[0][11]       196         T2_ColSpurVernierLUT_Cnt_s16[0][12]       225         T2_ColSpurVernierLUT_Cnt_s16[0][13]       261         T2_ColSpurVernierLUT_Cnt_s16[0][14]       294         T2_ColSpurVernierLUT_Cnt_s16[0][15]       327         T2_ColSpurVernierLUT_Cnt_s16[0][16]       356         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSp	3 30 33 36 629 61 64
T2_ColSpurVernierLUT_Cnt_s16[0][9]  T2_ColSpurVernierLUT_Cnt_s16[0][10]  T2_ColSpurVernierLUT_Cnt_s16[0][11]  T2_ColSpurVernierLUT_Cnt_s16[0][12]  T2_ColSpurVernierLUT_Cnt_s16[0][13]  T2_ColSpurVernierLUT_Cnt_s16[0][14]  T2_ColSpurVernierLUT_Cnt_s16[0][15]  T2_ColSpurVernierLUT_Cnt_s16[0][16]  T2_ColSpurVernierLUT_Cnt_s16[1][0]  T2_ColSpurVernierLUT_Cnt_s16[1][0]  T2_ColSpurVernierLUT_Cnt_s16[1][1]  T2_ColSpurVernierLUT_Cnt_s16[1][2]  T2_ColSpurVernierLUT_Cnt_s16[1][3]  T2_ColSpurVernierLUT_Cnt_s16[1][4]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][6]  T2_ColSpurVernierLUT_Cnt_s16[1][10]  T2_ColSpurVernierLUT_Cnt_s16[1][10]  T2_ColSpurVernierLUT_Cnt_s16[1][10]  T2_ColSpurVernierLUT_Cnt_s16[1][11]  T2_ColSpurVernierLUT_Cnt_s16[1][11]  T2_ColSpurVernierLUT_Cnt_s16[1][11]	30 33 96 29 61 94
T2_ColSpurVernierLUT_Cnt_s16[0][10]	53 96 29 61 94 27
T2_ColSpurVernierLUT_Cnt_s16[0][11]       196         T2_ColSpurVernierLUT_Cnt_s16[0][12]       225         T2_ColSpurVernierLUT_Cnt_s16[0][13]       261         T2_ColSpurVernierLUT_Cnt_s16[0][14]       294         T2_ColSpurVernierLUT_Cnt_s16[0][15]       327         T2_ColSpurVernierLUT_Cnt_s16[0][16]       358         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][5]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0	96 29 61 94 27
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T2_ColSpurVernierLUT_Cnt_s16[0][14]       294         T2_ColSpurVernierLUT_Cnt_s16[0][15]       327         T2_ColSpurVernierLUT_Cnt_s16[0][16]       358         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][5]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       3         T2_ColSpurVernierLUT_Cnt_s16[1][10]       3         T2_ColSpurVernierLUT_Cnt_s16[1][10]       3          T2_ColSpurVernierLUT_Cnt_s16[1][10]       3	94 27
T2_ColSpurVernierLUT_Cnt_s16[0][15]       327         T2_ColSpurVernierLUT_Cnt_s16[0][16]       358         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][5]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	27
T2_ColSpurVernierLUT_Cnt_s16[0][16]       358         T2_ColSpurVernierLUT_Cnt_s16[1][0]       0         T2_ColSpurVernierLUT_Cnt_s16[1][1]       4         T2_ColSpurVernierLUT_Cnt_s16[1][2]       3         T2_ColSpurVernierLUT_Cnt_s16[1][3]       2         T2_ColSpurVernierLUT_Cnt_s16[1][4]       1         T2_ColSpurVernierLUT_Cnt_s16[1][5]       0         T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][11]       3         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3          T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
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T2_ColSpurVernierLUT_Cnt_s16[1][6]       4         T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][7]       3         T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][8]       2         T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][9]       1         T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][10]       0         T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][11]       4         T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][12]       3         T2_ColSpurVernierLUT_Cnt_s16[1][13]       2	
T2_ColSpurVernierLUT_Cnt_s16[1][13] 2	
T2 ColSpurVernierLUT Cnt s16[1][14]	
_ ' '	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][0]	
T2_ColSpurVernierLUT_Cnt_s16[2][1] 8	
T2_ColSpurVernierLUT_Cnt_s16[2][2] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][3] 4	
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][9] 3	
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	
T2_ColSpurVernierLUT_Cnt_s16[2][13] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][14]	
T2_ColSpurVernierLUT_Cnt_s16[2][15]	
T2_ColSpurVernierLUT_Cnt_s16[2][16]	
T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	1
T2_ColSpurVernierLUT_Cnt_s16[3][3] 8	
T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	5
T2_ColSpurVernierLUT_Cnt_s16[3][7]	2
T2_ColSpurVernierLUT_Cnt_s16[3][8]	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	3
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	3
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15] 4	
T2_ColSpurVernierLUT_Cnt_s16[3][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][0] -39	
T2_DualSpurVernierLUT_Cnt_s16[0][1] -36	
T2_DualSpurVernierLUT_Cnt_s16[0][2] -32	96

DigColPs\_Per2



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2 DualSpurVernierLUT Cnt s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	9
T2_DualSpurVernierLUT_Cnt_s16[1][0]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2 DualSpurVernierLUT Cnt s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2 DualSpurVernierLUT Cnt s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3	3	
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	5	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7	7	
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.14		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006	55.30846006	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwD	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	1	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

ig_nte_mat_ou_bigoon on im_bigoon abou	tgt_i iii_bigooii dede		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	~
Param	0x04	0x04	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.4 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	105	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50	
DigColPs_ColTrimStatic_Deg_M_f32	14.8	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	24432	
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406	





Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1 1200.26039
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6] T3_ColSpurVernierLUT_Cnt_s16[2][7]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T0_ColOpurV(sprint UT_Cot_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	4
T2 ColSpurVernierLUT Cnt s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][4]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16] T0_DualSpurVernierLUT_Cnt_s16[0][47]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18] T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14] T0_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
TO Development of LIT Ont. 1070Y157	
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

DigColPs\_Per2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0 20		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	17		
k_VernCorrErrorDiag_Cnt_str.PStep k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid Cnt lgc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	1 - 1 - 1 - 1 - 1	
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

79.5637207

0

79.56374056 ± 0.00009

0

 $tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value$ 

 $tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value$ 

 $tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value$ 



Test Step 2.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs ColSensorFaultAcc Cnt M u16	101
DigColPs_ColTrimStatic_Deg_M_f32	25
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.4548138
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	80
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2 20
DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	80
DigColPs_TrimCompStatic_Cnt_M_u16	196
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2] T3_ColSpurVernierLUT_Cnt_s16[0][2]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][3] T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[1][0]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T0_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T3_ColSpurVernierLUT_Cnt_s16[2][40]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10
12_00/0pui voimoi 201_0m_310[2][11]	iv .





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.11		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1443.65869	1443.658758 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	543.658691	543.6587581 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	

0x0C

0x01

0x0C

0x01

Param

Status



Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)	v
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs ColTrimStatic Deg M f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs I2CHwColAngle Deg M f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs I2CHwSpurAngle Deg M f32	9.1
DigColPs I2CHwTrimTransCnts UIs M u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs PrevVernierLevelNo Cnt M u08	12
DigColPs SkipStepFltDetectAcc Cnt M u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs TrimCompStatic Cnt M u16	232
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_Igc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Input Value  1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

2014-10-14, 17:31:16+0530





	1	
Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2 DualSpurVernierLUT Cnt s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
	0	
T2_DualSpurVernierLUT_Cnt_s16[2][11]		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14	
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16	
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20	
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3	
	5	
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	1	
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9	
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11	
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13	
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15	
T2 DualSpurVernierLUT Cnt s16[3][19]	17	
T2 DualSpurVernierLUT Cnt s16[3][20]	19	
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21	
k_SelectFromColumn_Cnt_lgc	1	
k_SkipStepErrDiag_Cnt_str.Threshold	160	
k_SkipStepErrDiag_Cnt_str.PStep	23	
k_SkipStepErrDiag_Cnt_str.NStep	16	
k_VernCorrErrorDiag_Cnt_str.Threshold	82	
k_VernCorrErrorDiag_Cnt_str.PStep	43	
k_VernCorrErrorDiag_Cnt_str.NStep	12	
k_VernCorrErrorThresh_Deg_f32	16.35241604	
k_VernOORangeThresh_Deg_f32	106.19	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnf	t lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	
Name	Actual Value	Expected Value Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	312.661438	312.6614647 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-587.338562	-587.3385353 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	45.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	14286
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921
DigColPs_I2CHwSpurAngle_Deg_M_f32	10.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	10.2
DigColPs_TrimCompStatic_Cnt_M_u16	268
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1.
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2 ColSpurVernierLUT Cnt s16[0][5]	0
T2 ColSpurVernierLUT Cnt s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2 ColSpurVernierLUT Cnt s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360

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547.33 0





Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	298.7894		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644	103.8339644	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	973.389404	973.3894 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigCoIPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.8 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt GetCustData()	186
DigColPs ColParityError Cnt M Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	106
DigColPs_ColTrimStatic_Deg_M_f32	55.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1.
DigColPs_I2CHwColAngle_Cnt_M_u16	29294
DigColPs_I2CHwColAngle_Deg_M_f32	199.9994296
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49318
DigColPs_I2CHwSpurAngle_Deg_M_f32	11.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1048.767936
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	11.3
DigColPs_TrimCompStatic_Cnt_M_u16	304
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T3_ColSpurVernierLUT_Cnt_s46[4][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][0]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s46[3][40]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
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Name  T2_DualSpurVernierLUT_Cnt_s16[0][10]  T2_DualSpurVernierLUT_Cnt_s16[0][11]  T2_DualSpurVernierLUT_Cnt_s16[0][12]	Input Value -36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	-30
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2 DualSpurVernierLUT Cnt s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
	22
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
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DigColPs\_Per2 Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21  $k\_SelectFromColumn\_Cnt\_lgc$ 1 k\_SkipStepErrDiag\_Cnt\_str.Threshold 191 k\_SkipStepErrDiag\_Cnt\_str.PStep 16 k\_SkipStepErrDiag\_Cnt\_str.NStep 47 24  $k\_VernCorrErrorDiag\_Cnt\_str.Threshold$ k\_VernCorrErrorDiag\_Cnt\_str.PStep 21  $k\_VernCorrErrorDiag\_Cnt\_str.NStep$ 1 k\_VernCorrErrorThresh\_Deg\_f32 67.6606307 k\_VernOORangeThresh\_Deg\_f32 664 42 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value 2 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 199.9994296  $tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32$ 301.9312882 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 2922  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc$ tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum$ tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc tot Rte Inst Sa DigColPs Pim DigColPsEOL tat Pim DiaColPsEOL

tgt_fte_inst_da_bigcoirs.rim_bigcoirsEOL	tgt_Filli_DigColF3LOL	tgt_riii_bigcoirsece	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	504.399445	504.3994296 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-395.600555	-395.6005704 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.9 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	65.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.4
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0

DigColPs\_Per2

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Input Value DigColPs\_SpurSensorFaultAcc\_Cnt\_M\_u16 149 DigColPs\_SpurTrimStatic\_Deg\_M\_f32 12.4 340 DigColPs\_TrimCompStatic\_Cnt\_M\_u16 DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16 19  ${\tt DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc}$ Rte\_Inst\_Sa\_DigColPs tgt\_Rte\_Inst\_Sa\_DigColPs T2\_ColSpurVernierLUT\_Cnt\_s16[0][0] -163 T2\_ColSpurVernierLUT\_Cnt\_s16[0][1] -131 T2\_ColSpurVernierLUT\_Cnt\_s16[0][2] -99 T2\_ColSpurVernierLUT\_Cnt\_s16[0][3] -66 T2\_ColSpurVernierLUT\_Cnt\_s16[0][4] -33 T2\_ColSpurVernierLUT\_Cnt\_s16[0][5] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[0][6] 32 T2 ColSpurVernierLUT Cnt s16[0][7] 65 T2\_ColSpurVernierLUT\_Cnt\_s16[0][8] 98 T2\_ColSpurVernierLUT\_Cnt\_s16[0][9] 130 T2\_ColSpurVernierLUT\_Cnt\_s16[0][10] 163 T2\_ColSpurVernierLUT\_Cnt\_s16[0][11] 196 T2\_ColSpurVernierLUT\_Cnt\_s16[0][12] 229 T2\_ColSpurVernierLUT\_Cnt\_s16[0][13] 261 T2\_ColSpurVernierLUT\_Cnt\_s16[0][14] 294 T2\_ColSpurVernierLUT\_Cnt\_s16[0][15] 327 T2\_ColSpurVernierLUT\_Cnt\_s16[0][16] 359 T2\_ColSpurVernierLUT\_Cnt\_s16[1][0] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][1] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[1][2] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[1][3] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[1][4] T2\_ColSpurVernierLUT\_Cnt\_s16[1][5] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][6] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[1][7] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[1][8] 2 T2 ColSpurVernierLUT Cnt s16[1][9] 1 T2\_ColSpurVernierLUT\_Cnt\_s16[1][10] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][11] 4 T2 ColSpurVernierLUT Cnt s16[1][12] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[1][13] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[1][14] T2\_ColSpurVernierLUT\_Cnt\_s16[1][15] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][16] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[2][0] 0 T2 ColSpurVernierLUT\_Cnt\_s16[2][1] 8 T2\_ColSpurVernierLUT\_Cnt\_s16[2][2] 6 T2\_ColSpurVernierLUT\_Cnt\_s16[2][3] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[2][4] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[2][5] 0 9 T2\_ColSpurVernierLUT\_Cnt\_s16[2][6] T2\_ColSpurVernierLUT\_Cnt\_s16[2][7] 7 T2\_ColSpurVernierLUT\_Cnt\_s16[2][8] 5 T2\_ColSpurVernierLUT\_Cnt\_s16[2][9] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[2][10] 1 T2\_ColSpurVernierLUT\_Cnt\_s16[2][11] 10 T2\_ColSpurVernierLUT\_Cnt\_s16[2][12] 8 T2\_ColSpurVernierLUT\_Cnt\_s16[2][13] 6 T2\_ColSpurVernierLUT\_Cnt\_s16[2][14] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[2][15] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[2][16] 10 T2\_ColSpurVernierLUT\_Cnt\_s16[3][0] T2\_ColSpurVernierLUT\_Cnt\_s16[3][1] 14 T2\_ColSpurVernierLUT\_Cnt\_s16[3][2] 11 T2\_ColSpurVernierLUT\_Cnt\_s16[3][3] 8 T2\_ColSpurVernierLUT\_Cnt\_s16[3][4] 5 T2\_ColSpurVernierLUT\_Cnt\_s16[3][5] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[3][6] 15 T2\_ColSpurVernierLUT\_Cnt\_s16[3][7] 12 T2 ColSpurVernierLUT Cnt s16[3][8] 9 T2\_ColSpurVernierLUT\_Cnt\_s16[3][9] 6 T2 ColSpurVernierLUT Cnt s16[3][10] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[3][11] 16 T2\_ColSpurVernierLUT\_Cnt\_s16[3][12] 13 T2\_ColSpurVernierLUT\_Cnt\_s16[3][13] 10 T2\_ColSpurVernierLUT\_Cnt\_s16[3][14] T2\_ColSpurVernierLUT\_Cnt\_s16[3][15] 4

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duaiopui veitiieteu i Ott 510[3][3]	10

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.55		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
STORE WAY OF FROM MI			

9C	131_11311 1—11		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	153.27533	153.2753346 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-746.72467	-746.7246654 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	<b>✓</b>
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	76





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	13.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs PrevAngleDataAvailable Cnt M Igc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	13.5
DigColPs_TrimCompStatic_Cnt_M_u16	376
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2 ColSpurVernierLUT Cnt s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T3_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2 DualSpurVernierLUT Cnt s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
12 Duan-Opur volino (201 Ont 310   2   1   1	•
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][9] T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k VernCorrErrorThresh Deg f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.16		
tgt DigColPs Per2 MecState Cnt enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt Pim DigColPsEOL.TrimComp Cnt u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosVa	lid Cpt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos H		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs Per2 MecState Cnt er	<del></del>	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_I		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	<del>5</del> ~	
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M Igc	1	1	Kesult
			~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0 1065.17773	1065.177819 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc		1 1080 ± 0.0001220703125	
DigColPs_PrevColPos_Deg_M_f32	1080		~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08			_
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	•

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1065.17773	1065.177819 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	165.177734	165.177819 ± 0.0009	~
tat DigColPs Per2 TrimComp Cnt lac.value	1	1	<b>✓</b>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.11 (Repeat Count = 1)	
	Input Value
Name	Input Value
DigColPsInt_GetCustData()	120
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	86.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	325.6206695
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	14.6
DigColPs_TrimCompStatic_Cnt_M_u16	412
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSputVernierLUT_Cnt_s10[0][12] T2 ColSputVernierLUT Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	359
T2_ColSpurVernierLUT_Cnt_s16[0][16]	
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cst_s16[2][11]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cst_s16[2][12]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	99		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	13		
k_VernCorrErrorDiag_Cnt_str.Threshold	74		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	78.75594592		
k_VernOORangeThresh_Deg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum.value	1151.77		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.Contrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.Sput Trim_Deg_132 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cni	t lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	Resul
DigColPs I2CHwColAngleForTrim Deg M f32	1309.09082	1309.090909 ± 0.00048828125	

1309.09082

0

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

 ${\tt DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08}$ 

1309.090909 ± 0.00048828125

0





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1319.42065	1319.42067 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	419.420654	419.4206695 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

T 101 010/D 10 11	
Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	96.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129
DigColPs_I2CHwSpurAngle_Deg_M_f32	15.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	15.7
DigColPs_TrimCompStatic_Cnt_M_u16	448
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2 ColSpurVernierLUT Cnt s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
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DigColPs\_Per2

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Input Value T2\_ColSpurVernierLUT\_Cnt\_s16[1][4] T2\_ColSpurVernierLUT\_Cnt\_s16[1][5] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][6] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[1][7] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[1][8] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[1][9] 1 T2\_ColSpurVernierLUT\_Cnt\_s16[1][10] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][11] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[1][12] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[1][13] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[1][14] 1 T2\_ColSpurVernierLUT\_Cnt\_s16[1][15] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[1][16] 4 0 T2\_ColSpurVernierLUT\_Cnt\_s16[2][0] T2\_ColSpurVernierLUT\_Cnt\_s16[2][1] 8 6 T2\_ColSpurVernierLUT\_Cnt\_s16[2][2] T2\_ColSpurVernierLUT\_Cnt\_s16[2][3] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[2][4] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[2][5] 0 T2\_ColSpurVernierLUT\_Cnt\_s16[2][6] 9 T2\_ColSpurVernierLUT\_Cnt\_s16[2][7] 7 T2\_ColSpurVernierLUT\_Cnt\_s16[2][8] 5 T2\_ColSpurVernierLUT\_Cnt\_s16[2][9] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[2][10] 1 T2 ColSpurVernierLUT\_Cnt\_s16[2][11] 10 T2\_ColSpurVernierLUT\_Cnt\_s16[2][12] 8 T2\_ColSpurVernierLUT\_Cnt\_s16[2][13] 6 T2\_ColSpurVernierLUT\_Cnt\_s16[2][14] 4 T2 ColSpurVernierLUT Cnt s16[2][15] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[2][16] 10 T2\_ColSpurVernierLUT\_Cnt\_s16[3][0] T2\_ColSpurVernierLUT\_Cnt\_s16[3][1] 14 T2 ColSpurVernierLUT Cnt s16[3][2] 11 T2\_ColSpurVernierLUT\_Cnt\_s16[3][3] 8 T2\_ColSpurVernierLUT\_Cnt\_s16[3][4] 5 T2\_ColSpurVernierLUT\_Cnt\_s16[3][5] 2 T2\_ColSpurVernierLUT\_Cnt\_s16[3][6] 15 12  $T2\_ColSpurVernierLUT\_Cnt\_s16[3][7]$ T2\_ColSpurVernierLUT\_Cnt\_s16[3][8] 9 6 T2\_ColSpurVernierLUT\_Cnt\_s16[3][9] T2\_ColSpurVernierLUT\_Cnt\_s16[3][10] 3 T2\_ColSpurVernierLUT\_Cnt\_s16[3][11] 16 T2\_ColSpurVernierLUT\_Cnt\_s16[3][12] 13 T2\_ColSpurVernierLUT\_Cnt\_s16[3][13] 10 T2\_ColSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_ColSpurVernierLUT\_Cnt\_s16[3][15] 4 T2\_ColSpurVernierLUT\_Cnt\_s16[3][16] 17 T2\_DualSpurVernierLUT\_Cnt\_s16[0][0] -396 T2\_DualSpurVernierLUT\_Cnt\_s16[0][1] -360 T2\_DualSpurVernierLUT\_Cnt\_s16[0][2] -324 T2\_DualSpurVernierLUT\_Cnt\_s16[0][3] -288 T2\_DualSpurVernierLUT\_Cnt\_s16[0][4] -252 T2\_DualSpurVernierLUT\_Cnt\_s16[0][5] -216 T2\_DualSpurVernierLUT\_Cnt\_s16[0][6] -180 T2\_DualSpurVernierLUT\_Cnt\_s16[0][7] -144 T2\_DualSpurVernierLUT\_Cnt\_s16[0][8] -108 T2\_DualSpurVernierLUT\_Cnt\_s16[0][9] -72 T2\_DualSpurVernierLUT\_Cnt\_s16[0][10] -36 T2\_DualSpurVernierLUT\_Cnt\_s16[0][11] 0 36 T2\_DualSpurVernierLUT\_Cnt\_s16[0][12] T2\_DualSpurVernierLUT\_Cnt\_s16[0][13] 72 T2\_DualSpurVernierLUT\_Cnt\_s16[0][14] 108 T2\_DualSpurVernierLUT\_Cnt\_s16[0][15] 144 T2\_DualSpurVernierLUT\_Cnt\_s16[0][16] 180 T2\_DualSpurVernierLUT\_Cnt\_s16[0][17] 216 T2\_DualSpurVernierLUT\_Cnt\_s16[0][18] 252 T2\_DualSpurVernierLUT\_Cnt\_s16[0][19] 288 T2 DualSpurVernierLUT Cnt s16[0][20] 324 T2\_DualSpurVernierLUT\_Cnt\_s16[0][21] 360 T2\_DualSpurVernierLUT\_Cnt\_s16[1][0] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[1][1] 0 T2\_DualSpurVernierLUT\_Cnt\_s16[1][2] 1 2 T2\_DualSpurVernierLUT\_Cnt\_s16[1][3]





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T3_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][5]	6
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2 DualSpurVernierLUT Cnt s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21] T2_DualSpurVernierLUT_Cnt_s16[3][0]	10 22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	13 15
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	70
k_SkipStepErrDiag_Cnt_str.PStep	47
k_SkipStepErrDiag_Cnt_str.NStep	44
k_VernCorrErrorDiag_Cnt_str.Threshold	88
k_VernCorrErrorDiag_Cnt_str.PStep	0
k_VernCorrErrorDiag_Cnt_str.NStep	38
k_VernCorrErrorThresh_Deg_f32	78.63725519
k_VernOORangeThresh_Deg_f32	1720.3
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	115.010748



Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1458.61072	1458.610748 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	•
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

T + 0+ 0 +0 /D + +0 + +1	
Test Step 2.13 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	106.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25526
DigColPs_I2CHwColAngle_Deg_M_f32	216.7759984
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674
DigColPs_I2CHwSpurAngle_Deg_M_f32	16.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	16.8
DigColPs_TrimCompStatic_Cnt_M_u16	484
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

2014-10-14, 17:31:16+0530



12. Collago/Arminetty   Cest #19018   22   12. Collago/Arminetty   Cest #19018   28   12. Collago/Arminetty   Cest #19018   28   12. Collago/Arminetty   Cest #19018   28   12. Collago/Arminetty   Cest #19018   29   13. Collago/Arminetty   Cest #19018   29   14. Collago/Arminetty   Cest #19018   29   15. Collago/Arminetty   Cest #19018   29   17. Collago/Arminetty   Cest #19018   20   17. Collago/Arminetty	Immut Value	Name
12_Cospur/ement_U_Cot_s100	Input Value	Name
17_Colspar/memat U. Col.; still[18]   98   12_Colspar/memat U. Col.; still[18]   100   12_Colspar/memat U. C		
T2_CoSpar/weined_T2_Cot_s100[19] 12_CoSpar/weined_T2_Cot_s100[19] 13_CoSpar/weined_T2_Cot_s100[19] 14_CoSpar/weined_T2_Cot_s100[19] 15_CoSpar/weined_T2_Cot_s100[19] 16_CoSpar/weined_T2_Cot_s100[19] 17_CoSpar/weined_T2_Cot_s100[19] 18_CoSpar/weined_T2_Cot_s100[19] 19_CoSpar/weined_T2_Cot_s100[19] 10_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 12_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 11_CoSpar/weined_T2_Cot_s100[19] 12_CoSpar/weined_T2_Cot_s100[19] 12_CoSpar/weined_T2_Cot_s100[19]		
T. COSSPAYMENT L. Crit   150 11   150   P. C		
T_Colspar/went_U_Cot_stop  11   990		
T. Colspar/ment U. C. C. 15 (19)   12   22   23   23   24   24   25   25   25   25   25   25		
T2_CoSput/west_UC_Cd_stolp[14] 294 T2_CoSput/west_UC_Cd_stolp[14] 295 T2_CoSput/west_UC_Cd_stolp[16] 397 T2_CoSput/west_UC_Cd_stolp[16] 397 T2_CoSput/west_UC_Cd_stolp[16] 4 T2_CoSput/west_UC_Cd_stolp[16] 4 T2_CoSput/west_UC_Cd_stolp[16] 4 T2_CoSput/west_UC_Cd_stolp[16] 5 T2_CoSput/west_UC_Cd_stolp[16] 7 T2_CoSput/west_UC_Cd_stolp[17] 7 T2_CoSput/west_UC_Cd_stol		
12_Colsput/ment_UF_Cat_st(0) 16    12_Colsput/ment_UF_Cat_st(0) 16    12_Colsput/ment_UF_Cat_st(0) 16    12_Colsput/ment_UF_Cat_st(0) 16    12_Colsput/ment_UF_Cat_st(0) 17    13_Colsput/ment_UF_Cat_st(0) 17    13_Colsput/ment_UF_Cat_st(0) 17    13_Colsput/ment_UF_Cat_st(0) 17    13_Colsput/ment_UF_Cat_st(0) 17    13_Colsput/ment_UF_Cat_st(0) 17    13_Colsput/ment_UF_Cat_st(0) 17    14_Colsput/ment_UF_Cat_st(0) 17    15_Colsput/ment_UF_Cat_st(0) 17    16_Colsput/ment_UF_Cat_st(0) 17    17_Colsput/ment_UF_Cat_st(0) 17    17_Colsput/ment_UF_Cat_st(0) 17    17_Colsput/ment_UF_Cat_st(0) 17    18_Colsput/ment_UF_Cat_st(0) 17    18_Colsput/men		
12_Colsput/mindLVI_Col_st@0]16    369   12_Colsput/mindLVI_Col_st@0]16    12_Colsput/mindLVI_Col_st@0]16    13_Colsput/mindLVI_Col_st@0]16    14_Colsput/mindLVI_Col_st@0]16    15_Colsput/mindLVI_Col_st@0]16    15_Colsput/mindLVI_Col_st@0]16    16_Colsput/mindLVI_Col_st@0]16    17_Colsput/mindLVI_Col_st@0]16    18_Colsput/mindLVI_Col_st@0]16    19_Colsput/mindLVI_Col_st@0]16	261	
T2_CoSput/west_LU_Cnt_st@  s    2_CoSput/west_LU_Cnt_st@  s    3_CoSput/west_LU_Cnt_st@  s    4_CoSput/west_LU_Cnt_st@  s   s    4_COSput/west_LU_Cnt_st@  s   s   s   s   s   s   s  s   s	294	T2_ColSpurVernierLUT_Cnt_s16[0][14]
T2_CoSput/vermeUU_Cnt_18(1) 11   4	327	T2_ColSpurVernierLUT_Cnt_s16[0][15]
12, Colsput/emicHUT Cert, 19(1)   19   2   2   2   2   2   2   2   2   2	359	T2_ColSpurVernierLUT_Cnt_s16[0][16]
12, CoSpurVernicUT, Cet. 19(1) 13  2   2   2   2   2   2   2   2   2   2	0	T2_ColSpurVernierLUT_Cnt_s16[1][0]
17. CoSput/venetUT Cot.   16(1)	4	T2_ColSpurVernierLUT_Cnt_s16[1][1]
12, CoSpurVermeLUT, Cert. 14(1)(1)(1)(1)(1)(1)(1)(1)(1)(1)(1)(1)(1)(	3	T2_ColSpurVernierLUT_Cnt_s16[1][2]
12, CoSpavVermietUT. Cnt. s16(1) 60	2	T2_ColSpurVernierLUT_Cnt_s16[1][3]
T2_CoSparVermentUT_Cnt_st0f[10]   4	1	T2_ColSpurVernierLUT_Cnt_s16[1][4]
T2, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  13, CoSput/verniet.UT, Cnt, 15(1)[19]  14, CoSput/verniet.UT, Cnt, 15(1)[19]  17, CoSput/verniet.UT, Cnt, 15(1)[19]  18, CoSput/verniet.UT, Cnt, 15(1)[19]  19, CoSput/verniet.UT, Cnt, 15(1)[19]  10, CoSput/verniet.UT, Cnt, 15(1)[19]  11, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  13, CoSput/verniet.UT, Cnt, 15(1)[19]  14, CoSput/verniet.UT, Cnt, 15(1)[19]  15, CoSput/verniet.UT, Cnt, 15(1)[19]  16, CoSput/verniet.UT, Cnt, 15(1)[19]  17, CoSput/verniet.UT, Cnt, 15(1)[19]  18, CoSput/verniet.UT, Cnt, 15(1)[19]  19, CoSput/verniet.UT, Cnt, 15(1)[19]  10, CoSput/verniet.UT, Cnt, 15(1)[19]  11, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  13, CoSput/verniet.UT, Cnt, 15(1)[19]  14, CoSput/verniet.UT, Cnt, 15(1)[19]  17, CoSput/verniet.UT, Cnt, 15(1)[19]  18, CoSput/verniet.UT, Cnt, 15(1)[19]  19, CoSput/verniet.UT, Cnt, 15(1)[19]  10, CoSput/verniet.UT, Cnt, 15(1)[19]  11, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  13, CoSput/verniet.UT, Cnt, 15(1)[19]  14, CoSput/verniet.UT, Cnt, 15(1)[19]  15, CoSput/verniet.UT, Cnt, 15(1)[19]  16, CoSput/verniet.UT, Cnt, 15(1)[19]  17, CoSput/verniet.UT, Cnt, 15(1)[19]  18, CoSput/verniet.UT, Cnt, 15(1)[19]  19, CoSput/verniet.UT, Cnt, 15(1)[19]  10, CoSput/verniet.UT, Cnt, 15(1)[19]  11, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  13, CoSput/verniet.UT, Cnt, 15(1)[19]  14, CoSput/verniet.UT, Cnt, 15(1)[19]  15, CoSput/verniet.UT, Cnt, 15(1)[19]  16, CoSput/verniet.UT, Cnt, 15(1)[19]  17, CoSput/verniet.UT, Cnt, 15(1)[19]  18, CoSput/verniet.UT, Cnt, 15(1)[19]  19, CoSput/verniet.UT, Cnt, 15(1)[19]  10, CoSput/verniet.UT, Cnt, 15(1)[19]  11, CoSput/verniet.UT, Cnt, 15(1)[19]  12, CoSput/verniet.UT, Cnt, 15(1)[19]  13, CoSput/verniet.UT, Cnt, 15(1)[19]  14, CoSput/verniet.UT, Cnt, 15(1)[19]  15, CoSput/verniet	0	T2_ColSpurVernierLUT_Cnt_s16[1][5]
T2_CoSpavVementUT_Cnt_st8[1]8   2 T2_CoSpavVementUT_Cnt_st8[1]8   1 T2_CoSpavVementUT_Cnt_st8[1]8   1 T2_CoSpavVementUT_Cnt_st8[1]8   1 T2_CoSpavVementUT_Cnt_st8[1]17   3 T2_CoSpavVementUT_Cnt_st8[1]18   2 T2_CoSpavVementUT_Cnt_st8[1]18   2 T2_CoSpavVementUT_Cnt_st8[1]18   2 T2_CoSpavVementUT_Cnt_st8[1]18   0 T2_CoSpavVementUT_Cnt_st8[1]18   0 T2_CoSpavVementUT_Cnt_st8[1]18   0 T2_CoSpavVementUT_Cnt_st8[1]18   0 T2_CoSpavVementUT_Cnt_st8[2]19   0 T2_CoSpavVementUT_Cn	4	
T2_CoSpyVement_UT_Cnt_s16[1]8] 2 T2_CoSpyVement_UT_Cnt_s16[1](10) 0 T2_CoSpyVement_UT_Cnt_s16[1](10) 0 T2_CoSpyVement_UT_Cnt_s16[1](11) 4 T2_CoSpyVement_UT_Cnt_s16[1](12) 3 T2_CoSpyVement_UT_Cnt_s16[1](12) 3 T2_CoSpyVement_UT_Cnt_s16[1](14) 1 T2_CoSpyVement_UT_Cnt_s16[1](14) 1 T2_CoSpyVement_UT_Cnt_s16[1](14) 1 T2_CoSpyVement_UT_Cnt_s16[1](14) 1 T2_CoSpyVement_UT_Cnt_s16[1](14) 1 T2_CoSpyVement_UT_Cnt_s16[1](16) 4 T2_CoSpyVement_UT_Cnt_s16[1](16) 6 T2_CoSpyVement_UT_Cnt_s16[1](16) 7 T2_CoSpyVement_UT_Cnt_s16[1](16) 9 T2_CoSpyVement_UT_Cnt_s16[1](16) 9 T2_CoSpyVement_UT_Cnt_s16[1](16) 9 T2_CoSpyVement_UT_Cnt_s16[1](16) 1 T2_CoSpyVement_UT_	3	T2_ColSpurVernierLUT_Cnt_s16[1][7]
12. ColSput/venierLUT_Cnt_s16[1](1) 12. ColSput/venierLUT_Cnt_s16[1](1) 13. ColSput/venierLUT_Cnt_s16[1](1) 14. T2. ColSput/venierLUT_Cnt_s16[1](1) 15. ColSput/venierLUT_Cnt_s16[1](1) 16. ColSput/venierLUT_Cnt_s16[1](1) 17. ColSput/venierLUT_Cnt_s16[1](1) 18. ColSput/venierLUT_Cnt_s16[1](1) 19. ColSput/venierLUT_Cnt_s16[1](1) 19. ColSput/venierLUT_Cnt_s16[2](1) 19. ColSput/venierLUT_Cnt_	2	
12. ColSput/venierLUT_Cnt_s16[1](1) 12. ColSput/venierLUT_Cnt_s16[1](1) 13. ColSput/venierLUT_Cnt_s16[1](1) 14. T2. ColSput/venierLUT_Cnt_s16[1](1) 15. ColSput/venierLUT_Cnt_s16[1](1) 16. ColSput/venierLUT_Cnt_s16[1](1) 17. ColSput/venierLUT_Cnt_s16[1](1) 18. ColSput/venierLUT_Cnt_s16[1](1) 19. ColSput/venierLUT_Cnt_s16[1](1) 19. ColSput/venierLUT_Cnt_s16[2](1) 19. ColSput/venierLUT_Cnt_		
12. ColSpurVerniert.UT_Cnt_s16[1][15] 2. ColSpurVerniert.UT_Cnt_s16[1][12] 3. T2. ColSpurVerniert.UT_Cnt_s16[1][12] 3. T2. ColSpurVerniert.UT_Cnt_s16[1][16] 1. ColSpurVerniert.UT_Cnt_s16[1][16] 1. ColSpurVerniert.UT_Cnt_s16[1][16] 1. ColSpurVerniert.UT_Cnt_s16[1][16] 1. ColSpurVerniert.UT_Cnt_s16[1][16] 1. ColSpurVerniert.UT_Cnt_s16[2][10] 1.		
12, ColSpurVemet.UT_Cnt_s16[1]12   2   2   2   2   2   2   2   2   2		
12. ColSpurVemiet.UT_Cnt_s16[1]14] 1 12. ColSpurVemiet.UT_Cnt_s16[1]14] 1 13. ColSpurVemiet.UT_Cnt_s16[1]16] 4 14. ColSpurVemiet.UT_Cnt_s16[1]16] 4 15. ColSpurVemiet.UT_Cnt_s16[1]16] 4 17. ColSpurVemiet.UT_Cnt_s16[1]16] 6 17. ColSpurVemiet.UT_Cnt_s16[2]19 0 17. ColSpurVemiet.UT_Cnt_s16[2]19 6 18. ColSpurVemiet.UT_Cnt_s16[2]19 6 18. ColSpurVemiet.UT_Cnt_s16[2]19 6 19. ColSpurVemiet.UT_Cnt_s16[2]19 10 19. ColSpurVemiet.UT_Cnt_s16[2]19 10 19. ColSpurVemiet.UT_Cnt_s16[2]19 10 19. ColSpurVemiet.UT_Cnt_s16[2]19 11 19. ColSpurVemiet.UT_Cnt_s16[2]19 11 19. ColSpurVemiet.UT_Cnt_s16[2]19 11 19. ColSpurVemiet.UT_Cnt_s16[2]19 11 10. ColSpurVemiet.UT_Cnt_s16[2]19 11 11. ColSpurVemiet.UT_Cnt_s16[2]19 11 12. ColSpurVemiet.UT_Cnt_s16[2]19 10 13. ColSpurVemiet.UT_Cnt_s16[2]19 10 14. ColSpurVemiet.UT_Cnt_s16[2]19 10 15. ColSpurVemiet.UT_Cnt_s16[2]19 10 16. ColSpurVemiet.UT_Cnt_s16[2]19 10 17. ColSpurVemiet.UT_Cnt_s16[2]19 10 18. ColSpurVemiet.UT_Cnt_s16[2]19 10 19. ColSpurVemi		
12. ColSpurVemiet.UT_Cnt_s16[1]14] 12. ColSpurVemiet.UT_Cnt_s16[1]15] 12. ColSpurVemiet.UT_Cnt_s16[1]16] 13. ColSpurVemiet.UT_Cnt_s16[1]16] 14. ColSpurVemiet.UT_Cnt_s16[2]17 15. ColSpurVemiet.UT_Cnt_s16[2]17 16. ColSpurVemiet.UT_Cnt_s16[2]18 17. ColSpurVemiet.UT_Cnt_s16[2]19 18. ColSpurVemiet.UT_Cnt_s16[2]19 19. ColSpurVemiet.UT_Cnt_s16[2]14 19. ColSpurVemiet.UT_Cnt_s16[2]14 19. ColSpurVemiet.UT_Cnt_s16[2]19 10. ColSpurVemiet.UT_Cnt_s16[2]19 11. ColSpurVemiet.UT_Cnt_s16[2]19 12. ColSpurVemiet.UT_Cnt_s16[2]19 13. ColSpurVemiet.UT_Cnt_s16[2]19 14. ColSpurVemiet.UT_Cnt_s16[2]19 15. ColSpurVemiet.UT_Cnt_s16[2]19 16. ColSpurVemiet.UT_Cnt_s16[2]19 17. ColSpurVemiet.UT_Cnt_s16[2]19 18. ColSpurVemiet.UT_Cnt_s16[2]19 19. ColSpurVemiet.UT_Cnt_s16[2]19 19		
12. ColSpurVermietLUT_Cnt_st6[1](15)		
T2 CoSpurVemietLUT_Cnt_s16[2]10		
T2   ColSpurVernierLUT   Cnt   s16[2] 0		
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] 8 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] T3_ColSpurVernierLUT_Cnt_s16[2][7] T4_ColSpurVernierLUT_Cnt_s16[2][7] T5_ColSpurVernierLUT_Cnt_s16[2][7] T6_ColSpurVernierLUT_Cnt_s16[2][7] T1_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T3_ColSpurVernierLUT_Cnt_s16[2][7] T4_ColSpurVernierLUT_Cnt_s16[2][7] T5_ColSpurVernierLUT_Cnt_s16[2][7] T6_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T4_ColSpurVernierLUT_Cnt_s16[3][7] T5_ColSpurVe		
T2_ColSpurVemierLUT_Cnt_s16[2] 2    6     T2_ColSpurVemierLUT_Cnt_s16[2] 3    4     T2_ColSpurVemierLUT_Cnt_s16[2] 4    2     T2_ColSpurVemierLUT_Cnt_s16[2] 5    0     T2_ColSpurVemierLUT_Cnt_s16[2] 6    9     T2_ColSpurVemierLUT_Cnt_s16[2] 7    7     T3_ColSpurVemierLUT_Cnt_s16[2] 7    7     T3_ColSpurVemierLUT_Cnt_s16[2] 8    5     T2_ColSpurVemierLUT_Cnt_s16[2] 9    3     T2_ColSpurVemierLUT_Cnt_s16[2] 10    1     T2_ColSpurVemierLUT_Cnt_s16[2] 10    1     T2_ColSpurVemierLUT_Cnt_s16[2] 11    10     T2_ColSpurVemierLUT_Cnt_s16[2] 12    8     T2_ColSpurVemierLUT_Cnt_s16[2] 13    6     T2_ColSpurVemierLUT_Cnt_s16[2] 14    4     T2_ColSpurVemierLUT_Cnt_s16[2] 15    2     T2_ColSpurVemierLUT_Cnt_s16[2] 16    10     T2_ColSpurVemierLUT_Cnt_s16[2] 16    10     T2_ColSpurVemierLUT_Cnt_s16[2] 16    10     T2_ColSpurVemierLUT_Cnt_s16[3] 10    1     T2_ColSpurVemierLUT_Cnt_s16[3] 10    1     T2_ColSpurVemierLUT_Cnt_s16[3] 2    11     T2_ColSpurVemierLUT_Cnt_s16[3] 3    8     T2_ColSpurVemierLUT_Cnt_s16[3] 4    5     T2_ColSpurVemierLUT_Cnt_s16[3] 6    15     T2_ColSpurVemierLUT_Cnt_s16[3] 6    15     T2_ColSpurVemierLUT_Cnt_s16[3] 6    15     T2_ColSpurVemierLUT_Cnt_s16[3] 6    16     T2_ColSpurVemierLUT_Cnt_s16[3] 6    16     T2_ColSpurVemierLUT_Cnt_s16[3] 6    16     T2_ColSpurVemierLUT_Cnt_s16[3] 6    17     T2_DualSpurVemierLUT_Cnt_s16[0] 2    324     T2_DualSpurVemierLUT_Cnt_s16[0] 2    328     T2_DualSpurVemierLUT_Cnt_s16[0] 2    324     T2_DualSpurVemierLUT_Cnt_s16[0] 2    328		
T2_ColSpurVemierLUT_Cnt_st6[2][3]		
T2_ColSpurVernierLUT_Cnt_st6[2][4]   2   2   2   2   2   2   2   2   2		
T2_ColSpurVemierLUT_Cnt_s16[2][5] 9 T2_ColSpurVemierLUT_Cnt_s16[2][6] 9 T2_ColSpurVemierLUT_Cnt_s16[2][8] 5 T2_ColSpurVemierLUT_Cnt_s16[2][8] 5 T2_ColSpurVemierLUT_Cnt_s16[2][8] 5 T2_ColSpurVemierLUT_Cnt_s16[2][9] 3 T2_ColSpurVemierLUT_Cnt_s16[2][10] 1 T2_ColSpurVemierLUT_Cnt_s16[2][11] 10 T2_ColSpurVemierLUT_Cnt_s16[2][11] 10 T2_ColSpurVemierLUT_Cnt_s16[2][12] 8 T2_ColSpurVemierLUT_Cnt_s16[2][13] 6 T2_ColSpurVemierLUT_Cnt_s16[2][14] 4 T2_ColSpurVemierLUT_Cnt_s16[2][15] 2 T2_ColSpurVemierLUT_Cnt_s16[2][16] 10 T2_ColSpurVemierLUT_Cnt_s16[3][0] 11 T2_ColSpurVemierLUT_Cnt_s16[3][0] 12 T2_ColSpurVemierLUT_Cnt_s16[3][0] 12 T2_ColSpurVemierLUT_Cnt_s16[3][0] 15 T2_ColSpurVemierLUT_Cnt_s16[3][0] 15 T2_ColSpurVemierLUT_Cnt_s16[3][0] 15 T2_ColSpurVemierLUT_Cnt_s16[3][0] 15 T2_ColSpurVemierLUT_Cnt_s16[3][0] 15 T2_ColSpurVemierLUT_Cnt_s16[3][0] 16 T2_ColSpurVemierLUT_Cnt_s16[3][0] 11 T2_ColSpurVemi		
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][14] 7 T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0] T1_ColSpurVernierLUT_Cnt_s16[3][0] T1_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][3]		
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T2_ColSpurVernierLUT_Cnt_s16[2][9] 12_ColSpurVernierLUT_Cnt_s16[2][10] 12_ColSpurVernierLUT_Cnt_s16[2][11] 10 12_ColSpurVernierLUT_Cnt_s16[2][12] 8 12_ColSpurVernierLUT_Cnt_s16[2][13] 6 12_ColSpurVernierLUT_Cnt_s16[2][14] 4 12_ColSpurVernierLUT_Cnt_s16[2][16] 12_ColSpurVernierLUT_Cnt_s16[2][16] 12_ColSpurVernierLUT_Cnt_s16[2][16] 12_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16[3][0] 14 12_ColSpurVernierLUT_Cnt_s16[3][1] 14 12_ColSpurVernierLUT_Cnt_s16[3][2] 11 12_ColSpurVernierLUT_Cnt_s16[3][3] 8 12_ColSpurVernierLUT_Cnt_s16[3][3] 8 12_ColSpurVernierLUT_Cnt_s16[3][6] 12_ColSpurVernierLUT_Cnt_s16[3][6] 15 12_ColSpurVernierLUT_Cnt_s16[3][6] 15 12_ColSpurVernierLUT_Cnt_s16[3][6] 15 12_ColSpurVernierLUT_Cnt_s16[3][6] 16 17_ColSpurVernierLUT_Cnt_s16[3][6] 18_ColSpurVernierLUT_Cnt_s16[3][6] 19_ColSpurVernierLUT_Cnt_s16[3][6] 10_ColSpurVernierLUT_Cnt_s16[3][6] 11_ColSpurVernierLUT_Cnt_s16[3][6] 12_ColSpurVernierLUT_Cnt_s16[3][10] 13_ColSpurVernierLUT_Cnt_s16[3][10] 14_ColSpurVernierLUT_Cnt_s16[3][10] 15_ColSpurVernierLUT_Cnt_s16[3][10] 16_ColSpurVernierLUT_Cnt_s16[3][10] 17_ColSpurVernierLUT_Cnt_s16[3][10] 18_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVernierLUT_Cnt_s16[3][10] 10_ColSpurVernierLUT_Cnt_s16[3][10] 11_ColSpurVernierLUT_Cnt_s16[3][10] 12_ColSpurVernierLUT_Cnt_s16[3][10] 13_ColSpurVernierLUT_Cnt_s16[3][10] 14_ColSpurVernierLUT_Cnt_s16[3][10] 15_ColSpurVernierLUT_Cnt_s16[3][10] 16_ColSpurVernierLUT_Cnt_s16[3][10] 17_ColSpurVernierLUT_Cnt_s16[3][10] 18_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVernierLUT_Cnt_s16[3][10] 10_ColSpurVernierLUT_Cnt_s16[3][10] 11_ColSpurVernierLUT_Cnt_s16[3][10] 12_ColSpurVernierLUT_Cnt_s16[3][10] 13_ColSpurVernierLUT_Cnt_s16[3][10] 14_ColSpurVernierLUT_Cnt_s16[3][10] 15_ColSpurVernierLUT_Cnt_s16[3][10] 16_ColSpurVernierLUT_Cnt_s16[3][10] 17_ColSpurVernierLUT_Cnt_s16[3][10] 18_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVernierLUT_Cnt_s16[3][10] 19_ColSpurVer		T2_ColSpurVernierLUT_Cnt_s16[2][7]
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T2_ColSpurVernierLUT_Cnt_s16[2][12]	1	T2_ColSpurVernierLUT_Cnt_s16[2][10]
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T2_ColSpurVernierLUT_Cnt_s16[2][14]  T2_ColSpurVernierLUT_Cnt_s16[2][15]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[3][0]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][2]  T1_ColSpurVernierLUT_Cnt_s16[3][3]  T2_ColSpurVernierLUT_Cnt_s16[3][4]  T2_ColSpurVernierLUT_Cnt_s16[3][4]  T2_ColSpurVernierLUT_Cnt_s16[3][6]  T2_ColSpurVernierLUT_Cnt_s16[3][6]  T2_ColSpurVernierLUT_Cnt_s16[3][7]  T2_ColSpurVernierLUT_Cnt_s16[3][7]  T2_ColSpurVernierLUT_Cnt_s16[3][8]  9  T2_ColSpurVernierLUT_Cnt_s16[3][9]  6  T2_ColSpurVernierLUT_Cnt_s16[3][10]  3  T2_ColSpurVernierLUT_Cnt_s16[3][11]  16  T2_ColSpurVernierLUT_Cnt_s16[3][12]  13  T2_ColSpurVernierLUT_Cnt_s16[3][13]  T2_ColSpurVernierLUT_Cnt_s16[3][14]  T2_ColSpurVernierLUT_Cnt_s16[3][14]  T2_ColSpurVernierLUT_Cnt_s16[3][14]  T2_ColSpurVernierLUT_Cnt_s16[3][16]  T2_ColSpurVernierLUT_Cnt_s16[3][16]  T2_ColSpurVernierLUT_Cnt_s16[3][16]  T2_ColSpurVernierLUT_Cnt_s16[3][16]  T2_DualSpurVernierLUT_Cnt_s16[0][1]  -396  T2_DualSpurVernierLUT_Cnt_s16[0][1]  -324  T2_DualSpurVernierLUT_Cnt_s16[0][2]  -324	8	T2_ColSpurVernierLUT_Cnt_s16[2][12]
T2_ColSpurVernierLUT_Cnt_s16[2][16]	6	T2_ColSpurVernierLUT_Cnt_s16[2][13]
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T2_ColSpurVernierLUT_Cnt_s16[3][0]	2	T2_ColSpurVernierLUT_Cnt_s16[2][15]
T2_ColSpurVerniert.UT_Cnt_s16[3][1] 14  T2_ColSpurVerniert.UT_Cnt_s16[3][2] 111  T2_ColSpurVerniert.UT_Cnt_s16[3][3] 8  T2_ColSpurVerniert.UT_Cnt_s16[3][4] 5  T2_ColSpurVerniert.UT_Cnt_s16[3][5] 2  T2_ColSpurVerniert.UT_Cnt_s16[3][6] 15  T2_ColSpurVerniert.UT_Cnt_s16[3][7] 12  T2_ColSpurVerniert.UT_Cnt_s16[3][8] 9  T2_ColSpurVerniert.UT_Cnt_s16[3][8] 9  T2_ColSpurVerniert.UT_Cnt_s16[3][9] 6  T2_ColSpurVerniert.UT_Cnt_s16[3][10] 3  T2_ColSpurVerniert.UT_Cnt_s16[3][11] 16  T2_ColSpurVerniert.UT_Cnt_s16[3][11] 16  T2_ColSpurVerniert.UT_Cnt_s16[3][12] 13  T2_ColSpurVerniert.UT_Cnt_s16[3][14] 7  T2_ColSpurVerniert.UT_Cnt_s16[3][15] 4  T2_ColSpurVerniert.UT_Cnt_s16[3][16] 17  T2_DolSpurVerniert.UT_Cnt_s16[3][16] 17  T2_DolSpurVerniert.UT_Cnt_s16[0][0] -396  T2_DolSpurVernier.UT_Cnt_s16[0][1] -360  T2_DolSpurVernier.UT_Cnt_s16[0][1] -320  T2_DolSpurVernier.UT_Cnt_s16[0][1] -324  T2_DolSpurVernier.UT_Cnt_s16[0][1] -324  T2_DolSpurVernier.UT_Cnt_s16[0][1] -324  T2_DolSpurVernier.UT_Cnt_s16[0][1] -324  T2_DolSpurVernier.UT_Cnt_s16[0][1] -324  T2_DolSpurVernier.UT_Cnt_s16[0][1] -326	10	T2_ColSpurVernierLUT_Cnt_s16[2][16]
T2_ColSpurVernierLUT_Cnt_s16[3][2]	1	T2_ColSpurVernierLUT_Cnt_s16[3][0]
T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_ColSpurVernierLUT_Cnt_s16[0][1]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288	14	T2_ColSpurVernierLUT_Cnt_s16[3][1]
T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288	11	T2 ColSpurVernierLUT Cnt s16[3][2]
T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][16]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288		
T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288		
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288		
		T2_DualSpurVernierLUT_Cnt_s16[0][2]
	-288	T2_DualSpurVernierLUT_Cnt_s16[0][3]
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252	-252	T2_DualSpurVernierLUT_Cnt_s16[0][4]
T2_DualSpurVernierLUT_Cnt_s16[0][5] -216	-216	T2_DualSpurVernierLUT_Cnt_s16[0][5]
T2_DualSpurVernierLUT_Cnt_s16[0][6] -180	-180	
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144	-144	
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	-108	
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72	-72	
T2_DualSpurVernierLUT_Cnt_s16[0][10] -36		

2014-10-14, 17:31:16+0530



Input Value
0
36
72
108
144
180
216
252
288
324
360 9
0
1
2
3
4
5
6
7
8
9
0
1
2
3
4
5
6
7
8
9
0
0
1
2
3 4
5
6
7
8
9
10
0
1
2
3
4
5
6
7
8
9
10
22
2
4
6
8
10
12
14 16
16
18 20
1
1
3
3 5
3 5 7
3 5

DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16

DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

DiaColPs Per2

2014-10-14, 17:31:16+0530



DigCoiPs_Per2			CILLIO
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	99		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.58		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	830.176025	830.1759984 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V

-69.8239746

0

0

0

0

-69.82400159 ± 0.00009

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	116.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	17.9
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	17.9
DigColPs_TrimCompStatic_Cnt_M_u16	520
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs T3_CalSaud (arrival LIT_Cat_a48/0/0)	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0] T0_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
	9
T2_ColSpurVernierLUT_Cnt_s16[2][6]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11] T0_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_00/04/01 Verificit 0 1_0/1(_5 10[3][ 10]	lu .





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5]	-252 -216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	2 3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][5]	5
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T3_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][5] T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

2014-10-14, 17:31:16+0530





DigCoiPs_Per2			CILAB
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.29		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	818.811279	818.8112897 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
	_		

Param		0x0C 0x0C		~
Status		0x01 0x01		<b>✓</b>
Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	-
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>~</b>
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	<b>✓</b>
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>

0

0x6C

-81.8182373

0

0x6C

-81.81818182 ± 0.00009

Test Step 2.15 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	196
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	127
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0

Rte\_Call\_DigColPs\_Per2\_CP1\_CheckpointReached

Rte\_Call\_DigColPs\_Per2\_CP1\_CheckpointReached

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

NTC





DigCoiPs_Per2	
Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	276.8997883
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	19
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	625.0201091
DigColPs PrevVernierLevelNo Cnt M u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	5
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	19
DigColPs_TrimCompStatic_Deg_W_132	556
	8
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
Γ2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
Γ2_ColSpurVernierLUT_Cnt_s16[1][10]	0
Γ2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierEUT_Cnt_S16[1][11] T2_ColSpurVernierEUT_Cnt_S16[1][12]	3
T2_ColSpurVernierEUT_Cht_S16[1][12] T2_ColSpurVernierEUT_Cht_S16[1][13]	2
	1
[2_ColSpurVernierLUT_Cnt_s16[1][14]	
[2_ColSpurVernierLUT_Cnt_s16[1][15]	0
[2_ColSpurVernierLUT_Cnt_s16[1][16]	4
[2_ColSpurVernierLUT_Cnt_s16[2][0]	0
Γ2_ColSpurVernierLUT_Cnt_s16[2][1]	8
Γ2_ColSpurVernierLUT_Cnt_s16[2][2]	6
C2_ColSpurVernierLUT_Cnt_s16[2][3]	4
Γ2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
Γ2_ColSpurVernierLUT_Cnt_s16[2][6]	9
C2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
Γ2_ColSpurVernierLUT_Cnt_s16[2][10]	1
	10
[2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
Γ2_ColSpurVernierLUT_Cnt_s16[2][13]	6
Γ2_ColSpurVernierLUT_Cnt_s16[2][14]	4
Γ2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	14





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288 324
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2 DualSpurVernierLUT Cnt s16[1][4]	3
T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18] T3_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19] T2_DualSpurVernierLUT_Cnt_s16[1][20]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
	0
T2_DualSpurVernierLUT_Cnt_s16[2][10]	





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
	17		
T2_DualSpurVernierLUT_Cnt_s16[3][19] T3_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][20]			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	225		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	96		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	57.46032691		
k_VernOORangeThresh_Deg_f32	1341.97		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	276.8997883		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	232.8930412		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	g_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DisColDe 1901 hu/ColAngle For Trim Dog M 499		700 0577005 + 0 00040000405	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	720.957642	720.9577085 ± 0.00048828125	, and a second
DigColPs_I2CHwColAnglePol Him_Deg_M_i32  DigColPs_I2CHwTrimTransCnts_Uls_M_u08	720.957642 4	4	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08			•
	4	4	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08 DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	1	4	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32	4 1 720	4 1 720 ± 0.0001220703125	

DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	720.957642	720.9577085 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-179.042358	-179.0422915 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>✓</b>
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
	128
DigColPo. ColPorityError Cot M. Igo	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	137.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	258.0886749
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	12329
DigColPs_I2CHwSpurAngle_Deg_M_f32	20.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	30
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	87.60431278
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	20.1
DigColPs_TrimCompStatic_Cnt_M_u16	592
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
	-33
T2_ColSpurVernierLUT_Cnt_s16[0][4]	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4-
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 17:31:16+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][5] T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20] T0_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7		
T2_DualSpurVernierLUT_Cnt_s16[3][14] T3_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	254		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	47		
k_VernCorrErrorThroph_Dog_f32	12		
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	17.45087004 517.23		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	258.0886749		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.1939993		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1851		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
Disco-ID- 1001 by Colds als ForTries Does A4 600	400 000000	400 0000000 + 0 00040000405	

490.909088

5

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08

490.9090909 ± 0.00048828125

5





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	480.888672	480.8886749 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7	7	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-419.111328	-419.1113251 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

lame	Input Value
	124
ligColPsInt_GetCustData()	124
bigColPs_ColParityError_Cnt_M_lgc	163
igColPs_ColSensorFaultAcc_Cnt_M_u16	
igColPs_ColTrimStatic_Deg_M_f32	147.4
igColPs_HwAVernCorrFault_Cnt_M_lgc	1
igColPs_I2CColSensorFault_Cnt_M_Igc	0
igColPs_I2CHwColAngle_Cnt_M_u16	5894 248.0463682
igColPs_I2CHwColAngle_Deg_M_f32	248.0463682
igColPs_I2CHwDataType_Cnt_M_u08	· · · · · · · · · · · · · · · · · · ·
igColPs_I2CHwSpurAngle_Cnt_M_u16	64194
igColPs_I2CHwSpurAngle_Deg_M_f32	21.2
igColPs_I2CHwTrimTransCnts_Uls_M_u08	0
igColPs_I2CSensCommFlts_Cnt_M_u08	26
igColPs_I2CSpurSensorFault_Cnt_M_Igc	0
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
igColPs_PrevColPos_Deg_M_f32	1737.555742
igColPs_PrevVernierLevelNo_Cnt_M_u08	11
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
igColPs_SpurParityError_Cnt_M_lgc	1
gColPs_SpurSensorFaultAcc_Cnt_M_u16	124
igColPs_SpurTrimStatic_Deg_M_f32	21.2
igColPs_TrimCompStatic_Cnt_M_u16	628
igColPs_VernCorrDetectAcc_Cnt_M_u16	5
igColPs_VernierAngleOORange_Cnt_M_lgc	1
te_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5 6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2 DualSpurVernierLUT Cnt s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_S16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	111
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	48
k_VernCorrErrorDiag_Cnt_str.Threshold	75
k_VernCorrErrorDiag_Cnt_str.PStep	34
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	32.15087152
k_VernOORangeThresh_Deg_f32	1098.48
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	248.0463682



Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	112.600146		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	530		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	g_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	820.646362	820.6463682 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-79.3536377	-79.35363182 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x04	0x04	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)	<u>√</u>
Name	Input Value
DigColPsInt_GetCustData()	205
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	157.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	21646
DigColPs_I2CHwColAngle_Deg_M_f32	274.5377293
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	22.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1064.526832
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205
DigColPs_SpurTrimStatic_Deg_M_f32	22.3
DigColPs_TrimCompStatic_Cnt_M_u16	664
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0





32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
2 1 0 4 3 2 1 0 4 3
1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 2 1 0 4 3 2
4 3 2 1 0 4 3
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2 1 0 4 3 2
1 0 4 3 2
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-396
<b>-360</b>
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36

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Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216         T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       7         T2_DualSpurVernierLUT_Cnt_s16[1][6]       7         T2_DualSpurVernierLUT_Cnt_s16[1][6]       8	
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][17] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][0] 19  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7	
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  T2_DualSpurVernierLUT_Cnt_s16[0][19]  T2_DualSpurVernierLUT_Cnt_s16[0][20]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[1][0]  T2_DualSpurVernierLUT_Cnt_s16[1][1]  D0  T2_DualSpurVernierLUT_Cnt_s16[1][2]  T2_DualSpurVernierLUT_Cnt_s16[1][3]  T2_DualSpurVernierLUT_Cnt_s16[1][4]  T2_DualSpurVernierLUT_Cnt_s16[1][5]  T2_DualSpurVernierLUT_Cnt_s16[1][6]  T2_DualSpurVernierLUT_Cnt_s16[1][6]	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	
T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  0  T2_DualSpurVernierLUT_Cnt_s16[1][2]  1  T2_DualSpurVernierLUT_Cnt_s16[1][3]  22  T2_DualSpurVernierLUT_Cnt_s16[1][4]  33  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  7  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8	
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	
T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8	
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8	
T2_DualSpurVernierLUT_Cnt_s16[1][9] 8	
TO DO NO WILLIAM OF ACCUMANT	
T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T3_D	
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1	
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T2_DualSpurVernierLUT_Cnt_s16[1][20] 9	
T2_DualSpurVernierLUT_Cnt_s16[1][21] 0	
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T2_DualSpurVernierLUT_Cnt_s16[2][7] 7	
T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	
T2_DualSpurVernierLUT_Cnt_s16[2][9] 9	
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T2_DualSpurVernierLUT_Cnt_s16[2][11] 0	
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T2_DualSpurVernierLUT_Cnt_s16[2][13] 2	
T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T0_DualSpurVernierLUT_Cnt_s16[2][14] 4	
T2_DualSpurVernierLUT_Cnt_s16[2][15]  4  T3_DualSpurVernierLUT_Cnt_s16[2][15]  5	
T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6	
T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	
T2_DualSpurVernierLUT_Cnt_s16[2][19] 9	
T2_DualSpurVernierLUT_Cnt_s16[2][21] 10	
T2_DualSpurVernierLUT_Cnt_s16[3][0] 22	
T2_DualSpurVernierLUT_Cnt_s16[3][1] 2	
T2_DualSpurVernierLUT_Cnt_s16[3][2] 4	
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T2_DualSpurVernierLUT_Cnt_s16[3][4] 8	
T2_DualSpurVernierLUT_Cnt_s16[3][5] 10	
T2_DualSpurVernierLUT_Cnt_s16[3][6] 12	
T2_DualSpurVernierLUT_Cnt_s16[3][7] 14	
T2_DualSpurVernierLUT_Cnt_s16[3][8] 16	
T2_DualSpurVernierLUT_Cnt_s16[3][9] 18	
T2_DualSpurVernierLUT_Cnt_s16[3][10] 20	
T2_DualSpurVernierLUT_Cnt_s16[3][11] 1	
T2_DualSpurVernierLUT_Cnt_s16[3][12] 3	
T2_DualSpurVernierLUT_Cnt_s16[3][13] 5	
T2_DualSpurVernierLUT_Cnt_s16[3][14] 7	
T2_DualSpurVernierLUT_Cnt_s16[3][15] 9	
T2_DualSpurVernierLUT_Cnt_s16[3][16] 11	
T2_DualSpurVernierLUT_Cnt_s16[3][17] 13	

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	200		
k_SkipStepErrDiag_Cnt_str.PStep	24		
k_SkipStepErrDiag_Cnt_str.NStep	31		
k_VernCorrErrorDiag_Cnt_str.Threshold	95		
k_VernCorrErrorDiag_Cnt_str.PStep	48		
k_VernCorrErrorDiag_Cnt_str.NStep	10		
k_VernCorrErrorThresh_Deg_f32	47.7859745		
k_VernOORangeThresh_Deg_f32	674.82		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.5377293		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	86.89214289		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	655		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsI	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsI	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	476.937714	476.9377293 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>

Toot Ston Call Trans				
Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>~</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Pto Call DigColDs Per? CP1 CheckpointPeached	1	Pto Call DigColPs Par2 CP1 ChackpointPeached	1	-

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	210
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158
DigColPs_ColTrimStatic_Deg_M_f32	167.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	56914
DigColPs_I2CHwColAngle_Deg_M_f32	93.15782326
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	23.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	496.3249275
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	210





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	23.4
DigColPs_TrimCompStatic_Cnt_M_u16	700 0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][4]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-232 -216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2 DualSpurVernierLUT Cnt s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][13]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
	The state of the s
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10 12

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DigColPs\_Per2 Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[3][7] 14 T2\_DualSpurVernierLUT\_Cnt\_s16[3][8] 16 T2\_DualSpurVernierLUT\_Cnt\_s16[3][9] 18 T2\_DualSpurVernierLUT\_Cnt\_s16[3][10] 20 T2\_DualSpurVernierLUT\_Cnt\_s16[3][11] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[3][12] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[3][13] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[3][15] 11 T2\_DualSpurVernierLUT\_Cnt\_s16[3][16] T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21  $k\_SelectFromColumn\_Cnt\_lgc$ 1  $k\_SkipStepErrDiag\_Cnt\_str.Threshold$ 13 k\_SkipStepErrDiag\_Cnt\_str.PStep 33  $k\_SkipStepErrDiag\_Cnt\_str.NStep$ 14 82  $k\_VernCorrErrorDiag\_Cnt\_str.Threshold$  $k\_VernCorrErrorDiag\_Cnt\_str.PStep$ 9 k\_VernCorrErrorDiag\_Cnt\_str.NStep 46 90 42534328  $k\_VernCorrErrorThresh\_Deg\_f32$ k\_VernOORangeThresh\_Deg\_f32 855.99 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value 0 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 93.15782326 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 317.1723412  $tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16$ 1412 tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum tgt DigColPs Per2 MecState Cnt enum  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt Rte Inst Sa DigColPs.Pim DigColPsEOL tgt Pim DigColPsEOL

32 12 1212 311 2 311 1	3 3		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	645.357788	645.3578233 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-254.642212	-254.6421767 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	<b>✓</b>
Status	0x01	0x01	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 2.20 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	220
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	178
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0





Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	16977
DigColPs_I2CHwColAngle_Deg_M_f32	198.4525095
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43743
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	845.2340471
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	15
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	220
DigColPs_SpurTrimStatic_Deg_M_f32	24.5
DigColPs_TrimCompStatic_Cnt_M_u16	736
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2_ColSpurVernierLUT_Cnt_s16[0][1]	-163 -131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8] T2_ColSpurVernierLUT_Cnt_s16[1][9]	2
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	2
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	1 14
12_0010pui voimioi 201_0m_310[0][1]	· ·





<u> </u>	
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
	12
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cst_s46[3][7]	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	6
T2_ColSpurVernierLUT_Cnt_s16[3][9]	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
	180
T2_DualSpurVernierLUT_Cnt_s16[0][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][17] T0_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
	<del>-</del>
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	8 9
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	8 9 10
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	8 9 10 0
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	8 9 10





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
Γ2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2 DualSpurVernierLUT Cnt s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
<_SelectFromColumn_Cnt_lgc	1		
<pre>&lt;_SkipStepErrDiag_Cnt_str.Threshold</pre>	199		
k_SkipStepErrDiag_Cnt_str.PStep	6		
<pre>C_ONIPORPETIBLIST STEP </pre> <pre>C_SkipStepErrDiag_Cnt_str.NStep</pre>	36		
<pre>&lt;_OntpotepEnblag_on_ott.Notep </pre> <pre>&lt;_VernCorrErrorDiag_Cnt_str.Threshold</pre>	76		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
<pre>&lt;_VernCorrErrorDiag_Cnt_str.NStep</pre>	16		
<pre>&lt;_TomosizerorShag_oni_cantecop </pre> <pre>&lt;_VernCorrErrorThresh_Deg_f32</pre>	87.62320375		
<ul> <li>VernOORangeThresh Deg f32</li> </ul>	1774.591192		
gt_DigColPs_Per2_MecState_Cnt_enum.value	2		
gt_Pim_DigColPsEOL.ColTrim_Deg_f32	198.4525095		
gt_Pim_DigColPsEOL.SpurTrim_Deg_f32	46.80067945		
gt Pim DigColPsEOL.TrimComp Cnt u16	4447		
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos\	Valid Cnt Inc	
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_		
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_		
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	<u>-</u> '9°	
Name	Actual Value	Expected Value	Door
		Expected Value	Res
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1460.45251	1460.45251 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColDs VernCorrDetectAcc Cnt M u16	1.4	14	

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	<b>✓</b>
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1460.45251	1460.45251 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	560.452515	560.4525095 ± 0.0009	~
tat DigColPs Per2 TrimComp Cnt lac.value	0	0	<b>✓</b>



Test Step Call Trace	est Step Call Trace			✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	214
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
	188.2
DigColPs_ColTrimStatic_Deg_M_f32	1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	
DigColPs_I2CHwColAngle_Cnt_M_u16	17009 183.5
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	9502
DigColPs_I2CHwSpurAngle_Deg_M_f32	25.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	601.2839711
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	214
DigColPs_SpurTrimStatic_Deg_M_f32	25.6
DigColPs_TrimCompStatic_Cnt_M_u16	772
DigColPs_VernCorrDetectAcc_Cnt_M_u16	9
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 17:31:16+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11] T0_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][12]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	191		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	43		
k_VernCorrErrorDiag_Cnt_str.Threshold	47		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	8		
k_VernCorrErrorThresh_Deg_f32	39.43172193		
k_VernOORangeThresh_Deg_f32	1755.401681		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	183.5		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	205.6963653		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2712	lan.	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	102	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL		
tgt_rte_mst_sa_bigcoiPs.Piiii_bigcoiPsEOL  Name	Actual Value	Expected Value	Popult
	1	1	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	4000 00050	1000 000000 + 0 000 40000405	

1636.36353

3

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08

1636.363636 ± 0.00048828125

3





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1795.30005	1795.3 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>~</b>

laura.	Innut Value
lame	Input Value
DigColPsInt_GetCustData()	
igColPs_ColParityError_Cnt_M_lgc	0
igColPs_ColSensorFaultAcc_Cnt_M_u16	144
igColPs_ColTrimStatic_Deg_M_f32	198.4
igColPs_HwAVernCorrFault_Cnt_M_lgc	0
igColPs_I2CColSensorFault_Cnt_M_Igc	0
igColPs_I2CHwColAngle_Cnt_M_u16	11710 204.045151
igColPs_I2CHwColAngle_Deg_M_f32	
igColPs_I2CHwDataType_Cnt_M_u08	1
igColPs_I2CHwSpurAngle_Cnt_M_u16	16894
igColPs_I2CHwSpurAngle_Deg_M_f32	26.7
igColPs_I2CHwTrimTransCnts_Uls_M_u08	5
igColPs_I2CSensCommFlts_Cnt_M_u08	8
igColPs_I2CSpurSensorFault_Cnt_M_lgc	0
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
igColPs_PrevColPos_Deg_M_f32	1513.739851
igColPs_PrevVernierLevelNo_Cnt_M_u08	11
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	10
igColPs_SpurParityError_Cnt_M_lgc	0
gColPs_SpurSensorFaultAcc_Cnt_M_u16	152
igColPs_SpurTrimStatic_Deg_M_f32	26.7
igColPs_TrimCompStatic_Cnt_M_u16	808
igColPs_VernCorrDetectAcc_Cnt_M_u16	11
igColPs_VernierAngleOORange_Cnt_M_lgc	1
te_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T3_DualSpurVernierLUT_Cnt_s16[1][20]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22 2
T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	27
k_SkipStepErrDiag_Cnt_str.PStep	20
k_SkipStepErrDiag_Cnt_str.NStep	4
k_VernCorrErrorDiag_Cnt_str.Threshold	15
k_VernCorrErrorDiag_Cnt_str.PStep	49
k_VernCorrErrorThreeh_Dog_f32	10 97.54986858
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	97.54986858 1151.320251
k_vernOORange1nresn_Deg_r32 tgt_DigCoIPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	204.045151
.g.,sigooii ococ.comiii_bog_ioc	25 5 . 5 . 5 .



Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	219.1047057		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	730.725098	730.7251338 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	27	27	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-169.274902	-169.2748662 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	•
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0E	0x0E	•
Status	0x01	0x01	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

Test Step 2.23 (Repeat Count = 1)	<b>▼</b>
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	208.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22738
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1851
DigColPs_I2CHwSpurAngle_Deg_M_f32	27.8
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1273.742756
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	27.8
DigColPs_TrimCompStatic_Cnt_M_u16	844
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0





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130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
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261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
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327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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<b>-360</b>
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-288
-252
-216
-180
-144
-108
-72
-12 -36

DigColPs\_Per2

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2 DualSpurVernierLUT Cnt s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	9
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2 DualSpurVernierLUT Cnt s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_S10[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpur/craied_LT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	14 16
T2_DualSpurVernierLUT_Cnt_s16[3][8] T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

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DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16
DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc

 $tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value$ 

tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

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DigColPs\_Per2 Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21 k SelectFromColumn\_Cnt\_lgc 0 k\_SkipStepErrDiag\_Cnt\_str.Threshold 150 k\_SkipStepErrDiag\_Cnt\_str.PStep 8  $k\_SkipStepErrDiag\_Cnt\_str.NStep$ 29 k\_VernCorrErrorDiag\_Cnt\_str.Threshold 35  $k\_VernCorrErrorDiag\_Cnt\_str.PStep$ 37 k\_VernCorrErrorDiag\_Cnt\_str.NStep 6 51.31432509 k\_VernCorrErrorThresh\_Deg\_f32 k\_VernOORangeThresh\_Deg\_f32 1014.951933 tgt DigColPs Per2 MecState Cnt enum.value 2 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 -180 220.2809907 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32  $tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16$ tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32$  $tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32$ tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt\_Pim\_DigColPsEOL  $tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL$ **Actual Value Expected Value**  ${\tt DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc}$ DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32 894.326782 894.3268224 ± 0.00048828125 DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 5 DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc DigColPs\_PrevColPos\_Deg\_M\_f32 900 900 ± 0.0001220703125  ${\tt DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08}$ 9 9 DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08 1 1 DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16 1 1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	-

-5.67321777

0

0

0

0

-5.673217573 ± 0.000009

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	30
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	28.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1.
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLOT_Cnt_s16[2][11]	10
T2_ColSpurVernierLOT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLOT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLOT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2 ColSpurVernierLUT Cnt s16[2][14]	2
T2_ColSpurVernierLUT_Cnt_S16[2][15] T2_ColSpurVernierLUT_Cnt_S16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	1 14
T2_ColSpurVernierLUT_Cnt_s16[3][1]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2]	8
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
F2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
F2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17





	l
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7]	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2 DualSpurVernierLUT Cnt s16[0][18]	252
T2 DualSpurVernierLUT Cnt s16[0][19]	288
	324
T2_DualSpurVernierLUT_Cnt_s16[0][20]	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[2][3]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2 DualSpurVernierLUT Cnt s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
	8
T2_DualSpurVernierLUT_Cnt_s16[3][4]	0
T2_DualSpurVernierLUT_Cnt_s16[3][4] T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10 12

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DigColPs_Per2			CILAI	
Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	0			
K_SkipStepErrDiag_Cnt_str.Threshold	16			
k_SkipStepErrDiag_Cnt_str.PStep	4			
k_SkipStepErrDiag_Cnt_str.NStep	47			
k_VernCorrErrorDiag_Cnt_str.Threshold	98			
k_VernCorrErrorDiag_Cnt_str.PStep	3			
<pre>&lt;_VernCorrErrorDiag_Cnt_str.NStep</pre>	2			
k VernCorrErrorThresh Deg f32	99.41426611			
k VernOORangeThresh Deg f32	359.5822154			
tgt DigCoIPs Per2 MecState Cnt enum.value	1			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360			
tgt Pim DigColPsEOL.SpurTrim Deg f32	250.4857173			
gt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid Cnt Igc		
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt DigColPs Per2 I2CHwAbsPos			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cr			
tgt Rte Inst Sa DigColPs.Pim DigColPsEOL	tgt Pim DigColPsEOL			
Name	Actual Value	Expected Value	Resul	
DigColPs HwAVernCorrFault Cnt M Igc	1	1		
DigColPs I2CHwColAngleForTrim Deg M f32	490.909088	490.9090909 ± 0.00048828125		
DigColPs I2CHwTrimTransCnts UIs M u08	0	0		
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0		
DigColPs PrevColPos Deg M f32	501.200012	501.2 ± 0.0001220703125		
DigColPs PrevVernierLevelNo Cnt M u08	6	6	•	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	-	

32 12 1212 311 1 2 311 1	10 011		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	501.200012	501.2 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	✓
Param	0x04	0x04	✓
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	-
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Test Step 2.25 (Repeat Count = 1) ✓		
Name	Input Value	
DigColPsInt_GetCustData()	50	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131	
DigColPs_ColTrimStatic_Deg_M_f32	229	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	





Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	48650
DigColPs_I2CHwColAngle_Deg_M_f32	126.5327979
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51339
DigColPs_I2CHwSpurAngle_Deg_M_f32	30
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	373.8183561
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50 30
DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16	916
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5] T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2 ColSpurVernierLUT Cnt s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T3_ColSpurVernierLUT_Cnt_s18[2][14]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	2
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14

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Name  T2_ColSpurVernierLUT_Cnt_s16[3][2]  T2_ColSpurVernierLUT_Cnt_s16[3][3]  T2_ColSpurVernierLUT_Cnt_s16[3][4]	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	11
T2_ColSpurVernierLUT_Cnt_s16[3][4]	
	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2 ColSpurVernierLUT Cnt s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
	3
T2_ColSpurVernierLUT_Cnt_s16[3][10]	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
	180
T2_DualSpurVernierLUT_Cnt_s16[0][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2 DualSpurVernierLUT Cnt s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[1][20]	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[2][9]	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2 DualSpurVernierLUT Cnt s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	123		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k SkipStepErrDiag Cnt str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	64		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	45.68451142		
k VernOORangeThresh Deg f32	735.0528789		
tgt DigColPs Per2 MecState Cnt enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	126.5327979		
tgt Pim DigColPsEOL.SpurTrim Deg f32	306.8582928		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt_DigColPs_reiz_izchwAbsrbs_nwbeg_isz		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lg		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
	Actual Value	Expected Value	Result
Name	Actual value	Expected value	Resul

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	365.064392	365.0644124 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	3	3	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	<b>✓</b>
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0v01	0v01	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	-

Test Step 2.26 (Repeat Count = 1)	en e
Name	Input Value
DigColPsInt_GetCustData()	30
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigCoIPs I2CHwDataType Cnt M u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	28.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs SkipStepFltDetectAcc Cnt M u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30
DigCoIPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
	-66
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-00 -33
T2_ColSpurVernierLUT_Cnt_s16[0][4]	0
T2_ColSpurVernierLUT_Cnt_s16[0][5]	32
T2_ColSpurVernierLUT_Cnt_s16[0][6] T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
	98
T2_ColSpurVernierLUT_Cnt_s16[0][8]	130
T2_ColSpurVernierLUT_Cnt_s16[0][9]	163
T2_ColSpurVernierLUT_Cnt_s16[0][10]	
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2 ColSpurVernierLUT Cnt s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T3_DualSpurVernierLUT_Cnt_s16[1][2]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-74.24		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_132	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	1-	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~

490.909088

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

490.9090909 ± 0.00048828125

DigColPs\_Per2

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**Actual Value Expected Value** DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 0 0 DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc 0 0 DigColPs\_PrevColPos\_Deg\_M\_f32 501.2 ± 0.0001220703125 501.200012 DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08 6 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08 3 3 DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16 1 1 DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16 1 1 DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc 1 1 tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value -409.090912 -409.0909091 ± 0.0009 tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value 0 NTC 0x6C 0x6C Param 0x04 0x04 0x01 Status 0x01

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.27 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
DigColPsInt GetCustData()	152
DigColPs ColParityError Cnt M Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs ColTrimStatic Deg M f32	208.6
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22738
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1851
DigColPs_I2CHwSpurAngle_Deg_M_f32	27.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	O O
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1273.742756
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	27.8
DigColPs_TrimCompStatic_Cnt_M_u16	844
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
	-30
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2 3
T2_DualSpurVernierLUT_Cnt_s16[1][14] T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3] T3_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8
T2_DualSpurVernierLUT_Cnt_s16[3][4]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	150
k_SkipStepErrDiag_Cnt_str.PStep	8
k_SkipStepErrDiag_Cnt_str.NStep	29 35
k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep	37



Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	51.31432509		
k_VernOORangeThresh_Deg_f32	1014.951933		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220.2809907		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

tgt_Rte_inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_PIM_DIGColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	730.69043	730.6904588 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	-
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-169.30957	-169.3095412 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.28 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt GetCustData()	101
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	100
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.8777566
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	31.1
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T3_ColSpurVernierLUT_Cnt_s46[4][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][0]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s46[3][40]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
b	I .

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Name  T2_DualSpurVernierLUT_Cnt_s16[0][10]  T2_DualSpurVernierLUT_Cnt_s16[0][11]  T2_DualSpurVernierLUT_Cnt_s16[0][12]	Input Value -36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	-30
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2 DualSpurVernierLUT Cnt s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
	22
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
	11

 $DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08$ DigColPs SkipStepFltDetectAcc Cnt M u16

DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16

DigColPs VernierAngleOORange Cnt M Igc

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

DigColPs\_Per2

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Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21 k\_SelectFromColumn\_Cnt\_lgc 1 k\_SkipStepErrDiag\_Cnt\_str.Threshold 175  $k\_SkipStepErrDiag\_Cnt\_str.PStep$ 12 k\_SkipStepErrDiag\_Cnt\_str.NStep 41 48  $k\_VernCorrErrorDiag\_Cnt\_str.Threshold$ k\_VernCorrErrorDiag\_Cnt\_str.PStep 12 k\_VernCorrErrorDiag\_Cnt\_str.NStep 3 k\_VernCorrErrorThresh\_Deg\_f32 78.9135704 k\_VernOORangeThresh\_Deg\_f32 1722 743855 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value 2 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 350.8777566  $tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32$ -180 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum$ tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_Igc tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt\_Pim\_DigColPsEOL  $tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL$ Actual Value **Expected Value** Name Result DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc 1077.77271 1077.772727 ± 0.00048828125  ${\tt DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32}$ DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc 0 0 DigColPs\_PrevColPos\_Deg\_M\_f32 1080  $1080 \pm 0.0001220703125$ DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08 11

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>✓</b>

1

0

n

1

180

11

1

1

0

n

1

180 ± 0.0009

Test Step 2.29 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	144	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	182	
DigColPs_ColTrimStatic_Deg_M_f32	249.4	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	18257	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	21803	
DigColPs_I2CHwSpurAngle_Deg_M_f32	32.2	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	845.517553	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	15	
DigColPs_SpurParityError_Cnt_M_lgc	1	





	144 32.2 988 4 0 tgt_Rte_Inst_Sa_DigColPs -163 -131 -99 -66 -33 0 32 -65 98 130 1163 -196 -229 -261 -294 -327 -359 -0 -0 -4 -3 -2 -1 -0 -0 -4 -3 -3 -2 -1 -0 -0 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1
	988 4 0 1tgt_Rte_Inst_Sa_DigColPs -163 -131 -99 -66 -33 0 0 32 -65 98 130 -163 -196 -229 -261 -294 -327 -359 0 4 3 2 2 1 0 0 4 3 2 2 1 0 0
	4 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
	tgt_Rte_Inst_Sa_DigColPs -163 -131 -99 -66 -33 0 32 -65 98 130 163 196 229 261 294 337 3359 0 4 4 3 2 1 1 0 4 3 2 1 1 0 0
te_Inst_Sa_DigColPs 2_ColSpurVernierLUT_Cnt_s16[0][0] 2_ColSpurVernierLUT_Cnt_s16[0][1] 2_ColSpurVernierLUT_Cnt_s16[0][2] 2_ColSpurVernierLUT_Cnt_s16[0][2] 2_ColSpurVernierLUT_Cnt_s16[0][3] 2_ColSpurVernierLUT_Cnt_s16[0][4] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][7] 2_ColSpurVernierLUT_Cnt_s16[0][8] 2_ColSpurVernierLUT_Cnt_s16[0][9] 2_ColSpurVernierLUT_Cnt_s16[0][10] 2_ColSpurVernierLUT_Cnt_s16[0][11] 2_ColSpurVernierLUT_Cnt_s16[0][11] 2_ColSpurVernierLUT_Cnt_s16[0][12] 2_ColSpurVernierLUT_Cnt_s16[0][13] 2_ColSpurVernierLUT_Cnt_s16[0][14] 2_ColSpurVernierLUT_Cnt_s16[0][15] 2_ColSpurVernierLUT_Cnt_s16[0][16] 2_ColSpurVernierLUT_Cnt_s16[1][0] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	tgt_Rte_Inst_Sa_DigColPs -163 -131 -99 -66 -33 0 32 65 98 130 163 196 229 261 294 337 3359 0 4 4 3 2 1 0 0 4 3 3 2 1 1 0 0
2_ColSpurVernierLUT_Cnt_s16[0][0] 2_ColSpurVernierLUT_Cnt_s16[0][1] 2_ColSpurVernierLUT_Cnt_s16[0][2] 2_ColSpurVernierLUT_Cnt_s16[0][3] 2_ColSpurVernierLUT_Cnt_s16[0][3] 2_ColSpurVernierLUT_Cnt_s16[0][5] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][7] 2_ColSpurVernierLUT_Cnt_s16[0][8] 2_ColSpurVernierLUT_Cnt_s16[0][9] 2_ColSpurVernierLUT_Cnt_s16[0][10] 2_ColSpurVernierLUT_Cnt_s16[0][11] 2_ColSpurVernierLUT_Cnt_s16[0][12] 2_ColSpurVernierLUT_Cnt_s16[0][13] 2_ColSpurVernierLUT_Cnt_s16[0][14] 2_ColSpurVernierLUT_Cnt_s16[0][15] 2_ColSpurVernierLUT_Cnt_s16[0][16] 2_ColSpurVernierLUT_Cnt_s16[0][16] 2_ColSpurVernierLUT_Cnt_s16[1][0] 2_ColSpurVernierLUT_Cnt_s16[1][0] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][1	-163 -131 -99 -66 -33 0 32 65 98 130 163 196 229 261 294 327 3359 0 4 3 3 2 1 0 0 4 3 3 2 1 0 0 4 3 2
2_ColSpurVernierLUT_Cnt_s16[0][1] 2_ColSpurVernierLUT_Cnt_s16[0][2] 2_ColSpurVernierLUT_Cnt_s16[0][3] 2_ColSpurVernierLUT_Cnt_s16[0][4] 2_ColSpurVernierLUT_Cnt_s16[0][5] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][7] 2_ColSpurVernierLUT_Cnt_s16[0][7] 2_ColSpurVernierLUT_Cnt_s16[0][8] 2_ColSpurVernierLUT_Cnt_s16[0][8] 2_ColSpurVernierLUT_Cnt_s16[0][9] 2_ColSpurVernierLUT_Cnt_s16[0][10] 2_ColSpurVernierLUT_Cnt_s16[0][11] 2_ColSpurVernierLUT_Cnt_s16[0][11] 2_ColSpurVernierLUT_Cnt_s16[0][12] 2_ColSpurVernierLUT_Cnt_s16[0][13] 2_ColSpurVernierLUT_Cnt_s16[0][14] 2_ColSpurVernierLUT_Cnt_s16[0][16] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[1][2] 2_ColSpurVernierLUT_Cnt_s16[1][3] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6]	-131 -99 -66 -33 0 32 -65 98 130 163 196 229 261 294 3327 3359 0 4 3 2 1 0 0 4 3 2 1 0 0
2_ColSpurVernierLUT_Cnt_s16[0][2] 2_ColSpurVernierLUT_Cnt_s16[0][3] 2_ColSpurVernierLUT_Cnt_s16[0][3] 2_ColSpurVernierLUT_Cnt_s16[0][5] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][6] 2_ColSpurVernierLUT_Cnt_s16[0][8] 2_ColSpurVernierLUT_Cnt_s16[0][9] 2_ColSpurVernierLUT_Cnt_s16[0][9] 2_ColSpurVernierLUT_Cnt_s16[0][10] 2_ColSpurVernierLUT_Cnt_s16[0][11] 2_ColSpurVernierLUT_Cnt_s16[0][12] 2_ColSpurVernierLUT_Cnt_s16[0][12] 2_ColSpurVernierLUT_Cnt_s16[0][13] 2_ColSpurVernierLUT_Cnt_s16[0][14] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][17] 2_ColSpurVernierLUT_Cnt_s16[1][18] 2_ColSpurVernierLUT_Cnt_s16[1][18] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][19] 2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][6]	999 -666 -33 0 32 65 98 130 163 196 229 261 294 3327 3359 0 4 3 2 1 0 4 3 2 1 0 0
2 ColSpurVernierLUT_Cnt_s16[0][3] 2 ColSpurVernierLUT_Cnt_s16[0][4] 2 ColSpurVernierLUT_Cnt_s16[0][5] 2 ColSpurVernierLUT_Cnt_s16[0][6] 2 ColSpurVernierLUT_Cnt_s16[0][7] 2 ColSpurVernierLUT_Cnt_s16[0][8] 2 ColSpurVernierLUT_Cnt_s16[0][9] 2 ColSpurVernierLUT_Cnt_s16[0][9] 2 ColSpurVernierLUT_Cnt_s16[0][10] 2 ColSpurVernierLUT_Cnt_s16[0][11] 2 ColSpurVernierLUT_Cnt_s16[0][11] 2 ColSpurVernierLUT_Cnt_s16[0][12] 2 ColSpurVernierLUT_Cnt_s16[0][14] 2 ColSpurVernierLUT_Cnt_s16[0][14] 2 ColSpurVernierLUT_Cnt_s16[0][16] 2 ColSpurVernierLUT_Cnt_s16[0][16] 2 ColSpurVernierLUT_Cnt_s16[0][16] 2 ColSpurVernierLUT_Cnt_s16[1][0] 2 ColSpurVernierLUT_Cnt_s16[1][1] 2 ColSpurVernierLUT_Cnt_s16[1][3] 2 ColSpurVernierLUT_Cnt_s16[1][6] 2 ColSpurVernierLUT_Cnt_s16[1][1] 2 ColSpurVernierLUT_Cnt_s16[2][0] 2 ColSpurVernierLUT_Cnt_s16[2][0] 2 ColSpurVernierLUT_Cnt_s16[2][6]	-66 -33 0 32 65 98 130 163 196 229 261 294 327 3359 0 4 3 3 2 1 0 0 4 3 2 1 0 0
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2_ColSpurVernierLUT_Cnt_s16[0][13] 2_ColSpurVernierLUT_Cnt_s16[0][14] 2_ColSpurVernierLUT_Cnt_s16[0][15] 2_ColSpurVernierLUT_Cnt_s16[0][16] 2_ColSpurVernierLUT_Cnt_s16[1][0] 2_ColSpurVernierLUT_Cnt_s16[1][0] 2_ColSpurVernierLUT_Cnt_s16[1][1] 2_ColSpurVernierLUT_Cnt_s16[1][2] 2_ColSpurVernierLUT_Cnt_s16[1][3] 2_ColSpurVernierLUT_Cnt_s16[1][4] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][6] 2_ColSpurVernierLUT_Cnt_s16[1][7] 2_ColSpurVernierLUT_Cnt_s16[1][8] 2_ColSpurVernierLUT_Cnt_s16[1][9] 2_ColSpurVernierLUT_Cnt_s16[1][10] 2_ColSpurVernierLUT_Cnt_s16[1][10] 2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][12] 2_ColSpurVernierLUT_Cnt_s16[1][13] 2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][6]	294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0
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2 ColSpurVernierLUT_Cnt_s16[0][16] 2 ColSpurVernierLUT_Cnt_s16[1][0] 2 ColSpurVernierLUT_Cnt_s16[1][1] 2 ColSpurVernierLUT_Cnt_s16[1][1] 2 ColSpurVernierLUT_Cnt_s16[1][2] 2 ColSpurVernierLUT_Cnt_s16[1][3] 2 ColSpurVernierLUT_Cnt_s16[1][4] 2 ColSpurVernierLUT_Cnt_s16[1][6] 2 ColSpurVernierLUT_Cnt_s16[1][6] 2 ColSpurVernierLUT_Cnt_s16[1][7] 2 ColSpurVernierLUT_Cnt_s16[1][7] 2 ColSpurVernierLUT_Cnt_s16[1][8] 2 ColSpurVernierLUT_Cnt_s16[1][9] 2 ColSpurVernierLUT_Cnt_s16[1][10] 2 ColSpurVernierLUT_Cnt_s16[1][11] 2 ColSpurVernierLUT_Cnt_s16[1][11] 2 ColSpurVernierLUT_Cnt_s16[1][12] 2 ColSpurVernierLUT_Cnt_s16[1][14] 2 ColSpurVernierLUT_Cnt_s16[1][14] 2 ColSpurVernierLUT_Cnt_s16[1][16] 2 ColSpurVernierLUT_Cnt_s16[2][0] 2 ColSpurVernierLUT_Cnt_s16[2][1] 2 ColSpurVernierLUT_Cnt_s16[2][3] 2 ColSpurVernierLUT_Cnt_s16[2][4] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][7] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][8]	359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 0
2 ColSpurVernierLUT_Cnt_s16[1][0] 2 ColSpurVernierLUT_Cnt_s16[1][1] 2 ColSpurVernierLUT_Cnt_s16[1][2] 2 ColSpurVernierLUT_Cnt_s16[1][3] 2 ColSpurVernierLUT_Cnt_s16[1][4] 2 ColSpurVernierLUT_Cnt_s16[1][4] 2 ColSpurVernierLUT_Cnt_s16[1][5] 2 ColSpurVernierLUT_Cnt_s16[1][6] 2 ColSpurVernierLUT_Cnt_s16[1][7] 2 ColSpurVernierLUT_Cnt_s16[1][8] 2 ColSpurVernierLUT_Cnt_s16[1][9] 2 ColSpurVernierLUT_Cnt_s16[1][10] 2 ColSpurVernierLUT_Cnt_s16[1][10] 2 ColSpurVernierLUT_Cnt_s16[1][11] 2 ColSpurVernierLUT_Cnt_s16[1][11] 2 ColSpurVernierLUT_Cnt_s16[1][12] 2 ColSpurVernierLUT_Cnt_s16[1][14] 2 ColSpurVernierLUT_Cnt_s16[1][15] 2 ColSpurVernierLUT_Cnt_s16[1][16] 2 ColSpurVernierLUT_Cnt_s16[2][0] 2 ColSpurVernierLUT_Cnt_s16[2][1] 2 ColSpurVernierLUT_Cnt_s16[2][3] 2 ColSpurVernierLUT_Cnt_s16[2][4] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][8]	0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0
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2 ColSpurVernierLUT_Cnt_s16[1][6]  2 ColSpurVernierLUT_Cnt_s16[1][7]  2 ColSpurVernierLUT_Cnt_s16[1][8]  2 ColSpurVernierLUT_Cnt_s16[1][8]  2 ColSpurVernierLUT_Cnt_s16[1][9]  2 ColSpurVernierLUT_Cnt_s16[1][10]  2 ColSpurVernierLUT_Cnt_s16[1][11]  2 ColSpurVernierLUT_Cnt_s16[1][12]  2 ColSpurVernierLUT_Cnt_s16[1][13]  2 ColSpurVernierLUT_Cnt_s16[1][14]  2 ColSpurVernierLUT_Cnt_s16[1][16]  2 ColSpurVernierLUT_Cnt_s16[1][16]  2 ColSpurVernierLUT_Cnt_s16[2][0]  2 ColSpurVernierLUT_Cnt_s16[2][1]  2 ColSpurVernierLUT_Cnt_s16[2][2]  2 ColSpurVernierLUT_Cnt_s16[2][3]  2 ColSpurVernierLUT_Cnt_s16[2][4]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][7]  2 ColSpurVernierLUT_Cnt_s16[2][7]  2 ColSpurVernierLUT_Cnt_s16[2][8]	4 3 2 1 0
2 ColSpurVernierLUT_Cnt_s16[1][7]  2 ColSpurVernierLUT_Cnt_s16[1][8]  2 ColSpurVernierLUT_Cnt_s16[1][9]  2 ColSpurVernierLUT_Cnt_s16[1][10]  2 ColSpurVernierLUT_Cnt_s16[1][10]  2 ColSpurVernierLUT_Cnt_s16[1][11]  2 ColSpurVernierLUT_Cnt_s16[1][12]  2 ColSpurVernierLUT_Cnt_s16[1][14]  2 ColSpurVernierLUT_Cnt_s16[1][15]  2 ColSpurVernierLUT_Cnt_s16[1][16]  2 ColSpurVernierLUT_Cnt_s16[2][0]  2 ColSpurVernierLUT_Cnt_s16[2][1]  2 ColSpurVernierLUT_Cnt_s16[2][2]  2 ColSpurVernierLUT_Cnt_s16[2][3]  2 ColSpurVernierLUT_Cnt_s16[2][4]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][7]  2 ColSpurVernierLUT_Cnt_s16[2][7]  2 ColSpurVernierLUT_Cnt_s16[2][8]	3 2 1 0
2 ColSpurVernierLUT_Cnt_s16[1][8]  2 ColSpurVernierLUT_Cnt_s16[1][9]  2 ColSpurVernierLUT_Cnt_s16[1][10]  2 ColSpurVernierLUT_Cnt_s16[1][11]  2 ColSpurVernierLUT_Cnt_s16[1][11]  2 ColSpurVernierLUT_Cnt_s16[1][12]  2 ColSpurVernierLUT_Cnt_s16[1][13]  2 ColSpurVernierLUT_Cnt_s16[1][14]  2 ColSpurVernierLUT_Cnt_s16[1][15]  2 ColSpurVernierLUT_Cnt_s16[1][16]  2 ColSpurVernierLUT_Cnt_s16[2][0]  2 ColSpurVernierLUT_Cnt_s16[2][1]  2 ColSpurVernierLUT_Cnt_s16[2][2]  2 ColSpurVernierLUT_Cnt_s16[2][3]  2 ColSpurVernierLUT_Cnt_s16[2][4]  2 ColSpurVernierLUT_Cnt_s16[2][5]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][6]  2 ColSpurVernierLUT_Cnt_s16[2][7]  2 ColSpurVernierLUT_Cnt_s16[2][8]	2 1 0
2 ColSpurVernierLUT_Cnt_s16[1][9] 2 ColSpurVernierLUT_Cnt_s16[1][10] 2 ColSpurVernierLUT_Cnt_s16[1][11] 2 ColSpurVernierLUT_Cnt_s16[1][11] 2 ColSpurVernierLUT_Cnt_s16[1][12] 2 ColSpurVernierLUT_Cnt_s16[1][13] 2 ColSpurVernierLUT_Cnt_s16[1][14] 2 ColSpurVernierLUT_Cnt_s16[1][15] 2 ColSpurVernierLUT_Cnt_s16[1][16] 2 ColSpurVernierLUT_Cnt_s16[2][0] 2 ColSpurVernierLUT_Cnt_s16[2][1] 2 ColSpurVernierLUT_Cnt_s16[2][2] 2 ColSpurVernierLUT_Cnt_s16[2][3] 2 ColSpurVernierLUT_Cnt_s16[2][4] 2 ColSpurVernierLUT_Cnt_s16[2][5] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][6] 2 ColSpurVernierLUT_Cnt_s16[2][7] 2 ColSpurVernierLUT_Cnt_s16[2][8]	1 0
2_ColSpurVernierLUT_Cnt_s16[1][10] 2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][12] 2_ColSpurVernierLUT_Cnt_s16[1][13] 2_ColSpurVernierLUT_Cnt_s16[1][14] 2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	0
2_ColSpurVernierLUT_Cnt_s16[1][11] 2_ColSpurVernierLUT_Cnt_s16[1][12] 2_ColSpurVernierLUT_Cnt_s16[1][13] 2_ColSpurVernierLUT_Cnt_s16[1][14] 2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	
2_ColSpurVernierLUT_Cnt_s16[1][12] 2_ColSpurVernierLUT_Cnt_s16[1][13] 2_ColSpurVernierLUT_Cnt_s16[1][14] 2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	
2_ColSpurVernierLUT_Cnt_s16[1][13] 2_ColSpurVernierLUT_Cnt_s16[1][14] 2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	4
2_ColSpurVernierLUT_Cnt_s16[1][14] 2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	3
2_ColSpurVernierLUT_Cnt_s16[1][15] 2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][16] 2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	1
2_ColSpurVernierLUT_Cnt_s16[2][0] 2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	0
2_ColSpurVernierLUT_Cnt_s16[2][1] 2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	4
2_ColSpurVernierLUT_Cnt_s16[2][2] 2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	0
2_ColSpurVernierLUT_Cnt_s16[2][3] 2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	8
2_ColSpurVernierLUT_Cnt_s16[2][4] 2_ColSpurVernierLUT_Cnt_s16[2][5] 2_ColSpurVernierLUT_Cnt_s16[2][6] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	6
2_ColSpurVernierLUT_Cnt_s16[2][5] ( 2_ColSpurVernierLUT_Cnt_s16[2][6] ( 2_ColSpurVernierLUT_Cnt_s16[2][7] ( 2_ColSpurVernierLUT_Cnt_s16[2][8] ( 3_ColSpurVernierLUT_Cnt_s16[2][8]	4
2_ColSpurVernierLUT_Cnt_s16[2][6]       9         2_ColSpurVernierLUT_Cnt_s16[2][7]       2_ColSpurVernierLUT_Cnt_s16[2][8]         2_ColSpurVernierLUT_Cnt_s16[2][8]       9	2
2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	0
2_ColSpurVernierLUT_Cnt_s16[2][7] 2_ColSpurVernierLUT_Cnt_s16[2][8]	9
2_ColSpurVernierLUT_Cnt_s16[2][8]	7
	5
2 ColSpurVernierLUT Cnt s16[2][9]	3
	1
	10
	8
	6
	o 4
	2
	10
_ , , ,	1
	14
	11
	8
	5
	2
	15
	12
	9
2_ColSpurVernierLUT_Cnt_s16[3][9]	6
2_ColSpurVernierLUT_Cnt_s16[3][10]	
	3
	3 16
	16
2_ColSpurVernierLUT_Cnt_s16[3][15]	16 13

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duanopui veitiieleu i Ott 510[3][3]	10

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DigColPs_Per2			ACICAL
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	179		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	11		
k_VernCorrErrorDiag_Cnt_str.Threshold	8		
k_VernCorrErrorDiag_Cnt_str.PStep	1		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	31.8570087		
k_VernOORangeThresh_Deg_f32	390.7995283		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPe	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPe	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	14.6363688	14.63636364 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
		_	

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>~</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	<b>✓</b>
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>✓</b>

-885.363647

-885.3636364 ± 0.0009

Test Step 2.30 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	105	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150	
DigColPs_ColTrimStatic_Deg_M_f32	259.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	46069	
DigColPs_I2CHwColAngle_Deg_M_f32	360	

DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc tgt\_DigColPs\_Per2\_l2CHwAbsPosValid\_Cnt\_lgc.value

 $\label{tgt_digColPs_Per2_I2CHwAbsPos_HwDeg_f32.value} $$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value $$ tgt_Dig$ 





Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	33.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0 224.1625181
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12] T0_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSputVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSputVernierLUT_Cnt_s16[2][5] T2_ColSputVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
	11
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8_





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2 DualSpurVernierLUT Cnt s16[0][8]	-108
T2 DualSpurVernierLUT Cnt s16[0][9]	-72
	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][10] T3_DualSpurVernierLUT_Cst_s16[0][11]	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][3]	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T3_DualSpurVernierLUT_Cst_s16[1][18]	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[2][9]	
T2_DualSpurVernierLUT_Cnt_s16[2][10] T0_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

2014-10-14, 17:31:16+0530



DigColPs\_Per2
Name

		( )	
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cst_s16[3][21]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41 27		
k_SkipStepErrDiag_Cnt_str.PStep			
k_SkipStepErrDiag_Cnt_str.NStep	50 85		
k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt Pim DigColPsEOL.TrimComp Cnt u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosVa	alid Cnt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_H		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_er		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_I		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt Pim DigColPsEOL	3	
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	Resul
DigColPs I2CHwColAngleForTrim Deg M f32	371.030273	371.0302938 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	
DigColPs Regl2CSnsrDataType Cnt M u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-528.969727	-528.9697062 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	
-5-15-15-15		0x6C	
NTC	0x6C		
NTC Param	0x6C 0x0C	0x0C	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Γest Step 2.31 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
0igColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.8777566
ligColPs_I2CHwDataType_Cnt_M_u08	0
igColPs_I2CHwSpurAngle_Cnt_M_u16	51849
igColPs_I2CHwSpurAngle_Deg_M_f32	31.1
igColPs_I2CHwTrimTransCnts_Uls_M_u08	2
igColPs_I2CSensCommFlts_Cnt_M_u08	17
igColPs_I2CSpurSensorFault_Cnt_M_lgc	1
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
igColPs_PrevColPos_Deg_M_f32	200.3508072
igColPs_PrevVernierLevelNo_Cnt_M_u08	11
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
igColPs_SpurParityError_Cnt_M_lgc	0
higColPs_SpurSensorFaultAcc_Cnt_M_u16	101
higColPs_SpurTrimStatic_Deg_M_f32	31.1
higColPs_TrimCompStatic_Cnt_M_u16	952
igColPs_VernCorrDetectAcc_Cnt_M_u16	4
igColPs VernierAngleOORange Cnt M lgc	0
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2 ColSpurVernierLUT Cnt s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2 ColSpurVernierLUT Cnt s16[0][14]	294
	327
2_ColSpurVernierLUT_Cnt_s16[0][15]	359
2_ColSpurVernierLUT_Cnt_s16[0][16]	0
2_ColSpurVernierLUT_Cnt_s16[1][0]	· · · · · · · · · · · · · · · · · · ·
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
2_ColSpurVernierLUT_Cnt_s16[1][12]	3
2 ColSpurVernierLUT Cnt s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s16[2][2]	4
T2_ColSpurVernierLUT_Cnt_s16[2][3]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	8 9 0
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	8 9

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2		
T2 DualSpurVernierLUT Cnt s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2 DualSpurVernierLUT Cnt s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	12		
k_SkipStepErrDiag_Cnt_str.NStep	41		
k_VernCorrErrorDiag_Cnt_str.Threshold	48		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorThrash_Dog_f32	3 78 0135704		
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	78.9135704 1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-74.24		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	nt Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
		· ·	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.32 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	144
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	182
DigColPs_ColTrimStatic_Deg_M_f32	249.4
DigColPs HwAVernCorrFault Cnt M Igc	1
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	18257
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs I2CHwSpurAngle Cnt M u16	21803
DigColPs_I2CHwSpurAngle_Deg_M_f32	32.2
DigColPs I2CHwTrimTransCnts UIs M u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs I2CSpurSensorFault Cnt M Igc	1
DigColPs PrevAngleDataAvailable Cnt M lgc	1
DigColPs PrevColPos Deg M f32	845.517553
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs SkipStepFltDetectAcc Cnt M u16	15
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs SpurSensorFaultAcc Cnt M u16	144
DigColPs SpurTrimStatic Deg M f32	32.2
DigColPs_TrimCompStatic_Cnt_M_u16	988
DigColPs VernCorrDetectAcc Cnt M u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
12_00/04/04/1/1011_01/1_510[1][2]	

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Nama	Input Value
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2 DualSpurVernierLUT Cnt s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216 400
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
12 DUGIODUI VOITIOLEO I OTIL 3 IUI I II I	•





<u> </u>	
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2 DualSpurVernierLUT Cnt s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2 DualSpurVernierLUT Cnt s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2 DualSpurVernierLUT Cnt s16[3][12]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9
	11
T2_DualSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[3][17]	
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	13 15
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
T2_DualSpurVernierLUT_Cnt_s16[3][21]	
k_SelectFromColumn_Cnt_lgc	0 179
k_SkipStepErrDiag_Cnt_str.Threshold	
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	11
k_VernCorrErrorDiag_Cnt_str.Threshold	8
k_VernCorrErrorDiag_Cnt_str.PStep	1
k_VernCorrErrorDiag_Cnt_str.NStep	24 0570007
k_VernCorrErrorThresh_Deg_f32	31.8570087
	390.7995283
k_VernOORangeThresh_Deg_f32 tgt_DigCoIPs_Per2_MecState_Cnt_enum.value	1



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	14.636363	14.63636364 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	<b>~</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-885.363647	-885.3636364 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 2.33 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	106
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141
DigColPs_ColTrimStatic_Deg_M_f32	269.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26533
DigColPs_I2CHwColAngle_Deg_M_f32	60.248
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36379
DigColPs_I2CHwSpurAngle_Deg_M_f32	34.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	284.4795149
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106
DigColPs_SpurTrimStatic_Deg_M_f32	34.4
DigColPs_TrimCompStatic_Cnt_M_u16	1060
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
	294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][8]	
T2_DualSpurVernierLUT_Cnt_s16[0][9] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72
12 Duaispurvernierlu i Chi S'IbiUli'IUl	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
	0 36 72

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T. D. Designation and LTJ. Cell. 5 (1915)   144   148   149   14	Namo	Input Value
12_DasSparvemental_Cot_stoligits	Name	Input Value
T. DuaSquiverment_T. Co., 149(07)   26		
T2. DusSporkerentU. Cot. 3100179 T3. DusSporkerentU. Cot. 3100179 T4. DusSporkerentU. Cot. 3100179 T4. DusSporkerentU. Cot. 3100179 T5. DusSporkerentU. Cot. 3100179		
T2_Dustport/emed_TU_Crt_s100[19]   288    -T2_Dustport/emed_TU_Crt_s100[29]   294    -T2_Dustport/emed_TU_Crt_s100[29]   294    -T2_Dustport/emed_TU_Crt_s100[29]   300    -T2_Dustport/emed_TU_Crt_s100[29]   9    -T2_Dustport/emed_TU_Crt_s100[29]   9    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   5    -T2_Dustport/emed_TU_Crt_s100[29]   5    -T2_Dustport/emed_TU_Crt_s100[29]   7    -T2_Dustport/emed_TU_Crt_s100[29]   7    -T2_Dustport/emed_TU_Crt_s100[29]   7    -T2_Dustport/emed_TU_Crt_s100[29]   9    -T2_Dustport/emed_TU_Crt_s10		
T2_Dustport/emicUT_OL_st 90(91)		
T2_DusSpurVermed.U_Dus_14(0)[23]   T2_DusSpurVermed.U_Dus_14(0)[23]   T2_DusSpurVermed.U_Dus_14(0)[3]   T2		
T2_DusSprivement_U_Cnt_stqUp		
12_Dustpar/ment U_Cnt_sto[1]		
T2. DuslSpurVerneUT. Cnt.; 16(1)[2] 17. DuslSpurVerneUT. Cnt.; 16(1)[2] 17. DuslSpurVerneUT. Cnt.; 16(1)[3] 17. DuslSpurVerneUT. Cnt.; 16(1)[4] 18. DuslSpurVerneUT. Cnt.; 16(1)[4] 19. DuslSpurVerneUT. Cnt.; 16(1)[6] 19. DuslSpurVerneUT. Cnt.; 16(	T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DusSparvementU_Cor_16(9)  3   2   72_DusSparvementU_Cor_16(9)  3   2   72_DusSparvementU_Cor_16(9)  3   3   72_DusSparvementU_Cor_16(9)  4   3   72_DusSparvementU_Cor_16(9)  6   72_DusSparvementU_Cor_16(9)  6   72_DusSparvementU_Cor_16(9)  7   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DasSgov/Period UT_Ord_19(1)	T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
TP_DasSpawYene(UT_CM_16(1)):  TP_Das	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DusSpurVermeUT_Cnt_st0[19]	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T. Q. DuaSgout/vernetU. Cm. 1 sticl 191   5   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DasSparVerminUT_Cnt_s10[17] 6 T2_DasSparVerminUT_Cnt_s10[18] 7 T2_DasSparVerminUT_Cnt_s10[18] 9 T2_DasSparVerminUT_Cnt_s10[110] 9 T2_DasSparVerminUT_Cnt_s10[110] 9 T2_DasSparVerminUT_Cnt_s10[111] 10 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[114] 2 T2_DasSparVerminUT_Cnt_s10[114] 3 T2_DasSparVerminUT_Cnt_s10[116] 1 T2_DasSp	T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DusSprivement_UT_Cett_\$16(19)	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DusSparVermeUT_Cnt_stig1190 9 172_DusSparVermeUT_Cnt_stig1191 9 172_DusSparVermeUT_Cnt_stig1191 172_DusSparVermeUT_Cnt_stig	T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
12_Dust   Description   15   15   15   15   15   15   15   1	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2, DuniSpurVernict U. Fort, s16[115] 172, DuniSpurVernict U. Fort, s16[115] 172, DuniSpurVernict U. Fort, s16[115] 172, DuniSpurVernict U. Fort, s16[116] 172, DuniSpurVernict U. Fort, s16[116] 173, DuniSpurVernict U. Fort, s16[116] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 172, DuniSpurVernict U. Fort, s16[117] 173, DuniSpurVernict U. Fort, s16[117] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 172, DuniSpurVernict U. Fort, s16[117] 173, DuniSpurVernict U. Fort, s16[117] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 172, DuniSpurVernict U. Fort, s16[117] 173, DuniSpurVernict U. Fort, s16[117] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernic		8
17. DuaSgnufvemet.U. F. Cot. 19(1)(12) 12. DuaSgnufvemet.U. F. Cot. 19(1)(19) 12. DuaSgnufvemet.U. F. Cot. 19(1)(19) 13. DuaSgnufvemet.U. F. Cot. 19(1)(19) 14. DuaSgnufvemet.U. F. Cot. 19(1)(19) 15. DuaSgnufvemet.U. F. Cot. 19(1)(19) 16. T. DuaSgnufvemet.U. F. Cot. 19(1)(19) 17. DuaSgnufvemet.U. F. Cot. 19(1)(19) 18. T. DuaSgnufvemet.U. F. Cot. 19(1)(19) 19. DuaSgnuf	T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVermetU_T_Cnt_st0[1]2] 12_DualSpurVermetU_T_Cnt_st0[1]10] 12_DualSpurVermet	T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[117]  12_DualSpurVerme		
12_DuaSpar/emetLUT_Crt_s16[1]14    12_DuaSpar/emetLUT_Crt_s16[1]17    12_DuaSpar/emetLUT_Crt_s16[1]17    12_DuaSpar/emetLUT_Crt_s16[1]17    12_DuaSpar/emetLUT_Crt_s16[1]17    13_DuaSpar/emetLUT_Crt_s16[1]18    7_DuaSpar/emetLUT_Crt_s16[1]18    7_DuaSpar/emetLUT_Crt_s16[1]28    13_DuaSpar/emetLUT_Crt_s16[1]28    14_DuaSpar/emetLUT_Crt_s16[1]28    15_DuaSpar/emetLUT_Crt_s16[1]28    16_DuaSpar/emetLUT_Crt_s16[1]28    17_DuaSpar/emetLUT_Crt_s16[2]8    18_DuaSpar/emetLUT_Crt_s16[2]8    19_DuaSpar/emetLUT_Crt_s16[2]8    10_DuaSpar/emetLUT_Crt_s16[2]8    10_DuaSpar/emetLUT_Crt_s16[2]8    11_DuaSpar/emetLUT_Crt_s16[2]8    12_DuaSpar/emetLUT_Crt_s16[2]8    12_DuaSpar/emetLUT_Crt_s16[2		
12. DualSpurVernetLUT. Cnt. 518(1)15) 12. DualSpurVernetLUT. Cnt. 518(1)17) 13. DualSpurVernetLUT. Cnt. 518(1)17) 14. DualSpurVernetLUT. Cnt. 518(1)17) 15. DualSpurVernetLUT. Cnt. 518(1)19) 18. T. DualSpurVernetLUT. Cnt. 518(1)19) 18. T. DualSpurVernetLUT. Cnt. 518(1)19) 19. T. DualSpurVernetLUT. Cnt. 518(1)21) 19. DualSpurVernetLUT. Cnt. 518(1)21) 10. DualSpurVernetLUT. Cnt. 518(1)21) 11. DualSpurVernetLUT. Cnt. 518(1)21) 12. DualSpurVernetLUT. Cnt. 518(1)21) 12. DualSpurVernetLUT. Cnt. 518(1)21) 12. DualSpurVernetLUT. Cnt. 518(1)21) 13. DualSpurVernetLUT. Cnt. 518(1)21) 14. DualSpurVernetLUT. Cnt. 518(1)21) 15. DualSpurVernetLUT. Cnt. 518(1)21) 16. DualSpurVernetLUT. Cnt. 518(1)21) 17. DualSpurVernetLUT. Cnt. 518(1)21) 18. DualSpurVernetLUT. Cnt. 518(1)21) 19. DualSpurVernetLUT. Cnt. 518(1)211 19. DualSpurVernetLUT. Cnt. 518(1)21 19. DualSpurVernetLUT. Cnt. 5		
12_DusSpurVermetUT_Cnt_st@[1]16    12_DusSpurVerm		
12_DusSpurVemetLUT_Cut_s16(1)17		
T2_DusSpurVemietUT_Cnt_st6[1]18   7   7   7   7   7   7   7   7   7		
12   DuaSpurVernictUT Cnt		
12_DuaSpurVernietUT_Cnt_s16(1) 20    12_DuaSpurVernietUT_Cnt_s16(1) 21    12_DuaSpurVernietUT_Cnt_s16(1) 21    13_DuaSpurVernietUT_Cnt_s16(2) 11    14_DuaSpurVernietUT_Cnt_s16(2) 11    15_DuaSpurVernietUT_Cnt_s16(2) 12    12_DuaSpurVernietUT_Cnt_s16(2) 3    13_DuaSpurVernietUT_Cnt_s16(2) 3    14_DuaSpurVernietUT_Cnt_s16(2) 5    15_DuaSpurVernietUT_Cnt_s16(2) 5    15_DuaSpurVernietUT_Cnt_s16(2) 7    17_DuaSpurVernietUT_Cnt_s16(2) 7    17_DuaSpurVernietUT_Cnt_s16(2) 7    17_DuaSpurVernietUT_Cnt_s16(2) 7    18_DuaSpurVernietUT_Cnt_s16(2) 8    18_DuaSpurVernietUT_Cnt_s16(2) 8    19_DuaSpurVernietUT_Cnt_s16(2) 11    10_DuaSpurVernietUT_Cnt_s16(2) 11    10_DuaSpurVernietUT_Cnt_s16(2) 11    10_DuaSpurVernietUT_Cnt_s16(2) 12    11_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 14    13_DuaSpurVernietUT_Cnt_s16(2) 14    14_DuaSpurVernietUT_Cnt_s16(2) 15    15_DuaSpurVernietUT_Cnt_s16(2) 15    16_DuaSpurVernietUT_Cnt_s16(2) 15    17_DuaSpurVernietUT_Cnt_s16(2) 15    18_DuaSpurVernietUT_Cnt_s16(2) 16    19_DuaSpurVernietUT_Cnt_s16(2) 16    10_DuaSpurVernietUT_Cnt_s16(2) 16    10_DuaSpurVernietUT_Cnt_s16(2) 16    10_DuaSpurVernietUT_Cnt_s16(2) 16    10_DuaSpurVernietUT_Cnt_s16(2) 16    11_DuaSpurVernietUT_Cnt_s16(2) 16    12_DuaSpurVernietUT_Cnt_s16(2) 16    13_DuaSpurVernietUT		
12		
T2_DualSpurVernierLUT_Cnt_s16[2][0]   0   1   1   1   1   1   1   1   1   1		
T2		
T2_DualSpurVemierLUT_Cnt_s16[2][3]   2   2   2   2   2   2   2   2   2		
T2 DualSpurVermierLUT_Cnt_s16[2][4]   4		
T2   DualSpurVernierLUT_Cnt_s16[2][4]   5   5   5   5   5   5   5   5   5		
T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 9 T2_DualSpurVernierLUT_Cnt_s16[2][16] 10 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11		
T2 DualSpurVernierLUT_Cnt_st6[2][5]   6   6   7   2 DualSpurVernierLUT_Cnt_st6[2][7]   7   7   7   7   7   7   7   7   7		
T2_DualSpurVemierLUT_Cnt_s16[2][7] T2_DualSpurVemierLUT_Cnt_s16[2][8] 8 8 7 12_DualSpurVemierLUT_Cnt_s16[2][9] 9 72_DualSpurVemierLUT_Cnt_s16[2][10] 10 72_DualSpurVemierLUT_Cnt_s16[2][11] 0 72_DualSpurVemierLUT_Cnt_s16[2][12] 1 72_DualSpurVemierLUT_Cnt_s16[2][13] 1 72_DualSpurVemierLUT_Cnt_s16[2][14] 3 72_DualSpurVemierLUT_Cnt_s16[2][15] 4 72_DualSpurVemierLUT_Cnt_s16[2][16] 6 72_DualSpurVemierLUT_Cnt_s16[2][17] 6 72_DualSpurVemierLUT_Cnt_s16[2][17] 6 72_DualSpurVemierLUT_Cnt_s16[2][17] 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	T2_DualSpurVernierLUT_Cnt_s16[2][5]	
T2_DualSpurVemierLUT_Cnt_s16[2][8] 8 T2_DualSpurVemierLUT_Cnt_s16[2][9] 9 T2_DualSpurVemierLUT_Cnt_s16[2][10] 10 T2_DualSpurVemierLUT_Cnt_s16[2][11] 0 T2_DualSpurVemierLUT_Cnt_s16[2][11] 0 T2_DualSpurVemierLUT_Cnt_s16[2][12] 1 T2_DualSpurVemierLUT_Cnt_s16[2][13] 2 T2_DualSpurVemierLUT_Cnt_s16[2][14] 3 T2_DualSpurVemierLUT_Cnt_s16[2][15] 4 T2_DualSpurVemierLUT_Cnt_s16[2][16] 5 T2_DualSpurVemierLUT_Cnt_s16[2][17] 6 T2_DualSpurVemierLUT_Cnt_s16[2][18] 7 T2_DualSpurVemierLUT_Cnt_s16[2][18] 7 T2_DualSpurVemierLUT_Cnt_s16[2][19] 8 T2_DualSpurVemierLUT_Cnt_s16[2][19] 8 T2_DualSpurVemierLUT_Cnt_s16[2][19] 8 T2_DualSpurVemierLUT_Cnt_s16[2][19] 10 T2_DualSpurVemierLUT_Cnt_s16[2][19] 10 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 10 T2_DualSpurVemierLUT_Cnt_s16[3][1] 11	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVerni	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
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T2_DualSpurVernierLUT_Cnt_st6[2][11]  12_DualSpurVernierLUT_Cnt_st6[2][12]  12_DualSpurVernierLUT_Cnt_st6[2][14]  12_DualSpurVernierLUT_Cnt_st6[2][14]  12_DualSpurVernierLUT_Cnt_st6[2][15]  12_DualSpurVernierLUT_Cnt_st6[2][16]  12_DualSpurVernierLUT_Cnt_st6[2][17]  12_DualSpurVernierLUT_Cnt_st6[2][17]  12_DualSpurVernierLUT_Cnt_st6[2][18]  12_DualSpurVernierLUT_Cnt_st6[2][18]  12_DualSpurVernierLUT_Cnt_st6[2][19]  12_DualSpurVernierLUT_Cnt_st6[2][20]  12_DualSpurVernierLUT_Cnt_st6[2][21]  10_DualSpurVernierLUT_Cnt_st6[3][1]  12_DualSpurVernierLUT_Cnt_st6[3][1]  12_DualSpurVernierLUT_Cnt_st6[3][1]  12_DualSpurVernierLUT_Cnt_st6[3][2]  12_DualSpurVernierLUT_Cnt_st6[3][2]  12_DualSpurVernierLUT_Cnt_st6[3][3]  12_DualSpurVernierLUT_Cnt_st6[3][6]  13_DualSpurVernierLUT_Cnt_st6[3][6]  14_DualSpurVernierLUT_Cnt_st6[3][6]  15_DualSpurVernierLUT_Cnt_st6[3][6]  16_DualSpurVernierLUT_Cnt_st6[3][6]  17_DualSpurVernierLUT_Cnt_st6[3][6]  18_DualSpurVernierLUT_Cnt_st6[3][6]  19_DualSpurVernierLUT_Cnt_st6[3][6]  10_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  12_DualSpurVernierLUT_Cnt_st6[3][6]  13_DualSpurVernierLUT_Cnt_st6[3][6]  14_DualSpurVernierLUT_Cnt_st6[3][6]  15_DualSpurVernierLUT_Cnt_st6[3][6]  16_DualSpurVernierLUT_Cnt_st6[3][6]  17_DualSpurVernierLUT_Cnt_st6[3][6]  18_DualSpurVernierLUT_Cnt_st6[3][6]  19_DualSpurVernierLUT_Cnt_st6[3][6]  10_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  11_DualSpurVernierLUT_Cnt_st6[3][6]  12_DualSpurVernierLUT_Cnt_st6[3][6]	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
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T2_DualSpurVernierLUT_Cnt_s16[2][19]       8         T2_DualSpurVernierLUT_Cnt_s16[2][20]       9         T2_DualSpurVernierLUT_Cnt_s16[2][1]       10         T2_DualSpurVernierLUT_Cnt_s16[3][1]       2         T2_DualSpurVernierLUT_Cnt_s16[3][1]       2         T2_DualSpurVernierLUT_Cnt_s16[3][2]       4         T2_DualSpurVernierLUT_Cnt_s16[3][3]       6         T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][6]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11 <td></td> <td></td>		
T2_DualSpurVernierLUT_Cnt_s16[2][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11		
T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13		
T2_DualSpurVerniert.UT_Cnt_s16[3][0]       22         T2_DualSpurVerniert.UT_Cnt_s16[3][1]       2         T2_DualSpurVerniert.UT_Cnt_s16[3][2]       4         T2_DualSpurVerniert.UT_Cnt_s16[3][3]       6         T2_DualSpurVerniert.UT_Cnt_s16[3][4]       8         T2_DualSpurVerniert.UT_Cnt_s16[3][5]       10         T2_DualSpurVerniert.UT_Cnt_s16[3][6]       12         T2_DualSpurVerniert.UT_Cnt_s16[3][7]       14         T2_DualSpurVerniert.UT_Cnt_s16[3][8]       16         T2_DualSpurVerniert.UT_Cnt_s16[3][9]       18         T2_DualSpurVerniert.UT_Cnt_s16[3][10]       20         T2_DualSpurVerniert.UT_Cnt_s16[3][11]       1         T2_DualSpurVerniert.UT_Cnt_s16[3][12]       3         T2_DualSpurVerniert.UT_Cnt_s16[3][13]       5         T2_DualSpurVerniert.UT_Cnt_s16[3][14]       7         T2_DualSpurVerniert.UT_Cnt_s16[3][16]       11         T2_DualSpurVerniert.UT_Cnt_s16[3][16]       11         T2_DualSpurVerniert.UT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][1]       2         T2_DualSpurVernierLUT_Cnt_s16[3][2]       4         T2_DualSpurVernierLUT_Cnt_s16[3][3]       6         T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][6]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][2]		
T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13		
T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][5]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][5] 10  T2_DualSpurVernierLUT_Cnt_s16[3][6] 12  T2_DualSpurVernierLUT_Cnt_s16[3][7] 14  T2_DualSpurVernierLUT_Cnt_s16[3][8] 16  T2_DualSpurVernierLUT_Cnt_s16[3][9] 18  T2_DualSpurVernierLUT_Cnt_s16[3][10] 20  T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 1  T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 3  T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 5  T2_DualSpurVernierLUT_Cnt_s16[3][14] 7  T2_DualSpurVernierLUT_Cnt_s16[3][15] 9  T2_DualSpurVernierLUT_Cnt_s16[3][16] 11  T2_DualSpurVernierLUT_Cnt_s16[3][16] 11  T2_DualSpurVernierLUT_Cnt_s16[3][16] 11  T2_DualSpurVernierLUT_Cnt_s16[3][17] 13		
T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13	T2_DualSpurVernierLUT_Cnt_s16[3][13]	
T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13	T2_DualSpurVernierLUT_Cnt_s16[3][14]	
T2_DualSpurVernierLUT_Cnt_s16[3][17] 13	T2_DualSpurVernierLUT_Cnt_s16[3][15]	
	T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
TO Discloud/amical LIT Oct -44001401	T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
12_DualSpurvernierLU1_Cht_s16[3][18] 15	T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] 17	T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] 19	T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

DigColPs\_Per2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	22		
k_SkipStepErrDiag_Cnt_str.PStep	8		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	4		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	88.97686696		
k_VernOORangeThresh_Deg_f32	706.5625857		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	96.79570621		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	150.448013	150.448 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-749.552002	-749.552 ± 0.0009	
tgt DigColPs Per2 TrimComp Cnt Igc.value	0	0	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.34 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	103	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142	
DigColPs_ColTrimStatic_Deg_M_f32	280	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	53187	
DigColPs_I2CHwColAngle_Deg_M_f32	11.56588054	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	52647	
DigColPs_I2CHwSpurAngle_Deg_M_f32	35.5	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	23	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	899.7103484	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	103	
DigColPs_SpurTrimStatic_Deg_M_f32	35.5	
DigColPs_TrimCompStatic_Cnt_M_u16	1096	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15	





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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2 DualSpurVernierLUT Cnt s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][10]	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
	10
	1 10
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	131		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	40		
k_VernCorrErrorDiag_Cnt_str.PStep	34		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	58.9241991		
k_VernOORangeThresh_Deg_f32	866.7677131		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	11.56588054		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

ig_nte_mot_od_bigoon on im_bigoon obot	tgt_i iii_bigooii ococ		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	811.565918	811.5658805 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11	11	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1)		•
Name	Input Value	
DigColPsInt_GetCustData()	151	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	145	
DigColPs_ColTrimStatic_Deg_M_f32	290.2	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	48613	
DigColPs_I2CHwColAngle_Deg_M_f32	136.3651175	

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Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36961
DigColPs_I2CHwSpurAngle_Deg_M_f32	36.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs I2CSensCommFlts Cnt M u08	28
DigColPs I2CSpurSensorFault Cnt M Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1505.659877
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151
DigColPs_SpurTrimStatic_Deg_M_f32	36.6
DigColPs_TrimCompStatic_Cnt_M_u16	1132
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
	294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
	11
T2_ColSpurVernierLUT_Cnt_s16[3][2]	
	8
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	8 5





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T0_ColOpurV(sprint UT_Cot_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	4
T2 ColSpurVernierLUT Cnt s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16] T0_DualSpurVernierLUT_Cnt_s16[0][47]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18] T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14] T0_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
TO Development of LIT Out 10707157	
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5





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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2 DualSpurVernierLUT Cnt s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2 DualSpurVernierLUT Cnt s16[3][4]	8		
T2 DualSpurVernierLUT Cnt s16[3][5]	10		
T2 DualSpurVernierLUT Cnt s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	184		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	26		
k_VernCorrErrorDiag_Cnt_str.Threshold	20		
k_VernCorrErrorDiag_Cnt_str.PStep	46		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	87.16203666		
k_VernOORangeThresh_Deg_f32	1105.319018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt Pim DigColPsEOL.ColTrim Deg f32	136.3651175		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2271		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt DigColPs Per2 I2CHwAbsPos	Valid Cnt loc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_		
tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc	tgt DigColPs Per2 TrimComp Cn		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt Pim DigColPsEOL	9-	
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	Resul
DigColPs I2CHwColAngleForTrim Deg M f32	1636.36353	1636.363636 ± 0.00048828125	
DigColPs I2CHwTrimTransCnts Uls M u08	0	0	
DigColPs PrevAngleDataAvailable Cnt M Iqc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1646.16504	1646.165117 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	16	16	
	1	1	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_SkipStepFitDetectAcc_Crit_M_u10	1	1	

DigCoIPs_HwAVernCorrFault_Cnt_M_lgc         0         0           DigCoIPs_I2CHwColAngleForTrim_Deg_M_f32         1636.36353         1636.36363 ± 0.00048828125           DigCoIPs_I2CHwTrimTransCnts_Uls_M_u08         0         0           DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc         0         0	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08 0	~
	<b>✓</b>
DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0	
	•
DigCoIPs_PrevCoIPos_Deg_M_f32 1646.16504 1646.165117 ± 0.0001220703125	~
DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 16 16	•
DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1	~
DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1	~
DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1	~
DigCoIPs_VernierAngleOORange_Cnt_M_lgc 0	~
tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value 0 0	~
tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value 736.363525 736.3636364 ± 0.0009	~
tgt_DigCoIPs_Per2_TrimComp_Cnt_lgc.value 0 0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~



	🗸 🗸 - The control of the control o
Test Step 2.36 (Repeat Count = 1) Name	Input Value
DigColPsInt_GetCustData()	165
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	300.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	28682
DigColPs_I2CHwColAngle_Deg_M_f32	49.7053827
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16 DigColPs_I2CHwSpurAngle_Deg_M_f32	13341 37.7
DigColPs_12CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1225.322705
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
DigColPs_SpurTrimStatic_Deg_M_f32	37.7
DigColPs_TrimCompStatic_Cnt_M_u16	1168
DigColPs_VernCorrDetectAcc_Cnt_M_u16	9
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigCoIPs T2 CoISpurVernierLUT Cnt s16[0][0]	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14] T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6] T3_ColSpurVernierLUT_Cnt_s16[2][7]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	5
T2 ColSpurVernierLLT Cpt s16(2)(8)	
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	3 1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_\$ To[3][5] T2_ColSpurVernierLUT_Crit_\$ 16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18] T3_DualSpurVernierLUT_Cnt_s16[1][10]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	9
12_Dualopui verillerE01_Ont_\$10[1][21]	0
T2 DualSpurVernierI IT Cnt e16(2)(0)	l Control of the Cont
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2

DigColPs\_Per2

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Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[2][7] T2\_DualSpurVernierLUT\_Cnt\_s16[2][8] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[2][9] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[2][10] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[2][11] 0 T2\_DualSpurVernierLUT\_Cnt\_s16[2][12] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[2][13] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[2][14] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[2][15] T2\_DualSpurVernierLUT\_Cnt\_s16[2][16] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[2][17] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[2][18] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[2][19] 8 9 T2\_DualSpurVernierLUT\_Cnt\_s16[2][20] T2\_DualSpurVernierLUT\_Cnt\_s16[2][21] 10 22 T2\_DualSpurVernierLUT\_Cnt\_s16[3][0] T2\_DualSpurVernierLUT\_Cnt\_s16[3][1] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[3][2] 4 T2\_DualSpurVernierLUT\_Cnt\_s16[3][3] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[3][4] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[3][5] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][6] 12 T2\_DualSpurVernierLUT\_Cnt\_s16[3][7] 14 T2\_DualSpurVernierLUT\_Cnt\_s16[3][8] 16 T2\_DualSpurVernierLUT\_Cnt\_s16[3][9] 18 T2\_DualSpurVernierLUT\_Cnt\_s16[3][10] 20 T2\_DualSpurVernierLUT\_Cnt\_s16[3][11] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[3][12] 3 T2 DualSpurVernierLUT Cnt s16[3][13] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[3][15] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[3][16] 11 T2 DualSpurVernierLUT Cnt s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21 k SelectFromColumn Cnt lqc 0 k\_SkipStepErrDiag\_Cnt\_str.Threshold 223 k\_SkipStepErrDiag\_Cnt\_str.PStep 2 k\_SkipStepErrDiag\_Cnt\_str.NStep 36 11 k\_VernCorrErrorDiag\_Cnt\_str.Threshold k\_VernCorrErrorDiag\_Cnt\_str.PStep 6 k\_VernCorrErrorDiag\_Cnt\_str.NStep 81 95902205  $k\_VernCorrErrorThresh\_Deg\_f32$ k\_VernOORangeThresh\_Deg\_f32 1527.852543  $tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value$ 49.7053827 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32  $tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32$ 341 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32$ tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL tgt\_Pim\_DigColPsEOL Name **Actual Value Expected Value** Result  $DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc$ DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32 818.181763 818.1818182 ± 0.00048828125 DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 n Λ DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc 0 829.30542  ${\tt DigColPs\_PrevColPos\_Deg\_M\_f32}$ 829.3053827 ± 0.0001220703125 DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08 9 DigColPs Reql2CSnsrDataType Cnt M u08 1 DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16 2 2 DigColPs VernCorrDetectAcc Cnt M u16 2 2 DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc 1 1 tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value 0 0 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value -81.8182373 -81.81818182 ± 0.00009

0

0x6C

0x0C

0x01

0

0x6C

0x0C

0x01

tgt DigColPs Per2 TrimComp Cnt Igc.value

NTC Param

Status



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

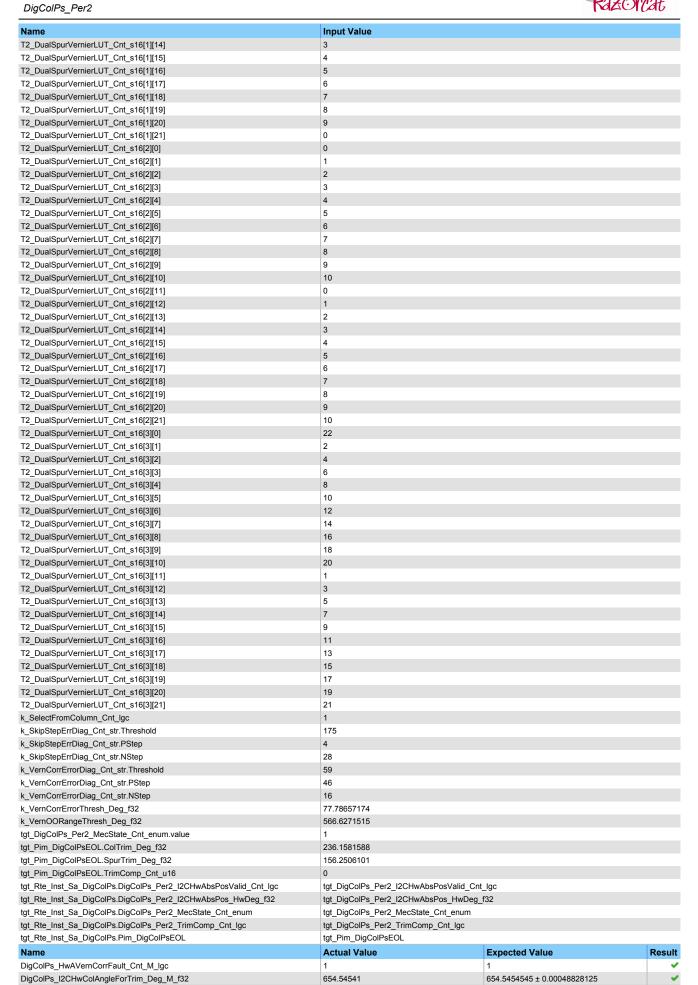
Test Step 2.37 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	175
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128
DigColPs_ColTrimStatic_Deg_M_f32	310.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	5110
DigColPs_I2CHwColAngle_Deg_M_f32	236.1581588
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	13604
DigColPs_I2CHwSpurAngle_Deg_M_f32	38.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1393.487479
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175
DigColPs_SpurTrimStatic_Deg_M_f32	38.8
DigColPs_TrimCompStatic_Cnt_M_u16	1204
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2 ColSpurVernierLUT Cnt s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T3_DualSpurVernierLUT_Cnt_s16[1][2]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2









Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	645.558167	645.5581588 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-254.441833	-254.4418412 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.38 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	185
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	168
DigColPs_ColTrimStatic_Deg_M_f32	320.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26911
DigColPs_I2CHwColAngle_Deg_M_f32	55.3166151
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	23900
DigColPs_I2CHwSpurAngle_Deg_M_f32	39.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	93.47087908
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	185
DigColPs_SpurTrimStatic_Deg_M_f32	39.9
DigColPs_TrimCompStatic_Cnt_M_u16	1240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	0.
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
Γ2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
Γ2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
Γ2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
Γ2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
Γ2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
Γ2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
Γ2_ColSpurVernierLUT_Cnt_s16[0][11]	196
Γ2_ColSpurVernierLUT_Cnt_s16[0][12]	229
Γ2_ColSpurVernierLUT_Cnt_s16[0][13]	261
Γ2 ColSpurVernierLUT Cnt s16[0][14]	294
Γ2 ColSpurVernierLUT Cnt s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][1]	4
T2_ColSpurVernierLUT_Cnt_S16[1][1] T2_ColSpurVernierLUT_Cnt_S16[1][2]	3

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360





Name T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4] T2_DualSpurVernierLUT_Cnt_s16[1][5]	Input Value 2
T2_DualSpurVernierLUT_Cnt_s16[1][4] T2_DualSpurVernierLUT_Cnt_s16[1][5]	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2 DualSpurVernierLUT Cnt s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T3_DualSpurVernierLUT_Cnt_s16[2][15]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18 20
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	96
k_SkipStepErrDiag_Cnt_str.PStep	42
k_SkipStepErrDiag_Cnt_str.NStep	34
k_VernCorrErrorDiag_Cnt_str.Threshold	98
k_VernCorrErrorDiag_Cnt_str.PStep	34
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	1574.365275
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0

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DigColPs\_Per2 Input Value  $tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32$ 55.3166151  $tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32$ 301.781571 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 4488  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_Igc tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc

tet Dto Jest Co DisColDo Disc DisColDoFOI	tet Dim DiaCalDaEOI		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	814.516602	814.5166151 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.39 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt GetCustData()	195
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	146
DigColPs ColTrimStatic Deg M f32	331
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22241
DigColPs_I2CHwColAngle_Deg_M_f32	71.4783923
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	37586
DigColPs_I2CHwSpurAngle_Deg_M_f32	141
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	414.2750131
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	195
DigColPs_SpurTrimStatic_Deg_M_f32	141
DigColPs_TrimCompStatic_Cnt_M_u16	1276
DigColPs_VernCorrDetectAcc_Cnt_M_u16	16
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7] T3_ColSpurVernierLUT_Cnt_s16[1][9]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	112

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[1][4]	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2 DualSpurVernierLUT Cnt s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[1][20]	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2 DualSpurVernierLUT Cnt s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19

DigColPs\_Per2



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	67		
k_SkipStepErrDiag_Cnt_str.PStep	39		
k_SkipStepErrDiag_Cnt_str.NStep	27		
k_VernCorrErrorDiag_Cnt_str.Threshold	5		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	245.4025523		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	71.4783923		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.98159581		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1481		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

19_1110_010_01_0190011 01.1 III_DI90011 0202			
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	820.478394	820.4783923 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-79.5216064	-79.5216077 ± 0.00009	<b>~</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Test Step 2.40 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123	
DigColPs_ColTrimStatic_Deg_M_f32	1.5	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	46566	
DigColPs_I2CHwColAngle_Deg_M_f32	135.5191227	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22556	
DigColPs_I2CHwSpurAngle_Deg_M_f32	42.1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1072.03711	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	14	
DigColPs_SpurParityError_Cnt_M_lgc	1	





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	42.1
DigColPs_TrimCompStatic_Cnt_M_u16	1312
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLOT_Cnt_S16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_\$16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierEUT_Cnt_S16[1][2] T2_ColSpurVernierEUT_Cnt_S16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1,
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLOT_Cnt_\$16[2][13] T2_ColSpurVernierLUT_Cnt_\$16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
	13
T2_ColSpurVernierLUT_Cnt_s16[3][12]	
	10
12_ColSpurVernierLU1_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_S10[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	71		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	38		
k_VernCorrErrorDiag_Cnt_str.Threshold	7		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	83.48664141		
k_VernOORangeThresh_Deg_f32	744.223277		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	135.5191227		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	263.9402983		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	302		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosV	/alid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_	HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_	enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs PrevColPos Deg M f32	494.019104	494.0191227 ± 0.0001220703125	•

0	0 = 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	494.019104	494.0191227 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>~</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	✓
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.41 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_ColTrimStatic_Deg_M_f32	5.6





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigCoIPs_I2CHwCoIAngle_Cnt_M_u16	20466
DigColPs_I2CHwColAngle_Deg_M_f32	34.50624543
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	34618
DigColPs_I2CHwSpurAngle_Deg_M_f32	43.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1779.91482
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	18
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	43.2
DigColPs_TrimCompStatic_Cnt_M_u16	1348
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-131 -99
T2_ColSpurVernierLUT_Cnt_S16[0][2] T2_ColSpurVernierLUT_Cnt_S16[0][3]	-66
	-00 -33
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	32
T2_ColSpurVernierLUT_Cnt_s16[0][6]	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T3_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
12 Duan-Opur volino (201 Ont 310   2   1   1	•
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2 DualSpurVernierLUT Cnt s16[2][18]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2 DualSpurVernierLUT Cnt s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2 DualSpurVernierLUT Cnt s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2 DualSpurVernierLUT Cnt s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	228		
	32		
k_SkipStepErrDiag_Cnt_str.PStep k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	40		
	32		
k_VernCorrErrorDiag_Cnt_str.PStep			
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCOPPageThresh_Deg_f32	23.81406522		
k_VernOORangeThresh_Deg_f32	1525.900935		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	34.50624543		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.5753602		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2878		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_t32	
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum			
	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL  Name		Expected Value	Result

0_ 1_ 1_ 1_ 1_ 1	132 = 311		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1468.90625	1468.906245 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	<b>~</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	568.90625	568.9062454 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	<b>✓</b>



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>~</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.42 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	163
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs ColSensorFaultAcc Cnt M u16	158
DigColPs_ColTrimStatic_Deg_M_f32	9.7
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	52348
DigColPs I2CHwColAngle Deg M f32	222.1544354
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27884
DigColPs_I2CHwSpurAngle_Deg_M_f32	44.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_12CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
	1 1405.727187
DigColPs_PrevColPos_Deg_M_f32	10
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	163
DigColPs_SpurTrimStatic_Deg_M_f32	44.3
DigColPs_TrimCompStatic_Cnt_M_u16	1384
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
	1-

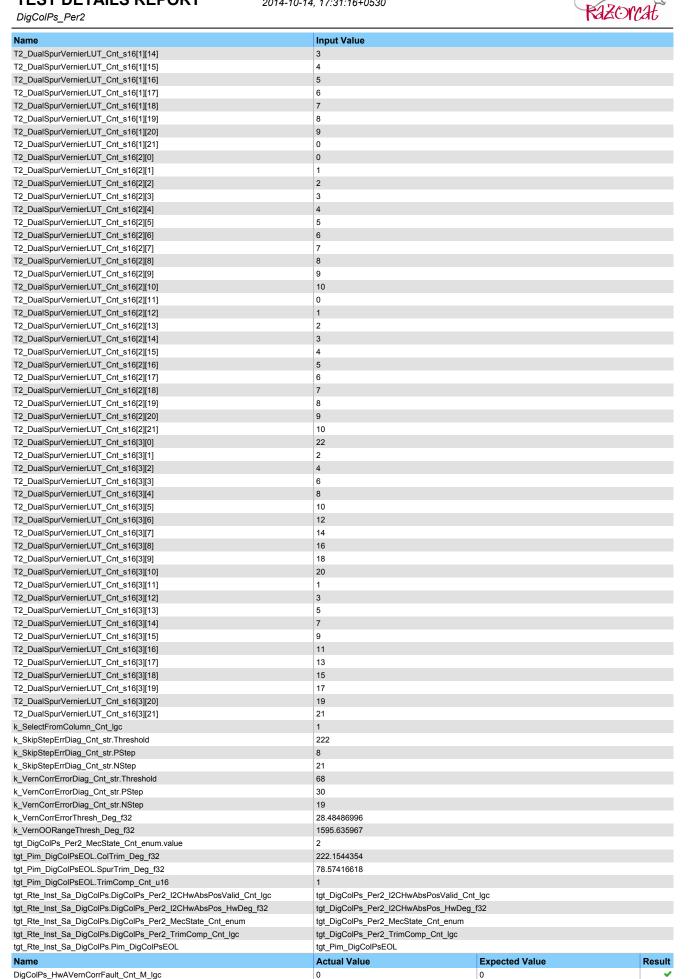
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Name		
12. Objoy/went-ULT On 1911/15  12. Objoy/went-ULT On 1911/15  13. Objoy/went-ULT On 1911/15  14. Objoy/went-ULT On 1911/15  15. Objoy/went-ULT On 1911/15  16. Objoy/went-ULT On 1911/15  17. Objoy/went-ULT On 1911/15  17. Objoy/went-ULT On 1911/15  18. Objoy/went-ULT On 1911/15  19. Objoy/went-ULT On 1911/15	Name	Input Value
12_Colsaychment (_Dr., Mrt  16  13_Colsaychment (_Dr., Mrt  16  13_Colsaychment (_Dr., Mrt  16  13_Colsaychment (_Dr., Mrt  16  14_Colsaychment (_Dr., Mrt  16  15_Colsaychment (_Dr., Mrt  16  16_Colsaychment (_Dr., Mrt  16  17_Colsaychment (_Dr., Mrt  16	T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
12_Colsystement_Cols_sequine   0	T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
17_Codepartment()	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
17_Codepartment()	T2 ColSpurVernierLUT Cnt s16[2][0]	0
T2_COSQuémentUT_Cs_120[2]   6		8
T. COSSUMMENTOL CR.   SPECIAL		
P_COSQAVermentU_Cn_squ[2]   2   P_COSQAVermentU_Cn_squ[2]   12   P_COSQAVermentU_Cn_squ[2]   13   P_COSQAVermentU_Cn_squ[2]   15   P_COSQAVermentU_Cn_squ[2]   17   P_COSQAVermentU_Cn_squ[2]   18   P_COSQAVermentU_Cn_squ[2]   18   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   11   P_COSQAVermentU_Cn_squ[2]   12   P_COSQAVermentU_Cn_squ[2]   13   P_COSQAVermentU_Cn_squ[2]   14   P_COSQAVermentU_Cn_squ[2]   15   P_COSQAVermentU_Cn_squ[2]   16   P_COSQAVermentU_Cn_squ[2]   17   P_COSQAVermentU_Cn_squ[2]   17   P_COSQAVermentU_Cn_squ[2]   18   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   11   P_COSQAVermentU_Cn_squ[2]   12   P_COSQAVermentU_Cn_squ[2]   13   P_COSQAVermentU_Cn_squ[2]   14   P_COSQAVermentU_Cn_squ[2]   15   P_COSQAVermentU_Cn_squ[2]   16   P_COSQAVermentU_Cn_squ[2]   17   P_COSQAVermentU_Cn_squ[2]   18   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   11   P_COSQAVermentU_Cn_squ[2]   11   P_COSQAVermentU_Cn_squ[2]   12   P_COSQAVermentU_Cn_squ[2]   13   P_COSQAVermentU_Cn_squ[2]   16   P_COSQAVermentU_Cn_squ[2]   17   P_COSQAVermentU_Cn_squ[2]   18   P_COSQAVermentU_Cn_squ[2]   19   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   10   P_COSQAVermentU_Cn_squ[2]   11   P_COSQAVermentU_Cn_squ[2]   12   P_COSQAVermentU_Cn_squ[2]   13   P_COSQAVermentU_Cn_squ[2]   14   P_COSQAVermentU_Cn_squ[2]   15   P_COSQAVermentU_Cn_squ[2]   16   P_COSQAVermentU_Cn_squ[2]   17   P_COSQAVermentU_Cn_squ[2]   18   P_COSQAVermentU_Cn_squ[2]   19		
T2_Collago/wenterU_Cn_1892[5]   T2_Collago/wenterU_Cn_1892[7]   T2_Collago/wenterU_Cn_1892[7]   T3_Collago/wenterU_Cn_1892[7]   T3_Collago/wenterU_Cn_1892[7]   T3_Collago/wenterU_Cn_1892[7]   T3_Collago/wenterU_Cn_1892[7]   T3_Collago/wenterU_Cn_1892[7]   T4_Collago/wenterU_Cn_1892[7]   T4_Collago/wenterU_Cn_1892[7]   T5_Collago/wenterU_Cn_1892[7]   T5_Collago/wenterU_Cn_1892[7]   T5_Collago/wenterU_Cn_1892[7]   T6_Collago/wenterU_Cn_1892[7]   T7_Collago/wenterU_Cn_1892[7]   T8_Collago/wenterU_Cn_1892[7]   T8_Collago/wenterU_Cn_1892[7		
12_Colspar/montul_Col_strigot    7		
12. CoSSAVermentUT_CRL 1612 71   7   12. CoSSAVermentUT_CRL 1612 71   6   12. CoSSAVermentUT_CRL 1612 71   10   13. CoSSAVermentUT_CRL 1612 71   10   14. CoSSAVermentUT_CRL 1612 71   10   15. CoSSAVermentUT_CRL 1612 71   10   16. CoSSAVermentUT_CRL 1612 71   10   17. CoSSAVermentUT_CRL 1612 71   10   18. CoSSAVermentUT_CRL 1612 71   11   18. CoSSAVermentUT_CRL 1612 71   11   18. CoSSAVermentUT_CRL 1612 71   10   19. CoSSAVermentUT_CRL 1612 71		
12,005ps/wrienctU_Ot_1502[9]   5   12,005ps/wrienctU_Ot_1502[9]   3   12,005ps/wrienctU_Ot_1502[1]   10   12,005ps/wrienctU_Ot_1502[1]   10   12,005ps/wrienctU_Ot_1502[1]   0   12,005ps/wrienctU_Ot_1502[1]   0   12,005ps/wrienctU_Ot_1502[1]   0   12,005ps/wrienctU_Ot_1502[1]   0   12,005ps/wrienctU_Ot_1502[1]   0   12,005ps/wrienctU_Ot_1502[1]   10   12,005ps/wrienctU_Ot_1502[1]   10   12,005ps/wrienctU_Ot_1502[1]   10   12,005ps/wrienctU_Ot_1502[1]   11   12,005ps/wrienctU_Ot_1502[1]   11   12,005ps/wrienctU_Ot_1502[1]   11   12,005ps/wrienctU_Ot_1502[1]   11   12,005ps/wrienctU_Ot_1502[1]   11   12,005ps/wrienctU_Ot_1502[1]   11   12,005ps/wrienctU_Ot_1502[1]   12   12,005ps/wrienctU_Ot_1502[1]   13		
12_CoSpar/weinetU_Cot_st@]0]   1   1   1   1   1   1   1   1   1		
12_DOSSA/VernetUT_Ort_15(2)  10_DOSSA/VernetUT_Ort_15(2)  11_DOSSA/VernetUT_Ort_15(2)  11_DOSSA/Verne		
12_CoSpa/viment UT_Cnt_stop  1    10		
12_CoSpin/Yennit U_Ort_stop 13  8   12_CoSpin/Yennit U_Ort_stop 13  0   12_CoSpin/Yennit U_Ort_stop 13  0   12_CoSpin/Yennit U_Ort_stop 13  2   12_CoSpin/Yennit U_Ort_stop 13  1   12_CoSpin/Yennit U_Ort_stop 13  2   12_CoSpin/Yennit U_Ort_stop 13  3   12_CoSpin/Yennit U_Ort_stop 13  1   13_CoSpin/Yennit U_Ort_stop 13  1   14_CoSpin/Yennit U_Ort_stop 13  1   15_CoSpin/Yennit U_Ort_stop 13  1   16_CoSpin/Yennit U_Ort_stop 13  1   17_CoSpin/Yennit U_Ort_stop 13  1   18_CoSpin/Yennit U_Ort_stop		
12_CoSpin/winest_U_Cnt_s100[14]		
12, CoSpa/wemsLU_Cnt_stq[1]+  4		
12, CoSpa/WameLII_CRL, 19(2)  16   17_, CoSpa/WameLII_CRL, 19(3)  1   17_, CoSpa/WameLII_CRL, 19(3)  1   18_, CoSpa/WameLII_CRL, 19(3)  1   19_, CoSpa/WameLII_CRL, 19(3)  1   10_, CoSpa/Wam		
12_COSDAYMENT   CM_1   15(5) 0    1   1   1   1   1   1   1   1   1		
12, CoSpar/venetUT, Cot.   160 31   14   12, CoSpar/venetUT, Cot.   160 31   15   16, CoSpar/venetUT, Cot.   160 31   16   17, CoSpar/venetUT, Cot.   160 31   17, CoSpar/venetUT, Cot.   160 31   18, CoSpar/venetUT, Cot.   160 31   19, CoSpar/vene	T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
12 CoSput/wentUT_Cot_st0[0]  14	T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
12, CoSpar/wentLUT, Cnt.; s169[13] 8 12, CoSpar/wentLUT, Cnt.; s169[14] 12, CoSpar/wentLUT, Cnt.; s169[16] 12, CoSpar/wentLUT, Cnt.; s169[16] 13, CoSpar/wentLUT, Cnt.; s169[16] 14, CoSpar/wentLUT, Cnt.; s169[17] 15, CoSpar/wentLUT, Cnt.; s169[17] 16, CoSpar/wentLUT, Cnt.; s169[17] 17, CoSpar/wentLUT, Cnt.; s169[18] 18 17, CoSpar/wentLUT, Cnt.; s169[18] 18 17, CoSpar/wentLUT, Cnt.; s169[18] 19 17, CoSpar/wentLUT, Cnt.; s169[18] 19 18 17, CoSpar/wentLUT, Cnt.; s169[18] 19 18 18, CoSpar/wentLUT, Cnt.; s169[18] 19 19 19, CoSpar/wentLUT, Cnt.; s169[18] 19 10, CoSpar/wentLUT, Cnt.; s169[18] 10, CoSpar/wentLUT, Cnt.; s169[18] 11, CoSpar/wentLUT, Cnt.; s169[18] 12, CoSpar/wentLUT, Cnt.; s169[18] 13, CoSpar/wentLUT, Cnt.; s169[18] 14, CoSpar/wentLUT, Cnt.; s169[18] 15, CoSpar/wentLUT, Cnt.; s169[18] 16, CoSpar/wentLUT, Cnt.; s169[18] 17, DoSpar/wentLUT, Cnt.; s169[18] 18, CoSpar/wentLUT, Cnt.; s169[18] 19, CoSpar/wentLUT, Cnt.; s169[18] 10, CoSpar/wentLUT, Cnt.; s169[18] 11, CoSpar/wentLUT, Cnt.; s169[18] 12, DoSpar/wentLUT, Cnt.; s169[18] 13, CoSpar/wentLUT, Cnt.; s169[18] 14, CoSpar/wentLUT, Cnt.; s169[18] 15, DoSpar/wentLUT, Cnt.; s169[18] 16, CoSpar/wentLUT, Cnt.; s169[18] 17, DoSpar/wentLUT, Cnt.; s169[18] 18, CoSpar/wentLUT, Cnt.; s169[18] 19, DoSpar/wentLUT, Cnt.; s169[18] 10, DoSpar/wentLUT, Cnt.; s169[18] 10, DoSpar/wentLUT, Cnt.; s169[18] 11, DoSpar/wentLUT, Cnt.; s169[18] 12, DoSpar/wentLUT, Cnt.; s169[18] 13, DoSpar/wentLUT, Cnt.; s169[18] 14, DoSpar/wentLUT, Cnt.; s169[18] 15, DoSpar/wentLUT, Cnt.; s169[18] 16, DoSpar/wentLUT, Cnt.; s169[18] 17, DoSpar/wentLUT, Cnt.; s169[18] 18, DoSpar/wentLUT, Cnt.; s169[18] 19, DoSpar/wentLUT,	T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
12_Colsput/emieLUT_Cnt_1603 4    5   12_Colsput/emieLUT_Cnt_1603 4    5   12_Colsput/emieLUT_Cnt_1603 4    15   12_Colsput/emieLUT_Cnt_1603 6    15   12_Colsput/emieLUT_Cnt_1603 6    15   12_Colsput/emieLUT_Cnt_1603 6    15   12_Colsput/emieLUT_Cnt_1603 6    16   12_Colsput/emieLUT_Cnt_1603 10   13   12_Colsput/emieLUT_Cnt_1603 10   16   12_Colsput/emieLUT_Cnt_1603 10   16   12_Colsput/emieLUT_Cnt_1603 10   17   12_Colsput/emieLUT_Cnt_1603 10	T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
12_Colsput/emieLUT_Cnt_1603 4    5   12_Colsput/emieLUT_Cnt_1603 4    5   12_Colsput/emieLUT_Cnt_1603 4    15   12_Colsput/emieLUT_Cnt_1603 6    15   12_Colsput/emieLUT_Cnt_1603 6    15   12_Colsput/emieLUT_Cnt_1603 6    15   12_Colsput/emieLUT_Cnt_1603 6    16   12_Colsput/emieLUT_Cnt_1603 10   13   12_Colsput/emieLUT_Cnt_1603 10   16   12_Colsput/emieLUT_Cnt_1603 10   16   12_Colsput/emieLUT_Cnt_1603 10   17   12_Colsput/emieLUT_Cnt_1603 10	T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 2 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 2 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 15 17_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 17_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 18_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 19_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 19_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 19_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 10_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 11_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 11_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 12_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 14_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 15_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 16_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 17_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 18_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 18_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 19_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 10_ColSpur/wemeLUT_Cnt_1 sti3[SIS] 10_ColSpu		8
12_OSpur/emetUT_On_1 sti33lis   2   12_OSpur/emetUT_On_1 sti33lis   15   12_OSpur/emetUT_On_1 sti33lis   15   12_OSpur/emetUT_On_1 sti33lis   16   12_OSpur/emetUT_On_1 sti33lis   9   12_OSpur/emetUT_On_1 sti33lis   9   12_OSpur/emetUT_On_1 sti33lis   9   12_OSpur/emetUT_On_1 sti33lis   9   12_OSpur/emetUT_On_1 sti33lis   16   12_OSpur/emetUT_On_1 sti33lis   16   12_OSpur/emetUT_On_1 sti33lis   16   12_OSpur/emetUT_On_1 sti33lis   17   12_OSpur/emetUT_On_1 sti30lis   18   12_OSpur/emetU		5
12. Colspur/emicHUT. Cnt.; s163 87  13. Colspur/emicHUT. Cnt.; s163 87  14. Colspur/emicHUT. Cnt.; s163 87  15. Colspur/emicHUT. Cnt.; s163 87  17. Colspur/emicHUT. Cnt.; s163 87  18. Colspur/emicHUT. Cnt.; s163 87  19. Colspur/emicHUT. Cnt.; s16	T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
12   ColspurVernietUT_Cnt, 15(9)  9    9		
12_CoSpulvementUT_Cnt_st8[3]8] 9 12_CoSpulvementUT_Cnt_st8[3]9] 6 12_CoSpulvementUT_Cnt_st8[3]9] 16 12_CoSpulvementUT_Cnt_st8[3]19] 17 12_CoSpulvementUT_Cnt_st8[3]19] 18 12_CoSpulvementUT_Cnt_st8[3]19] 19 12_CoSpulvementUT_Cnt_st8[3]19] 19 12_CoSpulvementUT_Cnt_st8[3]19] 17 12_CoSpulvementUT_Cnt_st8[3]19] 17 12_CoSpulvementUT_Cnt_st8[3]19] 17 12_CoSpulvementUT_Cnt_st8[3]19] 17 12_CoSpulvementUT_Cnt_st8[3]19] 17 12_CoSpulvementUT_Cnt_st8[3]19] 19 12_CoSpulvementUT_Cnt		12
T2_Colspur/emetUT_Cnt_st63[9]   6		
12_ColSput/emierLUT_Cnt_s16[3][10]   3   3   3   3   3   3   3   3   3		
12. ColSpurVemierLUT_Cnt_s16(3)[11]   16   13   13   13   13   13   13   13		
12 ColSpurVemierLUT_Cnt_st6[3][12]		
12. ColSpurVernierLUT_Cnt_s16(3)[14]   7   7   7   7   7   7   7   7   7		
72   ColSpurVerniert.UT_Cnt_s16[3]14    7     72   ColSpurVerniert.UT_Cnt_s16[3]15    4     73   ColSpurVerniert.UT_Cnt_s16[3]16    17     74   ColSpurVerniert.UT_Cnt_s16[3]16    17     75   ColSpurVerniert.UT_Cnt_s16[3]16    17     76   ColSpurVerniert.UT_Cnt_s16[3]16    380     76   ColSpurVerniert.UT_Cnt_s16[3]19    380     72   DualSpurVerniert.UT_Cnt_s16[3]2  324     73   DualSpurVerniert.UT_Cnt_s16[3]3  428     74   DualSpurVerniert.UT_Cnt_s16[3]3  428     75   DualSpurVerniert.UT_Cnt_s16[3]3  428     75   DualSpurVerniert.UT_Cnt_s16[3]4  425     75   DualSpurVerniert.UT_Cnt_s16[3]5  418     76   DualSpurVerniert.UT_Cnt_s16[3]5  418     77   DualSpurVerniert.UT_Cnt_s16[3]5  418     78   DualSpurVerniert.UT_Cnt_s16[3]5  418     79   DualSpurVerniert.UT_Cnt_s16[3]5  418     70   DualSpurVerniert.UT_Cnt_s16[3]5  418     71   DualSpurVerniert.UT_Cnt_s16[3]5  418     71   DualSpurVerniert.UT_Cnt_s16[3]5  418     72   DualSpurVerniert.UT_Cnt_s16[3]5  418     73   DualSpurVerniert.UT_Cnt_s16[3]5  418     74   DualSpurVerniert.UT_Cnt_s16[3]5  418     75   DualSpurVerniert.UT_Cnt_s16[3]5  418     76   DualSpurVerniert.UT_Cnt_s16[3]5  418     77   DualSpurVerniert.UT_Cnt_s16[3]5  418     78   DualSpurVerniert.UT_Cnt_s16[3]5  418     79   DualSpur		
12_ColSpurVemierLUT_Cnt_s16[3]15  4   17_ColSpurVemierLUT_Cnt_s16[3]15  17  17  2.   17_DualSpurVemierLUT_Cnt_s16[3]16  396   17_DualSpurVemierLUT_Cnt_s16[0]11  380   17_DualSpurVemierLUT_Cnt_s16[0]2  324   17_DualSpurVemierLUT_Cnt_s16[0]2  324   17_DualSpurVemierLUT_Cnt_s16[0]3  -288   17_DualSpurVemierLUT_Cnt_s16[0]4  -252   17_DualSpurVemierLUT_Cnt_s16[0]5  -216   17_DualSpurVemierLUT_Cnt_s16[0]6  -180   17_DualSpurVemierLUT_Cnt_s16[0]7  -144   17_DualSpurVemierLUT_Cnt_s16[0]8  -180   17_DualSpurVemierLUT_Cnt_s16[0]8  -180   17_DualSpurVemierLUT_Cnt_s16[0]9  -72   17_DualSpurVemierLUT_Cnt_s16[0]9  -72   17_DualSpurVemierLUT_Cnt_s16[0]11  0   17_DualSpurVemierLUT_Cnt_s16[0]11  0   17_DualSpurVemierLUT_Cnt_s16[0]11  0   17_DualSpurVemierLUT_Cnt_s16[0]11  0   17_DualSpurVemierLUT_Cnt_s16[0]11  1   17_DualSpurVemierLUT_Cnt_s16[0]11  1   18_DualSpurVemierLUT_Cnt_s16[0]11  2   18_DualSpurVemierLUT_Cnt_s16[0]11  3   18_DualSpurVemierLUT_Cnt_s16[0]11  3   18_DualSpurVemierLUT_Cnt_s16[0]11  3   18_DualSpurVemierLUT_Cnt_s16[0]11  3   18_DualSpurVemierLUT_Cnt_s16[0]11  3   18_DualSpurVemierLUT_Cnt_s16[0]12  3   18_DualSpurVemierLUT_Cnt_s16[0]12  3   18_DualSpurVemierLUT_Cnt_s16[0]12  3   18_DualSpurVemierLUT_Cnt_s16[0]12  5   18_DualSpurVemierLUT_Cnt_s16[0]12  5   18_DualSpurVemierLUT_Cnt_s16[0]12  5   18_DualSpurVemierLUT_Cnt_s16[0]12  5   18_DualSpurVemierLUT_Cnt_s16[0]12  6   18_DualSpurVemierLUT_Cnt_s16[0]12  6   18_DualSpurVemierLUT_Cnt_s16[0]12  6   18_DualSpurVemierLUT_Cnt_s16[0]12  6   18_DualSpurVemierLUT_Cnt_s16[0]12  7   18_DualSpurVemierLUT_Cnt_s16[0]12  6   18_DualSpurVemierLUT_Cnt_s16[0]12  7   18_DualSpurVemierLUT_Cnt_s16[0]12  7   18_DualSpurVemierLUT_Cnt_s16[0]12  7   18_DualSpurVemierLUT_Cnt_s		
17		
12_DualSpurVemierLUT_Cnt_s16[0][1]   386		
12_Dus SpurVernierLUT_Cnt_st6[0][1]   380     12_Dus SpurVernierLUT_Cnt_st6[0][2]   324     12_Dus SpurVernierLUT_Cnt_st6[0][3]   288     12_Dus SpurVernierLUT_Cnt_st6[0][4]   252     12_Dus SpurVernierLUT_Cnt_st6[0][6]   480     12_Dus SpurVernierLUT_Cnt_st6[0][6]   480     12_Dus SpurVernierLUT_Cnt_st6[0][7]   444     12_Dus SpurVernierLUT_Cnt_st6[0][9]   472     12_Dus SpurVernierLUT_Cnt_st6[0][9]   472     12_Dus SpurVernierLUT_Cnt_st6[0][9]   472     12_Dus SpurVernierLUT_Cnt_st6[0][1]   480     12_Dus SpurVernierLUT_Cnt_st6[0][1]   580     12_Dus SpurVernierLUT_Cnt_st6[0][1]   580     12_Dus SpurVernierLUT_Cnt_st6[0][1]   680     12_Dus SpurVernierLUT_Cnt_st6[0][1]   680     12_Dus SpurVernierLUT_Cnt_st6[0][1]   780     180     180     180     180     180     180     180     180     18		
12_DualSpurVernierLUT_Cnt_s16[0][2]   -288       12_DualSpurVernierLUT_Cnt_s16[0][3]   -288       12_DualSpurVernierLUT_Cnt_s16[0][4]   -252       12_DualSpurVernierLUT_Cnt_s16[0][5]   -216       12_DualSpurVernierLUT_Cnt_s16[0][7]   -144       12_DualSpurVernierLUT_Cnt_s16[0][7]   -144       12_DualSpurVernierLUT_Cnt_s16[0][8]   -108       12_DualSpurVernierLUT_Cnt_s16[0][9]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -38       12_DualSpurVernierLUT_Cnt_s16[0][10]   -38       12_DualSpurVernierLUT_Cnt_s16[0][10]   -38       12_DualSpurVernierLUT_Cnt_s16[0][10]   -36       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72   -72       12_DualSpurVernierLUT_Cnt_s16[0][10]   -72   -		
72_DualSpurVernierLUT_Cnt_st6[0] 3    -288     72_DualSpurVernierLUT_Cnt_st6[0] 4    -252     72_DualSpurVernierLUT_Cnt_st6[0] 5    -180     72_DualSpurVernierLUT_Cnt_st6[0] 7    -144     72_DualSpurVernierLUT_Cnt_st6[0] 8    -108     72_DualSpurVernierLUT_Cnt_st6[0] 9    -72     72_DualSpurVernierLUT_Cnt_st6[0] 9    -72     72_DualSpurVernierLUT_Cnt_st6[0] 10    -36     72_DualSpurVernierLUT_Cnt_st6[0] 11    0     72_DualSpurVernierLUT_Cnt_st6[0] 12    36     72_DualSpurVernierLUT_Cnt_st6[0] 13    72     72_DualSpurVernierLUT_Cnt_st6[0] 14    108     72_DualSpurVernierLUT_Cnt_st6[0] 15    144     72_DualSpurVernierLUT_Cnt_st6[0] 16    180     72_DualSpurVernierLUT_Cnt_st6[0] 17    216     72_DualSpurVernierLUT_Cnt_st6[0] 17    216     72_DualSpurVernierLUT_Cnt_st6[0] 19    288     72_DualSpurVernierLUT_Cnt_st6[0] 19    288     72_DualSpurVernierLUT_Cnt_st6[0] 20    324     72_DualSpurVernierLUT_Cnt_st6[0] 20    324     72_DualSpurVernierLUT_Cnt_st6[0] 20    324     72_DualSpurVernierLUT_Cnt_st6[0] 20    324     72_DualSpurVernierLUT_Cnt_st6[0] 20    9     72_DualSpurVernierLUT_Cnt_st6[0] 20    9     72_DualSpurVernierLUT_Cnt_st6[0] 20    9     72_DualSpurVernierLUT_Cnt_st6[0] 20    3     72_DualSpurVernierLUT_Cnt_st6[0] 20    9     72_DualSpurVernierLUT_Cnt_st6[0] 20    5     72_DualSpurVernierLUT_Cnt_st6[0] 20    5     72_DualSpurVernierLUT_Cnt_st6[0] 20    5     72_DualSpurVernierLUT_Cnt_st6[0] 20    6     72_DualSpurVernierLUT_Cnt_st6[0] 20    7     72_DualSpurVernierLUT_Cnt_st6[0] 20    7     72_DualSpurVernierLUT_Cnt_st6[0] 20    8     72_DualSpurVernierLUT_Cnt_st6[0] 20    9     72_DualSpurVernierLUT_Cnt_		
12_DualSpurVernierLUT_Cnt_st6[0][4]   -252		
12   DualSpurVermierLUT_Cnt_s16(0)  5    -216   -		
T2_DualSpurVernierLUT_Cnt_s16[0][6]   -180     -180       -180       -180       -180       -180         -180		
T2_DualSpurVernierLUT_Cnt_s16(0)[8]       -108         T2_DualSpurVernierLUT_Cnt_s16(0)[8]       -72         T2_DualSpurVernierLUT_Cnt_s16(0)[10]       -36         T2_DualSpurVernierLUT_Cnt_s16(0)[11]       0         T2_DualSpurVernierLUT_Cnt_s16(0)[12]       36         T2_DualSpurVernierLUT_Cnt_s16(0)[12]       36         T2_DualSpurVernierLUT_Cnt_s16(0)[14]       108         T2_DualSpurVernierLUT_Cnt_s16(0)[14]       108         T2_DualSpurVernierLUT_Cnt_s16(0)[15]       144         T2_DualSpurVernierLUT_Cnt_s16(0)[16]       180         T2_DualSpurVernierLUT_Cnt_s16(0)[17]       216         T2_DualSpurVernierLUT_Cnt_s16(0)[18]       252         T2_DualSpurVernierLUT_Cnt_s16(0)[19]       324         T2_DualSpurVernierLUT_Cnt_s16(0)[19]       324         T2_DualSpurVernierLUT_Cnt_s16(0)[19]       324         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       0         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       0         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       0         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       1         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       4         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       4         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       6         T2_DualSpurVernierLUT_Cnt_s16(1)[1]       6		
T2_DualSpurVernierLUT_Cnt_s16[0][8]   -108     -72		
T2_DualSpurVernierLUT_Cnt_s16[0][19]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][11]       36         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216         T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       1         T2_DualSpurVernierLUT_Cnt_s16[1][1]       2         T2_DualSpurVernierLUT_Cnt_s16[1][1]       3         T2_DualSpurVernierLUT_Cnt_s16[1][1]       4         T2_DualSpurVernierLUT_Cnt_s16[1][1]       5         T2_DualSpurVernierLUT_Cnt_s16[1][1]       6         T2_DualSpurVernierLUT_Cnt_s16[1][1]       6         T2_DualSpurVernierLUT_Cnt_s16[1][1]       8         T2_DualSpurVernierLUT_Cnt_s16[1][1]       9		
T2_DualSpurVernierLUT_Cnt_s16[0][10]   -36     T2_DualSpurVernierLUT_Cnt_s16[0][11]   0     T2_DualSpurVernierLUT_Cnt_s16[0][12]   36     T2_DualSpurVernierLUT_Cnt_s16[0][13]   72     T2_DualSpurVernierLUT_Cnt_s16[0][14]   108     T2_DualSpurVernierLUT_Cnt_s16[0][15]   144     T2_DualSpurVernierLUT_Cnt_s16[0][16]   180     T2_DualSpurVernierLUT_Cnt_s16[0][17]   216     T2_DualSpurVernierLUT_Cnt_s16[0][18]   252     T2_DualSpurVernierLUT_Cnt_s16[0][19]   288     T2_DualSpurVernierLUT_Cnt_s16[0][20]   324     T2_DualSpurVernierLUT_Cnt_s16[0][21]   360     T2_DualSpurVernierLUT_Cnt_s16[1][0]   9     T2_DualSpurVernierLUT_Cnt_s16[1][1]   0     T2_DualSpurVernierLUT_Cnt_s16[1][1]   0     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][1]   4     T2_DualSpurVernierLUT_Cnt_s16[1][1]   6     T2_DualSpurVernierLUT_Cnt_s16[1][1]   6     T2_DualSpurVernierLUT_Cnt_s16[1][1]   6     T2_DualSpurVernierLUT_Cnt_s16[1][1]   6     T2_DualSpurVernierLUT_Cnt_s16[1][1]   6     T2_DualSpurVernierLUT_Cnt_s16[1][1]   7     T2_DualSpurVernierLUT_Cnt_s16[1][1]   8     T2_DualSpurVernierLUT_Cnt_s16[1][1]   9     T2_DualSpurVernierLUT_Cnt_s16[1][1]   0     T2_DualSpurVernierLUT_Cnt_s16[1]		
T2_DualSpurVernierLUT_Cnt_s16[0][11]         0           T2_DualSpurVernierLUT_Cnt_s16[0][12]         36           T2_DualSpurVernierLUT_Cnt_s16[0][13]         72           T2_DualSpurVernierLUT_Cnt_s16[0][14]         108           T2_DualSpurVernierLUT_Cnt_s16[0][15]         144           T2_DualSpurVernierLUT_Cnt_s16[0][16]         180           T2_DualSpurVernierLUT_Cnt_s16[0][17]         216           T2_DualSpurVernierLUT_Cnt_s16[0][18]         252           T2_DualSpurVernierLUT_Cnt_s16[0][19]         288           T2_DualSpurVernierLUT_Cnt_s16[0][20]         324           T2_DualSpurVernierLUT_Cnt_s16[0][21]         360           T2_DualSpurVernierLUT_Cnt_s16[1][1]         9           T2_DualSpurVernierLUT_Cnt_s16[1][1]         0           T2_DualSpurVernierLUT_Cnt_s16[1][1]         1           T2_DualSpurVernierLUT_Cnt_s16[1][4]         3           T2_DualSpurVernierLUT_Cnt_s16[1][6]         4           T2_DualSpurVernierLUT_Cnt_s16[1][6]         5           T2_DualSpurVernierLUT_Cnt_s16[1][6]         6           T2_DualSpurVernierLUT_Cnt_s16[1][6]         6           T2_DualSpurVernierLUT_Cnt_s16[1][6]         6           T2_DualSpurVernierLUT_Cnt_s16[1][6]         8           T2_DualSpurVernierLUT_Cnt_s16[1][6]         6		
T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216         T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       6         T2_DualSpurVernierLUT_Cnt_s16[1][6]       8         T2_DualSpurVernierLUT_Cnt_s16[1][1]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0      <		
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T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  144  T2_DualSpurVernierLUT_Cnt_s16[0][16]  180  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][0]  12_DualSpurVernierLUT_Cnt_s16[1][1]  0  T2_DualSpurVernierLUT_Cnt_s16[1][2]  1  T2_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][7]  6  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9		
T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1  D0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][1] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1  D0  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1  D1  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1  D1  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1  D1  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1  D1  T2_DualSpurVernierLUT_Cnt_s16[1][1] 1		
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T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0          T2_DualSpurVernierLUT_Cnt_s16[1][10]       0 <td></td> <td></td>		
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][6] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 1  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 1		
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       1		
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T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
- ' '		
12_Dual-open verifier.C01_Crit_510[1][13]		
	12_DuaiSpui VerilletLU1_Citt_510[1][13]	4

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148.05719

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 ${\tt DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32}$ 

148.0571972 ± 0.00048828125

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>~</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	68	68	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	50	50	<b>~</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	13.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	9945
DigColPs_I2CHwColAngle_Deg_M_f32	253.5686912
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	37553
DigColPs_I2CHwSpurAngle_Deg_M_f32	45.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	4
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	319.1410994
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs SpurTrimStatic Deg M f32	45.4
DigColPs TrimCompStatic Cnt M u16	1420
DigColPs VernCorrDetectAcc Cnt M u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2 ColSpurVernierLUT Cnt s16[0][5]	0
T2 ColSpurVernierLUT Cnt s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2 ColSpurVernierLUT Cnt s16[0][9]	130
T2 ColSpurVernierLUT Cnt s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15] T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	124
k_SkipStepErrDiag_Cnt_str.PStep	50
k_SkipStepErrDiag_Cnt_str.NStep	31
k_VernCorrErrorDiag_Cnt_str.Threshold	80
k_VernCorrErrorDiag_Cnt_str.PStep	3
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	1
k_VernOORangeThresh_Deg_f32	414.5643529
_ = 0_	



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	253.5686912		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.5532733		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2452		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	<b>~</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1319.76868	1319.768691 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6	6	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	409.09082	409.0909091 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.44 (Repeat Count = 1)	<b>√</b>
Name	Input Value
DigColPsInt GetCustData()	158
DigColPs ColParityError Cnt M Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs ColTrimStatic Deg M f32	17.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs I2CHwColAngle Cnt M u16	37674
DigColPs I2CHwColAngle Deg M f32	266.7729402
DigColPs I2CHwDataType Cnt M u08	0
DigColPs I2CHwSpurAngle Cnt M u16	64843
DigColPs I2CHwSpurAngle Deg M f32	46.5
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	12
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1117.242519
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158
DigColPs_SpurTrimStatic_Deg_M_f32	46.5
DigColPs_TrimCompStatic_Cnt_M_u16	1456
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T3_ColSpurVernierLUT_Cnt_s46[4][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][0]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s46[3][40]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2 DualSpurVernierLUT Cnt s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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DigColPs\_Per2

Status

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	132		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	27		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	43		
k_VernCorrErrorThresh_Deg_f32	59.61001611		
k_VernOORangeThresh_Deg_f32	220.0944071		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	266.7729402		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.5881469		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1187		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAb	sPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState	e_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimCom	p_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	968.872925	968.8729402 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	<b>✓</b>
Param	0x00	0x00	~
0	0.00	0.00	

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x00

0x00

Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	125
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	22
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	16067
DigColPs_I2CHwColAngle_Deg_M_f32	272.6490288
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16937
DigColPs_I2CHwSpurAngle_Deg_M_f32	47.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1





Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	1733.007516
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	47.6 1492
DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigCoIPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1 0
T2_ColSpurVernierLUT_Cnt_s16[1][5] T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3

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	l
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
	5
T2_DualSpurVernierLUT_Cnt_s16[2][16]	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	9 10
	9

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DigCoiPs_Per2		(02	2010ab
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2 DualSpurVernierLUT Cnt s16[3][6]	12		
T2 DualSpurVernierLUT Cnt s16[3][7]	14		
T2 DualSpurVernierLUT Cnt s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	80		
k_SkipStepErrDiag_Cnt_str.PStep	43		
k_SkipStepErrDiag_Cnt_str.NStep	7		
k_VernCorrErrorDiag_Cnt_str.Threshold	6		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	14		
k_VernCorrErrorThresh_Deg_f32	86.69760323		
k_VernOORangeThresh_Deg_f32	1173.76136		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	272.6490288		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.17228091		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	621		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cn		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	970.649048	970.6490288 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7	7	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	-
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tet Discoulds Dark 1901 by Alsa Dark Islands	•	•	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	<b>✓</b>
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	<b>~</b>
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>V</b>

70.6490479

0

0

0

70.64902878 ± 0.00009

Test Step 2.46 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	165
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	26.1

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56371
DigColPs_I2CHwColAngle_Deg_M_f32	296.9508778
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11434
DigColPs_I2CHwSpurAngle_Deg_M_f32	48.7
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	17 0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	267.2598278
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
DigColPs_SpurTrimStatic_Deg_M_f32	48.7
DigColPs_TrimCompStatic_Cnt_M_u16	1528
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130 163
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1]	0 8
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1.
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T3_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
12 Duan-Opur volino (201 Ont 310   2   1   1	•
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

DigColPs\_Per2





Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[2][13]		2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3			
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4			
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5			
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6			
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22			
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4			
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6			
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8			
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	1			
k_SkipStepErrDiag_Cnt_str.Threshold	57			
k_SkipStepErrDiag_Cnt_str.PStep	9			
k_SkipStepErrDiag_Cnt_str.NStep	18			
k_VernCorrErrorDiag_Cnt_str.Threshold	42			
k_VernCorrErrorDiag_Cnt_str.PStep	11			
k_VernCorrErrorDiag_Cnt_str.NStep	16			
k_VernCorrErrorThresh_Deg_f32	9.823269606			
k_VernOORangeThresh_Deg_f32	664.8207433			
tgt DigColPs Per2 MecState Cnt enum.value	1			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	296.9508778			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	182.5995052			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	189			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt DigColPs Per2 I2CHwAbsPos	Valid Cnt Inc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum				
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cn	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt Rte Inst Sa DigColPs.Pim DigColPsEOL	tgt_DigColPs_Per2_Trifficomp_Cri	i.c.igo		
		Expected Value	Doord	
Name	Actual Value	Expected Value	Resul	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	•	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	•	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•	
DigColPs_PrevColPos_Deg_M_f32	990.85083	990.8508778 ± 0.0001220703125	•	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	,	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•	
DI O ID OLI OL FUD L LA OLI MA 40	4			

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	990.85083	990.8508778 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	90.8508301	90.85087785 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>~</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.47 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
DigColPsInt_GetCustData()	144
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	30.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	4911
DigColPs I2CHwColAngle Deg M f32	75.69248641
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	45521
DigColPs I2CHwSpurAngle Deg M f32	49.8
DigColPs I2CHwTrimTransCnts Uls M u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	21
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1606.298487
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs SpurParityError Cnt M Igc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
DigColPs_SpurTrimStatic_Deg_M_f32	49.8
DigColPs TrimCompStatic Cnt M u16	1564
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Input Value  1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

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Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T0_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
T2 DualSpurVernierLUT Cnt s16[2][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	14 16	
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20	
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3	
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7	
T2 DualSpurVernierLUT Cnt s16[3][15]	9	
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11	
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13	
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15	
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17	
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19	
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21	
k_SelectFromColumn_Cnt_lgc	1	
k_SkipStepErrDiag_Cnt_str.Threshold	27	
k_SkipStepErrDiag_Cnt_str.PStep	36	
k_SkipStepErrDiag_Cnt_str.NStep	31	
k_VernCorrErrorDiag_Cnt_str.Threshold	8	
k_VernCorrErrorDiag_Cnt_str.PStep	37	
k_VernCorrErrorDiag_Cnt_str.NStep	5	
k_VernCorrErrorThresh_Deg_f32	21.03098726	
k_VernOORangeThresh_Deg_f32	132.1493682	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.69248641	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.71645284	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1 tot DigColDo Dorg 12CHwAhoDooValid Cot Jac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	D
Name	Actual Value Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1  1	

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.48 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	34.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	28758
DigColPs_I2CHwColAngle_Deg_M_f32	169.2136934
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1113
DigColPs I2CHwSpurAngle Deg M f32	50.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	1
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs PrevAngleDataAvailable Cnt M Igc	1
DigColPs PrevColPos Deg M f32	1698.48323
DigColPs PrevVernierLevelNo Cnt M u08	3
DigColPs SkipStepFltDetectAcc Cnt M u16	20
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	50.9
DigColPs_TrimCompStatic_Cnt_M_u16	1600
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





	(
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][6]	
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
	144
T2_DualSpurVernierLUT_Cnt_s16[0][15]	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	90
k_SkipStepErrDiag_Cnt_str.PStep	34
k_SkipStepErrDiag_Cnt_str.NStep	39
It VaraCareFreeDiag Cat at Threehold	10
k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep	33





Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	64.4036839		
k_VernOORangeThresh_Deg_f32	1423.580669		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	169.2136934		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	135.3572482		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3790		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

tgt_tttc_mot_cd_bigcon o.r im_bigcon obcc	tgC III_Digoon obot		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	494.913696	494.9136934 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-405.086304	-405.0863066 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.49 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetCustData()	143		
DigColPs ColParityError Cnt M Igc	0		
DigColPs ColSensorFaultAcc Cnt M u16	184		
DigColPs ColTrimStatic Deg M f32	38.4		
DigColPs HwAVernCorrFault Cnt M Igc	0		
DigColPs 12CColSensorFault Cnt M Igc	0		
DigColPs I2CHwColAngle Cnt M u16	49805		
DigColPs_I2CHwColAngle_Deg_M_f32	313.3494742		
DigColPs I2CHwDataType Cnt M u08	3		
DigColPs I2CHwSpurAngle Cnt M u16	22222		
DigColPs_I2CHwSpurAngle_Deg_M_f32	52		
DigColPs I2CHwTrimTransCnts UIs M u08	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	20		
DigColPs I2CSpurSensorFault Cnt M Igc	0		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1		
DigColPs_PrevColPos_Deg_M_f32	687.96434		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0		
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0		
DigColPs_SpurParityError_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	143		
DigColPs_SpurTrimStatic_Deg_M_f32	52		
DigColPs_TrimCompStatic_Cnt_M_u16	1636		
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10		
DigColPs_VernierAngleOORange_Cnt_M_lgc	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163		
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131		
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99		
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66		
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33		





	( 10 10 10 10
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2 ColSpurVernierLUT Cnt s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
Γ2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierEUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
Γ2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
F2_ColSpurVernierLUT_Cnt_s16[3][15]	4
C2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
rz_bualspurvernierL01_cnt_s16[0][4]  F2_bualspurvernierLUT_cnt_s16[0][5]	-216
	-216 -180
T2_DualSpurVernierLUT_Cnt_s16[0][6] T2_DualSpurVernierLUT_Cnt_s16[0][7]	
	-144
	400
T2_DualSpurVernierLUT_Cht_s16[0][8] T2_DualSpurVernierLUT_Cht_s16[0][8] T2_DualSpurVernierLUT_Cht_s16[0][9]	-108 -72

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2 DualSpurVernierLUT Cnt s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	218		
k_SkipStepErrDiag_Cnt_str.PStep	31		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	47.04804516		
k_VernOORangeThresh_Deg_f32	914.2227411		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	313.3494742		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	162.8974475		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2322		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	994.949463	994.9494742 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	94.9494629	94.94947419 ± 0.00009	<b>✓</b>
tot DigColDo Dor? TrimComp Cot Igo value	0	0	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

0

0

Test Step 2.50 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	131	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	
DigColPs_ColTrimStatic_Deg_M_f32	42.5	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	16100	
DigColPs_I2CHwColAngle_Deg_M_f32	299.0314264	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	45386	
DigColPs_I2CHwSpurAngle_Deg_M_f32	53.1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	
DigColPs_I2CSensCommFlts_Cnt_M_u08	28	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1756.602492	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	255	
DigColPs_SpurParityError_Cnt_M_lgc	0	

 $\underline{\mathsf{tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value}}$ 





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	131
DigColPs_SpurTrimStatic_Deg_M_f32	53.1
DigColPs_TrimCompStatic_Cnt_M_u16	1672
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2 ColSpurVernierLUT Cnt s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSputVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

DigColPs\_Per2

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Input Value T2\_ColSpurVernierLUT\_Cnt\_s16[3][16] 17 T2\_DualSpurVernierLUT\_Cnt\_s16[0][0] -396 T2\_DualSpurVernierLUT\_Cnt\_s16[0][1] -360 T2\_DualSpurVernierLUT\_Cnt\_s16[0][2] -324 T2\_DualSpurVernierLUT\_Cnt\_s16[0][3] -288 T2\_DualSpurVernierLUT\_Cnt\_s16[0][4] -252 T2\_DualSpurVernierLUT\_Cnt\_s16[0][5] -216 T2\_DualSpurVernierLUT\_Cnt\_s16[0][6] -180 T2\_DualSpurVernierLUT\_Cnt\_s16[0][7] -144 T2\_DualSpurVernierLUT\_Cnt\_s16[0][8] -108 T2\_DualSpurVernierLUT\_Cnt\_s16[0][9] -72 T2\_DualSpurVernierLUT\_Cnt\_s16[0][10] -36 T2\_DualSpurVernierLUT\_Cnt\_s16[0][11] 0 T2\_DualSpurVernierLUT\_Cnt\_s16[0][12] 36 T2\_DualSpurVernierLUT\_Cnt\_s16[0][13] 72 108 T2\_DualSpurVernierLUT\_Cnt\_s16[0][14] T2\_DualSpurVernierLUT\_Cnt\_s16[0][15] 144 T2\_DualSpurVernierLUT\_Cnt\_s16[0][16] 180 T2\_DualSpurVernierLUT\_Cnt\_s16[0][17] 216 T2\_DualSpurVernierLUT\_Cnt\_s16[0][18] 252 T2\_DualSpurVernierLUT\_Cnt\_s16[0][19] 288 T2\_DualSpurVernierLUT\_Cnt\_s16[0][20] 324 T2\_DualSpurVernierLUT\_Cnt\_s16[0][21] 360 T2\_DualSpurVernierLUT\_Cnt\_s16[1][0] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[1][1] 0 T2\_DualSpurVernierLUT\_Cnt\_s16[1][2] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[1][3] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[1][4] 3 T2 DualSpurVernierLUT Cnt s16[1][5] 4 T2\_DualSpurVernierLUT\_Cnt\_s16[1][6] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[1][7] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[1][8] 7 T2 DualSpurVernierLUT Cnt s16[1][9] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[1][10] 9 0 T2\_DualSpurVernierLUT\_Cnt\_s16[1][11] T2\_DualSpurVernierLUT\_Cnt\_s16[1][12] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[1][13] 2 3 T2\_DualSpurVernierLUT\_Cnt\_s16[1][14] T2\_DualSpurVernierLUT\_Cnt\_s16[1][15] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[1][16] T2\_DualSpurVernierLUT\_Cnt\_s16[1][17] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[1][18] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[1][19] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[1][20] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[1][21] n T2\_DualSpurVernierLUT\_Cnt\_s16[2][0] 0 T2 DualSpurVernierLUT Cnt s16[2][1] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[2][2] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[2][3] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[2][4] 4 T2 DualSpurVernierLUT Cnt s16[2][5] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[2][6] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[2][7] T2\_DualSpurVernierLUT\_Cnt\_s16[2][8] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[2][9] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[2][10] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[2][11] 0 T2\_DualSpurVernierLUT\_Cnt\_s16[2][12] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[2][13] 2 3 T2\_DualSpurVernierLUT\_Cnt\_s16[2][14] T2\_DualSpurVernierLUT\_Cnt\_s16[2][15] 4 5 T2 DualSpurVernierLUT Cnt s16[2][16] T2\_DualSpurVernierLUT\_Cnt\_s16[2][17] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[2][18] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[2][19] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[2][20] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[2][21] 10 T2 DualSpurVernierLUT Cnt s16[3][0] 22 T2\_DualSpurVernierLUT\_Cnt\_s16[3][1] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[3][2] T2\_DualSpurVernierLUT\_Cnt\_s16[3][3] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[3][4] 8 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][5]

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	161		
k_SkipStepErrDiag_Cnt_str.PStep	1		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	79		
k_VernCorrErrorDiag_Cnt_str.PStep	19		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	29.57760787		
k_VernOORangeThresh_Deg_f32	320.9261016		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	299.0314264		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	107.5765935		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3593		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwD	0eg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~

0= = = 0 = 0	0 = 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	976.531433	976.5314264 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	161	161	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0E	0x0E	~
Status	0x01	0x01	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>~</b>
ReleaseResource	1	ReleaseResource	1	<b>~</b>
ConstrainOneRev	2	ConstrainOneRev	2	<b>~</b>
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.51 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	46.6

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Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	24812
DigColPs_I2CHwColAngle_Deg_M_f32	148.1551865
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7234
DigColPs_I2CHwSpurAngle_Deg_M_f32	54.2 2
DigColPs_I2CHwTrimTransCnts_UIs_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_12CSenscommrits_Cnt_w_uoo  DigColPs_12CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	724.8810905
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	54.2
DigColPs_TrimCompStatic_Cnt_M_u16	1708
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130 163
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSputVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	8
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2]	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
	-
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10





Name	
	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
	-360
T2_DualSpurVernierLUT_Cnt_s16[0][1]	
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2 DualSpurVernierLUT Cnt s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2 DualSpurVernierLUT Cnt s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[1][14] T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][8]	
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][9]	

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T2_DusSynVernetUT_Cnt_st@2[15]   2   T2_DusSynVernetUT_Cnt_st@2[15]   4   T2_DusSynVernetUT_Cnt_st@2[15]   5   T2_DusSynVernetUT_Cnt_st@2[15]   5   T2_DusSynVernetUT_Cnt_st@2[17]   6   T2_DusSynVernetUT_Cnt_st@2[17]   7   T2_DusSynVernetUT_Cnt_st@3[17]   2   T2_DusSynVernetUT_Cnt_st@3[17]   2   T2_DusSynVernetUT_Cnt_st@3[17]   7   T2_DusSyn	Name	Input Value		
12, DuaSpurVemet.UT_Cnt_s162  16    5     12, DuaSpurVemet.UT_Cnt_s162  16    5     12, DuaSpurVemet.UT_Cnt_s162  17    6     12, DuaSpurVemet.UT_Cnt_s162  18    7     12, DuaSpurVemet.UT_Cnt_s162  19    8     12, DuaSpurVemet.UT_Cnt_s162  19    9     12, DuaSpurVemet.UT_Cnt_s162  20    9     12, DuaSpurVemet.UT_Cnt_s162  21    10     17, DuaSpurVemet.UT_Cnt_s162  21    10     17, DuaSpurVemet.UT_Cnt_s162  21    10     18, DuaSpurVemet.UT_Cnt_s163  31    2     19, DuaSpurVemet.UT_Cnt_s163  31    2     12, DuaSpurVemet.UT_Cnt_s163  31    4     12, DuaSpurVemet.UT_Cnt_s163  31    6     12, DuaSpurVemet.UT_Cnt_s163  31    8     12, DuaSpurVemet.UT_Cnt_s163  31    10     13, DuaSpurVemet.UT_Cnt_s163  31    10     14, DuaSpurVemet.UT_Cnt_s163  31    10     15, DuaSpurVemet.UT_Cnt_s163  31    10     16, DuaSpurVemet.UT_Cnt_s163  31    10     17, DuaSpurVemet.UT_Cnt_s163  31    10     18, DuaSpurVemet.UT_Cnt_s163  31    10     19, DuaSpurVemet.UT_Cnt_s163  31    10     10, DuaSpurVemet.UT_Cnt_s163  31    10     11, DuaSpurVemet.UT_Cnt_s163  31    10     12, DuaSpurVemet.UT_Cnt_s163  31    10     13, DuaSpurVemet.UT_Cnt_s163  31    10     14, DuaSpurVemet.UT_Cnt_s163  31    10     15, DuaSpurVemet.UT_Cnt_s163  31    10     16, DuaSpurVemet.UT_Cnt_s163  31    10     17, DuaSpurVemet.UT_Cnt_s163  31    10     18, DuaSpurVemet.UT_Cnt_s163  31    10     19, DuaSpurVemet.UT_Cnt_s163  31	T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
12_DuaSpurVement.UT_Cnt_stig2[16]   5	T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
17_ DualSpurVemietUT_Cnt_s16[2][17]   6     17_ DualSpurVemietUT_Cnt_s16[2][18]   7     17_ DualSpurVemietUT_Cnt_s16[2][19]   8     18_ DualSpurVemietUT_Cnt_s16[2][2]   9     19_ DualSpurVemietUT_Cnt_s16[2][2]   10     19_ DualSpurVemietUT_Cnt_s16[3][0]   22     19_ DualSpurVemietUT_Cnt_s16[3][0]   2     19_ DualSpurVemietUT_Cnt_s16[3][0]   2     19_ DualSpurVemietUT_Cnt_s16[3][0]   4     19_ DualSpurVemietUT_Cnt_s16[3][0]   4     19_ DualSpurVemietUT_Cnt_s16[3][0]   6     19_ DualSpurVemietUT_Cnt_s16[3][0]   8     19_ DualSpurVemietUT_Cnt_s16[3][0]   10     19_ DualSpurVemietUT_Cnt_s16[3][0]   10     19_ DualSpurVemietUT_Cnt_s16[3][0]   10     19_ DualSpurVemietUT_Cnt_s16[3][0]   10     19_ DualSpurVemietUT_Cnt_s16[3][0]   16     19_ DualSpurVemietUT_Cnt_s16[3][0]   16     19_ DualSpurVemietUT_Cnt_s16[3][0]   16     19_ DualSpurVemietUT_Cnt_s16[3][0]   18     19_ DualSpurVemietUT_Cnt_s16[3][0]   19     19_ DualSpurVemietUT_Cnt_s16[3][0]   10     10_ DualSpurVemietUT_Cnt_s1	T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
12_DualSpurVemietUT_Cnt_s16[2][18]   7   8   8   8   8   8   8   8   8   8	T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
12_DualSpurVernierLUT_Cnt_s16[2]19]   8   12_DualSpurVernierLUT_Cnt_s16[2]20]   9   12_DualSpurVernierLUT_Cnt_s16[2]21]   10   12_DualSpurVernierLUT_Cnt_s16[3]0]   22   12_DualSpurVernierLUT_Cnt_s16[3]1]   2   12_DualSpurVernierLUT_Cnt_s16[3]2]   4   12_DualSpurVernierLUT_Cnt_s16[3]2]   4   12_DualSpurVernierLUT_Cnt_s16[3]3]   6   12_DualSpurVernierLUT_Cnt_s16[3]4  8   12_DualSpurVernierLUT_Cnt_s16[3]6]   10   12_DualSpurVernierLUT_Cnt_s16[3]6]   10   12_DualSpurVernierLUT_Cnt_s16[3]6]   12   12_DualSpurVernierLUT_Cnt_s16[3]6]   12   12_DualSpurVernierLUT_Cnt_s16[3]7]   14   12_DualSpurVernierLUT_Cnt_s16[3]7]   14   12_DualSpurVernierLUT_Cnt_s16[3]8]   16   17_DualSpurVernierLUT_Cnt_s16[3]8]   16   17_DualSpurVernierLUT_Cnt_s16[3]19   18   17_DualSpurVernierLUT_Cnt_s16[3]19   19   18_DualSpurVernierLUT_Cnt_s16[3]19   20   17_DualSpurVernierLUT_Cnt_s16[3]19   3   17_DualSpurVernierLUT_Cnt_s16[3]19   3   17_DualSpurVernierLUT_Cnt_s16[3]19   5   17_DualSpurVernierLUT_Cnt_s16[3]19   9   17_DualSpurVernierLUT_Cnt_s16[3]19   9   17_DualSpurVernierLUT_Cnt_s16[3]19   11   17_DualSpurVernierLUT_Cnt_s16[3]19   17   18_DualSpurVernierLUT_Cnt_s16[3]19   17   19_DualSpurVernierLUT_Cnt_s16[3]19   17   19_DualSpurVernierLUT_Cnt_s16[3]19   19   10_DualSpurVernierLUT_Cnt_s16[3]19   19   11_DualSpurVernierLUT_Cnt_s16[3]19   19   12_DualSpurVernierLUT_Cnt_s16[3]20   19	T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
12_DualSpurVemierLUT_Cnt_stic[2][20]   9   12_DualSpurVemierLUT_Cnt_stic[2][21]   10   12_DualSpurVemierLUT_Cnt_stic[3][0]   22   12_DualSpurVemierLUT_Cnt_stic[3][1]   2   12_DualSpurVemierLUT_Cnt_stic[3][2]   4   12_DualSpurVemierLUT_Cnt_stic[3][2]   4   12_DualSpurVemierLUT_Cnt_stic[3][2]   6   12_DualSpurVemierLUT_Cnt_stic[3][3]   6   12_DualSpurVemierLUT_Cnt_stic[3][4]   8   12_DualSpurVemierLUT_Cnt_stic[3][6]   10   12_DualSpurVemierLUT_Cnt_stic[3][6]   12   12_DualSpurVemierLUT_Cnt_stic[3][7]   14   12_DualSpurVemierLUT_Cnt_stic[3][8]   16   12_DualSpurVemierLUT_Cnt_stic[3][9]   18   12_DualSpurVemierLUT_Cnt_stic[3][9]   18   12_DualSpurVemierLUT_Cnt_stic[3][9]   19   12_DualSpurVemierLUT_Cnt_stic[3][1]   1   13_DualSpurVemierLUT_Cnt_stic[3][1]   1   14_DualSpurVemierLUT_Cnt_stic[3][1]   1   15_DualSpurVemierLUT_Cnt_stic[3][1]   1   16_DualSpurVemierLUT_Cnt_stic[3][1]   1   17_DualSpurVemierLUT_Cnt_stic[3][1]   1   18_DualSpurVemierLUT_Cnt_stic[3][1]   1   19_DualSpurVemierLUT_Cnt_stic[3][2]   1   19_DualSpurVemierLUT_Cnt_stic[3][2]   1   10_DualSpurVemierLUT_Cnt_stic[3][2]   1   11_DualSpurVemierLUT_Cnt_stic[3][2]   1   12_DualSpurVemierLUT_Cnt_stic[3][2]   1   13_DualSpurVemierLUT_Cnt_stic[3][2]   1   14_DualSpurVemierLUT_Cnt_stic[3][2]   1   15_DualSpurVemierLUT_Cnt_stic[3][2]   1   16_DualSpurVemierLUT_Cnt_stic[3][2]   1   17_DualSpurVemierLUT_Cnt_stic[3][2]   1   18_DualSpurVemierLUT_Cnt_stic[3][2]   1   19_DualSpurVemierLUT_Cnt_stic[3][2]   1   10_DualSpurVemier		7		
12_DualSpur/vernierLUT_Cnt_s16[3][1]   10   22   22   22   23   23   23   23   2	T2 DualSpurVernierLUT Cnt s16[2][19]	8		
12_DualSpur/vernierLUT_Cnt_s16[3][1]   10   22   22   22   23   23   23   23   2	T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
12_DualSpurVemierLUT_Cnt_s16[3][1]   2   2   2   2   2   2   2   2   2		10		
T2_DualSpurVermierLUT_Cnt_s16[3][1]         2           T2_DualSpurVermierLUT_Cnt_s16[3][2]         4           T2_DualSpurVermierLUT_Cnt_s16[3][4]         6           T2_DualSpurVermierLUT_Cnt_s16[3][4]         8           T2_DualSpurVermierLUT_Cnt_s16[3][5]         10           T2_DualSpurVermierLUT_Cnt_s16[3][6]         12           T2_DualSpurVermierLUT_Cnt_s16[3][7]         14           T2_DualSpurVermierLUT_Cnt_s16[3][8]         16           T2_DualSpurVermierLUT_Cnt_s16[3][9]         18           T2_DualSpurVermierLUT_Cnt_s16[3][10]         20           T2_DualSpurVermierLUT_Cnt_s16[3][11]         1           T2_DualSpurVermierLUT_Cnt_s16[3][12]         3           T2_DualSpurVermierLUT_Cnt_s16[3][13]         5           T2_DualSpurVermierLUT_Cnt_s16[3][14]         7           T2_DualSpurVermierLUT_Cnt_s16[3][16]         11           T2_DualSpurVermierLUT_Cnt_s16[3][16]         11           T2_DualSpurVermierLUT_Cnt_s16[3][19]         15           T2_DualSpurVermierLUT_Cnt_s16[3][19]         17           T2_DualSpurVermierLUT_Cnt_s16[3][20]         19           T2_DualSpurVermierLUT_Cnt_s16[3][21]         16           K_SelectFroilag_Cnt_str.Threshold         195           K_SkipStepErrollag_Cnt_str.Pstep         49	T2 DualSpurVernierLUT Cnt s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][2]		2		
T2_DualSpurVermiert.UT_Cnt_s16[3][4] 8 T2_DualSpurVermiert.UT_Cnt_s16[3][4] 8 T2_DualSpurVermiert.UT_Cnt_s16[3][6] 10 T2_DualSpurVermiert.UT_Cnt_s16[3][7] 14 T2_DualSpurVermiert.UT_Cnt_s16[3][7] 14 T2_DualSpurVermiert.UT_Cnt_s16[3][8] 16 T2_DualSpurVermiert.UT_Cnt_s16[3][9] 18 T2_DualSpurVermiert.UT_Cnt_s16[3][10] 20 T2_DualSpurVermiert.UT_Cnt_s16[3][11] 1 1 T2_DualSpurVermiert.UT_Cnt_s16[3][11] 1 1 T2_DualSpurVermiert.UT_Cnt_s16[3][12] 3 3 T2_DualSpurVermiert.UT_Cnt_s16[3][13] 5 5 T2_DualSpurVermiert.UT_Cnt_s16[3][14] 7 7 T2_DualSpurVermiert.UT_Cnt_s16[3][14] 7 7 T2_DualSpurVermiert.UT_Cnt_s16[3][16] 11 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		4		
T2_DualSpurVernierLUT_Cnt_s16[3][4]   8   10   10   10   10   10   10   10		6		
T2_DualSpurVernierLUT_Cnt_s16[3][6] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 1 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 1 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 LoualSpurVernierLUT_Cnt_s16[3][21] 21 LoualSpurVernierLUT_Cnt_s16[3		8		
T2_DualSpurVernierLUT_Cnt_s16[3][6]   12   14   15   15   15   15   17   14   16   15   15   15   17   15   16   17   15   15   17   15   15   17   15   15				
T2_DualSpurVernierLUT_Cnt_s16[3][7]  14  T2_DualSpurVernierLUT_Cnt_s16[3][8]  16  T2_DualSpurVernierLUT_Cnt_s16[3][10]  T2_DualSpurVernierLUT_Cnt_s16[3][11]  T2_DualSpurVernierLUT_Cnt_s16[3][11]  T2_DualSpurVernierLUT_Cnt_s16[3][11]  T2_DualSpurVernierLUT_Cnt_s16[3][12]  T2_DualSpurVernierLUT_Cnt_s16[3][14]  T2_DualSpurVernierLUT_Cnt_s16[3][14]  T2_DualSpurVernierLUT_Cnt_s16[3][14]  T2_DualSpurVernierLUT_Cnt_s16[3][16]  T2_DualSpurVernierLUT_Cnt_s16[3][16]  T2_DualSpurVernierLUT_Cnt_s16[3][16]  T2_DualSpurVernierLUT_Cnt_s16[3][17]  T3_DualSpurVernierLUT_Cnt_s16[3][17]  T2_DualSpurVernierLUT_Cnt_s16[3][18]  T2_DualSpurVernierLUT_Cnt_s16[3][19]  T2_DualSpurVernierLUT_Cnt_s16[3][20]  T2_DualSpurVernierLUT_Cnt_s16[3][21]  ExpurierLut_Cnt_s16[3][21]  ExpurierLut_Ent_S16[3][21]  Ex				
T2_DualSpurVernierLUT_Cnt_s16[3][8]				
T2_DualSpurVernierLUT_Cnt_s16[3][9]   18				
T2_DualSpurVernierLUT_Cnt_s16[3][10]   20				
T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_selectFromColum_Cnt_tlgc 1 k_skipStepErrDiag_Cnt_str.Prsehold 195 k_skipStepErrDiag_Cnt_str.Pstep 49 k_skipStepErrDiag_Cnt_str.Pstep 49 k_skipStepErrDiag_Cnt_str.Pstep 49 k_vernCorrErrorDiag_Cnt_str.Pstep 0 k_vernCorrErrorDiag_Cnt_str.Pstep 10 k_vernCorrErrorDiag_Cnt_str.Pstep 12 k_vernCorrErrorDiag_Cnt_str.NStep 12 k_vernCorrErrorDiag_Cnt_str.Deg_f32 84.35098028 k_vernOoRangeThresh_Deg_f32 1554.614787 tgt_Pim_DigColPsEOL_ColTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL_ColTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL_TrimComp_Cnt_u16 1				
T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][16]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19         T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SelipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.Nstep       49         k_SkipStepErrDiag_Cnt_str.Nstep       10         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.Nstep       12         k_VernCorrErrorDiag_Cnt_str.Nstep       12         k_VernCorrErrorDiag_Cnt_str.Nstep       12         k_VernCorrErrorDiag_Cnt_str.Nstep       12         k_VernCorrErrorDiag_Cnt_str.Nstep       12         k_VernCorrErrorDiag_Cnt_str.Nstep       154.614787         tgt_pim_DigColPsEol.ColTrim_Deg_132       148.1551865         tgt_pim_DigColPsEol.ColTrim_Deg_132       349.5774245				
T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19         T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.PStep       49         k_VernCorrErrorDiag_Cnt_str.PStep       10         k_VernCorrErrorDiag_Cnt_str.Threshold       58         k_VernCorrErrorDiag_Cnt_str.NStep       1         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernCorrErrorThresh_Deg_f32       1554.614787         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 1 k_SkipStepErrDiag_Cnt_str.Threshold 195 k_SkipStepErrDiag_Cnt_str.Threshold 195 k_SkipStepErrDiag_Cnt_str.Threshold 58 k_VernCorrErrorDiag_Cnt_str.Threshold 58 k_VernCorrErrorDiag_Cnt_str.NStep 0 k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorDiag_Cnt_str.Deg_f32 84.35098028 k_VernCorrErrorDiag_Cnt_str.Deg_f32 1554.614787 tgt_DigColPs_Per2_MecState_Cnt_enum.value 2 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16				
T2_DualSpurVerniert.UT_Cnt_s16[3][15] 9 T2_DualSpurVerniert.UT_Cnt_s16[3][16] 11 T2_DualSpurVerniert.UT_Cnt_s16[3][17] 13 T2_DualSpurVerniert.UT_Cnt_s16[3][18] 15 T2_DualSpurVerniert.UT_Cnt_s16[3][19] 17 T2_DualSpurVerniert.UT_Cnt_s16[3][20] 19 T2_DualSpurVerniert.UT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 1 1 k_SkipStepErrDiag_Cnt_str.Threshold 195 k_SkipStepErrDiag_Cnt_str.Threshold 195 k_SkipStepErrDiag_Cnt_str.NStep 49 k_SkipStepErrDiag_Cnt_str.Threshold 58 k_VernCorrErrorDiag_Cnt_str.Threshold 58 k_VernCorrErrorDiag_Cnt_str.NStep 10 k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorDiag_Cnt_str.Deg_f32 84.35098028 k_VernCorrErrorThresh_Deg_f32 1554.614787 tgt_DigColPs_Per2_MecState_Cnt_enum.value 12 tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 155.774245 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 148.1551865 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16				
T2_DualSpurVerniert_UT_Cnt_s16[3][16] 11  T2_DualSpurVerniert_UT_Cnt_s16[3][17] 13  T2_DualSpurVerniert_UT_Cnt_s16[3][18] 15  T2_DualSpurVerniert_UT_Cnt_s16[3][19] 17  T2_DualSpurVerniert_UT_Cnt_s16[3][20] 19  T2_DualSpurVerniert_UT_Cnt_s16[3][21] 21  k_SelectFromColumn_Cnt_lgc 1 1  k_SkipStepErrDiag_Cnt_str.Threshold 195  k_SkipStepErrDiag_Cnt_str.PStep 49  k_SkipStepErrDiag_Cnt_str.Nstep 10  k_VernCorrErrorDiag_Cnt_str.Threshold 58  k_VernCorrErrorDiag_Cnt_str.PStep 0  k_VernCorrErrorDiag_Cnt_str.NStep 12  k_VernCorrErrorDiag_Cnt_str.NStep 12  k_VernCorrErrorDiag_Cnt_str.NStep 12  k_VernCorrErrorDiag_Cnt_str.Deg_f32 84.35098028  k_VernORRengeThresh_Deg_f32 1554.614787  tgt_DigColPs_Per2_MecState_Cnt_enum.value 2 2  tgt_Pim_DigColPsEOL_SpurTrim_Deg_f32 349.5774245  tgt_Pim_DigColPsEOL_SpurTrim_Deg_f32 349.5774245  tgt_Pim_DigColPsEOL_TrimComp_Cnt_u16 1				
T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19         T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.NStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][20]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19         T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.NStep       58         k_VernCorrErrorDiag_Cnt_str.NStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorDiag_Cnt_str.NStep       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19         T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernCorrBrorThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
T2_DualSpurVernierLUT_Cnt_s16[3][20]       19         T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOQRangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
T2_DualSpurVernierLUT_Cnt_s16[3][21]       21         k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOQRangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_SelectFromColumn_Cnt_lgc       1         k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.Threshold       58         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOQRangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_SkipStepErrDiag_Cnt_str.Threshold       195         k_SkipStepErrDiag_Cnt_str.PStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.Threshold       58         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOQRangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_SkipStepErrDiag_Cnt_str.NStep       49         k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.Threshold       58         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_SkipStepErrDiag_Cnt_str.NStep       10         k_VernCorrErrorDiag_Cnt_str.Threshold       58         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_VernCorrErrorDiag_Cnt_str.Threshold       58         k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1	_ , , , , , , , , , , , , , , , , , , ,			
k_VernCorrErrorDiag_Cnt_str.PStep       0         k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1		·		
k_VernCorrErrorDiag_Cnt_str.NStep       12         k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_VernCorrErrorThresh_Deg_f32       84.35098028         k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
k_VernOORangeThresh_Deg_f32       1554.614787         tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
tgt_DigColPs_Per2_MecState_Cnt_enum.value       2         tgt_Pim_DigColPsEOL.ColTrim_Deg_f32       148.1551865         tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32       349.5774245         tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16       1				
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32				
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 349.5774245 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 1				
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 1				
02 2 0 12 2				
tgr_nte_iriat_da_digodir-a.digodir-a_retz_izofiwadardavatiid_orit_igc   tgr_digodir-a_retz_izofiwadardavatiid_orit_igC	· ·			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32				
		tgt_DigColPs_Per2_MecState_Cnt_enum		
V V V V V V- V- V-		tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
		tgt_Pim_DigColPsEOL		
Name Actual Value Expected Value	Name	Actual Value Expected Value	Resul	

32	3 3		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	356.646606	356.6466252 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	✓



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.52 (Repeat Count = 1)	·
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	134
DigColPs_ColTrimStatic_Deg_M_f32	50.7
DigColPs_HwAVernCorrFault_Cnt_M_Igc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26033
DigColPs_I2CHwColAngle_Deg_M_f32	166.9625559
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7191
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	3
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	55.3
DigColPs_TrimCompStatic_Cnt_M_u16	1744
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s16[2][2]	4
T2_ColSpurVernierLUT_Cnt_s16[2][3]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	8 9 0
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	8 9

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Marina	Invest Males		
Name	Input Value		
T2_DualSpur/ernierLUT_Cnt_s16[1][14] T2_DualSpur/ernierLUT_Cnt_s16[1][15]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15] T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2 DualSpurVernierLUT Cnt s16[1][17]	7		
T2 DualSpurVernierLUT Cnt s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2 DualSpurVernierLUT Cnt s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2 DualSpurVernierLUT Cnt s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	8		
k_VernCorrErrorDiag_Cnt_str.PStep	41		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k_VernCOPangeThresh_Deg_f32	31.69468141 1512.089929		
k_VernOORangeThresh_Deg_f32 tot_DinCoIPs_Per2_MecState_Cnt_enum_value	1512.089929		
tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	166.9625559		
tgt_Pim_DigColPsEOL.Confini_Deg_i32	4.647624195		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2354		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	nt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeq		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	,	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	

490.909088

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

490.9090909 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	476.262573	476.2625559 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-423.737427	-423.7374441 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.53 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	184
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135
DigColPs_ColTrimStatic_Deg_M_f32	54.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	34509
DigColPs_I2CHwColAngle_Deg_M_f32	125.941998
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	19043
DigColPs_I2CHwSpurAngle_Deg_M_f32	56.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	18
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184
DigColPs_SpurTrimStatic_Deg_M_f32	56.4
DigColPs_TrimCompStatic_Cnt_M_u16	1780
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2 ColSpurVernierLUT Cnt s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2 ColSpurVernierLUT Cnt s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2 ColSpurVernierLUT Cnt s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T3_DualSpurVernierLUT_Cnt_s16[0][11]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][15]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
	360 9

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Input Value
1
2
3
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6 7
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0
1
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7
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0
0
1
2
3
4
5   6
7
8
9
10
0
1
2
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4
5
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7
8
9
10
22 2
4
6
8
10
12
14
16
18
20
1
3
5
7
9
11
13
15
17
19
21
0
223   11
45
45
39



Name	Input Value		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	125.941998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	234.6564466		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	305		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1151.14197	1151.141998 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	245.454468	245.4545455 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	<b>✓</b>
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	-
GetResource	1	GetResource	1	-
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	-

Test Step 2.54 (Repeat Count = 1)	🗸
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	138
DigColPs_ColTrimStatic_Deg_M_f32	58.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	17718
DigColPs_I2CHwColAngle_Deg_M_f32	89.98652095
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26140
DigColPs_I2CHwSpurAngle_Deg_M_f32	57.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	686.9139401
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	57.5
DigColPs_TrimCompStatic_Cnt_M_u16	1816
DigColPs_VernCorrDetectAcc_Cnt_M_u16	16
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][13]	229 261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2 ColSpurVernierLUT Cnt s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T3_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T3_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-396 -360
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][4]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4] T3_DualSpurVernierLUT_Cnt_s16[0][5]	-252 -216
T2_DualSpurVernierLUT_Cnt_s16[0][5] T3_DualSpurVernierLUT_Cnt_s16[0][6]	-216 -180
T2_DualSpurVernierLUT_Cnt_s16[0][6] T2_DualSpurVernierLUT_Cnt_s16[0][7]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][7]	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	360   9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2
T2 DualSpurVernierLUT Cnt s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2 DualSpurVernierLUT Cnt s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17] T3_DualSpurVernierLUT_Cnt_s16[2][18]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][10]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9

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DigColPs\_Per2

		(	- 100
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	168		
k_SkipStepErrDiag_Cnt_str.PStep	25		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	56		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	40.71416354		
k_VernOORangeThresh_Deg_f32	852.5587618		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	89.98652095		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	221.6592153		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2805		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	-
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	-
DigColPs_PrevColPos_Deg_M_f32	1471.08655	1471.086521 ± 0.0001220703125	-
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	-
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	<b>✓</b>
tat DiaColDo Dar2 TrimComp Cot Igo volue	0	0	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>V</b>

Test Step 2.55 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	156
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	63
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	38087
DigColPs_I2CHwColAngle_Deg_M_f32	291.3419048
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36636
DigColPs_I2CHwSpurAngle_Deg_M_f32	58.6
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	886.4049975
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0

 $tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value$ 

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Name	Input Value
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156
DigColPs_SpurTrimStatic_Deg_M_f32	58.6
DigColPs_TrimCompStatic_Cnt_M_u16	1852
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][2] T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_coloputVcmicrLGT_cnt_s16[1][0] T2 ColSpurVcmicrLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17 -396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2 DualSpurVernierLUT Cnt s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9 0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8

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DigColPs_Per2		10	ACITAL	
Name	Input Value			
T2 DualSpurVernierLUT Cnt s16[3][5]	10			
T2 DualSpurVernierLUT Cnt s16[3][6]	12			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	0			
k_SkipStepErrDiag_Cnt_str.Threshold	171			
k_SkipStepErrDiag_Cnt_str.PStep	44			
k_SkipStepErrDiag_Cnt_str.NStep	4			
k_VernCorrErrorDiag_Cnt_str.Threshold	61			
k_VernCorrErrorDiag_Cnt_str.PStep	13			
k_VernCorrErrorDiag_Cnt_str.NStep	11			
k_VernCorrErrorThresh_Deg_f32	80.16494608			
k_VernOORangeThresh_Deg_f32	995.0178322			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	291.3419048			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	192.5007017			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	t_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1084.59058	1084.59059 ± 0.00048828125	~	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~	
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✓	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	184.590576	184.5905902 ± 0.0009	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

0x6C

0x00

0x00

0x6C

0x00

0x00

Test Step 2.56 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	134
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

NTC

Param

Status





DigColFs_Fer2	- CACIMI
Name	Input Value
DigColPs_ColTrimStatic_Deg_M_f32	67.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	13742
DigColPs_I2CHwColAngle_Deg_M_f32	196.4963954
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	20378
DigColPs_I2CHwSpurAngle_Deg_M_f32	59.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigCoIPs I2CSensCommFlts Cnt M u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	
DigColPs_PrevColPos_Deg_M_f32	1340.457155
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	134
DigColPs_SpurTrimStatic_Deg_M_f32	59.7
DigColPs_TrimCompStatic_Cnt_M_u16	1888
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
0igColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
Γ2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
C2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[0][8]	98
<sup>7</sup> 2_ColSpurVernierLUT_Cnt_s16[0][9]	130
<sup>7</sup> 2_ColSpurVernierLUT_Cnt_s16[0][10]	163
[2_ColSpurVernierLUT_Cnt_s16[0][11]	196
Γ2_ColSpurVernierLUT_Cnt_s16[0][12]	229
Γ2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
Γ2_ColSpurVernierLUT_Cnt_s16[0][15]	327
Γ2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
Γ2_ColSpurVernierLUT_Cnt_s16[1][1]	4
Γ2_ColSpurVernierLUT_Cnt_s16[1][2]	3
Γ2_ColSpurVernierLUT_Cnt_s16[1][3]	2
[2_ColSpurVernierLUT_Cnt_s16[1][4]	1
C2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_00lSpurVernierEUT_Cnt_s16[1][9]	1
2_ColSpurVernierL0T_Cnt_s16[1][9]  72_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
<sup>2</sup> 2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[1][14]	1
<sup>7</sup> 2_ColSpurVernierLUT_Cnt_s16[1][15]	0
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[1][16]	4
2_ColSpurVernierLUT_Cnt_s16[2][0]	0
2_ColSpurVernierLUT_Cnt_s16[2][1]	8
2_ColSpurVernierLUT_Cnt_s16[2][2]	6
2_ColSpurVernierLUT_Cnt_s16[2][3]	4
2_ColSpurVernierLUT_Cnt_s16[2][4]	2
2_ColSpurVernierLUT_Cnt_s16[2][5]	0
2_ColSpurVernierLUT_Cnt_s16[2][6]	9
2_ColSpurVernierLUT_Cnt_s16[2][7]	7
2_ColSpurVernierLUT_Cnt_s16[2][8]	5
2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
<sup>7</sup> 2_ColSpurVernierLUT_Cnt_s16[2][11]	10
Γ2_ColSpurVernierLUT_Cnt_s16[2][12]	8
Γ2_ColSpurVernierLUT_Cnt_s16[2][13]	6
Γ2_ColSpurVernierLUT_Cnt_s16[2][14]	4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][15]	10





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3 16
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288 324
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21] T3_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	7
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	7
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	7 8





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	72		
k_SkipStepErrDiag_Cnt_str.PStep	22		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	17		
k_VernCorrErrorDiag_Cnt_str.PStep	14		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k VernCorrErrorThresh Deg f32	81.58188558		
k VernOORangeThresh Deg f32	510.2277182		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	196.4963954		
tgt Pim DigColPsEOL.SpurTrim Deg f32	10.61436504		
tgt_Pim_DigColPsEOL.TrimComp Cnt u16	1249		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	osValid Cnt Igo	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_sa_DigColPs.DigColPs_Fer2_IzChwAbsFos_Hwbeg_isz  tgt_Rte_Inst_sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_l2CHWADSP0		
tgt_Rte_inst_sa_bigColPs.bigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_0	_	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	J.Igo	
		Expected Value	Dooule
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	V
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>V</b>
DigColPs_PrevColPos_Deg_M_f32	489.396393	489.3963954 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	<b>✓</b>

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Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	489.396393	489.3963954 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	•
Param	0x04	0x04	✓
Status	0x01	0x01	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.57 (Repeat Count = 1)	🗸
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs ColTrimStatic Deg M f32	71.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	5158
DigColPs I2CHwColAngle Deg M f32	194.3084972
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	19371
DigColPs I2CHwSpurAngle Deg M f32	60.8
DigColPs I2CHwTrimTransCnts Uls M u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1210.662194
DigColPs PrevVernierLevelNo Cnt M u08	8
DigColPs SkipStepFltDetectAcc Cnt M u16	14
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	60.8
DigColPs TrimCompStatic Cnt M u16	1924
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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12_Configenment   Grag   19    1   1   1   1   1   1   1   1		I
T. Codeys/mental Cost 1981 10	Name	Input Value
12. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. CoSportwenset UT_Del. 1981[910] 10. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981	T2_ColSpurVernierLUT_Cnt_s16[1][14]	
17. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(2) 3. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 5. Costaya/ment.U. Cut. 1902(3) 6. Costaya/ment.U. Cut. 1902(3) 7. Costaya/ment.U. Cut. 1902(1) 7. Costaya/ment.U. Cut. 1902(	T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
17_0085pvvrinstrut _Cot_st@  0	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
17_CORSE/vernetLT_CR_15[2]  6	T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
P. Collego-Vernicut   Ces. 1982    6	T2 ColSpurVernierLUT Cnt s16[2][1]	8
TE, COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 13. COSSA/VerineLUT, Cri.; 195(20) 15. COSSA/VerineLUT, Cri.; 195(20) 16. COSSA/VerineLUT, Cri.; 195(20) 17. COSSA/VerineLUT, Cri.; 195(20) 18. COSSA/VerineLUT, Cri.; 195(20) 19. COSSA/VerineLUT, Cri.; 195(20) 10. COSSA/VerineLUT, Cri.; 195(20) 11. COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 13. COSSA/VerineLUT, Cri.; 195(20) 14. COSSA/VerineLUT, Cri.; 195(20) 15. COSSA/VerineLUT, Cri.; 195(20) 17. COSSA/VerineLUT, Cri.; 195(20) 18. COSSA/VerineLUT, Cri.; 195(20) 19. COSSA/Verine		6
PCOS_AVENNELT_CR_15[0]  2   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  7   PC		
12 COSSAVVenneLUT Cut = 1619[15]   0		
12_Colspan/woment_Col_statigned   0		
T2_CuSignaviernet_UT_Cut_stip[15] 2_CuSignaviernet_UT_Cut_stip[15] 3_CuSignaviernet_UT_Cut_stip[15] 4_CuSignaviernet_UT_Cut_stip[15] 1_CuSignaviernet_UT_Cut_stip[15]		
T2_Colsput/emetU_Cot_stq09  5		
T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1911		
T. C. Colsput/ment U. Crit. st 1921   1		
T2_CoSpur'ementU_Cot_stQ111	T2_ColSpurVernierLUT_Cnt_s16[2][9]	
T. Colling/months   T. Colling   T. Collin	T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_CoSpur/emetUT_Cnt_sto[0]16  4 72_CoSpur/emetUT_Cnt_sto[0]16  172_CoSpur/emetUT_Cnt_sto[0]16  182_CoSpur/emetUT_Cnt_sto[0]16	T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   13_CoSput/venietU_Cot_s100214   1   14_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   1   15_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   17_CoSput/venietU_Cot_s100216   0   18_CoSput/venietU_Cot_s100216   0   18_Cosput/venietU_Cot_s10	T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   13_CoSput/venietU_Cot_s100214   1   14_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   1   15_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   17_CoSput/venietU_Cot_s100216   0   18_CoSput/venietU_Cot_s100216   0   18_Cosput/venietU_Cot_s10	T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T. Colspur/emicAUT Cnt. srie[]16   10   12   Colspur/emicAUT Cnt. srie[]16   10   12   Colspur/emicAUT Cnt. srie[]16   11   12   Colspur/emicAUT Cnt. srie[]16   14   12   Colspur/emicAUT Cnt. srie[]17   14   12   Colspur/emicAUT Cnt. srie[]18   14   12   Colspur/emicAUT Cnt. srie[]18   15   Colspur/emicAUT Cnt. srie[]18   16   Colspur/emicAUT Cnt. srie[]18   16   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   18   Colspur/emicAUT Cnt. srie[]18   19   T. Colspur/emicAUT		4
12, CoSparVeneUT, Cot. 918(319) 12, CoSparVeneUT, Cot. 918(319) 13, CoSparVeneUT, Cot. 918(319) 14, CoSparVeneUT, Cot. 918(319) 15, CoSparVeneUT, Cot. 918(319) 16, CoSparVeneUT, Cot. 918(319) 17, CoSparVeneUT, Cot. 918(319) 18, CoSparVeneUT, Cot. 918(319) 18, CoSparVeneUT, Cot. 918(319) 19, CoSparVeneUT, Cot. 918(319		
17_CoSput/venicUT_Cot_1 1613[1] 17_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 12_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 14_CoSput/venicUT_Cot_1 1613[2] 15_CoSput/venicUT_Cot_1 1613[2] 16_CoSput/venicUT_Cot_1 1613[2] 17_CoSput/venicUT_Cot_1 1613[2] 18_CoSput/venicUT_Cot_1 1613[2] 19_CoSput/venicUT_Cot_1 1613[2] 10_CoSput/venicUT_Cot_1 1613[2] 10_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2		
12, CoSppt/vermicHUT_Cnt, 18(3)[2]   11   12, CoSppt/vermicHUT_Cnt, 18(3)[2]   11   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   8   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   8   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   12   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   17, CoSppt		
17_CoSput/venieUT_Cnt_180S S S S S S S S S S S S S S S S S S S		
T2_CoSput/vemet.UT_Cnt_s160  16   5     T2_CoSput/vemet.UT_Cnt_s160  16   5     T2_CoSput/vemet.UT_Cnt_s160  16   15     T2_CoSput/vemet.UT_Cnt_s160  16   15     T2_CoSput/vemet.UT_Cnt_s160  16   16     T2_CoSput/vemet.UT_Cnt_s160  17   16     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  18   19     T2_CoSput/vemet.UT_Cnt_s160  18   17     T2_DusSput/vemet.UT_Cnt_s160  18   18     T2_DusSput/vem		
12, CoSparVermentUT, Cnt. \$163[15] 2, CoSparVermentUT, Cnt. \$163[15] 2, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, DuaSparVermentUT, Cnt. \$163[15] 12, DuaSparVermentUT, Cnt. \$160[15] 13, CoSparVermentUT, Cnt. \$160[15] 14, CoSparVermentUT, Cnt. \$160[15] 15, CoSparVermentUT, Cnt. \$160[15] 16, CoSparVermentUT, Cnt. \$160[15] 17, CoSparVermentUT, Cnt. \$160[15] 18, CoSparVermentUT, Cnt. \$160[15] 19, CoSparV		
12_CobSparVement_UT_Cnt_steQt  5    2_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  7    12_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  5    13_CobSparVement_UT_Cnt_steQt  5    14_CobSparVement_UT_Cnt_steQt  5    15_CobSparVement_UT_Cnt_steQt  5    16_CobSparVement_UT_Cnt_steQt  5    17_CobSparVement_UT_Cnt_steQt  5    18_CobSparVement_UT_Cnt_steQt  5    19_CobSparVement_UT_Cnt_steQt  6    19_CobSparVement_UT_Cnt_steQt		
T2_CoSpuVerinetUT_Cnt_s160]10   15   T2_CoSpuVerinetUT_Cnt_s160]11   12   T2_CoSpuVerinetUT_Cnt_s160]10   0   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   16   T2_CoSpuVerinetUT_Cnt_s160]11   18   T2_CoSpuVerinetUT_Cnt_s160]12   13   T2_CoSpuVerinetUT_Cnt_s160]13   10   T2_CoSpuVerinetUT_Cnt_s160]14   7   T2_CoSpuVerinetUT_Cnt_s160]15   4   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]10   17   T2_CoSpuVerinetUT_Cnt_s160]10   360   T2_CoSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuV		5
T2_CoSpuVerinetUT_Cnt_s160]10   15   T2_CoSpuVerinetUT_Cnt_s160]11   12   T2_CoSpuVerinetUT_Cnt_s160]10   0   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   16   T2_CoSpuVerinetUT_Cnt_s160]11   18   T2_CoSpuVerinetUT_Cnt_s160]12   13   T2_CoSpuVerinetUT_Cnt_s160]13   10   T2_CoSpuVerinetUT_Cnt_s160]14   7   T2_CoSpuVerinetUT_Cnt_s160]15   4   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]10   17   T2_CoSpuVerinetUT_Cnt_s160]10   360   T2_CoSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuV	T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
12. CoSpurVemierLUT_Cnt_s16(3)[8] 9 17. CoSpurVemierLUT_Cnt_s16(3)[8] 9 17. CoSpurVemierLUT_Cnt_s16(3)[10] 13 17. CoSpurVemierLUT_Cnt_s16(3)[11] 16 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[16] 17 17. CospurVemierLUT_Cnt_s16(3)[16] 18 18. CospurVemierLUT_Cnt_s16(3)[16] 18 1		15
12. ColspurVement.UT. Cnt. 15(8)[9] 6 12. ColspurVement.UT. Cnt. 15(8)[11] 16 12. ColspurVement.UT. Cnt. 15(8)[12] 13 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[14] 7 12. ColspurVement.UT. Cnt. 15(8)[15] 19 12. ColspurVement.UT. Cnt. 15(8)[16] 19 13. ColspurVement.UT. Cnt. 15(8)[16] 19 14. ColspurVement.UT. Cnt. 15(8)[16] 19 15. DualspurVement.UT. Cnt. 15(8)[16] 19 16. DualspurVement.UT. Cnt. 15(8)[16] 19 17. DualspurVement.UT. Cnt. 15(8)[17] 144 18. DualspurVement.UT. Cnt. 15(8)[18] 19 18. DualspurVement.UT. Cnt. 15(8)[19] 19 18. DualspurV	T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
12. ColspurVement.UT. Cnt. 15(8)[9] 6 12. ColspurVement.UT. Cnt. 15(8)[11] 16 12. ColspurVement.UT. Cnt. 15(8)[12] 13 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[14] 7 12. ColspurVement.UT. Cnt. 15(8)[15] 19 12. ColspurVement.UT. Cnt. 15(8)[16] 19 13. ColspurVement.UT. Cnt. 15(8)[16] 19 14. ColspurVement.UT. Cnt. 15(8)[16] 19 15. DualspurVement.UT. Cnt. 15(8)[16] 19 16. DualspurVement.UT. Cnt. 15(8)[16] 19 17. DualspurVement.UT. Cnt. 15(8)[17] 144 18. DualspurVement.UT. Cnt. 15(8)[18] 19 18. DualspurVement.UT. Cnt. 15(8)[19] 19 18. DualspurV		9
12 ColSpurVemiet.UT_Cnt_s16[3][10] 12 ColSpurVemiet.UT_Cnt_s16[3][11] 16 17 ColSpurVemiet.UT_Cnt_s16[3][12] 13 17 ColSpurVemiet.UT_Cnt_s16[3][13] 10 10 ColSpurVemiet.UT_Cnt_s16[3][14] 17 12 ColSpurVemiet.UT_Cnt_s16[3][16] 18 19 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 10 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][16] 12 DualSpurVemiet.UT_Cnt_s16[3][16] 13 ColSpurVemiet.UT_Cnt_s16[3][16] 14 ColSpurVemiet.UT_Cnt_s16[3][16] 15 ColSpurVemiet.UT_Cnt_s16[3][16] 16 ColSpurVemiet.UT_Cnt_s16[3][16] 17 ColSpurVemiet.UT_Cnt_s16[3][16] 18 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 10 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 13 ColSpurVemiet.UT_Cnt_s16[3][17] 14 ColSpurVemiet.UT_Cnt_s16[3][17] 15 ColSpurVemiet.UT_Cnt_s16[3][17] 16 ColSpurVemiet.UT_Cnt_s16[3][17] 17 ColSpurVemiet.UT_Cnt_s16[3][17] 18 ColSpurVemiet.UT_Cnt_s16[3][17] 19 ColSpurVemiet.UT_Cnt_s16[3][17] 10 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 13 ColSpurVemiet.UT_Cnt_s16[3][17] 14 ColSpurVemiet.UT_Cnt_s16[3][17] 15 ColSpurVemiet.UT_Cnt_s16[3][17] 16 ColSpurVemiet.UT_Cnt_s16[3][17] 17 ColSpurVemiet.UT_Cnt_s16[3][17] 18 ColSpurVemiet.UT_Cnt_s16[3][17] 19 ColSpurVemiet.UT_Cnt_s16[3][17] 10 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 13 ColSpurVemiet.UT_Cnt_s16[3][17] 14 ColSpurVemiet.UT_Cnt_s16[3][17] 15 ColSpurVemiet.UT_Cnt_s16[3][17] 16 ColSpurVemiet.UT_Cnt_s16[3][17] 17 ColSpurVemiet.UT_Cnt_s16[3][17] 18 ColSpurVemiet.UT_Cnt_s16[3][17] 19 ColSpurVemiet.UT_Cnt_s16[3][17] 10 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17]		
12 ColSpurVemiet.UT_Cnt_s16[3][11]   16   17 ColSpurVemiet.UT_Cnt_s16[3][12]   13   18   18   18   18   18   18   18		
12		
12 CoSparVement.UT_Cnt_s16[3]1:3]   10   7   12 CoSparVement.UT_Cnt_s16[3]1:4  7   7   12 CoSparVement.UT_Cnt_s16[3]1:6  4   7   7   7   7   7   7   7   7   7		
12 ColSpurVemietLUT_Cnt_st6[3][14]   7   7   2 ColSpurVemietLUT_Cnt_st6[3][15]   4   7   7   2 ColSpurVemietLUT_Cnt_st6[3][15]   4   7   7   7   7   7   7   7   7   7		
T2_ColSpurVemierLUT_Cnt_s16[3]15		
T2   DualspurVemierLUT_Cnt_s16(0)(1)   396		
T2 DualSpurVemierLUT_Cnt_st6[0][0]   396     T2 DualSpurVemierLUT_Cnt_st6[0][1]   360     T2 DualSpurVemierLUT_Cnt_st6[0][2]   324     T2 DualSpurVemierLUT_Cnt_st6[0][3]   286     T2 DualSpurVemierLUT_Cnt_st6[0][3]   286     T2 DualSpurVemierLUT_Cnt_st6[0][6]   252     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -108     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -72     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -72     T2 DualSpurVemierLUT_Cnt_st6[0][1]   -73     T2 DualSpurVemierLUT_Cnt_st6[0][1]   -74     T2 DualSpurVemierL	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_DualSpurVemiert.UT_Cnt_st6[0][1]   360     T2_DualSpurVemiert.UT_Cnt_st6[0][2]   324     T2_DualSpurVemiert.UT_Cnt_st6[0][4]   252     T2_DualSpurVemiert.UT_Cnt_st6[0][5]   216     T2_DualSpurVemiert.UT_Cnt_st6[0][6]   -180     T2_DualSpurVemiert.UT_Cnt_st6[0][7]   -144     T2_DualSpurVemiert.UT_Cnt_st6[0][8]   -108     T2_DualSpurVemiert.UT_Cnt_st6[0][9]   -72     T2_DualSpurVemiert.UT_Cnt_st6[0][9]   -72     T2_DualSpurVemiert.UT_Cnt_st6[0][1]   -73     T2_DualSpurVemiert.UT_Cnt_st6[0][1]   -74     T2_DualSpurVemier	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVemiert.UT_Cnt_st 6[0] 2    324     T2_DualSpurVemiert.UT_Cnt_st 6[0] 3    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    252     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 8    -108     T2_DualSpurVemiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 10    -36     T2_DualSpurVemiert.UT_Cnt_st 6[0] 11    0     T2_DualSpurVemiert.UT_Cnt_st 6[0] 12    36     T2_DualSpurVemiert.UT_Cnt_st 6[0] 13    72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 14    108     T2_DualSpurVemiert.UT_Cnt_st 6[0] 15    144     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 20    324     T2_DualSpurVemiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVemiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    5     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    8     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    8     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVerniertUT_Cnt_s16[0][3]	T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][4]   2552   216   12_DualSpurVernierLUT_Cnt_s16[0][5]   2-16   180	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][4]   2552   216   12_DualSpurVernierLUT_Cnt_s16[0][5]   2-16   180		-288
T2_DualSpurVernierLUT_Cnt_s16(0)[5] T2_DualSpurVernierLUT_Cnt_s16(0)[6] T2_DualSpurVernierLUT_Cnt_s16(0)[6] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[9] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[11] T2_DualSpurVernierLUT_Cnt_s16(0)[12] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[15] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[17] T2_DualSpurVernierLUT_Cnt_s16(0)[18] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[19] T2_DualSpurVernierLU		
T2_DualSpurVernierLUT_Cnt_s16(0)[6]   -180   -144		
12   DualSpurVernierLUT_Cnt_s16[0][7]   -144         12   DualSpurVernierLUT_Cnt_s16[0][8]   -108       12   DualSpurVernierLUT_Cnt_s16[0][10]   -36       12   DualSpurVernierLUT_Cnt_s16[0][11]   0       12   DualSpurVernierLUT_Cnt_s16[0][12]   36       12   DualSpurVernierLUT_Cnt_s16[0][13]   72       12   DualSpurVernierLUT_Cnt_s16[0][14]   108       12   DualSpurVernierLUT_Cnt_s16[0][15]   144       12   DualSpurVernierLUT_Cnt_s16[0][16]   180       12   DualSpurVernierLUT_Cnt_s16[0][17]   216       12   DualSpurVernierLUT_Cnt_s16[0][17]   216       12   DualSpurVernierLUT_Cnt_s16[0][19]   288       12   DualSpurVernierLUT_Cnt_s16[0][19]   288       12   DualSpurVernierLUT_Cnt_s16[0][21]   360       12   DualSpurVernierLUT_Cnt_s16[0][21]   360       12   DualSpurVernierLUT_Cnt_s16[1][2]   1       12   DualSpurVernierLUT_Cnt_s16[1][2]   1       12   DualSpurVernierLUT_Cnt_s16[1][3]   2       12   DualSpurVernierLUT_Cnt_s16[1][3]   2       12   DualSpurVernierLUT_Cnt_s16[1][4]   3       12   DualSpurVernierLUT_Cnt_s16[1][6]   5       12   DualSpurVernierLUT_Cnt_s16[1][6]   6       13   DualSpurVernierLUT_Cnt_s16[1][6]   7       14   12   DualSpurVernierLUT_Cnt_s16[1][6]   6       15   DualSpurVernierLUT_Cnt_s16[1][6]   7       16   DualSpurVernierLUT_Cnt_s16[1][6]   9       17   DualSpurVernierLUT_Cnt_s16[1][6]   9       18   DualSpurVernierLUT_Cnt_s16[1][6]   9       19   DualSpurVernierLUT_Cnt_s16[1][6]   9       10   DualSpurVernierLUT_Cnt_s16[1][6]   9       11   DualSpurVernierLUT_Cnt_s16[1][6]   9       12   DualSpurVernierLUT_Cnt_s16[1][6]   9         13   DualSpurVernierLUT_Cnt_s16[1][6]   9		
T2_DualSpurVerniertLUT_Cnt_s16[0][8]   -108     -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72		
T2_DualSpurVernierLUT_Cnt_s16[0][9]		
T2_DualSpurVernierLUT_Cnt_s16[0][10]  72_DualSpurVernierLUT_Cnt_s16[0][11]  72_DualSpurVernierLUT_Cnt_s16[0][12]  72_DualSpurVernierLUT_Cnt_s16[0][13]  72_DualSpurVernierLUT_Cnt_s16[0][14]  72_DualSpurVernierLUT_Cnt_s16[0][15]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][17]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][19]  72_DualSpurVernierLUT_Cnt_s16[0][20]  72_DualSpurVernierLUT_Cnt_s16[0][21]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][1]  72_DualSpurVernierLUT_Cnt_s16[1][2]  72_DualSpurVernierLUT_Cnt_s16[1][3]  72_DualSpurVernierLUT_Cnt_s16[1][4]  72_DualSpurVernierLUT_Cnt_s16[1][6]  73_DualSpurVernierLUT_Cnt_s16[1][6]  74_DualSpurVernierLUT_Cnt_s16[1][6]  75_DualSpurVernierLUT_Cnt_s16[1][6]  76_DualSpurVernierLUT_Cnt_s16[1][6]  77_DualSpurVernierLUT_Cnt_s16[1][6]  78_DualSpurVernierLUT_Cnt_s16[1][6]  79_DualSpurVernierLUT_Cnt_s16[1][6]  70_DualSpurVernierLUT_Cnt_s16[1][6]  71_DualSpurVernierLUT_Cnt_s16[1][6]		
T2_DualSpurVernierLUT_Cnt_s16[0][11]  T2_DualSpurVernierLUT_Cnt_s16[0][12]  T2_DualSpurVernierLUT_Cnt_s16[0][13]  T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  T2_DualSpurVernierLUT_Cnt_s16[0][19]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[1][0]  T2_DualSpurVernierLUT_Cnt_s16[1][1]  T3_DualSpurVernierLUT_Cnt_s16[1][1]  T4_DualSpurVernierLUT_Cnt_s16[1][1]  T5_DualSpurVernierLUT_Cnt_s16[1][1]  T6_DualSpurVernierLUT_Cnt_s16[1][1]  T6_DualSpurVernierLUT_Cnt_s16[1][1]  T6_DualSpurVernierLUT_Cnt_s16[1][1]  T6_DualSpurVernierLUT_Cnt_s16[1][1]  T6_DualSpurVernierLUT_Cnt_s16[1][1		
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 12_DualSpurVernierLUT_Cnt_s16[0][17] 12_DualSpurVernierLUT_Cnt_s16[0][19] 12_DualSpurVernierLUT_Cnt_s16[0][19] 12_DualSpurVernierLUT_Cnt_s16[0][20] 12_DualSpurVernierLUT_Cnt_s16[0][21] 1360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 12_DualSpurVernierLUT_Cnt_s16[1][0] 12_DualSpurVernierLUT_Cnt_s16[1][1] 12_DualSpurVernierLUT_Cnt_s16[1][1] 12_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][3] 12_DualSpurVernierLUT_Cnt_s16[1][4] 13_DualSpurVernierLUT_Cnt_s16[1][5] 14_DualSpurVernierLUT_Cnt_s16[1][6] 15_DualSpurVernierLUT_Cnt_s16[1][6] 16_DualSpurVernierLUT_Cnt_s16[1][6] 17_DualSpurVernierLUT_Cnt_s16[1][6] 18_DualSpurVernierLUT_Cnt_s16[1][6] 19_DualSpurVernierLUT_Cnt_s16[1][11] 10_DualSpurVernierLUT_Cnt_s16[1][11] 11_DualSpurVernierLUT_Cnt_s16[1][11] 11_DualSpurVernierLUT_Cnt_s16[1][11] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12]		
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 9  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0		0
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  0  T2_DualSpurVernierLUT_Cnt_s16[1][1]  12_DualSpurVernierLUT_Cnt_s16[1][2]  12_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][4]  3  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][11]  10	T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  10  T2_DualSpurVernierLUT_Cnt_s16[1][1]  11  T2_DualSpurVernierLUT_Cnt_s16[1][3]  21  T2_DualSpurVernierLUT_Cnt_s16[1][4]  32  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9	T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][15]		108
T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 10  T2_DualSpurVernierLUT_Cnt_s16[1][10] 11		144
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10		
T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0		
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 1		
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2 DualSpur/erniert LT_Cnt_s16[1][13]		
	T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19] T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	7 8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	14 16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpur/vernierLUT_Cnt_s16[3][17]	13 15		
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	128		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	80		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep k_VernCorrErrorThresh_Deg_f32	18 61.77320576		
k_VernOORangeThresh_Deg_f32	1180.024269		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	194.3084972		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	258.7965072		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3065		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	Expected Value	Poou!t
Name DigColPs_HwAVernCorrFault_Cnt_M_lgc	Actual Value	Expected Value	Result
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	~
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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	483.108521	483.1084972 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6E	0x6E	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.58 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs ColTrimStatic Deg M f32	75.3
DigColPs HwAVernCorrFault Cnt M Igc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58683
DigColPs_I2CHwColAngle_Deg_M_f32	226.2329707
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	52949
DigColPs_I2CHwSpurAngle_Deg_M_f32	61.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	61.9
DigColPs_TrimCompStatic_Cnt_M_u16	1960
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
	-30
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	2 3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	1 2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21] T3_DualSpurVernierLUT_Cnt_s16[2][0]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	7 8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10 12
T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	9 11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
	240
k_SkipStepErrDiag_Cnt_str.Threshold	
k_SkipStepErrDiag_Cnt_str.PStep	11
	11 37 70



Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	36.6228292		
k_VernOORangeThresh_Deg_f32	992.7934918		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.2329707		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	113.3681837		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1804		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

igi_Rie_insi_Sa_bigColPs.Plin_bigColPsEOL	tgt_Pim_bigColPSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	150.932968	150.9329707 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363647	-736.3636364 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Test Step 2.59 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	175
DigColPs_ColParityError_Cnt_M_Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	79.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	7302
DigColPs_I2CHwColAngle_Deg_M_f32	31.81471384
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	20291
DigColPs_I2CHwSpurAngle_Deg_M_f32	63
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175
DigColPs_SpurTrimStatic_Deg_M_f32	63
DigColPs_TrimCompStatic_Cnt_M_u16	1996
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
	196
T2_ColSpurVernierLUT_Cnt_s16[0][11]	
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][1]	
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][7]	
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	14
T2_ColSpurVernierLUT_Cnt_s16[3][1]	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
	4
T2_ColSpurVernierLUT_Cnt_s16[3][15]	
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
12_Buaiopui veriici Eo 1_cin_3 fo[o][o]	
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2 DualSpurVernierLUT Cnt s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17] T3_DualSpurVernierLUT_Cnt_s16[2][18]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][20] T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
	3

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	161		
k_SkipStepErrDiag_Cnt_str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	14		
k_VernCorrErrorDiag_Cnt_str.Threshold	63		
k_VernCorrErrorDiag_Cnt_str.PStep	40		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	59.55320692		
k_VernOORangeThresh_Deg_f32	1084.696699		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	31.81471384		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.2363453		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	358.528931	358.528934 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
= = =	1		

Status		0x01 0x01		<b>✓</b>
Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	-
GetResource	1	GetResource	1	<b> </b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	<b> </b>
ConstrainOneRev	2	ConstrainOneRev	2	<b> </b>
VernierLookup	1	VernierLookup	1	<b>→</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	<b> </b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	-

56

3

1

0

-540

0x6C

0x0C

56

3

0

0x6C

0x0C

-540 ± 0.0009

Test Step 2.60 (Repeat Count = 1)		<u> </u>
Name	Input Value	
DigColPsInt_GetCustData()	0	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125	
DigColPs_ColTrimStatic_Deg_M_f32	83.5	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	42754	
DigColPs_I2CHwColAngle_Deg_M_f32	256.6914936	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	24629	
DigColPs_I2CHwSpurAngle_Deg_M_f32	64.1	

DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16

DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value

 $\label{tgt_digColPs_Per2_I2CHwAbsPos_HwDeg_f32.value} $$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value $$$ 

DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16

NTC

Param





Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	16
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	688
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	3
DigColPs_SpurParityError_Cnt_M_Igc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	64.1
DigColPs_TrimCompStatic_Cnt_M_u16	2032
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs VernierAngleOORange Cnt M Igc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T3_ColSpurVernierLUT_Cnt_s16[2][10]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	2   15

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T3_DualSpurVernierLUT_Cst_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	5
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	0





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2 DualSpurVernierLUT Cnt s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	5		
k SkipStepErrDiag Cnt str.NStep	11		
k_VernCorrErrorDiag_Cnt_str.Threshold	11		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	67.07432961		
k_VernOORangeThresh_Deg_f32	836.2919484		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	256.6914936		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105.0697877		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	921		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid Cnt lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0		
tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc	tgt_DigColPs_Per2_TrimComp_		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt Pim DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M Igc	1	1	- Nosuit
DigColPs I2CHwColAngleForTrim Deg M f32	163.636353	163.6363636 ± 0.00048828125	
DigCoIPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	-
DigColPs_PrevColPos_Deg_M_f32	173.191498	173.1914936 ± 0.0001220703125	
	3		
DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	4	3	
	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16		1	Ž
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1		
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	Ž
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value		-736.3636364 ± 0.0009	Ž
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363647		Ĭ
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	-
NTC	0x6C	0x6C	<b>*</b>
Param	0x0C	0x0C	<b>*</b>
Status	0x01	0x01	~
NTC	0x6F	0x6F	<b>~</b>
Param	0x00	0x00	<b>V</b>
Status	0x00	0x00	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

	· · · · · · · ·
Name	Input Value
DigColPsInt_GetCustData()	255
DigColPs_ColParityError_Cnt_M_Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	243
DigColPs_ColTrimStatic_Deg_M_f32	87.6
ligColPs_HwAVernCorrFault_Cnt_M_lgc	1
ligColPs_I2CColSensorFault_Cnt_M_lgc	0
ligColPs_I2CHwColAngle_Cnt_M_u16	59004
ligColPs_I2CHwColAngle_Deg_M_f32	93.13262653
ligColPs_I2CHwDataType_Cnt_M_u08	4
igColPs_I2CHwSpurAngle_Cnt_M_u16	26282
igColPs_I2CHwSpurAngle_Deg_M_f32	65.2
igColPs_I2CHwTrimTransCnts_Uls_M_u08	5
igColPs_I2CSensCommFlts_Cnt_M_u08	5
igColPs_I2CSpurSensorFault_Cnt_M_lgc	0
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
igColPs_PrevColPos_Deg_M_f32	1600.2344
igColPs_PrevVernierLevelNo_Cnt_M_u08	5
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	14
igColPs_SpurParityError_Cnt_M_lgc	1
igColPs_SpurSensorFaultAcc_Cnt_M_u16	255
higColPs_SpurTrimStatic_Deg_M_f32	65.2
igColPs_TrimCompStatic_Cnt_M_u16	2068
igColPs VernCorrDetectAcc Cnt M u16	3
igColPs VernierAngleOORange Cnt M lgc	0
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2 ColSpurVernierLUT Cnt s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2 ColSpurVernierLUT Cnt s16[0][14]	294
	327
2_ColSpurVernierLUT_Cnt_s16[0][15]	359
2_ColSpurVernierLUT_Cnt_s16[0][16]	0
2_ColSpurVernierLUT_Cnt_s16[1][0]	*
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
2_ColSpurVernierLUT_Cnt_s16[1][12]	3
2 ColSpurVernierLUT Cnt s16[1][13]	2





Input Value  1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
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14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
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8
9
0
1
2

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Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][0] T0_DualSpurVernierLUT_Cnt_s16[3][0]	22	
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
T2_DualSpurVernierLUT_Cnt_s16[3][2] T0_DualSpurVernierLUT_Cnt_s16[3][2]	4	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14	
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16	
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][11]	20	
	3	
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9	
T2_DualSpurVernierLUT_Cnt_s16[3][15]	11	
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13	
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	15 17	
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	19	
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	21	
k_SelectFromColumn_Cnt_lgc	0	
k SkipStepErrDiag Cnt str.Threshold	222	
	17	
k_SkipStepErrDiag_Cnt_str.PStep	31	
k_SkipStepErrDiag_Cnt_str.NStep k VernCorrErrorDiag_Cnt_str.Threshold	22	
	38	
k_VernCorrErrorDiag_Cnt_str.PStep	1	
k_VernCorrErrorThresh_Deg_f32	59.32419395	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	59.32419395	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	93.13262653	
	131.6931788	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3932	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	l=
Name	Actual Value Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1 1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0 ± 0.00048828125	

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	5.53263092	5.532626534 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	155
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	91.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1.
DigColPs_I2CHwColAngle_Cnt_M_u16	15177
DigColPs_I2CHwColAngle_Deg_M_f32	20.78231114
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	55143
DigColPs_I2CHwSpurAngle_Deg_M_f32	66.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	596.9864027
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155
DigColPs_SpurTrimStatic_Deg_M_f32	66.3
DigColPs_TrimCompStatic_Cnt_M_u16	2104
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1.
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
[2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2 ColSpurVernierLUT Cnt s16[0][2]	-99
Γ2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
Γ2_ColSpurVernierLUT_Cnt_s16[0][5]	0
Γ2_ColSpurVernierLUT_Cnt_s16[0][6]	32
Γ2_ColSpurVernierLUT_Cnt_s16[0][7]	65
Γ2_ColSpurVernierLUT_Cnt_s16[0][8]	98
Γ2_ColSpurVernierLUT_Cnt_s16[0][9]	130
Γ2_ColSpurVernierLUT_Cnt_s16[0][10]	163
Γ2_ColSpurVernierLUT_Cnt_s16[0][11]	196
Γ2_ColSpurVernierLUT_Cnt_s16[0][12]	229
F2_ColSpurVernierEUT_Cnt_s16[0][13]	261
[2_GolspurVernierEUT_Gnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2 ColSpurVernierLUT Cnt s16[1][2]	3

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
	0
T2_ColSpurVernierLUT_Cnt_s16[2][0] T3_ColSpurVernierLUT_Cnt_s16[2][1]	
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2 ColSpurVernierLUT Cnt s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2 ColSpurVernierLUT Cnt s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T3_DualSpurVernierLUT_Cnt_s16[0][0]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396 360
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2] T0_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
544.5pdi.tolillole01_5in_510[1][2]	

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17] T3_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2 4
T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10] T3_DualSpurVernierLUT_Cst_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	1 3
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cst_s16[3][21]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21] k_SelectFromColumn_Cnt_lgc	21 0
k_SkipStepErrDiag_Cnt_str.Threshold	19
k_SkipStepErrDiag_Cnt_str.PStep	22
k_SkipStepErrDiag_Cnt_str.NStep	49
k_VernCorrErrorDiag_Cnt_str.Threshold	91
k_VernCorrErrorDiag_Cnt_str.PStep	48
k_VernCorrErrorDiag_Cnt_str.NStep	19
k_VernCorrErrorThresh_Deg_f32	6.884903669
k_VernOORangeThresh_Deg_f32	605.936505
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	20.78231114		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	116.1393507		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	678		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	649.082275	649.0823111 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	~
Status	0x01	0x01	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.63 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	125
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	95.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	62253
DigColPs_I2CHwColAngle_Deg_M_f32	99.35150683
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26843
DigColPs_I2CHwSpurAngle_Deg_M_f32	67.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	822.8970824
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	67.4
DigColPs_TrimCompStatic_Cnt_M_u16	2140
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2 ColSpurVernierLUT Cnt s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2 ColSpurVernierLUT Cnt s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
	11
T2_ColSpurVernierLUT_Cnt_s16[3][2]	
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
	10
T2_ColSpurVernierLUT_Cnt_s16[3][13]	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
	202
	216
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][6] T2_DualSpurVernierLUT_Cnt_s16[0][7]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180

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Name  T2_DualSpurVernierLUT_Cnt_s16[0][10]  T2_DualSpurVernierLUT_Cnt_s16[0][11]  T2_DualSpurVernierLUT_Cnt_s16[0][12]	Input Value -36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	-30
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2 DualSpurVernierLUT Cnt s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
	22
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
	11

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DigColPs\_Per2

		•	
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	206		
k_SkipStepErrDiag_Cnt_str.PStep	31		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	80		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	85.02186632		
k_VernOORangeThresh_Deg_f32	1061.295247		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	99.35150683		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	127.7892992		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cr	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1445.27759	1445.277591 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	32	32	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	-
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Test Step 2.64 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	243	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105	
DigColPs_ColTrimStatic_Deg_M_f32	99.9	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	55455	
DigColPs_I2CHwColAngle_Deg_M_f32	346.7766712	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	47484	
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.5	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	
DigColPs_I2CSensCommFlts_Cnt_M_u08	31	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1630.352482	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	
DigColPs_SpurParityError_Cnt_M_lgc	0	

 $\underline{\mathsf{tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value}}$ 





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	243
DigColPs_SpurTrimStatic_Deg_M_f32	68.5
DigColPs_TrimCompStatic_Cnt_M_u16	2176
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLOT_Cht_\$10[0][10] T2_ColSpurVernierLUT_Cht_\$10[0][10]	0
T2_ColSpurVernierLOT_Cht_\$10[1][0] T2_ColSpurVernierLUT_Cht_\$16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_S16[1][2] T2_ColSpurVernierLUT_Cnt_S16[1][3]	2
T2_ColSpurVernierLUT_Cnt_S16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLOT_Cnt_\$16[2][13] T2_ColSpurVernierLUT_Cnt_\$16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
TO ColCourt/Corpical LIT Cot e46[01[40]	10
T2_ColSpurVernierLUT_Cnt_s16[3][13]	
T2_ColSpurVernierLUT_Cnt_S16[3][13] T2_ColSpurVernierLUT_Cnt_S16[3][14]	7

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duaiopui veitiieteu i Ott 510[3][3]	10

DigColPs\_Per2



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	98		
k_SkipStepErrDiag_Cnt_str.PStep	47		
k_SkipStepErrDiag_Cnt_str.NStep	39		
k_VernCorrErrorDiag_Cnt_str.Threshold	47		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	6.903702974		
k_VernOORangeThresh_Deg_f32	1481.66354		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	346.7766712		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	85.06156057		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2733		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAb	sPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState	e_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimCom	p_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
Dis ColDo LluvA\/ors Cors Foult Cot M Iso	4	4	-

9C	131_1		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	966.876709	966.8766712 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	18	18	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18	18	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	<b>✓</b>
Status	0x01	0x01	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.65 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123
DigColPs_ColTrimStatic_Deg_M_f32	104

2014-10-14, 17:31:16+0530



Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	28915
DigColPs I2CHwColAngle Deg M f32	118.0404236
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43172
DigColPs_I2CHwSpurAngle_Deg_M_f32	69.6
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs I2CSensCommFlts Cnt M u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs PrevAngleDataAvailable Cnt M lgc	1
DigColPs_PrevColPos_Deg_M_f32	943.3614662
	1
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	69.6
DigColPs_TrimCompStatic_Cnt_M_u16	2212
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T3_ColSpurVernierLUT_Cnt_s46[4][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12] T0_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	
	8
T2 ColSpurVernierLUT Cnt s16[2][13]	
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	6 4 2
T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4





T. C. OSSAVVANICUT, CM   1907  1	Nama	Input Value
12_CoSquirement_Cot_statp[0]   11	Name	Input Value
P. Colispa/remontall_Colstiligid   5   12   Colispa/remontall_Colstiligid   5   12   Colispa/remontall_Colstiligid   5   15   Colispa/remontall_Colstiligid   5   15   Colispa/remontall_Colstiligid   15   Colispa/remontall_Colstiligid   16   Colispa/remontall_Colstiligid   16   Colispa/remontall_Colstiligid   17   Colispa/remontall_Colstiligid   18		
T. Collego/vernient U. Col. 4 (1978)		
12_00spx/remail_Coll_Statistics    2		
T2_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 13_COSQAVeniment_U_Cnt_visit(SIS) 14_COSQAVeniment_U_Cnt_visit(SIS) 15_COSQAVeniment_U_Cnt_visit(SIS) 16_COSQAVeniment_U_Cnt_visit(SIS) 17_COSQAVeniment_U_Cnt_visit(SIS) 18_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit		
12, Osspar/weinstall, Cot, 14(3)(1)		
Tz. COSSpur/venseLT. Cot. s163789 6 Tz. COSSpur/venseLT. Cot. s163719 6 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 19 Tz. COSSpur/v		
To Conspired ment LT, Det. 310(19)  To Conspired ment LT, Det. 510(19)  To Conspired ment LT, Det. 510		
To Colsput/went LT Ont \$180]119 10		
T. Colsput/ment.U. Col.; st (20)112   13   13   13   13   13   13   13	T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[15]	T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T. C. OSBOUYMONE LT. COL. \$16(3)*14   7   7   7   7   7   7   7   7   7	T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_Colspar/membut/D_cnt_st093115  4	T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
17, CoSpur/wentUT, Cott, \$103115	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_CosSpurVement UT_Cnt_s160[110] 388  12_DusSpurVement UT_Cnt_s160[11] 380  12_DusSpurVement UT_Cnt_s160[12] 384  12_DusSpurVement UT_Cnt_s160[13] 384  12_DusSpurVement UT_Cnt_s160[13] 384  12_DusSpurVement UT_Cnt_s160[14] 382  12_DusSpurVement UT_Cnt_s160[16] 388  12_DusSpurVement UT_Cnt_s160[16] 380  12_DusSpurVement UT_Cnt_s160[16] 380  12_DusSpurVement UT_Cnt_s160[17] 380  12_DusSpurVement UT_Cnt_s160[18] 380  12_DusSpurVement UT_Cnt_s160[18	T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
12. DualSparVermicht UT. Cit.; 1490191   396 172. DualSparVermicht UT. Cit.; 1490191   356 173. DualSparVermicht UT. Cit.; 1490191   324 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   316 174. DualSparVermicht UT. Cit.; 1490191   316 175. DualSparVermicht UT. Cit.; 1490191   316 177. DualSparVermicht UT. Cit.; 1490191   317 177. DualSparVermicht UT. Cit.; 1490191   317 177. DualSparVermicht UT. Cit.; 1490191   318 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   322 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   327 177. DualSparVermicht UT. Cit.; 3490191   327 177. DualSparVermicht UT. Cit.; 3490191   329 177. DualSparVermicht U	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
12_DasSgru/FemerU_T_Crt_s160[12]	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_DasSgru/FemerU_T_Crt_s160[12]	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2, DuaSpurVermiet.UT_Cnt_s160[15]   288		-360
T. DualSparVermicLUT_Cnt_s180[H]   252	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_Dus SpurVernieLUT_Cnt_s16[0][5]   216     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -72     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -36     T2_Dus SpurVern		-288
T2_Dus SpurVernieLUT_Cnt_s16[0][5]   216     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -72     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -36     T2_Dus SpurVern		
12, DualSparVermetLUT_Cnt_s16(0)(8)   .189		
12. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -74 17. DuaSgurVemet.UT_Cnt_s160[12] -74 17. DuaSgurVemet.UT_Cnt_s160[13] -74 17. DuaSg		
12_DusSpurVermetUT_Cnt_s160[18]   -102   T2_DusSpurVermetUT_Cnt_s160[10]   -72   T2_DusSpurVermetUT_Cnt_s160[10]   -36   T2_DusSpurVermetUT_Cnt_s160[11]   0   T2_DusSpurVermetUT_Cnt_s160[11]   0   T2_DusSpurVermetUT_Cnt_s160[11]   36   T2_DusSpurVermetUT_Cnt_s160[11]   36   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   109   T2_DusSpurVermetUT_Cnt_s160[12]   109   T2_DusSpurVermetUT_Cnt_s160[13]   109   T2_DusSpu		
12. DualSpur/vernietUT_Cnt_s16(0) 10   36   36   36   36   36   36   36   3		
12. DualSpur/vernierLUT_Cnt_s16()[11]   0   0   0   0   0   0   0   0   0		
12   DusiSpur/VernietUT_Cnt, 1610[11]   12   2   2   2   2   2   2   2   2		
12 DuaiSpurVernierLUT_Cnt_s16[0][12]   36   72   72   73   73   74   74   74   74   74   74		
T2   DualSpurVermierLUT_Cnt_s16[0][14]   108		
172   DuaiSpurVernierLUT_Cnt_sticip[14]   108     172   DuaiSpurVernierLUT_Cnt_sticip[16]   144     172   DuaiSpurVernierLUT_Cnt_sticip[17]   126     172   DuaiSpurVernierLUT_Cnt_sticip[17]   126     172   DuaiSpurVernierLUT_Cnt_sticip[17]   126     172   DuaiSpurVernierLUT_Cnt_sticip[18]   125     172   DuaiSpurVernierLUT_Cnt_sticip[18]   126     173   DuaiSpurVernierLUT_Cnt_sticip[18]   127     174   DuaiSpurVernierLUT_Cnt_sticip[18]   127     175   DuaiSpurVernierLUT_Cnt_sticip[18]   137     175   DuaiSpurVernierLUT_Cnt_sticip[18]   147     175   DuaiSpurVernierLUT_Cnt_sticip[18]   14		
T2   DualSpurVermict.UT   Cnt   s16(0) 15    144     T2   DualSpurVermict.UT   Cnt   s16(0) 16    180     T2   DualSpurVermict.UT   Cnt   s16(0) 17    216     T2   DualSpurVermict.UT   Cnt   s16(0) 17    216     T2   DualSpurVermict.UT   Cnt   s16(0) 18    252     T3   DualSpurVermict.UT   Cnt   s16(0) 20    324     T2   DualSpurVermict.UT   Cnt   s16(0) 20    324     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T3   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T3   DualSpurVermict.UT   Cnt   s16(1) 10    1     T4   DualSpurVermict.UT   Cnt   s16(1) 10    1     T2   DualSpurVermict.UT   Cnt   s16(1) 10    1     T3   DualSpurVermict.UT   Cnt   s16(1) 10    3     T4   DualSpurVermict.UT   Cnt   s16(1) 10    5     T5   DualSpurVermict.UT   Cnt   s16(1) 10    5     T5   DualSpurVermict.UT   Cnt   s16(1) 10    5     T5   DualSpurVermict.UT   Cnt   s16(1) 10    6     T5   DualSpurVermict.UT   Cnt   s16(1) 10    7     T5   DualSpurVermict.UT   Cnt   s16(1) 10    8     T6   DualSpurVermict.UT   Cnt   s16(1) 10    9     T7   DualSpurVermict.UT   Cnt   s16(1) 10    9     T8   DualSpurVermict.UT   Cnt   s16(1) 10    9     T8   DualSpurVermict.UT   Cnt   s16(1) 10    9     T7   DualSpurVermict.UT   Cnt   s16(1) 10    9     T8   DualSpurVermict.UT   Cnt   s16(1) 10		
12		
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 2 T2_DualSpurVernierLUT_Cnt_s16[1][1] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 4 T2_DualSpurVernierLUT_Cnt_s16[1][1] 5 T2_DualSpurVernierLUT_Cnt_s16[1][1] 6 T2_DualSpurVernierLUT_Cnt_s16[1][1] 7 T2_DualSpurVernierLUT_Cnt_s16[1][1] 7 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 10 T2_DualSpurVernierLUT_Cnt_s16[1][1] 10 T2_DualSpurVernierLUT_Cnt_s16[1][1] 11 T2_D		
12   DualSpurVemierLUT_Cnt_st6[0][16]   288   72   DualSpurVemierLUT_Cnt_st6[0][20]   324   32		
T2_DualSpurVemierLUT_Cnt_st6[0][19]  72_DualSpurVemierLUT_Cnt_st6[0][20]  72_DualSpurVemierLUT_Cnt_st6[0][21]  72_DualSpurVemierLUT_Cnt_st6[1][0]  72_DualSpurVemierLUT_Cnt_st6[1][0]  72_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][3]  72_DualSpurVemierLUT_Cnt_st6[1][3]  72_DualSpurVemierLUT_Cnt_st6[1][8]  73_DualSpurVemierLUT_Cnt_st6[1][8]  74_DualSpurVemierLUT_Cnt_st6[1][8]  75_DualSpurVemierLUT_Cnt_st6[1][8]  76_DualSpurVemierLUT_Cnt_st6[1][8]  77_DualSpurVemierLUT_Cnt_st6[1][8]  78_DualSpurVemierLUT_Cnt_st6[1][8]  79_DualSpurVemierLUT_Cnt_st6[1][8]  70_DualSpurVemierLUT_Cnt_st6[1][8]  71_DualSpurVemierLUT_Cnt_st6[1][8]  72_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][1]  73_DualSpurVemierLUT_Cnt_st6[1][1]  74_DualSpurVemierLUT_Cnt_st6[1][1]  75_DualSpurVemierLUT_Cnt_st6[1][1]  76_DualSpurVemierLUT_Cnt_st6[1][1]  77_DualSpurVemierLUT_Cnt_st6[1][1]  78_DualSpurVemierLUT_Cnt_st6[1][1]  79_DualSpurVemierLUT_Cnt_st6[1][1]  70_DualSpurVemierLUT_Cnt_st6[1][1]  71_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][1]  71_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][1]  73_DualSpurVemierLUT_Cnt_st6[1][1]  74_DualSpurVemierLUT_Cnt_st6[1][1]  75_DualSpurVemierLUT_Cnt_st6[1][1]  76_DualSpurVemierLUT_Cnt_st6[1][1]  77_DualSpurVemierLUT_Cnt_st6[1][1]  78_DualSpurVemierLUT_Cnt_st6[1][1]  79_DualSpurVemierLUT_Cnt_st6[1][1]  70_DualSpurVemierLUT_Cnt_st6[1][1]  71_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][1]  73_DualSpurVemierLUT_Cnt_st6[1][1]  74_DualSpurVemierLUT_Cnt_st6[1][1]  75_DualSpurVemierLUT_Cnt_st6[1][1]  76_DualSpurVemierLUT_Cnt_st6[1][1]  77_DualSpurVemierLUT_Cnt_st6[1][1]  78_DualSpurVemierLUT_Cnt_st6[1][1]  79_DualSpurVemierLUT_Cnt_st6[1][1]  70_DualSpurVemierLUT_Cnt_st6[1][1]  71_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][1]  73_DualSpurVemierLUT_Cnt_st6[1][1]  74_DualSpurVemierLUT_Cnt_st6[1][1]  75_DualSpurVemierLUT_Cnt_st6[1][1]  76_DualSpurVemierLUT_Cnt_st		
T2_DualSpurVerniertUT_Cnt_s16[0] 20    324		
T2_DualSpurVerniertUT_Cnt_s16[0][21] T2_DualSpurVerniertUT_Cnt_s16[1][0] T2_DualSpurVerniertUT_Cnt_s16[1][1] T2_DualSpurVerniertUT_Cnt_s16[1][2] T2_DualSpurVerniertUT_Cnt_s16[1][3] T2_DualSpurVerniertUT_Cnt_s16[1][3] T2_DualSpurVerniertUT_Cnt_s16[1][4] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][7] 6 T2_DualSpurVerniertUT_Cnt_s16[1][8] T2_DualSpurVerniertUT_Cnt_s16[1][8] T2_DualSpurVerniertUT_Cnt_s16[1][9] 8 T2_DualSpurVerniertUT_Cnt_s16[1][1] T2_DualSpurVerniertUT_Cnt_s16[2][1] T2_DualSpurVerniertUT_Cnt		
T2_DualSpurVernierLUT_Cnt_s16[1][0]   9		
T2_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][3] 12_DualSpurVernierLUT_Cnt_s16[1][4] 13_DualSpurVernierLUT_Cnt_s16[1][6] 14_DualSpurVernierLUT_Cnt_s16[1][6] 15_DualSpurVernierLUT_Cnt_s16[1][6] 16_DualSpurVernierLUT_Cnt_s16[1][6] 17_DualSpurVernierLUT_Cnt_s16[1][8] 17_DualSpurVernierLUT_Cnt_s16[1][8] 17_DualSpurVernierLUT_Cnt_s16[1][8] 18_DualSpurVernierLUT_Cnt_s16[1][8] 19_DualSpurVernierLUT_Cnt_s16[1][9] 10_DualSpurVernierLUT_Cnt_s16[1][1] 10_DualSpurVernierLUT_Cnt_s16[1][1] 11_DualSpurVernierLUT_Cnt_s16[1][1] 11_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[2][2] 13_DualSpurVernierLUT_Cnt_s16[2][2] 14_DualSpurVernierLUT_Cnt_s16[2][2] 15_DualSpurVernierLUT_Cnt_s16[2][2] 16_DualSpurVernierLUT_Cnt_s16[2][2] 17_DualSpurVernierLUT_Cnt_s16[2][2] 18_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[	T2_DualSpurVernierLUT_Cnt_s16[0][21]	
T2_DualSpurVerniert_UT_Cnt_s16[1][2]	T2_DualSpurVernierLUT_Cnt_s16[1][0]	
T2_DualSpurVerniert.UT_Cnt_s16[1][3]  12_DualSpurVerniert.UT_Cnt_s16[1][4]  3	T2_DualSpurVernierLUT_Cnt_s16[1][1]	
T2_DualSpurVernierLUT_Cnt_s16[1][4]   3   4   72_DualSpurVernierLUT_Cnt_s16[1][6]   5   5   72_DualSpurVernierLUT_Cnt_s16[1][6]   5   6   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][9]   8   8   7   72_DualSpurVernierLUT_Cnt_s16[1][10]   9   72_DualSpurVernierLUT_Cnt_s16[1][11]   0   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][16] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 10 T2_DualSpurVernierLUT_Cnt_s16[2][2] 9 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 7 T2_DualSpurVernierLUT_Cnt_s16[2][6] 8	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][6]  72_DualSpurVernierLUT_Cnt_s16[1][8]  72_DualSpurVernierLUT_Cnt_s16[1][8]  72_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][12]  1  T2_DualSpurVernierLUT_Cnt_s16[1][13]  2  T2_DualSpurVernierLUT_Cnt_s16[1][14]  3  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][17]  6  T2_DualSpurVernierLUT_Cnt_s16[1][18]  7  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][20]  7  T2_DualSpurVernierLUT_Cnt_s16[1][21]  0  T2_DualSpurVernierLUT_Cnt_s16[1][21]  10  T2_DualSpurVernierLUT_Cnt_s16[2][1]  11  T2_DualSpurVernierLUT_Cnt_s16[2][2]  2  T2_DualSpurVernierLUT_Cnt_s16[2][3]  3  T2_DualSpurVernierLUT_Cnt_s16[2][4]  4  T2_DualSpurVernierLUT_Cnt_s16[2][6]  5  T2_DualSpurVernierLUT_Cnt_s16[2][6]  6  T2_DualSpurVernierLUT_Cnt_s16[2][6]  7  T2_DualSpurVernierLUT_Cnt_s16[2][6]  7  T2_DualSpurVernierLUT_Cnt_s16[2][6]  8	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][7]  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][12]  12_DualSpurVernierLUT_Cnt_s16[1][12]  12_DualSpurVernierLUT_Cnt_s16[1][14]  12_DualSpurVernierLUT_Cnt_s16[1][15]  4  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][17]  6  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][20]  9  T2_DualSpurVernierLUT_Cnt_s16[1][20]  9  T2_DualSpurVernierLUT_Cnt_s16[1][21]  10_DualSpurVernierLUT_Cnt_s16[2][0]  10_DualSpurVernierLUT_Cnt_s16[2][1]  11_DualSpurVernierLUT_Cnt_s16[2][1]  12_DualSpurVernierLUT_Cnt_s16[2][1]  12_DualSpurVernierLUT_Cnt_s16[2][2]  12_DualSpurVernierLUT_Cnt_s16[2][3]  12_DualSpurVernierLUT_Cnt_s16[2][6]  13_DualSpurVernierLUT_Cnt_s16[2][6]  14_DualSpurVernierLUT_Cnt_s16[2][6]  15_DualSpurVernierLUT_Cnt_s16[2][6]  16_DualSpurVernierLUT_Cnt_s16[2][6]  17_DualSpurVernierLUT_Cnt_s16[2][6]  18_DualSpurVernierLUT_Cnt_s16[2][6]  19_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]	T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVemiert.UT_Cnt_s16[1][8] 7 T2_DualSpurVemiert.UT_Cnt_s16[1][9] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][10] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][12] 1 T2_DualSpurVemiert.UT_Cnt_s16[1][13] 2 T2_DualSpurVemiert.UT_Cnt_s16[1][15] 4 T2_DualSpurVemiert.UT_Cnt_s16[1][16] 5 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][20] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 0 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][2] 2 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 3 T2_DualSpurVemiert.UT_Cnt_s16[2][4] 4 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 5 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 6 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVemiert.UT_Cnt_s16[1][10] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][12] 1 T2_DualSpurVemiert.UT_Cnt_s16[1][13] 2 T2_DualSpurVemiert.UT_Cnt_s16[1][14] 3 T2_DualSpurVemiert.UT_Cnt_s16[1][15] 4 T2_DualSpurVemiert.UT_Cnt_s16[1][16] 5 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 7 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][20] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][20] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 0 T2_DualSpurVemiert.UT_Cnt_s16[2][0] 12_DualSpurVemiert.UT_Cnt_s16[2][2] 2 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 3 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 4 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 5 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 6 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][8]	T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 2 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][15] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		9
T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][16]       5         T2_DualSpurVernierLUT_Cnt_s16[1][17]       6         T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][6]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6		
T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][0] 2 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][16]       4         T2_DualSpurVernierLUT_Cnt_s16[1][16]       5         T2_DualSpurVernierLUT_Cnt_s16[1][17]       6         T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][15]		
T2_DualSpurVernierLUT_Cnt_s16[1][16]  T2_DualSpurVernierLUT_Cnt_s16[1][17]  T2_DualSpurVernierLUT_Cnt_s16[1][18]  T2_DualSpurVernierLUT_Cnt_s16[1][19]  T2_DualSpurVernierLUT_Cnt_s16[1][20]  T2_DualSpurVernierLUT_Cnt_s16[1][21]  T2_DualSpurVernierLUT_Cnt_s16[2][0]  T2_DualSpurVernierLUT_Cnt_s16[2][0]  T2_DualSpurVernierLUT_Cnt_s16[2][1]  T2_DualSpurVernierLUT_Cnt_s16[2][2]  T2_DualSpurVernierLUT_Cnt_s16[2][2]  T2_DualSpurVernierLUT_Cnt_s16[2][3]  T2_DualSpurVernierLUT_Cnt_s16[2][4]  T2_DualSpurVernierLUT_Cnt_s16[2][5]  T2_DualSpurVernierLUT_Cnt_s16[2][6]  T2_DualSpurVernierLUT_Cnt_s16[2][6]  T2_DualSpurVernierLUT_Cnt_s16[2][7]  T2_DualSpurVernierLUT_Cnt_s16[2][8]  8		
T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][20]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8	T2_DualSpurVernierLUT_Cnt_s16[2][5]	
T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] 9	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] 10	T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] 0	T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] 1	T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2 DualSpurVernierLUT Cnt s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	37		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	27		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k VernCorrErrorThresh Deg f32	16.13001919		
k_VernOORangeThresh_Deg_f32	708.4126034		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0404236		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	214.0159558		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2465	and the land	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	I=	
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	14.0404205	14.04042363 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tot DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value	0	0	•

0

0

-900

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

0

0

-900 ± 0.0009



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.66 (Repeat Count = 1)	
lame	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	108.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	32244
DigColPs_I2CHwColAngle_Deg_M_f32	233.8189296
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26632
DigColPs_I2CHwSpurAngle_Deg_M_f32	70.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	19
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	837.3964648
0igColPs_PrevVernierLevelNo_Cnt_M_u08	14
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	70.7
DigColPs_TrimCompStatic_Cnt_M_u16	2248
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2 ColSpurVernierLUT Cnt s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2 ColSpurVernierLUT Cnt s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[1][12]	3
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[1][13]	2
	· ·





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2] T3_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18] T3_DualSpurVernierLUT_Cnt_s16[2][19]	7 8		
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	3		
T2_DualSpur\/craicri_UT_Cnt_s16[3][12] T3_DualSpur\/craicri_UT_Cnt_s46[3][12]			
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	188		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	32		
k_VernCorrErrorDiag_Cnt_str.Threshold	36		
k_VernCorrErrorDiag_Cnt_str.PStep	9		
k_VernCorrErrorDiag_Cnt_str.NStep k_VernCorrErrorThresh_Deg_f32	0 32.58559203		
k_VernOORangeThresh_Deg_f32	1033.041085		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	233.8189296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	253.5267325		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3274		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

490.909088

2

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

 ${\tt DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08}$ 

490.9090909 ± 0.00048828125

2

DigColPs\_Per2



Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	485.718933	485.7189296 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	3	3	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-414.281067	-414.2810704 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.67 (Repeat Count = 1)	Innut Value
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	127
DigColPs_ColTrimStatic_Deg_M_f32	112.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50430
DigColPs_I2CHwColAngle_Deg_M_f32	131.2116221
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	62280
DigColPs_I2CHwSpurAngle_Deg_M_f32	71.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	339.5431169
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	14
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	71.8
DigColPs_TrimCompStatic_Cnt_M_u16	2284
DigColPs_VernCorrDetectAcc_Cnt_M_u16	5
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359

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Name		
P. CASSA/VerinetUT, Cot. 54(1)  2   2   2   CASSA/VerinetUT, Cot. 54(1)  3   3   1   1   1   1   1   1   1   1		•
Tz. Colspa/winestuff. Ces. 14(1)   2   1   1   1   1   1   1   1   1   1	olSpurVernierLUT_Cnt_s16[1][0]	
12_CoSsystement_Cost_Stiglist	olSpurVernierLUT_Cnt_s16[1][1]	4
1705.ps/v/emied.UOrt_51(1)   1805.ps/v/emied.UOrt_51(1)   1905.ps/v/emied.UOrt_51(1)   1905.ps/v/emied.UOrt_51(1)   1905.ps/v/emied.UOrt_51(1)   1905.ps/v/emied.UOrt_51(1)	olSpurVernierLUT_Cnt_s16[1][2]	3
12. Colsp. viewnet U. Dot. 510   15	olSpurVernierLUT_Cnt_s16[1][3]	2
T. COSSO/WentLUT. Ont. 910 19	olSpurVernierLUT_Cnt_s16[1][4]	1
T. COSSO/WentLUT. Ont. 910 19		0
T. CoSparvement U. Ort., strip  5		
12_colspar/venetut_D_cnt_stip  s		
T. C. OSSAV Primer L. D. C. S. 1981   1		
12. Colspay/ment LT, Onu. 14(1) 119    4     12. Colspay/ment LT, Onu. 14(1) 119    5     12. Colspay/ment LT, Onu. 14(1) 119    5     12. Colspay/ment LT, Onu. 14(1) 119    7     13. Colspay/ment LT, Onu. 14(1) 119    7     14. Colspay/ment LT, Onu. 14(1) 119    7     15. Colspay/ment LT, Onu. 14(1) 119    7     16. Colspay/ment LT, Onu. 14(1) 119    7     17. Colspay/ment LT, Onu. 14(1) 119    7     18. Colspay/ment LT		
T. Colsput/ment U.F. Col., 19(1)   12		
12_CoSport/emetUT_Cnt_st@[118]   2   2   2   2   2   2   2   2   2		
12_CoSput/ment_LT_Cnt_sts[1] 14    1   12_CoSput/ment_LT_Cnt_sts[1] 14    1   12_CoSput/ment_LT_Cnt_sts[1] 14    1   12_CoSput/ment_LT_Cnt_sts[1] 16    1   12_CoSput/ment_LT_Cnt_sts[2] 16    1   12_CoSput/ment_LT_Cnt_sts[2] 16    1   12_CoSput/ment_LT_Cnt_sts[2] 17    1   12_CoSput/ment_L		
T2_Colsput/venerUT_Cot_1 \$10[114]	olSpurVernierLUT_Cnt_s16[1][12]	
T. CoSSpur/emetU. Cet. s 19(1)15	olSpurVernierLUT_Cnt_s16[1][13]	2
T2_CoSput/venicUT_Cot_16(2)	olSpurVernierLUT_Cnt_s16[1][14]	1
17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   7   17_COSput	olSpurVernierLUT_Cnt_s16[1][15]	0
17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   7   17_COSput	olSpurVernierLUT_Cnt_s16[1][16]	4
TZ_COSSput/venietUT_Cnt_1602[1] 6 TZ_COSSput/venietUT_Cnt_1602[2] 6 TZ_COSSput/venietUT_Cnt_1602[2] 2 TZ_COSSput/venietUT_Cnt_1602[3] 2 TZ_COSSput/venietUT_Cnt_1602[3] 9 TZ_COSSput/venietUT_Cnt_1602[3] 9 TZ_COSSput/venietUT_Cnt_1602[3] 9 TZ_COSSput/venietUT_Cnt_1602[3] 7 TZ_COSSput/venietUT_Cnt_1602[3] 8 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 8 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1603[3] 11 TZ_COSSput/venietUT_Cnt_1603[3]		0
T2_Colspur/emetUT_Cnt_s16Q13  6		
T2_Colspuvement_UT_Cot_s102[15]   4		
12, CoSpurVernierLUT, Cot., \$16(2)[5]   2		
T2_CoSpuv/emerLUT_Cnt_st82[J8] 9 T2_CoSpuv/emerLUT_Cnt_st82[J8] 9 T2_CoSpuv/emerLUT_Cnt_st82[J8] 5 T2_CoSpuv/emerLUT_Cnt_st82[J8] 5 T2_CoSpuv/emerLUT_Cnt_st82[J8] 1 T2_DosSpuv/emerLUT_Cnt_st82[J8] 1 T2_DosSpuv/emerLUT		
12. CoSpurVement.UT_Cnt_s16[2][9] 9 17. CoSpurVement.UT_Cnt_s16[2][7] 7 17. CoSpurVement.UT_Cnt_s16[2][9] 5 17. CoSpurVement.UT_Cnt_s16[2][9] 3 17. CoSpurVement.UT_Cnt_s16[2][10] 1 17. CoSpurVement.UT_Cnt_s16[2][10] 1 17. CoSpurVement.UT_Cnt_s16[2][11] 10 17. CoSpurVement.UT_Cnt_s16[2][12] 8 17. CoSpurVement.UT_Cnt_s16[2][12] 8 17. CoSpurVement.UT_Cnt_s16[2][13] 6 17. CoSpurVement.UT_Cnt_s16[2][14] 4 17. CoSpurVement.UT_Cnt_s16[2][15] 2 17. CoSpurVement.UT_Cnt_s16[2][16] 10 17. CoSpurVement.UT_Cnt_s16[2][16] 10 17. CoSpurVement.UT_Cnt_s16[2][16] 11 17. CoSpurVement.UT_Cnt_s16[2][16] 15 17. CoSpurVement.UT_Cnt_s16[2][16] 15 17. CoSpurVement.UT_Cnt_s16[2][16] 15 17. CoSpurVement.UT_Cnt_s16[2][17] 12 17. CoSpurVement.UT_Cnt_s16[2][17] 13 17. CoSpurVement.UT_Cnt_s16[2][17] 14 18. CoSpu		
17. CoSpurVemerLUT_Cnt_s16[2][7] 7. CoSpurVemerLUT_Cnt_s16[2][8] 7. CoSpurVemerLUT_Cnt_s16[2][8] 7. CoSpurVemerLUT_Cnt_s16[2][9] 7. CoSpurVemerLUT_Cnt_s16[2][1] 7. CoSpurVemerLUT_Cnt_s16[2][		
17. ColSput/emierLUT_Cnt_s16[2] 8    5     7. ColSput/emierLUT_Cnt_s16[2] 8    3     7. ColSput/emierLUT_Cnt_s16[2] 10    1     7. ColSput/emierLUT_Cnt_s16[2] 11    10     7. ColSput/emierLUT_Cnt_s16[2] 12    8     8     8     7. ColSput/emierLUT_Cnt_s16[2] 12    8     8     7. ColSput/emierLUT_Cnt_s16[2] 13    6     7. ColSput/emierLUT_Cnt_s16[2] 14    4     8     9     9     9     9     9     10     10     10     11     12     12     13     14     15     15     16     17     18     18     18     18     19     19     10     10     10     10     10     10     10     10     10     10     10     10     10     10     11     12     12     13     14     15     15     16     16     17     18     18     18     19     19     10     10     10     10     10     10     11     12     12     13     14     15     15     16     17     18     18     18     19     19     10     10     10     11     12     12     13     14     15     15     16     17     18     18     18     19     19     10     10     10     10     11     12     13     14     15     15     16     17     18     18     18     18     19     19     10     10     10     10     10     10     10     10     10     10     10     10     10     10     11     12     13     14     15     15     16     16     17     18     18     18     19     19     10     1		
T2_ColSpurVemierLUT_Cnt_s162[10]   1   1   1   1   1   1   1   1   1	olSpurVernierLUT_Cnt_s16[2][7]	
12_ColSpurVerniet.UT_Cnt_s16[2][10]   1   17_ColSpurVerniet.UT_Cnt_s16[2][11]   10   17_ColSpurVerniet.UT_Cnt_s16[2][12]   8   8   17_ColSpurVerniet.UT_Cnt_s16[2][13]   6   6   17_ColSpurVerniet.UT_Cnt_s16[2][14]   4   17_ColSpurVerniet.UT_Cnt_s16[2][14]   4   17_ColSpurVerniet.UT_Cnt_s16[2][15]   2   17_ColSpurVerniet.UT_Cnt_s16[2][16]   10   17_ColSpurVerniet.UT_Cnt_s16[2][16]   10   17_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   17   18_ColSpurVerniet.UT_Cnt_s16[3][1]   19   18_ColSpurVerniet	olSpurVernierLUT_Cnt_s16[2][8]	5
12, ColSpurVement.UT_Cnt_s16[2][10]   1   10   10   10   10   10   10	olSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVemierLUT_Cnt_st02[]11]   10	olSpurVernierLUT Cnt s16[2][10]	1
12 ColSpurVemiet.UT_Cnt_s16(2)[12]   8   72 ColSpurVemiet.UT_Cnt_s16(2)[15]   6   6   72 ColSpurVemiet.UT_Cnt_s16(2)[15]   4   4   72 ColSpurVemiet.UT_Cnt_s16(2)[15]   2   72 ColSpurVemiet.UT_Cnt_s16(2)[16]   10   72 ColSpurVemiet.UT_Cnt_s16(2)[16]   10   72 ColSpurVemiet.UT_Cnt_s16(2)[16]   11   72 ColSpurVemiet.UT_Cnt_s16(2)[17]   14   72 ColSpurVemiet.UT_Cnt_s16(2)[17]   14   72 ColSpurVemiet.UT_Cnt_s16(2)[17]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   8   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   8   73 ColSpurVemiet.UT_Cnt_s16(2)[18]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   16   73 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   18 ColSpurVemiet.UT_Cnt_s16(2)[18]   19 ColSpurVemiet.UT_Cnt_s16(2)[18]   19 ColSpurVemiet.UT_Cnt_s16(2)[18]   18 ColSpurVemiet.UT_Cnt_s16(2)[18]   19 ColSpurVemiet.UT_Cnt_s16(2)[18]   1		
12		
T2_ColSpurVemietUT_Cnt_st6[2][14]		
T2_ColSpurVernierLUT_Cnt_st6[2][16]   10		
T2_ColSpurVernierLUT_Cnt_st6[3][16]		
T2_ColSpurVernierLUT_Cnt_st6[3][0]   1   14   12_ColSpurVernierLUT_Cnt_st6[3][1]   14   14   12_ColSpurVernierLUT_Cnt_st6[3][3]   11   12_ColSpurVernierLUT_Cnt_st6[3][3]   8   12_ColSpurVernierLUT_Cnt_st6[3][4]   5   12_ColSpurVernierLUT_Cnt_st6[3][6]   2   12_ColSpurVernierLUT_Cnt_st6[3][6]   2   12_ColSpurVernierLUT_Cnt_st6[3][7]   12_ColSpurVernierLUT_Cnt_st6[3][7]   12_ColSpurVernierLUT_Cnt_st6[3][7]   12_ColSpurVernierLUT_Cnt_st6[3][8]   9   12_ColSpurVernierLUT_Cnt_st6[3][9]   6   12_ColSpurVernierLUT_Cnt_st6[3][9]   6   12_ColSpurVernierLUT_Cnt_st6[3][10]   3   12_ColSpurVernierLUT_Cnt_st6[3][10]   16_ColSpurVernierLUT_Cnt_st6[3][10]   16_ColSpurVernierLUT_Cnt_st6[3][10]   16_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   18_ColSpurVernierLUT_Cnt_st6[3][10]   18_ColSpurVernierLUT_Cnt_		
T2		
T2_ColSpurVernierLUT_Cnt_s16[3][2]   11   12   20   20   20   20   20   20		
T2_ColSpurVernierLUT_Cnt_s16[3][4]   5   5   7   2   ColSpurVernierLUT_Cnt_s16[3][4]   5   7   2   ColSpurVernierLUT_Cnt_s16[3][6]   15   7   2   ColSpurVernierLUT_Cnt_s16[3][6]   15   7   7   7   7   7   7   7   7   7	olSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_st6[3][4] 5 T2_ColSpurVernierLUT_Cnt_st6[3][5] 2 T2_ColSpurVernierLUT_Cnt_st6[3][7] 15 T2_ColSpurVernierLUT_Cnt_st6[3][7] 12 T2_ColSpurVernierLUT_Cnt_st6[3][7] 12 T2_ColSpurVernierLUT_Cnt_st6[3][8] 9 T2_ColSpurVernierLUT_Cnt_st6[3][9] 6 T2_ColSpurVernierLUT_Cnt_st6[3][10] 3 T2_ColSpurVernierLUT_Cnt_st6[3][11] 16 T2_ColSpurVernierLUT_Cnt_st6[3][11] 16 T2_ColSpurVernierLUT_Cnt_st6[3][12] 13 T2_ColSpurVernierLUT_Cnt_st6[3][13] 10 T2_ColSpurVernierLUT_Cnt_st6[3][14] 7 T2_ColSpurVernierLUT_Cnt_st6[3][16] 17 T2_ColSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 19 T2_DualSpurVernierLUT_Cnt_st6[3][1	olSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][5]   15   15   15   15   15   15   15	olSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][6]	olSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16(3)[7] T2_ColSpurVernierLUT_Cnt_s16(3)[8] 9 T2_ColSpurVernierLUT_Cnt_s16(3)[9] 6 T2_ColSpurVernierLUT_Cnt_s16(3)[10] 3 T2_ColSpurVernierLUT_Cnt_s16(3)[11] 12_ColSpurVernierLUT_Cnt_s16(3)[11] 13_ColSpurVernierLUT_Cnt_s16(3)[12] 13_ColSpurVernierLUT_Cnt_s16(3)[12] 13_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[15] 4 T2_ColSpurVernierLUT_Cnt_s16(3)[16] 17_ColSpurVernierLUT_Cnt_s16(3)[16] 17_ColSpurVernierLUT_Cnt_s16(3)[16] 17_DualSpurVernierLUT_Cnt_s16(0)[0] 12_DualSpurVernierLUT_Cnt_s16(0)[1] 12_DualSpurVernierLUT_Cnt_s16(0)[2] 12_DualSpurVernierLUT_Cnt_s16(0)[3] 12_DualSpurVernierLUT_Cnt_s16(0)[6] 12_DualSpurVernierLUT_Cnt_s16(0)[10] 12_DualSpurVernierLUT_Cnt_s16(0)[10	olSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16(3)[7] T2_ColSpurVernierLUT_Cnt_s16(3)[8] 9 T2_ColSpurVernierLUT_Cnt_s16(3)[9] 6 T2_ColSpurVernierLUT_Cnt_s16(3)[10] 3 T2_ColSpurVernierLUT_Cnt_s16(3)[11] 12_ColSpurVernierLUT_Cnt_s16(3)[11] 13_ColSpurVernierLUT_Cnt_s16(3)[12] 13_ColSpurVernierLUT_Cnt_s16(3)[12] 13_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[15] 4 T2_ColSpurVernierLUT_Cnt_s16(3)[16] 17_ColSpurVernierLUT_Cnt_s16(3)[16] 17_ColSpurVernierLUT_Cnt_s16(3)[16] 17_DualSpurVernierLUT_Cnt_s16(0)[0] 12_DualSpurVernierLUT_Cnt_s16(0)[1] 12_DualSpurVernierLUT_Cnt_s16(0)[2] 12_DualSpurVernierLUT_Cnt_s16(0)[3] 12_DualSpurVernierLUT_Cnt_s16(0)[6] 12_DualSpurVernierLUT_Cnt_s16(0)[10] 12_DualSpurVernierLUT_Cnt_s16(0)[10		15
T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] 396 T2_DualSpurVernierLUT_Cnt_s16[0][1] 360 T2_DualSpurVernierLUT_Cnt_s16[0][2] 324 T2_DualSpurVernierLUT_Cnt_s16[0][3] 288 T2_DualSpurVernierLUT_Cnt_s16[0][4] 252 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -72 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][10] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10		
T2_ColSpurVernierLUT_Cnt_s16[3][9]  12_ColSpurVernierLUT_Cnt_s16[3][10]  12_ColSpurVernierLUT_Cnt_s16[3][11]  12_ColSpurVernierLUT_Cnt_s16[3][12]  13_T2_ColSpurVernierLUT_Cnt_s16[3][13]  10_T2_ColSpurVernierLUT_Cnt_s16[3][14]  7_ColSpurVernierLUT_Cnt_s16[3][14]  7_ColSpurVernierLUT_Cnt_s16[3][15]  1_ColSpurVernierLUT_Cnt_s16[3][16]  1_ColSpurVernierLUT_Cnt_s16[3][16]  1_ColSpurVernierLUT_Cnt_s16[3][16]  1_ColSpurVernierLUT_Cnt_s16[3][16]  1_ColSpurVernierLUT_Cnt_s16[0][0]  396  1_ColSpurVernierLUT_Cnt_s16[0][1]  380  1_ColSpurVernierLUT_Cnt_s16[0][1]  380  1_ColSpurVernierLUT_Cnt_s16[0][2]  324  1_ColSpurVernierLUT_Cnt_s16[0][3]  288  1_ColSpurVernierLUT_Cnt_s16[0][4]  252  1_ColSpurVernierLUT_Cnt_s16[0][6]  1_ColSpurVernierLUT_Cnt_s16[0][6]		
T2_ColSpurVernierLUT_Cnt_s16[3][10] T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DoulSpurVernierLUT_Cnt_s16[0][0] T2_DoulSpurVernierLUT_Cnt_s16[0][1] T2_DoulSpurVernierLUT_Cnt_s16[0][1] T2_DoulSpurVernierLUT_Cnt_s16[0][2] T2_DoulSpurVernierLUT_Cnt_s16[0][3] T2_DoulSpurVernierLUT_Cnt_s16[0][5] T2_DoulSpurVernierLUT_Cnt_s16[0][6] T2_DoulSpurVernierLUT_Cnt_s16[0][6] T2_DoulSpurVernierLUT_Cnt_s16[0][6] T2_DoulSpurVernierLUT_Cnt_s16[0][6] T2_DoulSpurVernierLUT_Cnt_s16[0][7] T2_DoulSpurVernierLUT_Cnt_s16[0][7] T2_DoulSpurVernierLUT_Cnt_s16[0][8] T2_DoulSpurVernierLUT_Cnt_s16[0][9] T2_DoulSpurVernierLUT_Cnt_s16[0][1] T2_DoulSpurVernierLUT_Cnt_s16[0][1] T2_DoulSpurVernierLUT_Cnt_s16[0][1] T2_DoulSpurVernierLUT_Cnt_s16[0][1] T2_DoulSpurVernierLUT_Cnt_s16[0][13] T2_DoulSpurVernierLUT_Cnt_s16[0][13] T2_DoulSpurVernierLUT_Cnt_s16[0][14] T2_DoulSpurVernierLUT_Cnt_s16[0][15] T3_DoulSpurVernierLUT_Cnt_s16[0][15] T		
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T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	ualSpurVernierLUT_Cnt_s16[0][9]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216	ualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	ualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	ualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2 DualSpur/ornigrI LT Cnt c16(0)(19)		
	ualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288		
T2_DualSpurVernierLUT_Cnt_s16[0][20] 324	ualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360	ualSpurVernierLUT_Cnt_s16[0][21]	360

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2 DualSpurVernierLUT Cnt s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2 DualSpurVernierLUT Cnt s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10] T3_DualSpurVernierLUT_Cnt_s16[3][11]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	182
k_SkipStepErrDiag_Cnt_str.PStep	8
k SkinStonErrDing Ont atr NSton	
k_SkipStepErrDiag_Cnt_str.NStep	30
k_VernCorrErrorDiag_Cnt_str.Threshold	90





Name	Input Value		
k_VernCorrErrorThresh_Deg_f32	44.56530905		
k_VernOORangeThresh_Deg_f32	835.2161256		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	131.2116221		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	96.8100152		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	g_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	152.268173	152.2681749 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	34	34	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12	12	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>~</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.68 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	123
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	186
DigColPs_ColTrimStatic_Deg_M_f32	116.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	54715
DigColPs_I2CHwColAngle_Deg_M_f32	133.5364133
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42308
DigColPs_I2CHwSpurAngle_Deg_M_f32	72.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	27
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1016.035717
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	72.9
DigColPs_TrimCompStatic_Cnt_M_u16	2320
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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2 1 0 4 3 2 1 0 4 3
1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 2 1 0 4 3 2
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3 2 1 0 4 3
2 1 0 4 3 2
1 0 4 3 2
0 4 3 2
4 3 2
3 2
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-396
<b>-360</b>
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36





Input Value
0
36
72
108
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216
252
288
324
360 9
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10
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0x01

Name	Input Value		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2 DualSpurVernierLUT Cnt s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt Igc	0		
k SkipStepErrDiag Cnt str.Threshold	255		
k SkipStepErrDiag Cnt str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	34		
k_VernCorrErrorDiag_Cnt_str.Threshold	96		
k_VernCorrErrorDiag_Cnt_str.PStep	41		
k_VernCorrErrorDiag_Cnt_str.NStep	33		
k_VernCorrErrorThresh_Deg_f32	43.33685136		
k_VernOORangeThresh_Deg_f32	1120.447047		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	133.5364133		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	207.7008287		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1458		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1457.23645	1457.236413 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x01

Test Step 2.69 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	149
DigColPs_ColTrimStatic_Deg_M_f32	120.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25072
DigColPs_I2CHwColAngle_Deg_M_f32	117.9909339
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	33822
DigColPs_I2CHwSpurAngle_Deg_M_f32	74
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	424.6977491

Status

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Name	Input Value
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	74
DigColPs_TrimCompStatic_Cnt_M_u16	2356
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][2] T2 ColSpurVernierLUT Cnt s16[0][3]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T3_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][11] T3_ColSpurVernierLUT_Cnt_s16[2][12]	10 8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
	2
12 ColSpurvernierLUT Cnt s16(3)(5)	
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][8]	12 9
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][8] T2_ColSpurVernierLUT_Cnt_s16[3][9]	12 9 6
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][8]	12 9





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2 DualSpurVernierLUT Cnt s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	9
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	
T2 DualSpurVernierLUT Cnt s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	9
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20] T2_DualSpurVernierLUT_Cnt_s16[2][21]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][21] T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4

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DigColPs_Per2		Ida	Citab
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	105		
k_SkipStepErrDiag_Cnt_str.PStep	34		
k_SkipStepErrDiag_Cnt_str.NStep	24		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	86.64014435		
k_VernOORangeThresh_Deg_f32	232.6736557		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	117.9909339		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	208.2439033		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	35		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1797.59094	1797.590934 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6	6	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tot DiaColDo Dar2 (2CHwAhoDoo\/olid Cot Igo volue	0	0	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>✓</b>

897.590942

897.5909339 ± 0.0009

Test Step 2.70 (Repeat Count = 1)	·
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	124.5
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value





Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	12814
DigColPs_I2CHwColAngle_Deg_M_f32	77.52818984
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	14635 75.1
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	923.4796569
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	127 75.1
DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16	2392
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66 22
T2_ColSpurVernierLUT_Cnt_s16[0][4] T2_ColSpurVernierLUT_Cnt_s16[0][5]	-33 0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5 3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
	10
T2_ColSpurVernierLUT_Cnt_s16[2][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	1 14

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Name  T2_ColSpurVernierLUT_Cnt_s16[3][2]  T2_ColSpurVernierLUT_Cnt_s16[3][3]  T2_ColSpurVernierLUT_Cnt_s16[3][4]	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	11
T2_ColSpurVernierLUT_Cnt_s16[3][4]	
	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2 ColSpurVernierLUT Cnt s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
	3
T2_ColSpurVernierLUT_Cnt_s16[3][10]	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
	180
T2_DualSpurVernierLUT_Cnt_s16[0][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2 DualSpurVernierLUT Cnt s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[1][20]	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[2][9]	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	148		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	3		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	6.626505613		
k_VernOORangeThresh_Deg_f32	759.6732113		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	77.52818984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	357.6556342		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3516		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn	ıt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	313.028198	313.0281898 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•

0

0x6C

0x04

0x01

-586.971802

0

0

0x6C

0x04

0x01

-586.9718102 ± 0.0009

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DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

NTC

Param

Status

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.71 (Repeat Count = 1)	and the second of the second o
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	126
DigColPs ColTrimStatic Deg M f32	128.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21375
DigColPs_I2CHwColAngle_Deg_M_f32	76.6514684
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	38191
DigColPs_I2CHwSpurAngle_Deg_M_f32	76.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1339.267418
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	76.2
DigColPs_TrimCompStatic_Cnt_M_u16	2428
DigColPs VernCorrDetectAcc Cnt M u16	5
DigColPs VernierAngleOORange Cnt M lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
	2
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	0 4 3

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s46[2][2]	4
T2_ColSpurVernierLUT_Cnt_s16[2][3]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2 DualSpurVernierLUT Cnt s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2 DualSpurVernierLUT Cnt s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][15]	180
T2_DualSpurVernierLUT_Cnt_s16[0][16] T3_DualSpurVernierLUT_Cnt_s16[0][17]	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9

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Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
T2 DualSpurVernierLUT Cnt s16[2][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	14	
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20	
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	
T2 DualSpurVernierLUT Cnt s16[3][12]	3	
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7	
T2 DualSpurVernierLUT Cnt s16[3][15]	9	
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11	
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13	
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15	
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17	
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19	
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21	
k_SelectFromColumn_Cnt_lgc	1	
k_SkipStepErrDiag_Cnt_str.Threshold	118	
k_SkipStepErrDiag_Cnt_str.PStep	15	
k_SkipStepErrDiag_Cnt_str.NStep	42	
k_VernCorrErrorDiag_Cnt_str.Threshold	46	
k_VernCorrErrorDiag_Cnt_str.PStep	50	
k_VernCorrErrorDiag_Cnt_str.NStep	4	
k_VernCorrErrorThresh_Deg_f32	90.72870111	
k_VernOORangeThresh_Deg_f32	378.3238977	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	76.6514684	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	336.2350776	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1 tot DigColDo Por? 12CHwAboPoo\/olid Cot Igo	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_MecState_Crit_enum  tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPsEOL	tgt_Pim_DigColPsEOL	
Name	Actual Value Expected	Value Resu
Traille	Actual value Expected	Rest
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>~</b>
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.72 (Repeat Count = 1)	Inner Meller
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	127
DigColPs_ColTrimStatic_Deg_M_f32	132.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	27081
DigColPs_I2CHwColAngle_Deg_M_f32	152.7639936
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49055
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	96.19118387
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	77.3
DigColPs_TrimCompStatic_Cnt_M_u16	2464
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
Γ2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2 ColSpurVernierLUT Cnt s16[0][15]	327





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
	-30
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14] T0_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2 DualSpurVernierLUT Cnt s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2 DualSpurVernierLUT Cnt s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18] T3_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21 0
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	10
k_SkipStepErrDiag_Cnt_str.Fistep	27
k_SkipStepErrDiag_Cnt_str.NStep	36
k_VernCorrErrorDiag_Cnt_str.Threshold	3
k_VernCorrErrorDiag_Cnt_str.PStep	33
n_vaniounenoiDidy_ont_su.rotep	JJ





Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	24.98827672		
k_VernOORangeThresh_Deg_f32	1644.361279		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	152.7639936		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.24033874		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1344		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

tgt_tte_inst_3a_bigcoir s.r im_bigcoir sect	tgt_Filli_DigColF3LOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1460.06396	1460.063994 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 2.73 (Repeat Count = 1)	<b>√</b>
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	136.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	10005
DigColPs_I2CHwColAngle_Deg_M_f32	222.9168355
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29915
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.4
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	998.4962399
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	78.4
DigColPs_TrimCompStatic_Cnt_M_u16	2500
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	327 359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2 0
T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2 ColSpurVernierLUT Cnt s16[2][7]	7
T2_ColSpurVernierEUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2 ColSpurVernierLUT Cnt s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T3_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2 DualSpurVernierLUT Cnt s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T3_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Crit_S16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3

DigColPs\_Per2



T2_DualSpurVernierLUT_Cnt_s16[3][13] 5	nput Value		
_ ' ' .			
T2_DualSpurVernierLUT_Cnt_s16[3][14] 7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	1		
k_SelectFromColumn_Cnt_lgc 0			
k_SkipStepErrDiag_Cnt_str.Threshold 25	55		
k_SkipStepErrDiag_Cnt_str.PStep 32	2		
k_SkipStepErrDiag_Cnt_str.NStep 21	1		
k_VernCorrErrorDiag_Cnt_str.Threshold 95	5		
k_VernCorrErrorDiag_Cnt_str.PStep 18	8		
k_VernCorrErrorDiag_Cnt_str.NStep 16	6		
k_VernCorrErrorThresh_Deg_f32 6.2	.261063576		
k_VernOORangeThresh_Deg_f32 16	626.468312		
tgt_DigColPs_Per2_MecState_Cnt_enum.value			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 22	22.9168355		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 29	92.4312814		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 10	014		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc tgt_	gt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_	gt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f	32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_	gt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_	gt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_	gt_Pim_DigColPsEOL		
Name Ac	ctual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc 0		0	~

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	806.116821	806.1168355 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.74 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	126	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	241	
DigColPs_ColTrimStatic_Deg_M_f32	140.9	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	40621	
DigColPs_I2CHwColAngle_Deg_M_f32	87.17455715	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	59269	
DigColPs_I2CHwSpurAngle_Deg_M_f32	79.5	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	

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Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	806.5395069
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	79.5
DigColPs_TrimCompStatic_Cnt_M_u16	2536
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66 -33
T2_ColSpurVernierLUT_Cnt_s16[0][4] T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2 ColSpurVernierLUT Cnt s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15] T3_ColSpurVernierLUT_Cnt_s16[2][16]	2 10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T3_ColSpurVernierLUT_Cnt_s16[3][0]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
	9 6
T2_ColSpurVernierLUT_Cnt_s16[3][8]	

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
	5
T2_DualSpurVernierLUT_Cnt_s16[2][16]	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	9 10
	9

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DigColPs_Per2		IMA	Citab
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2 DualSpurVernierLUT Cnt s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	193		
k SkipStepErrDiag Cnt str.PStep	35		
k_SkipStepErrDiag_Cnt_str.NStep	18		
k VernCorrErrorDiag Cnt str.Threshold	3		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	79.03816199		
k_VernOORangeThresh_Deg_f32	1714.927183		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	87.17455715		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	277.9257751		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4278		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	-
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	•
DigColPs I2CHwTrimTransCnts UIs M u08	3	3	
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	666.274536	666.2745571 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs SkipStepFltDetectAcc Cnt M u16	4	4	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
0 0 = = = 0			

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	<b>~</b>
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	<b>~</b>
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>V</b>

-233.725464

0

-233.7254429 ± 0.0009

0

Test Step 2.75 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	196
DigColPs_ColTrimStatic_Deg_M_f32	145

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	6514
DigColPs_I2CHwColAngle_Deg_M_f32	247.6901497
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18547
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	14
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs PrevAngleDataAvailable Cnt M Igc	1
DigColPs_PrevColPos_Deg_M_f32	928.9719008
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_tgc	0
	127
DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32	80.6
	1
DigCoIPs_TrimCompStatic_Cnt_M_u16	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	
T2_ColSpurVernierLUT_Cnt_s16[1][11] T3_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12] T3_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14] T0_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1.
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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T. C. OSSAVVANICUT, CM   1907  1	Nama	Input Value
12_CoSquirement_Cot_statp[0]   11	Name	Input Value
P. Colispa/remontall_Colstiligid   5   12   Colispa/remontall_Colstiligid   5   12   Colispa/remontall_Colstiligid   5   15   Colispa/remontall_Colstiligid   5   15   Colispa/remontall_Colstiligid   15   Colispa/remontall_Colstiligid   16   Colispa/remontall_Colstiligid   16   Colispa/remontall_Colstiligid   17   Colispa/remontall_Colstiligid   18		
T. Collego/vernient U. Col. 4 (1978)		
12_00spx/remail_Coll_Statistics    2		
T2_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 13_COSQAVeniment_U_Cnt_visit(SIS) 14_COSQAVeniment_U_Cnt_visit(SIS) 15_COSQAVeniment_U_Cnt_visit(SIS) 16_COSQAVeniment_U_Cnt_visit(SIS) 17_COSQAVeniment_U_Cnt_visit(SIS) 18_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit		
12, Osspar/weinstall, Cot, 14(3)(1)		
Tz. COSSpur/venseLT. Cot. s163789 6 Tz. COSSpur/venseLT. Cot. s163719 6 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 19 Tz. COSSpur/v		
To Conspired ment LT, Det. 310(19)  To Conspired ment LT, Det. 510(19)  To Conspired ment LT, Det. 510		
To Colsput/went LT Ont \$180]119 10		
T. Colsput/ment.U. Col.; st (20)112   13   13   13   13   13   13   13	T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[15]	T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T. C. OSBOUYMONE LT. COL. \$16(3)*14   7   7   7   7   7   7   7   7   7	T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_Colspar/membut/D_cnt_st093115  4	T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
17, CoSpur/wentUT, Cott, \$103115	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_CosSpurVement UT_Cnt_s160[110] 388  12_DusSpurVement UT_Cnt_s160[11] 380  12_DusSpurVement UT_Cnt_s160[12] 384  12_DusSpurVement UT_Cnt_s160[13] 384  12_DusSpurVement UT_Cnt_s160[13] 384  12_DusSpurVement UT_Cnt_s160[14] 382  12_DusSpurVement UT_Cnt_s160[16] 388  12_DusSpurVement UT_Cnt_s160[16] 380  12_DusSpurVement UT_Cnt_s160[16] 380  12_DusSpurVement UT_Cnt_s160[17] 380  12_DusSpurVement UT_Cnt_s160[18] 380  12_DusSpurVement UT_Cnt_s160[18	T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
12. DualSparVermicht UT. Cit.; 1490191   396 172. DualSparVermicht UT. Cit.; 1490191   356 173. DualSparVermicht UT. Cit.; 1490191   324 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   316 174. DualSparVermicht UT. Cit.; 1490191   316 175. DualSparVermicht UT. Cit.; 1490191   316 177. DualSparVermicht UT. Cit.; 1490191   317 177. DualSparVermicht UT. Cit.; 1490191   317 177. DualSparVermicht UT. Cit.; 1490191   318 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   322 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   327 177. DualSparVermicht UT. Cit.; 3490191   327 177. DualSparVermicht UT. Cit.; 3490191   329 177. DualSparVermicht U	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
12_DasSgru/FemerU_T_Crt_s160[12]	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_DasSgru/FemerU_T_Crt_s160[12]	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2, DuaSpurVermiet.UT_Cnt_s160[15]   288		-360
T. DualSparVermicLUT_Cnt_s180[H]   252	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_Dus SpurVernieLUT_Cnt_s16[0][5]   216     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -72     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -36     T2_Dus SpurVern		-288
T2_Dus SpurVernieLUT_Cnt_s16[0][5]   216     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -72     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -36     T2_Dus SpurVern		
12, DualSparVermetLUT_Cnt_s16(0)(8)   .189		
12. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -74 17. DuaSgurVemet.UT_Cnt_s160[12] -74 17. DuaSgurVemet.UT_Cnt_s160[13] -74 17. DuaSg		
12_DusSpurVermetUT_Cnt_s160[18]   -102   T2_DusSpurVermetUT_Cnt_s160[10]   -72   T2_DusSpurVermetUT_Cnt_s160[10]   -36   T2_DusSpurVermetUT_Cnt_s160[11]   0   T2_DusSpurVermetUT_Cnt_s160[11]   0   T2_DusSpurVermetUT_Cnt_s160[11]   36   T2_DusSpurVermetUT_Cnt_s160[11]   36   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   109   T2_DusSpurVermetUT_Cnt_s160[12]   109   T2_DusSpurVermetUT_Cnt_s160[13]   109   T2_DusSpu		
12. DualSpur/vernietUT_Cnt_s16(0) 10   36   36   36   36   36   36   36   3		
12. DualSpur/vernierLUT_Cnt_s16()[11]   0   0   0   0   0   0   0   0   0		
12   DusiSpur/VernietUT_Cnt, 1610[11]   12   136   1		
12 DuaiSpurVernierLUT_Cnt_s16[0][12]   36   72   72   73   73   74   74   74   74   74   74		
T2   DualSpurVermierLUT_Cnt_s16[0][14]   108		
172   DualSpurVernierLUT_Cnt_stic[0]14    108     172   DualSpurVernierLUT_Cnt_stic[0]15    144     172   DualSpurVernierLUT_Cnt_stic[0]16    180     173   DualSpurVernierLUT_Cnt_stic[0]17    216     173   DualSpurVernierLUT_Cnt_stic[0]17    216     174   DualSpurVernierLUT_Cnt_stic[0]19    288     172   DualSpurVernierLUT_Cnt_stic[0]19    288     173   DualSpurVernierLUT_Cnt_stic[0]20    324     174   DualSpurVernierLUT_Cnt_stic[0]21    360     175   DualSpurVernierLUT_Cnt_stic[0]21    360     175   DualSpurVernierLUT_Cnt_stic[1]2    1     175   DualSpurVernierLUT_Cnt_stic[1]2    1     175   DualSpurVernierLUT_Cnt_stic[1]2    1     175   DualSpurVernierLUT_Cnt_stic[1]3    2     175   DualSpurVernierLUT_Cnt_stic[1]4    3     175   DualSpurVernierLUT_Cnt_stic[1]6    5     175   DualSpurVernierLUT_Cnt_stic[1]6    5     175   DualSpurVernierLUT_Cnt_stic[1]6    6     175   DualSpurVernierLUT_Cnt_stic[1]6    7     175   DualSpurVernierLUT_Cnt_stic[1]6    7     175   DualSpurVernierLUT_Cnt_stic[1]10    9     175   DualSpurVernierLUT_Cnt_stic[1]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLU		
T2   DualSpurVermict.UT   Cnt   s16(0) 15    144     T2   DualSpurVermict.UT   Cnt   s16(0) 16    180     T2   DualSpurVermict.UT   Cnt   s16(0) 17    216     T2   DualSpurVermict.UT   Cnt   s16(0) 17    216     T2   DualSpurVermict.UT   Cnt   s16(0) 18    252     T3   DualSpurVermict.UT   Cnt   s16(0) 20    324     T2   DualSpurVermict.UT   Cnt   s16(0) 20    324     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    1     T3   DualSpurVermict.UT   Cnt   s16(1) 10    1     T4   DualSpurVermict.UT   Cnt   s16(1) 10    1     T2   DualSpurVermict.UT   Cnt   s16(1) 10    3     T2   DualSpurVermict.UT   Cnt   s16(1) 10    3     T3   DualSpurVermict.UT   Cnt   s16(1) 10    5     T4   DualSpurVermict.UT   Cnt   s16(1) 10    5     T5   DualSpurVermict.UT   Cnt   s16(1) 10    6     T5   DualSpurVermict.UT   Cnt   s16(1) 10    7     T2   DualSpurVermict.UT   Cnt   s16(1) 10    8     T3   DualSpurVermict.UT   Cnt   s16(1) 10    9     T4   DualSpurVermict.UT   Cnt   s16(1) 10    9     T5   DualSpurVermict.UT   Cnt   s16(1) 10    9     T6   DualSpurVermict.UT   Cnt   s16(1) 10    9     T7   DualSpurVermict.UT   Cnt   s16(1) 10    9     T8   DualSpurVermict.UT   Cnt   s16(1) 10		
12		
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 2 T2_DualSpurVernierLUT_Cnt_s16[1][1] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 4 T2_DualSpurVernierLUT_Cnt_s16[1][1] 5 T2_DualSpurVernierLUT_Cnt_s16[1][1] 6 T2_DualSpurVernierLUT_Cnt_s16[1][1] 7 T2_DualSpurVernierLUT_Cnt_s16[1][1] 7 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 10 T2_D		
12   DualSpurVemierLUT_Cnt_st6[0][16]   288   72   DualSpurVemierLUT_Cnt_st6[0][20]   324   32		
T2_DualSpurVemierLUT_Cnt_st6[0][19]  72_DualSpurVemierLUT_Cnt_st6[0][20]  72_DualSpurVemierLUT_Cnt_st6[0][21]  72_DualSpurVemierLUT_Cnt_st6[1][0]  72_DualSpurVemierLUT_Cnt_st6[1][0]  72_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][3]  72_DualSpurVemierLUT_Cnt_st6[1][3]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[1][6]  76_DualSpurVemierLUT_Cnt_st6[1][6]  77_DualSpurVemierLUT_Cnt_st6[1][6]  78_DualSpurVemierLUT_Cnt_st6[1][6]  79_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  71_DualSpurVemierLUT_Cnt_st6[1][6]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[1][6]  76_DualSpurVemierLUT_Cnt_st6[1][6]  77_DualSpurVemierLUT_Cnt_st6[1][6]  78_DualSpurVemierLUT_Cnt_st6[1][6]  79_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  71_DualSpurVemierLUT_Cnt_st6[1][6]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[1][6]  76_DualSpurVemierLUT_Cnt_st6[1][6]  77_DualSpurVemierLUT_Cnt_st6[1][6]  78_DualSpurVemierLUT_Cnt_st6[1][6]  79_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  71_DualSpurVemierLUT_Cnt_st6[1][6]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[2][6]  76_DualSpurVemierLUT_Cnt_st6[2][6]  77_DualSpurVemierLUT_Cnt_st6[2][6]  78_DualSpurVemierLUT_Cnt_st6[2][6]  79_DualSpurVemierLUT_Cnt_st6[2][6]  70_DualSpurVemierLUT_Cnt_st6[2][6]  71_DualSpurVemierLUT_Cnt_st6[2][6]  72_DualSpurVemierLUT_Cnt_st6[2][6]  73_DualSpurVemierLUT_Cnt_st6[2][6]  74_DualSpurVemierLUT_Cnt_st6[2][6]  75_DualSpurVemierLUT_Cnt_st6[2][6]  76_DualSpurVemierLUT_Cnt_st6[2][6]  77_DualSpurVemierLUT_Cnt_st6[2][6]  78_DualSpurVemierLUT_Cnt_st		
T2_DualSpurVerniertUT_Cnt_s16[0] 20    324		
T2_DualSpurVerniertUT_Cnt_s16[0][21] T2_DualSpurVerniertUT_Cnt_s16[1][0] T2_DualSpurVerniertUT_Cnt_s16[1][1] T2_DualSpurVerniertUT_Cnt_s16[1][2] T2_DualSpurVerniertUT_Cnt_s16[1][3] T2_DualSpurVerniertUT_Cnt_s16[1][3] T2_DualSpurVerniertUT_Cnt_s16[1][4] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][7] 6 T2_DualSpurVerniertUT_Cnt_s16[1][8] 7 T2_DualSpurVerniertUT_Cnt_s16[1][8] 7 T2_DualSpurVerniertUT_Cnt_s16[1][9] 8 T2_DualSpurVerniertUT_Cnt_s16[1][1] 9 T2_DualSpurVerniertUT_Cnt_s16[1][1] 10_DualSpurVerniertUT_Cnt_s16[1][1] 11_DualSpurVerniertUT_Cnt_s16[1][1] 12_DualSpurVerniertUT_Cnt_s16[1][1] 13_DualSpurVerniertUT_Cnt_s16[1][1] 14_DualSpurVerniertUT_Cnt_s16[1][1] 15_DualSpurVerniertUT_Cnt_s16[1][1] 16_DualSpurVerniertUT_Cnt_s16[1][1] 17_DualSpurVerniertUT_Cnt_s16[1][1] 18_DualSpurVerniertUT_Cnt_s16[2][1] 19_DualSpurVerniertUT_Cnt_s16[2][1] 10_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 13_DualSpurVerniertUT_Cnt_s16[2][1] 14_DualSpurVerniertUT_Cnt_s16[2][1] 15_DualSpurVerniertUT_Cnt_s16[2][1] 16_DualSpurVerniertUT_Cnt_s16[2][1] 17_DualSpurVerniertUT_Cnt_s16[2][1] 18_DualSpurVerniertUT_Cnt_s16[2][1] 19_DualSpurVerniertUT_Cnt_s16[2][1] 10_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 13_DualSpurVerniert		
T2_DualSpurVernierLUT_Cnt_s16[1][0]   9		
T2_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][3] 12_DualSpurVernierLUT_Cnt_s16[1][4] 13_DualSpurVernierLUT_Cnt_s16[1][6] 14_DualSpurVernierLUT_Cnt_s16[1][6] 15_DualSpurVernierLUT_Cnt_s16[1][6] 16_DualSpurVernierLUT_Cnt_s16[1][6] 17_DualSpurVernierLUT_Cnt_s16[1][8] 17_DualSpurVernierLUT_Cnt_s16[1][8] 17_DualSpurVernierLUT_Cnt_s16[1][8] 18_DualSpurVernierLUT_Cnt_s16[1][8] 19_DualSpurVernierLUT_Cnt_s16[1][8] 10_DualSpurVernierLUT_Cnt_s16[1][1] 10_DualSpurVernierLUT_Cnt_s16[1][1] 11_DualSpurVernierLUT_Cnt_s16[1][1] 11_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[2][2] 13_DualSpurVernierLUT_Cnt_s16[2][2] 14_DualSpurVernierLUT_Cnt_s16[2][2] 15_DualSpurVernierLUT_Cnt_s16[2][2] 16_DualSpurVernierLUT_Cnt_s16[2][2] 17_DualSpurVernierLUT_Cnt_s16[2][2] 18_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[	T2_DualSpurVernierLUT_Cnt_s16[0][21]	
T2_DualSpurVerniert_UT_Cnt_s16[1][2]	T2_DualSpurVernierLUT_Cnt_s16[1][0]	
T2_DualSpurVerniert.UT_Cnt_s16[1][3]  12_DualSpurVerniert.UT_Cnt_s16[1][4]  3	T2_DualSpurVernierLUT_Cnt_s16[1][1]	
T2_DualSpurVernierLUT_Cnt_s16[1][4]   3   4   72_DualSpurVernierLUT_Cnt_s16[1][6]   5   5   72_DualSpurVernierLUT_Cnt_s16[1][6]   5   6   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][9]   8   8   7   72_DualSpurVernierLUT_Cnt_s16[1][10]   9   72_DualSpurVernierLUT_Cnt_s16[1][11]   0   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][16] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 10 T2_DualSpurVernierLUT_Cnt_s16[2][2] 9 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 7 T2_DualSpurVernierLUT_Cnt_s16[2][6] 8	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][6]  72_DualSpurVernierLUT_Cnt_s16[1][8]  72_DualSpurVernierLUT_Cnt_s16[1][8]  72_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][12]  1  T2_DualSpurVernierLUT_Cnt_s16[1][13]  2  T2_DualSpurVernierLUT_Cnt_s16[1][14]  3  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][17]  6  T2_DualSpurVernierLUT_Cnt_s16[1][18]  7  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][20]  7  T2_DualSpurVernierLUT_Cnt_s16[1][21]  0  T2_DualSpurVernierLUT_Cnt_s16[1][21]  10  T2_DualSpurVernierLUT_Cnt_s16[2][1]  11  T2_DualSpurVernierLUT_Cnt_s16[2][2]  2  T2_DualSpurVernierLUT_Cnt_s16[2][3]  3  T2_DualSpurVernierLUT_Cnt_s16[2][4]  4  T2_DualSpurVernierLUT_Cnt_s16[2][6]  5  T2_DualSpurVernierLUT_Cnt_s16[2][6]  6  T2_DualSpurVernierLUT_Cnt_s16[2][6]  7  T2_DualSpurVernierLUT_Cnt_s16[2][6]  7  T2_DualSpurVernierLUT_Cnt_s16[2][6]  8	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][7]  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][12]  12_DualSpurVernierLUT_Cnt_s16[1][12]  12_DualSpurVernierLUT_Cnt_s16[1][14]  12_DualSpurVernierLUT_Cnt_s16[1][15]  4  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][17]  6  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][20]  9  T2_DualSpurVernierLUT_Cnt_s16[1][20]  9  T2_DualSpurVernierLUT_Cnt_s16[1][21]  10_DualSpurVernierLUT_Cnt_s16[2][0]  10_DualSpurVernierLUT_Cnt_s16[2][1]  11_DualSpurVernierLUT_Cnt_s16[2][1]  12_DualSpurVernierLUT_Cnt_s16[2][1]  12_DualSpurVernierLUT_Cnt_s16[2][2]  12_DualSpurVernierLUT_Cnt_s16[2][3]  12_DualSpurVernierLUT_Cnt_s16[2][6]  13_DualSpurVernierLUT_Cnt_s16[2][6]  14_DualSpurVernierLUT_Cnt_s16[2][6]  15_DualSpurVernierLUT_Cnt_s16[2][6]  16_DualSpurVernierLUT_Cnt_s16[2][6]  17_DualSpurVernierLUT_Cnt_s16[2][6]  18_DualSpurVernierLUT_Cnt_s16[2][6]  19_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]	T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
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T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][16]       5         T2_DualSpurVernierLUT_Cnt_s16[1][17]       6         T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][6]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6		
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T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][20]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8	T2_DualSpurVernierLUT_Cnt_s16[2][5]	
T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] 9	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] 10	T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] 0	T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] 1	T2_DualSpurVernierLUT_Cnt_s16[2][12]	1





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
	9		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17]			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15 17		
T2_DualSpurVernierLUT_Cnt_s16[3][19]			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	71		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	43		
k_VernCorrErrorDiag_Cnt_str.Threshold	18		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	4.670489788		
k_VernOORangeThresh_Deg_f32	1238.898165		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	247.6901497		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	147.1778288		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Ci	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1442.4646	1442.464623 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	•

9	194	1912   WILL   1914			
Name	Actual Value	Expected Value	Result		
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1442.4646	1442.464623 ± 0.00048828125	<b>✓</b>		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	<b>✓</b>		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>		
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	•		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	<b>✓</b>		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~		
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8	8	<b>✓</b>		
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~		
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~		
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~		
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	<b>✓</b>		
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	•		
NTC	0x6C	0x6C	~		
Param	0x0C	0x0C	~		
Status	0x01	0x01	<b>✓</b>		



Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.76 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128
DigColPs_ColTrimStatic_Deg_M_f32	149.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	13437
DigColPs_I2CHwColAngle_Deg_M_f32	212.9646001
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43747
DigColPs_I2CHwSpurAngle_Deg_M_f32	81.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	19
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	458.6756344
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	81.7
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





12_Configenment   Grag   19    1   1   1   1   1   1   1   1		I
T. Codeys/mental Cost 1981 10	Name	Input Value
12. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. CoSportwenset UT_Del. 1981[910] 10. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981	T2_ColSpurVernierLUT_Cnt_s16[1][14]	
17. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(2) 3. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 5. Costaya/ment.U. Cut. 1902(3) 6. Costaya/ment.U. Cut. 1902(3) 7. Costaya/ment.U. Cut. 1902(	T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
17_0085pvvrinstrut _Cot_st@  0	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
17_CORSE/vernetLT_CR_15[2]  6	T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
P. Collego-Vernicut   Ces. 1982    6	T2 ColSpurVernierLUT Cnt s16[2][1]	8
TE, COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 13. COSSA/VerineLUT, Cri.; 195(20) 15. COSSA/VerineLUT, Cri.; 195(20) 16. COSSA/VerineLUT, Cri.; 195(20) 17. COSSA/VerineLUT, Cri.; 195(20) 18. COSSA/VerineLUT, Cri.; 195(20) 19. COSSA/VerineLUT, Cri.; 195(20) 10. COSSA/VerineLUT, Cri.; 195(20) 11. COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 13. COSSA/VerineLUT, Cri.; 195(20) 14. COSSA/VerineLUT, Cri.; 195(20) 15. COSSA/VerineLUT, Cri.; 195(20) 17. COSSA/VerineLUT, Cri.; 195(20) 18. COSSA/VerineLUT, Cri.; 195(20) 19. COSSA/Verine		6
PCOS_AVENNELT_CR_15[0]  2   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  7   PC		
12 COSSAVVenneLUT Cut = 1619[15]   0		
12_Colspan/woment_Col_statigned   0		
T2_CuSignaviernet_UT_Cut_stip[15] 2_CuSignaviernet_UT_Cut_stip[15] 3_CuSignaviernet_UT_Cut_stip[15] 4_CuSignaviernet_UT_Cut_stip[15] 1_CuSignaviernet_UT_Cut_stip[15]		
T2_Colsput/emetU_Cot_stq09  5		
T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1911		
T. C. Colsput/ment U. Crit. st 1921   1		
T2_CoSpur'ementU_Cot_stQ111	T2_ColSpurVernierLUT_Cnt_s16[2][9]	
T. Colling/months   T. Colling   T. Collin	T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_CoSpur/emetUT_Cnt_sto[0]16  4 72_CoSpur/emetUT_Cnt_sto[0]16  172_CoSpur/emetUT_Cnt_sto[0]16  182_CoSpur/emetUT_Cnt_sto[0]16	T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   13_CoSput/venietU_Cot_s100214   1   14_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   1   15_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   17_CoSput/venietU_Cot_s100216   0   18_CoSput/venietU_Cot_s100216   0   19_Cosput/venietU_Cot_s100216   0   19_Cosput/venietU_Cot_s10	T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   13_CoSput/venietU_Cot_s100214   1   14_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   1   15_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   17_CoSput/venietU_Cot_s100216   0   18_CoSput/venietU_Cot_s100216   0   19_Cosput/venietU_Cot_s100216   0   19_Cosput/venietU_Cot_s10	T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T. Colspur/emicAUT Cnt. srie[]16   10   12   Colspur/emicAUT Cnt. srie[]16   10   12   Colspur/emicAUT Cnt. srie[]16   11   12   Colspur/emicAUT Cnt. srie[]16   14   12   Colspur/emicAUT Cnt. srie[]17   14   12   Colspur/emicAUT Cnt. srie[]18   14   12   Colspur/emicAUT Cnt. srie[]18   15   Colspur/emicAUT Cnt. srie[]18   16   Colspur/emicAUT Cnt. srie[]18   16   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   18   Colspur/emicAUT Cnt. srie[]18   19   T. Colspur/emicAUT		4
12, CoSparVeneUT, Cot. 918(319) 12, CoSparVeneUT, Cot. 918(319) 13, CoSparVeneUT, Cot. 918(319) 14, CoSparVeneUT, Cot. 918(319) 15, CoSparVeneUT, Cot. 918(319) 16, CoSparVeneUT, Cot. 918(319) 17, CoSparVeneUT, Cot. 918(319) 18, CoSparVeneUT, Cot. 918(319) 18, CoSparVeneUT, Cot. 918(319) 19, CoSparVeneUT, Cot. 918(319		
17_CoSput/venicUT_Cot_1 1613[1] 17_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 12_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 14_CoSput/venicUT_Cot_1 1613[2] 15_CoSput/venicUT_Cot_1 1613[2] 16_CoSput/venicUT_Cot_1 1613[2] 17_CoSput/venicUT_Cot_1 1613[2] 18_CoSput/venicUT_Cot_1 1613[2] 19_CoSput/venicUT_Cot_1 1613[2] 10_CoSput/venicUT_Cot_1 1613[2] 10_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2		
12, CoSppt/vermicHUT_Cnt, 18(3)[2]   11   12, CoSppt/vermicHUT_Cnt, 18(3)[2]   11   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   8   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   8   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   12   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   17, CoSppt		
17_CoSput/venieUT_Cnt_180S S S S S S S S S S S S S S S S S S S		
T2_CoSput/vemet.UT_Cnt_s160  16   5     T2_CoSput/vemet.UT_Cnt_s160  16   5     T2_CoSput/vemet.UT_Cnt_s160  16   15     T2_CoSput/vemet.UT_Cnt_s160  16   15     T2_CoSput/vemet.UT_Cnt_s160  16   16     T2_CoSput/vemet.UT_Cnt_s160  17   16     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  18   19     T2_CoSput/vemet.UT_Cnt_s160  18   17     T2_DusSput/vemet.UT_Cnt_s160  18   18     T2_DusSput/vem		
12, CoSparVermentUT, Cnt. \$163[15] 2, CoSparVermentUT, Cnt. \$163[15] 2, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 10, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 10, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 10, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentU		
12_CobSparVement_UT_Cnt_steQt  5    2_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  7    12_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  5    13_CobSparVement_UT_Cnt_steQt  5    14_CobSparVement_UT_Cnt_steQt  5    15_CobSparVement_UT_Cnt_steQt  5    16_CobSparVement_UT_Cnt_steQt  5    17_CobSparVement_UT_Cnt_steQt  5    18_CobSparVement_UT_Cnt_steQt  5    19_CobSparVement_UT_Cnt_steQt  6    19_CobSparVement_UT_Cnt_steQt		
T2_CoSpuVerinetUT_Cnt_s160]10   15   T2_CoSpuVerinetUT_Cnt_s160]11   12   T2_CoSpuVerinetUT_Cnt_s160]10   0   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   16   T2_CoSpuVerinetUT_Cnt_s160]11   18   T2_CoSpuVerinetUT_Cnt_s160]12   13   T2_CoSpuVerinetUT_Cnt_s160]13   10   T2_CoSpuVerinetUT_Cnt_s160]14   7   T2_CoSpuVerinetUT_Cnt_s160]15   4   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]10   360   T2_CoSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuV		5
T2_CoSpuVerinetUT_Cnt_s160]10   15   T2_CoSpuVerinetUT_Cnt_s160]11   12   T2_CoSpuVerinetUT_Cnt_s160]10   0   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   16   T2_CoSpuVerinetUT_Cnt_s160]11   18   T2_CoSpuVerinetUT_Cnt_s160]12   13   T2_CoSpuVerinetUT_Cnt_s160]13   10   T2_CoSpuVerinetUT_Cnt_s160]14   7   T2_CoSpuVerinetUT_Cnt_s160]15   4   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]10   360   T2_CoSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuV	T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
12. CoSpurVemierLUT_Cnt_s16(3)[8] 9 17. CoSpurVemierLUT_Cnt_s16(3)[8] 9 17. CoSpurVemierLUT_Cnt_s16(3)[10] 13 17. CoSpurVemierLUT_Cnt_s16(3)[11] 16 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[16] 17 17. CospurVemierLUT_Cnt_s16(3)[16] 18 18. CospurVemierLUT_Cnt_s16(3)[16] 18 1		15
12. ColspurVement.UT. Cnt. 15(8)[9] 6 12. ColspurVement.UT. Cnt. 15(8)[11] 16 12. ColspurVement.UT. Cnt. 15(8)[12] 13 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[14] 7 12. ColspurVement.UT. Cnt. 15(8)[15] 19 12. ColspurVement.UT. Cnt. 15(8)[16] 19 13. ColspurVement.UT. Cnt. 15(8)[16] 19 14. ColspurVement.UT. Cnt. 15(8)[16] 19 15. DualspurVement.UT. Cnt. 15(8)[16] 19 16. DualspurVement.UT. Cnt. 15(8)[16] 19 17. DualspurVement.UT. Cnt. 15(8)[17] 144 18. DualspurVement.UT. Cnt. 15(8)[18] 19 18. DualspurVement.UT. Cnt. 15(8)[19] 19 18. DualspurV	T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
12. ColspurVement.UT. Cnt. 15(8)[9] 6 12. ColspurVement.UT. Cnt. 15(8)[11] 16 12. ColspurVement.UT. Cnt. 15(8)[12] 13 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[14] 7 12. ColspurVement.UT. Cnt. 15(8)[15] 19 12. ColspurVement.UT. Cnt. 15(8)[16] 19 13. ColspurVement.UT. Cnt. 15(8)[16] 19 14. ColspurVement.UT. Cnt. 15(8)[16] 19 15. DualspurVement.UT. Cnt. 15(8)[16] 19 16. DualspurVement.UT. Cnt. 15(8)[16] 19 17. DualspurVement.UT. Cnt. 15(8)[17] 144 18. DualspurVement.UT. Cnt. 15(8)[18] 19 18. DualspurVement.UT. Cnt. 15(8)[19] 19 18. DualspurV		9
12 ColSparVemiet.UT_Cnt_s16[3][10] 12 ColSparVemiet.UT_Cnt_s16[3][11] 16 17 ColSparVemiet.UT_Cnt_s16[3][12] 13 17 ColSparVemiet.UT_Cnt_s16[3][13] 10 10 ColSparVemiet.UT_Cnt_s16[3][14] 17 12 ColSparVemiet.UT_Cnt_s16[3][16] 18 19 ColSparVemiet.UT_Cnt_s16[3][16] 19 ColSparVemiet.UT_Cnt_s16[3][16] 19 ColSparVemiet.UT_Cnt_s16[3][16] 19 ColSparVemiet.UT_Cnt_s16[3][16] 10 ColSparVemiet.UT_Cnt_s16[3][16] 11 ColSparVemiet.UT_Cnt_s16[3][16] 11 ColSparVemiet.UT_Cnt_s16[3][16] 12 DualSparVemiet.UT_Cnt_s16[3][16] 13 ColSparVemiet.UT_Cnt_s16[3][16] 14 ColSparVemiet.UT_Cnt_s16[3][16] 15 ColSparVemiet.UT_Cnt_s16[3][16] 16 ColSparVemiet.UT_Cnt_s16[3][16] 17 ColSparVemiet.UT_Cnt_s16[3][16] 18 ColSparVemiet.UT_Cnt_s16[3][16] 19 ColSparVemiet.UT_Cnt_s16[3][16] 10 ColSparVemiet.UT_Cnt_s16[3][16] 11 ColSparVemiet.UT_Cnt_s16[3][17] 11 ColSparVemiet.UT_Cnt_s16[3][17] 12 ColSparVemiet.UT_Cnt_s16[3][17] 13 ColSparVemiet.UT_Cnt_s16[3][17] 14 ColSparVemiet.UT_Cnt_s16[3][17] 15 ColSparVemiet.UT_Cnt_s16[3][17] 16 ColSparVemiet.UT_Cnt_s16[3][17] 17 ColSparVemiet.UT_Cnt_s16[3][17] 18 ColSparVemiet.UT_Cnt_s16[3][17] 19 ColSparVemiet.UT_Cnt_s16[3][17] 19 ColSparVemiet.UT_Cnt_s16[3][17] 19 ColSparVemiet.UT_Cnt_s16[3][17] 10 ColSparVemiet.UT_Cnt_s16[3][17] 11 ColSparVemiet.UT_Cnt_s16[3][17] 12 ColSparVemiet.UT_Cnt_s16[3][17] 12 ColSparVemiet.UT_Cnt_s16[3][17] 13 ColSparVemiet.UT_Cnt_s16[3][17] 14 ColSparVemiet.UT_Cnt_s16[3][17] 15 ColSparVemiet.UT_Cnt_s16[3][17] 16 ColSparVemiet.UT_Cnt_s16[3][17] 17 ColSparVemiet.UT_Cnt_s16[3][17] 18 ColSparVemiet.UT_Cnt_s16[3][17] 19 ColSparVemiet.UT_Cnt_s16[3][17] 10 ColSparVemiet.UT_Cnt_s16[3][17] 11 ColSparVemiet.UT_Cnt_s16[3][17] 11 ColSparVemiet.UT_Cnt_s16[3][17] 12 ColSparVemiet.UT_Cnt_s16[3][17] 12 ColSparVemiet.UT_Cnt_s16[3][17] 13 ColSparVemiet.UT_Cnt_s16[3][17] 14 ColSparVemiet.UT_Cnt_s16[3][17] 15 ColSparVemiet.UT_Cnt_s16[3][17] 16 ColSparVemiet.UT_Cnt_s16[3][17] 17 ColSparVemiet.UT_Cnt_s16[3][17] 18 ColSparVemiet.UT_Cnt_s16[3][17] 19 ColSparVemiet.UT_Cnt_s16[3][17] 10 ColSparVemiet.UT_Cnt_s16[3][17]		
12 ColSpurVemiet.UT_Cnt_s16[3][11]   16   17 ColSpurVemiet.UT_Cnt_s16[3][12]   13   10   10   10   10   10   10   10		
12		
12 CoSparVement LIT_Cnt_s16[3]1:3]   10   7   2   CoSparVement LIT_Cnt_s16[3]1:4]   7   7   7   7   7   7   7   7   7		
12 ColSpurVemietLUT_Cnt_st6[3][14]   7   7   2 ColSpurVemietLUT_Cnt_st6[3][15]   4   7   7   2 ColSpurVemietLUT_Cnt_st6[3][15]   4   7   7   7   7   7   7   7   7   7		
T2_ColSpurVemierLUT_Cnt_s16[3]15		
T2   DualspurVemierLUT_Cnt_s16(0)(1)   396		
T2 DualSpurVemierLUT_Cnt_st6[0][0]   396     T2 DualSpurVemierLUT_Cnt_st6[0][1]   360     T2 DualSpurVemierLUT_Cnt_st6[0][2]   324     T2 DualSpurVemierLUT_Cnt_st6[0][3]   286     T2 DualSpurVemierLUT_Cnt_st6[0][3]   286     T2 DualSpurVemierLUT_Cnt_st6[0][6]   252     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -108     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -72     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -72     T2 DualSpurVemierLUT_Cnt_st6[0][1]   -73     T2 DualSpurVemierLUT_Cnt_st6[0][1]   -74     T2 DualSpurVemierL	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_DualSpurVemiert.UT_Cnt_st6[0][1]   360     T2_DualSpurVemiert.UT_Cnt_st6[0][2]   324     T2_DualSpurVemiert.UT_Cnt_st6[0][4]   252     T2_DualSpurVemiert.UT_Cnt_st6[0][5]   216     T2_DualSpurVemiert.UT_Cnt_st6[0][6]   -180     T2_DualSpurVemiert.UT_Cnt_st6[0][7]   -144     T2_DualSpurVemiert.UT_Cnt_st6[0][8]   -108     T2_DualSpurVemiert.UT_Cnt_st6[0][9]   -72     T2_DualSpurVemiert.UT_Cnt_st6[0][9]   -72     T2_DualSpurVemiert.UT_Cnt_st6[0][1]   -73     T2_DualSpurVemiert.UT_Cnt_st6[0][1]   -74     T2_DualSpurVemier	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVemiert.UT_Cnt_st 6[0] 2    324     T2_DualSpurVemiert.UT_Cnt_st 6[0] 3    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    252     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 8    -108     T2_DualSpurVemiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 10    -36     T2_DualSpurVemiert.UT_Cnt_st 6[0] 11    0     T2_DualSpurVemiert.UT_Cnt_st 6[0] 12    36     T2_DualSpurVemiert.UT_Cnt_st 6[0] 13    72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 14    108     T2_DualSpurVemiert.UT_Cnt_st 6[0] 15    144     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 20    324     T2_DualSpurVemiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVemiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    5     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    8     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    8     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVerniertUT_Cnt_s16[0][3]	T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][4]   2552   216   12_DualSpurVernierLUT_Cnt_s16[0][5]   2-16   180	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][4]   2552   216   12_DualSpurVernierLUT_Cnt_s16[0][5]   2-16   180		-288
T2_DualSpurVernierLUT_Cnt_s16(0)[5] T2_DualSpurVernierLUT_Cnt_s16(0)[6] T2_DualSpurVernierLUT_Cnt_s16(0)[6] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[9] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[11] T2_DualSpurVernierLUT_Cnt_s16(0)[12] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[15] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[17] T2_DualSpurVernierLUT_Cnt_s16(0)[18] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[19] T2_DualSpurVernierLU		
T2_DualSpurVernierLUT_Cnt_s16(0)[6]   -180     -144   -		
12   DualSpurVernierLUT_Cnt_s16[0][7]   -144         12   DualSpurVernierLUT_Cnt_s16[0][8]   -108       12   DualSpurVernierLUT_Cnt_s16[0][10]   -36       12   DualSpurVernierLUT_Cnt_s16[0][11]   0   0       12   DualSpurVernierLUT_Cnt_s16[0][12]   36       12   DualSpurVernierLUT_Cnt_s16[0][13]   72       12   DualSpurVernierLUT_Cnt_s16[0][14]   108       12   DualSpurVernierLUT_Cnt_s16[0][15]   144       12   DualSpurVernierLUT_Cnt_s16[0][16]   180       12   DualSpurVernierLUT_Cnt_s16[0][17]   216       12   DualSpurVernierLUT_Cnt_s16[0][17]   216       12   DualSpurVernierLUT_Cnt_s16[0][19]   288       12   DualSpurVernierLUT_Cnt_s16[0][19]   288       12   DualSpurVernierLUT_Cnt_s16[0][21]   360       12   DualSpurVernierLUT_Cnt_s16[0][21]   360       12   DualSpurVernierLUT_Cnt_s16[1][2]   1       12   DualSpurVernierLUT_Cnt_s16[1][2]   1       12   DualSpurVernierLUT_Cnt_s16[1][3]   2       12   DualSpurVernierLUT_Cnt_s16[1][3]   2       12   DualSpurVernierLUT_Cnt_s16[1][4]   3       12   DualSpurVernierLUT_Cnt_s16[1][6]   5       12   DualSpurVernierLUT_Cnt_s16[1][6]   6       13   DualSpurVernierLUT_Cnt_s16[1][6]   7       14   12   DualSpurVernierLUT_Cnt_s16[1][6]   6       15   DualSpurVernierLUT_Cnt_s16[1][6]   7       16   DualSpurVernierLUT_Cnt_s16[1][6]   9       17   DualSpurVernierLUT_Cnt_s16[1][6]   9       18   DualSpurVernierLUT_Cnt_s16[1][6]   9       19   DualSpurVernierLUT_Cnt_s16[1][6]   9       10   DualSpurVernierLUT_Cnt_s16[1][6]   9       11   DualSpurVernierLUT_Cnt_s16[1][6]   9       12   DualSpurVernierLUT_Cnt_s16[1][6]   9		
T2_DualSpurVerniertLUT_Cnt_s16[0][8]   -108     -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72		
T2_DualSpurVernierLUT_Cnt_s16[0][9]		
T2_DualSpurVernierLUT_Cnt_s16[0][10]  72_DualSpurVernierLUT_Cnt_s16[0][11]  72_DualSpurVernierLUT_Cnt_s16[0][12]  72_DualSpurVernierLUT_Cnt_s16[0][13]  72_DualSpurVernierLUT_Cnt_s16[0][14]  72_DualSpurVernierLUT_Cnt_s16[0][15]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][17]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][19]  72_DualSpurVernierLUT_Cnt_s16[0][20]  72_DualSpurVernierLUT_Cnt_s16[0][21]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][1]  72_DualSpurVernierLUT_Cnt_s16[1][2]  72_DualSpurVernierLUT_Cnt_s16[1][3]  72_DualSpurVernierLUT_Cnt_s16[1][4]  72_DualSpurVernierLUT_Cnt_s16[1][6]  73_DualSpurVernierLUT_Cnt_s16[1][6]  74_DualSpurVernierLUT_Cnt_s16[1][6]  75_DualSpurVernierLUT_Cnt_s16[1][6]  76_DualSpurVernierLUT_Cnt_s16[1][6]  77_DualSpurVernierLUT_Cnt_s16[1][6]  78_DualSpurVernierLUT_Cnt_s16[1][6]  79_DualSpurVernierLUT_Cnt_s16[1][6]  70_DualSpurVernierLUT_Cnt_s16[1][6]  71_DualSpurVernierLUT_Cnt_s16[1][6]		
T2_DualSpurVernierLUT_Cnt_s16[0][11]  72_DualSpurVernierLUT_Cnt_s16[0][12]  72_DualSpurVernierLUT_Cnt_s16[0][13]  72 T2_DualSpurVernierLUT_Cnt_s16[0][14]  72_DualSpurVernierLUT_Cnt_s16[0][15]  72_DualSpurVernierLUT_Cnt_s16[0][15]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][17]  72_DualSpurVernierLUT_Cnt_s16[0][17]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][19]  72_DualSpurVernierLUT_Cnt_s16[0][21]  72_DualSpurVernierLUT_Cnt_s16[0][21]  72_DualSpurVernierLUT_Cnt_s16[0][21]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][1]  72_DualSpurVernierLUT_Cnt_s16[1][3]  72_DualSpurVernierLUT_Cnt_s16[1][4]  73_DualSpurVernierLUT_Cnt_s16[1][6]  74_DualSpurVernierLUT_Cnt_s16[1][6]  75_DualSpurVernierLUT_Cnt_s16[1][6]  76_DualSpurVernierLUT_Cnt_s16[1][6]  77_DualSpurVernierLUT_Cnt_s16[1][6]  78_DualSpurVernierLUT_Cnt_s16[1][6]  79_DualSpurVernierLUT_Cnt_s16[1][6]  70_DualSpurVernierLUT_Cnt_s16[1][6]  71_DualSpurVernierLUT_Cnt_s16[1][6]  72_DualSpurVernierLUT_Cnt_s16[1][6]  73_DualSpurVernierLUT_Cnt_s16[1][6]  74_DualSpurVernierLUT_Cnt_s16[1][6]  75_DualSpurVernierLUT_Cnt_s16[1][6]  76_DualSpurVernierLUT_Cnt_s16[1][6]  77_DualSpurVernierLUT_Cnt_s16[1][6]  78_DualSpurVernierLUT_Cnt_s16[1][6]  79_DualSpurVernierLUT_Cnt_s16[1][6]  70_DualSpurVernierLUT_Cnt_s16[1][6]  71_DualSpurVernierLUT_Cnt_s16[1][6]  72_DualSpurVernierLUT_Cnt_s16[1][6]  73_DualSpurVernierLUT_Cnt_s16[1][6]  74_DualSpurVernierLUT_Cnt_s16[1][6]  75_DualSpurVernierLUT_Cnt_s16[1][6]  76_DualSpurVernierLUT_Cnt_s16[1][6]  77_DualSpurVernierLUT_Cnt_s16[1][6]  78_DualSpurVernierLUT_Cnt_s16[1][6]  79_DualSpurVernierLUT_Cnt_s16[1][6]  70_DualSpurVernierLUT_Cnt_s16[1][6]  71_DualSpurVernierLUT_Cnt_s16[1][6]		
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 12_DualSpurVernierLUT_Cnt_s16[0][17] 12_DualSpurVernierLUT_Cnt_s16[0][19] 12_DualSpurVernierLUT_Cnt_s16[0][19] 12_DualSpurVernierLUT_Cnt_s16[0][20] 12_DualSpurVernierLUT_Cnt_s16[0][21] 1360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 12_DualSpurVernierLUT_Cnt_s16[1][0] 12_DualSpurVernierLUT_Cnt_s16[1][1] 12_DualSpurVernierLUT_Cnt_s16[1][1] 12_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][3] 12_DualSpurVernierLUT_Cnt_s16[1][4] 13_DualSpurVernierLUT_Cnt_s16[1][5] 14_DualSpurVernierLUT_Cnt_s16[1][6] 15_DualSpurVernierLUT_Cnt_s16[1][6] 16_DualSpurVernierLUT_Cnt_s16[1][6] 17_DualSpurVernierLUT_Cnt_s16[1][6] 18_DualSpurVernierLUT_Cnt_s16[1][6] 19_DualSpurVernierLUT_Cnt_s16[1][11] 10_DualSpurVernierLUT_Cnt_s16[1][11] 11_DualSpurVernierLUT_Cnt_s16[1][11] 11_DualSpurVernierLUT_Cnt_s16[1][11] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12]		
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 9  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0		0
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  0  T2_DualSpurVernierLUT_Cnt_s16[1][1]  12_DualSpurVernierLUT_Cnt_s16[1][2]  12_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][4]  3  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][11]  10	T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  10  T2_DualSpurVernierLUT_Cnt_s16[1][1]  11  T2_DualSpurVernierLUT_Cnt_s16[1][3]  21  T2_DualSpurVernierLUT_Cnt_s16[1][4]  32  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9	T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][15]		108
T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 10  T2_DualSpurVernierLUT_Cnt_s16[1][10] 11		144
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10		
T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0          T2_DualSpurVernierLUT_Cnt_s16[1][11]       0		
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 1		
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2 DualSpur/erniert LT_Cnt_s16[1][13]		
	T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	8
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18 20
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][11]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	23
k_SkipStepErrDiag_Cnt_str.PStep	50
k_SkipStepErrDiag_Cnt_str.NStep	6
k_VernCorrErrorDiag_Cnt_str.Threshold	0
k_VernCorrErrorDiag_Cnt_str.PStep	38
k_VernCorrErrorDiag_Cnt_str.NStep	15
k_VernCorrErrorThresh_Deg_f32	14.63263726
k_VernOORangeThresh_Deg_f32	215.8799315
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	212.9646001
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	227.4025638 1
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos_HwDog_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_igc  tgt_Rte_inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_InmComp_Cnt_igc tgt_Pim_DigColPsEOL
Name DigColPs_HwAVernCorrFault_Cnt_M_lgc	Actual Value Expected Value Resul
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1079.22607 1079.226107 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	23	23	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0E	0x0E	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Test Step 2.77 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	153.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	45384
DigColPs_I2CHwColAngle_Deg_M_f32	217.6150646
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58335
DigColPs_I2CHwSpurAngle_Deg_M_f32	82.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1165.499187
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	82.8
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs VernierAngleOORange Cnt M Igc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2 ColSpurVernierLUT Cnt s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2 ColSpurVernierLUT Cnt s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
	-30
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2] T0_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	2 3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	1 2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	7 8
T2 DualSpurVernierLUT Cnt s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10 12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpur/ornierLUT_Cnt_s16[3][14] T3_DualSpur/ornierLUT_Cst_s16[3][15]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	9
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	162
k_SkipStepErrDiag_Cnt_str.PStep	20
k_SkipStepErrDiag_Cnt_str.NStep	48
k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep	100 38



Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	67.91880226		
k_VernOORangeThresh_Deg_f32	1176.43799		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	217.6150646		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	178.2231709		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

tgt_Pim_bigColPseOL		
Actual Value	Expected Value	Result
1	1	~
1429.35303	1429.353104 ± 0.00048828125	~
0	0	~
1	1	~
1440	1440 ± 0.0001220703125	~
14	14	~
1	1	~
42	42	~
1	1	~
0	0	~
0	0	~
540	540 ± 0.0009	~
1	1	~
	Actual Value  1 1429.35303 0 1 14440 14 1 42 1 0 0	Actual Value         Expected Value           1         1           1429.35303         1429.353104 ± 0.00048828125           0         0           1         1           1440         1440 ± 0.0001220703125           14         14           1         1           42         42           1         1           0         0           0         0

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.78 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt GetCustData()	196
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	205
DigColPs ColTrimStatic Deg M f32	157.3
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	33060
DigColPs_I2CHwColAngle_Deg_M_f32	28.42972344
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18440
DigColPs_I2CHwSpurAngle_Deg_M_f32	83.9
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1.
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	594.4117691
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	83.9
DigColPs_TrimCompStatic_Cnt_M_u16	2680
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T3_ColSpurVernierLUT_Cnt_s46[4][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][0]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s46[3][40]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
b	I .

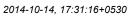
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Name  T2_DualSpurVernierLUT_Cnt_s16[0][10]  T2_DualSpurVernierLUT_Cnt_s16[0][11]  T2_DualSpurVernierLUT_Cnt_s16[0][12]	Input Value -36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	-30
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2 DualSpurVernierLUT Cnt s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
	22
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	22
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
	11

DigColPs\_Per2

Status





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	50		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	57		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	27.292485		
k_VernOORangeThresh_Deg_f32	686.6912438		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	28.42972344		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	179.9644135		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1458		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M lgc	1	1	~
DigColPs I2CHwColAngleForTrim Deg M f32	1309.09082	1309.090909 ± 0.00048828125	~
DigCoIPs I2CHwTrimTransCnts UIs M u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs PrevColPos Deg M f32	1311.12976	1311.129723 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	<b>✓</b>
DigColPs RegI2CSnsrDataType Cnt M u08	1	1	
DigColPs SkipStepFltDetectAcc Cnt M u16	7	7	<b>✓</b>
DigColPs VernCorrDetectAcc Cnt M u16	1	1	
DigColPs VernierAngleOORange Cnt M lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	·
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	411.129761	411.1297234 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	
Chabus	0,401	0,01	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

0x01

0x01

Test Step 2.79 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	128
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	210
DigColPs_ColTrimStatic_Deg_M_f32	161.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	11998
DigColPs_I2CHwColAngle_Deg_M_f32	16.12509024
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	24120
DigColPs_I2CHwSpurAngle_Deg_M_f32	285
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	4
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0

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Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	1148.961804
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	5
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	285
DigColPs_TrimCompStatic_Cnt_M_u16	2716
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][3]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][5] T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_S16[2][15] T2_ColSpurVernierLUT_Cnt_S16[2][16]	10
T2_ColSpurVernierLUT_Cnt_S16[2][16] T2_ColSpurVernierLUT_Cnt_S16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108 -72
T2_DualSpurVernierLUT_Cnt_s16[0][9] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_S16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][11]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T3_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[2][18]	
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][18]	8
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	8 9

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DigCoirs_rei2			CION
Name	Input Value		
T2 DualSpurVernierLUT Cnt s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2 DualSpurVernierLUT Cnt s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2 DualSpurVernierLUT Cnt s16[3][21]	21		
k SelectFromColumn Cnt lgc	0		
k SkipStepErrDiag Cnt str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k VernCorrErrorDiag Cnt str.Threshold	52		
k_VernCorrErrorDiag_Cnt_str.PStep	20		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	85.22490358		
k VernOORangeThresh Deg f32	1677.836695		
tgt DigColPs Per2 MecState Cnt enum.value	1		
tgt Pim DigColPsEOL.ColTrim Deg f32	16.12509024		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	48.06899381		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4397		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid Cnt loc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt DigColPs Per2 I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Ci		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	. <del></del> 9-	
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	11000
DigColPs I2CHwColAngleForTrim Deg M f32	1309.09082	1309.090909 ± 0.00048828125	
DigColPs I2CHwTrimTransCnts UIs M u08	1	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs PrevColPos Deg M f32	1294.7251	1294.72509 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs SkipStepFltDetectAcc Cnt M u16	2	2	
DigColPs VernCorrDetectAcc Cnt M u16	2	2	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tot DigColDs Dark 1900 by Aba Dark (alid Oat Jacous)			

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	-
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	-
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>✓</b>

409.09082

0

0

0

409.0909091 ± 0.0009

Test Step 2.80 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	220
DigColPs_ColTrimStatic_Deg_M_f32	165.5

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

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Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	12814
DigColPs_I2CHwColAngle_Deg_M_f32	117.9909339
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	14635
DigColPs_I2CHwSpurAngle_Deg_M_f32	86.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs I2CSpurSensorFault Cnt M Igc	0
	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	86.1
DigColPs_TrimCompStatic_Cnt_M_u16	2752
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
	196
T2_ColSpurVernierLUT_Cnt_s16[0][11]	229
T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	359
T2_ColSpurVernierLUT_Cnt_s16[0][16]	0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][2]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
	5
T2_ColSpurVernierLUT_Cnt_s16[2][8]	
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3 1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14] T3_ColSpurVernierLUT_Cnt_s16[2][15]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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T. C. OSSAVVANICUT, CM   1907  1	Nama	Input Value
12_CoSquirement_Cot_statp[0]   11	Name	Input Value
P. Colispa/remontall_Colstiligid   5   12   Colispa/remontall_Colstiligid   5   12   Colispa/remontall_Colstiligid   5   15   Colispa/remontall_Colstiligid   5   15   Colispa/remontall_Colstiligid   15   Colispa/remontall_Colstiligid   16   Colispa/remontall_Colstiligid   16   Colispa/remontall_Colstiligid   17   Colispa/remontall_Colstiligid   18		
T. Collego/vernient U. Col. 4 (1978)		
12_00spx/remail_Coll_Statistics    2		
T2_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 13_COSQAVeniment_U_Cnt_visit(SIS) 14_COSQAVeniment_U_Cnt_visit(SIS) 15_COSQAVeniment_U_Cnt_visit(SIS) 16_COSQAVeniment_U_Cnt_visit(SIS) 17_COSQAVeniment_U_Cnt_visit(SIS) 18_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit		
12, Osspar/weinstall, Cot, 14(3)(1)		
Tz. COSSpur/venseLT. Cot. s163789 6 Tz. COSSpur/venseLT. Cot. s163719 6 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 19 Tz. COSSpur/v		
To Conspired ment LT, Det. 310(19)  To Conspired ment LT, Det. 510(19)  To Conspired ment LT, Det. 510		
To Colsput/went LT Ont \$180]119 10		
T. Colsput/ment.U. Col.; st (20)112   13   13   13   13   13   13   13	T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[15]	T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T. C. OSBOUYMONE LT. COL. \$16(3)*14   7   7   7   7   7   7   7   7   7	T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_Colspar/membut/D_cnt_st093115  4	T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
17, CoSpur/wentUT, Cott, \$103115	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_CosSpurVement UT_Cnt_s160[110] 388  12_DusSpurVement UT_Cnt_s160[11] 380  12_DusSpurVement UT_Cnt_s160[12] 384  12_DusSpurVement UT_Cnt_s160[13] 384  12_DusSpurVement UT_Cnt_s160[13] 384  12_DusSpurVement UT_Cnt_s160[14] 382  12_DusSpurVement UT_Cnt_s160[16] 388  12_DusSpurVement UT_Cnt_s160[16] 380  12_DusSpurVement UT_Cnt_s160[16] 380  12_DusSpurVement UT_Cnt_s160[17] 380  12_DusSpurVement UT_Cnt_s160[18] 380  12_DusSpurVement UT_Cnt_s160[18	T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
12. DualSparVermicht UT. Cit.; 1490191   396 172. DualSparVermicht UT. Cit.; 1490191   356 173. DualSparVermicht UT. Cit.; 1490191   324 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   252 173. DualSparVermicht UT. Cit.; 1490191   316 174. DualSparVermicht UT. Cit.; 1490191   316 175. DualSparVermicht UT. Cit.; 1490191   316 177. DualSparVermicht UT. Cit.; 1490191   317 177. DualSparVermicht UT. Cit.; 1490191   317 177. DualSparVermicht UT. Cit.; 1490191   318 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   319 177. DualSparVermicht UT. Cit.; 1490191   322 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   326 177. DualSparVermicht UT. Cit.; 3490191   327 177. DualSparVermicht UT. Cit.; 3490191   327 177. DualSparVermicht UT. Cit.; 3490191   329 177. DualSparVermicht U	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
12_DasSgru/FemerU_T_Crt_s160[12]	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_DasSgru/FemerU_T_Crt_s160[12]	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2, DuaSpurVermiet.UT_Cnt_s160[15]   288		-360
T. DualSparVermicLUT_Cnt_s180[H]   252	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_Dus SpurVernieLUT_Cnt_s16[0][5]   216     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -72     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -36     T2_Dus SpurVern		-288
T2_Dus SpurVernieLUT_Cnt_s16[0][5]   216     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -144     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -148     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -72     T2_Dus SpurVernieLUT_Cnt_s16[0][7]   -36     T2_Dus SpurVern		
12, DualSparVermetLUT_Cnt_s16(0)(8)   .189		
12. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -74 17. DuaSgurVemet.UT_Cnt_s160[12] -74 17. DuaSgurVemet.UT_Cnt_s160[13] -74 17. DuaSg		
12_DusSpurVermetUT_Cnt_s160[18]   -102   T2_DusSpurVermetUT_Cnt_s160[10]   -72   T2_DusSpurVermetUT_Cnt_s160[10]   -36   T2_DusSpurVermetUT_Cnt_s160[11]   0   T2_DusSpurVermetUT_Cnt_s160[11]   0   T2_DusSpurVermetUT_Cnt_s160[11]   36   T2_DusSpurVermetUT_Cnt_s160[11]   36   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   108   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[11]   109   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   360   T2_DusSpurVermetUT_Cnt_s160[12]   109   T2_DusSpurVermetUT_Cnt_s160[12]   109   T2_DusSpurVermetUT_Cnt_s160[13]   109   T2_DusSpu		
12. DualSpur/vernietUT_Cnt_s16(0) 10   36   36   36   36   36   36   36   3		
12. DualSpur/vernierLUT_Cnt_s16()[11]   0   0   0   0   0   0   0   0   0		
12   DusiSpur/VernietUT_Cnt, 1610[11]   12   136   1		
12 DuaiSpurVernierLUT_Cnt_s16[0][12]   36   72   72   73   73   74   74   74   74   74   74		
T2   DualSpurVermierLUT_Cnt_s16[0][14]   108		
172   DualSpurVernierLUT_Cnt_stic[0]14    108     172   DualSpurVernierLUT_Cnt_stic[0]15    144     172   DualSpurVernierLUT_Cnt_stic[0]16    180     173   DualSpurVernierLUT_Cnt_stic[0]17    216     173   DualSpurVernierLUT_Cnt_stic[0]17    216     174   DualSpurVernierLUT_Cnt_stic[0]19    288     172   DualSpurVernierLUT_Cnt_stic[0]19    288     173   DualSpurVernierLUT_Cnt_stic[0]20    324     174   DualSpurVernierLUT_Cnt_stic[0]21    360     175   DualSpurVernierLUT_Cnt_stic[0]21    360     175   DualSpurVernierLUT_Cnt_stic[1]2    1     175   DualSpurVernierLUT_Cnt_stic[1]2    1     175   DualSpurVernierLUT_Cnt_stic[1]2    1     175   DualSpurVernierLUT_Cnt_stic[1]3    2     175   DualSpurVernierLUT_Cnt_stic[1]4    3     175   DualSpurVernierLUT_Cnt_stic[1]6    5     175   DualSpurVernierLUT_Cnt_stic[1]6    5     175   DualSpurVernierLUT_Cnt_stic[1]6    6     175   DualSpurVernierLUT_Cnt_stic[1]6    7     175   DualSpurVernierLUT_Cnt_stic[1]6    7     175   DualSpurVernierLUT_Cnt_stic[1]10    9     175   DualSpurVernierLUT_Cnt_stic[1]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLUT_Cnt_stic[2]2    9     175   DualSpurVernierLU		
T2   DualSpurVermict.UT   Cnt   s16(0) 15    144     T2   DualSpurVermict.UT   Cnt   s16(0) 16    180     T2   DualSpurVermict.UT   Cnt   s16(0) 17    216     T2   DualSpurVermict.UT   Cnt   s16(0) 17    216     T2   DualSpurVermict.UT   Cnt   s16(0) 18    252     T3   DualSpurVermict.UT   Cnt   s16(0) 20    324     T2   DualSpurVermict.UT   Cnt   s16(0) 20    324     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    9     T2   DualSpurVermict.UT   Cnt   s16(1) 10    1     T3   DualSpurVermict.UT   Cnt   s16(1) 10    1     T4   DualSpurVermict.UT   Cnt   s16(1) 10    1     T2   DualSpurVermict.UT   Cnt   s16(1) 10    3     T2   DualSpurVermict.UT   Cnt   s16(1) 10    3     T3   DualSpurVermict.UT   Cnt   s16(1) 10    5     T4   DualSpurVermict.UT   Cnt   s16(1) 10    5     T5   DualSpurVermict.UT   Cnt   s16(1) 10    6     T5   DualSpurVermict.UT   Cnt   s16(1) 10    7     T2   DualSpurVermict.UT   Cnt   s16(1) 10    8     T3   DualSpurVermict.UT   Cnt   s16(1) 10    9     T4   DualSpurVermict.UT   Cnt   s16(1) 10    9     T5   DualSpurVermict.UT   Cnt   s16(1) 10    9     T6   DualSpurVermict.UT   Cnt   s16(1) 10    9     T7   DualSpurVermict.UT   Cnt   s16(1) 10    9     T8   DualSpurVermict.UT   Cnt   s16(1) 10		
12		
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 2 T2_DualSpurVernierLUT_Cnt_s16[1][1] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 3 T2_DualSpurVernierLUT_Cnt_s16[1][1] 4 T2_DualSpurVernierLUT_Cnt_s16[1][1] 5 T2_DualSpurVernierLUT_Cnt_s16[1][1] 6 T2_DualSpurVernierLUT_Cnt_s16[1][1] 7 T2_DualSpurVernierLUT_Cnt_s16[1][1] 7 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][1] 10 T2_D		
12   DualSpurVemierLUT_Cnt_st6[0][16]   288   72   DualSpurVemierLUT_Cnt_st6[0][20]   324   32		
T2_DualSpurVemierLUT_Cnt_st6[0][19]  72_DualSpurVemierLUT_Cnt_st6[0][20]  72_DualSpurVemierLUT_Cnt_st6[0][21]  72_DualSpurVemierLUT_Cnt_st6[1][0]  72_DualSpurVemierLUT_Cnt_st6[1][0]  72_DualSpurVemierLUT_Cnt_st6[1][1]  72_DualSpurVemierLUT_Cnt_st6[1][3]  72_DualSpurVemierLUT_Cnt_st6[1][3]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[1][6]  76_DualSpurVemierLUT_Cnt_st6[1][6]  77_DualSpurVemierLUT_Cnt_st6[1][6]  78_DualSpurVemierLUT_Cnt_st6[1][6]  79_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  71_DualSpurVemierLUT_Cnt_st6[1][6]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[1][6]  76_DualSpurVemierLUT_Cnt_st6[1][6]  77_DualSpurVemierLUT_Cnt_st6[1][6]  78_DualSpurVemierLUT_Cnt_st6[1][6]  79_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  71_DualSpurVemierLUT_Cnt_st6[1][6]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[1][6]  76_DualSpurVemierLUT_Cnt_st6[1][6]  77_DualSpurVemierLUT_Cnt_st6[1][6]  78_DualSpurVemierLUT_Cnt_st6[1][6]  79_DualSpurVemierLUT_Cnt_st6[1][6]  70_DualSpurVemierLUT_Cnt_st6[1][6]  71_DualSpurVemierLUT_Cnt_st6[1][6]  72_DualSpurVemierLUT_Cnt_st6[1][6]  73_DualSpurVemierLUT_Cnt_st6[1][6]  74_DualSpurVemierLUT_Cnt_st6[1][6]  75_DualSpurVemierLUT_Cnt_st6[2][6]  76_DualSpurVemierLUT_Cnt_st6[2][6]  77_DualSpurVemierLUT_Cnt_st6[2][6]  78_DualSpurVemierLUT_Cnt_st6[2][6]  79_DualSpurVemierLUT_Cnt_st6[2][6]  70_DualSpurVemierLUT_Cnt_st6[2][6]  71_DualSpurVemierLUT_Cnt_st6[2][6]  72_DualSpurVemierLUT_Cnt_st6[2][6]  73_DualSpurVemierLUT_Cnt_st6[2][6]  74_DualSpurVemierLUT_Cnt_st6[2][6]  75_DualSpurVemierLUT_Cnt_st6[2][6]  76_DualSpurVemierLUT_Cnt_st6[2][6]  77_DualSpurVemierLUT_Cnt_st6[2][6]  78_DualSpurVemierLUT_Cnt_st		
T2_DualSpurVerniertUT_Cnt_s16[0] 20    324		
T2_DualSpurVerniertUT_Cnt_s16[0][21] T2_DualSpurVerniertUT_Cnt_s16[1][0] T2_DualSpurVerniertUT_Cnt_s16[1][1] T2_DualSpurVerniertUT_Cnt_s16[1][2] T2_DualSpurVerniertUT_Cnt_s16[1][3] T2_DualSpurVerniertUT_Cnt_s16[1][3] T2_DualSpurVerniertUT_Cnt_s16[1][4] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][6] T2_DualSpurVerniertUT_Cnt_s16[1][7] 6 T2_DualSpurVerniertUT_Cnt_s16[1][8] 7 T2_DualSpurVerniertUT_Cnt_s16[1][8] 7 T2_DualSpurVerniertUT_Cnt_s16[1][9] 8 T2_DualSpurVerniertUT_Cnt_s16[1][1] 9 T2_DualSpurVerniertUT_Cnt_s16[1][1] 10_DualSpurVerniertUT_Cnt_s16[1][1] 11_DualSpurVerniertUT_Cnt_s16[1][1] 12_DualSpurVerniertUT_Cnt_s16[1][1] 13_DualSpurVerniertUT_Cnt_s16[1][1] 14_DualSpurVerniertUT_Cnt_s16[1][1] 15_DualSpurVerniertUT_Cnt_s16[1][1] 16_DualSpurVerniertUT_Cnt_s16[1][1] 17_DualSpurVerniertUT_Cnt_s16[1][1] 18_DualSpurVerniertUT_Cnt_s16[2][1] 19_DualSpurVerniertUT_Cnt_s16[2][1] 10_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 13_DualSpurVerniertUT_Cnt_s16[2][1] 14_DualSpurVerniertUT_Cnt_s16[2][1] 15_DualSpurVerniertUT_Cnt_s16[2][1] 16_DualSpurVerniertUT_Cnt_s16[2][1] 17_DualSpurVerniertUT_Cnt_s16[2][1] 18_DualSpurVerniertUT_Cnt_s16[2][1] 19_DualSpurVerniertUT_Cnt_s16[2][1] 10_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 11_DualSpurVerniertUT_Cnt_s16[2][1] 12_DualSpurVerniertUT_Cnt_s16[2][1] 13_DualSpurVerniert		
T2_DualSpurVernierLUT_Cnt_s16[1][0]   9		
T2_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][3] 12_DualSpurVernierLUT_Cnt_s16[1][4] 13_DualSpurVernierLUT_Cnt_s16[1][6] 14_DualSpurVernierLUT_Cnt_s16[1][6] 15_DualSpurVernierLUT_Cnt_s16[1][6] 16_DualSpurVernierLUT_Cnt_s16[1][6] 17_DualSpurVernierLUT_Cnt_s16[1][8] 17_DualSpurVernierLUT_Cnt_s16[1][8] 17_DualSpurVernierLUT_Cnt_s16[1][8] 18_DualSpurVernierLUT_Cnt_s16[1][8] 19_DualSpurVernierLUT_Cnt_s16[1][8] 10_DualSpurVernierLUT_Cnt_s16[1][1] 10_DualSpurVernierLUT_Cnt_s16[1][1] 11_DualSpurVernierLUT_Cnt_s16[1][1] 11_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[2][2] 13_DualSpurVernierLUT_Cnt_s16[2][2] 14_DualSpurVernierLUT_Cnt_s16[2][2] 15_DualSpurVernierLUT_Cnt_s16[2][2] 16_DualSpurVernierLUT_Cnt_s16[2][2] 17_DualSpurVernierLUT_Cnt_s16[2][2] 18_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[2][2] 19_DualSpurVernierLUT_Cnt_s16[	T2_DualSpurVernierLUT_Cnt_s16[0][21]	
T2_DualSpurVerniert_UT_Cnt_s16[1][2]	T2_DualSpurVernierLUT_Cnt_s16[1][0]	
T2_DualSpurVerniert.UT_Cnt_s16[1][3]  12_DualSpurVerniert.UT_Cnt_s16[1][4]  3	T2_DualSpurVernierLUT_Cnt_s16[1][1]	
T2_DualSpurVernierLUT_Cnt_s16[1][4]   3   4   72_DualSpurVernierLUT_Cnt_s16[1][6]   5   5   72_DualSpurVernierLUT_Cnt_s16[1][6]   5   6   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][8]   7   72_DualSpurVernierLUT_Cnt_s16[1][9]   8   8   7   72_DualSpurVernierLUT_Cnt_s16[1][10]   9   72_DualSpurVernierLUT_Cnt_s16[1][11]   0   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][16] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 10 T2_DualSpurVernierLUT_Cnt_s16[2][2] 9 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 7 T2_DualSpurVernierLUT_Cnt_s16[2][6] 8	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][6]  72_DualSpurVernierLUT_Cnt_s16[1][8]  72_DualSpurVernierLUT_Cnt_s16[1][8]  72_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][12]  1  T2_DualSpurVernierLUT_Cnt_s16[1][13]  2  T2_DualSpurVernierLUT_Cnt_s16[1][14]  3  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][17]  6  T2_DualSpurVernierLUT_Cnt_s16[1][18]  7  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][20]  7  T2_DualSpurVernierLUT_Cnt_s16[1][21]  0  T2_DualSpurVernierLUT_Cnt_s16[1][21]  10  T2_DualSpurVernierLUT_Cnt_s16[2][1]  11  T2_DualSpurVernierLUT_Cnt_s16[2][2]  2  T2_DualSpurVernierLUT_Cnt_s16[2][3]  3  T2_DualSpurVernierLUT_Cnt_s16[2][4]  4  T2_DualSpurVernierLUT_Cnt_s16[2][6]  5  T2_DualSpurVernierLUT_Cnt_s16[2][6]  6  T2_DualSpurVernierLUT_Cnt_s16[2][6]  7  T2_DualSpurVernierLUT_Cnt_s16[2][6]  7  T2_DualSpurVernierLUT_Cnt_s16[2][6]  8	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][7]  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][12]  12_DualSpurVernierLUT_Cnt_s16[1][12]  12_DualSpurVernierLUT_Cnt_s16[1][14]  12_DualSpurVernierLUT_Cnt_s16[1][15]  4  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][16]  5  T2_DualSpurVernierLUT_Cnt_s16[1][17]  6  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][19]  8  T2_DualSpurVernierLUT_Cnt_s16[1][20]  9  T2_DualSpurVernierLUT_Cnt_s16[1][20]  9  T2_DualSpurVernierLUT_Cnt_s16[1][21]  10_DualSpurVernierLUT_Cnt_s16[2][0]  10_DualSpurVernierLUT_Cnt_s16[2][1]  11_DualSpurVernierLUT_Cnt_s16[2][1]  12_DualSpurVernierLUT_Cnt_s16[2][1]  12_DualSpurVernierLUT_Cnt_s16[2][2]  12_DualSpurVernierLUT_Cnt_s16[2][3]  12_DualSpurVernierLUT_Cnt_s16[2][6]  13_DualSpurVernierLUT_Cnt_s16[2][6]  14_DualSpurVernierLUT_Cnt_s16[2][6]  15_DualSpurVernierLUT_Cnt_s16[2][6]  16_DualSpurVernierLUT_Cnt_s16[2][6]  17_DualSpurVernierLUT_Cnt_s16[2][6]  18_DualSpurVernierLUT_Cnt_s16[2][6]  19_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]  10_DualSpurVernierLUT_Cnt_s16[2][6]	T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVemiert.UT_Cnt_s16[1][8] 7 T2_DualSpurVemiert.UT_Cnt_s16[1][9] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][10] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][12] 1 T2_DualSpurVemiert.UT_Cnt_s16[1][13] 2 T2_DualSpurVemiert.UT_Cnt_s16[1][14] 3 T2_DualSpurVemiert.UT_Cnt_s16[1][15] 4 T2_DualSpurVemiert.UT_Cnt_s16[1][16] 5 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][20] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][2] 2 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 3 T2_DualSpurVemiert.UT_Cnt_s16[2][4] 4 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 5 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 6 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemier.UT_Cnt_s16[2][7] 7	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
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T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 2 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
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T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][16]       5         T2_DualSpurVernierLUT_Cnt_s16[1][17]       6         T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][6]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6		
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T2_DualSpurVernierLUT_Cnt_s16[1][14]       3         T2_DualSpurVernierLUT_Cnt_s16[1][16]       4         T2_DualSpurVernierLUT_Cnt_s16[1][16]       5         T2_DualSpurVernierLUT_Cnt_s16[1][17]       6         T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
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T2_DualSpurVernierLUT_Cnt_s16[1][18]       7         T2_DualSpurVernierLUT_Cnt_s16[1][20]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
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T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8		
T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8	T2_DualSpurVernierLUT_Cnt_s16[2][5]	
T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] 9	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] 10	T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] 0	T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] 1	T2_DualSpurVernierLUT_Cnt_s16[2][12]	1





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2 DualSpurVernierLUT Cnt s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_\$10[3][10] T2_DualSpurVernierLUT_Cnt_\$16[3][11]	1		
	3		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][16]			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13 15		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	21		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	1		
k_SelectFromColumn_Cnt_lgc			
k_SkipStepErrDiag_Cnt_str.Threshold	87 0		
k_SkipStepErrDiag_Cnt_str.PStep	20		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	17		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	73.6750493		
k_VernCorrErrorThresh_Deg_f32	824.5773324		
k_VernOORangeThresh_Deg_f32	1		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	117.9909339		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	208.2439033		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		Cot Igo	
tgt_Rte_inst_sa_bigColPs.bigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	Evnested Value	Danul!
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1089.93457	1089.934589 ± 0.00048828125	<b>V</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>Y</b>
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	~

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1089.93457	1089.934589 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	~
tgt DigColPs Per2 TrimComp Cnt Igc.value	1	1	<b>✓</b>



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>~</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>~</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.81 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
DigColPsInt_GetCustData()	205
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	214
DigColPs_ColTrimStatic_Deg_M_f32	169.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	21375
DigColPs_I2CHwColAngle_Deg_M_f32	77.52818984
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	38191
DigColPs_I2CHwSpurAngle_Deg_M_f32	87.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205
DigColPs_SpurTrimStatic_Deg_M_f32	87.2
DigColPs_TrimCompStatic_Cnt_M_u16	2788
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][3]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpur/orpierLUT_Cnt_s16[1][5]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][7] T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][19] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
	2		
T2_DualSpurVernierLUT_Cnt_s16[3][1]			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k VernCorrErrorThresh Deg f32	90.55352902		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32			
	77.52818984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	357.6556342		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2646		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	<b>→</b>
Di O ID IDOU O IA I E Ti D IA 00	i		_

981.818176

3

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08

981.8181818 ± 0.00048828125

3

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Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	987.928223	987.9281898 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.82 (Repeat Count = 1) Name	Innut Value
	Input Value
DigColPsInt_GetCustData()	196
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	173.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	27081
DigColPs_I2CHwColAngle_Deg_M_f32	76.6514684
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49055
DigColPs_I2CHwSpurAngle_Deg_M_f32	88.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	88.3
DigColPs_TrimCompStatic_Cnt_M_u16	2824
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359

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Name		
P. CASSA/VerinetUT, Cot. 54(1)  2   2   2   CASSA/VerinetUT, Cot. 54(1)  3   3   1   1   1   1   1   1   1   1		•
Tz. Colspa/winestuff. Ces. 14(1)   2   1   1   1   1   1   1   1   1   1	olSpurVernierLUT_Cnt_s16[1][0]	
12_CoSsystement_Cost_Stiglist	olSpurVernierLUT_Cnt_s16[1][1]	4
1705.ps/v/emied.UOrt_51(1)   1805.ps/v/emied.UOrt_51(1)   1905.ps/v/emied.UOrt_51(1)	olSpurVernierLUT_Cnt_s16[1][2]	3
12. Colsp. viewnet U. Dot. 510   15	olSpurVernierLUT_Cnt_s16[1][3]	2
T. COSSO/WentLUT. Ont. 910 19	olSpurVernierLUT_Cnt_s16[1][4]	1
T. COSSO/WentLUT. Ont. 910 19		0
T. CoSparvement U. Ort., strip  5		
12_colspar/venetut_D_cnt_stip  s		
T. C. OSSAV Primer L. D. C. S. 1981   1		
12. Colspay/ment LT, Onu. 14(1) 119    4     12. Colspay/ment LT, Onu. 14(1) 119    5     12. Colspay/ment LT, Onu. 14(1) 119    5     12. Colspay/ment LT, Onu. 14(1) 119    7     13. Colspay/ment LT, Onu. 14(1) 119    7     14. Colspay/ment LT, Onu. 14(1) 119    7     15. Colspay/ment LT, Onu. 14(1) 119    7     17. Colspay/ment LT, Onu. 14(1) 119    7     18. Colspay/ment LT		
T. Colsput/ment U.F. Col., 19(1)   12		
12_CoSport/emetUT_Cnt_st@[118]   2   2   2   2   2   2   2   2   2		
12_CoSput/ment_LT_Cnt_sts[1] 14    1   12_CoSput/ment_LT_Cnt_sts[1] 14    1   12_CoSput/ment_LT_Cnt_sts[1] 14    1   12_CoSput/ment_LT_Cnt_sts[1] 16    1   12_CoSput/ment_LT_Cnt_sts[2] 16    1   12_CoSput/ment_LT_Cnt_sts[2] 16    1   12_CoSput/ment_LT_Cnt_sts[2] 17    1   12_CoSput/ment_L		
T2_Colsput/venerUT_Cot_1 \$10[114]	olSpurVernierLUT_Cnt_s16[1][12]	
T. CoSSpur/emetU. Cet. s 19(1)15	olSpurVernierLUT_Cnt_s16[1][13]	2
T2_CoSput/venicUT_Cot_16(2)	olSpurVernierLUT_Cnt_s16[1][14]	1
17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   7   17_COSput	olSpurVernierLUT_Cnt_s16[1][15]	0
17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   8   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   6   17_COSput/venicUT_Ont_16[2][1]   7   17_COSput	olSpurVernierLUT_Cnt_s16[1][16]	4
TZ_COSSput/venietUT_Cnt_1602[1] 6 TZ_COSSput/venietUT_Cnt_1602[2] 6 TZ_COSSput/venietUT_Cnt_1602[2] 2 TZ_COSSput/venietUT_Cnt_1602[3] 2 TZ_COSSput/venietUT_Cnt_1602[3] 9 TZ_COSSput/venietUT_Cnt_1602[3] 9 TZ_COSSput/venietUT_Cnt_1602[3] 9 TZ_COSSput/venietUT_Cnt_1602[3] 7 TZ_COSSput/venietUT_Cnt_1602[3] 8 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 8 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1602[3] 10 TZ_COSSput/venietUT_Cnt_1603[3] 11 TZ_COSSput/venietUT_Cnt_1603[3]		0
T2_Colspur/emetUT_Cnt_s16Q13  6		
T2_Colspuvement_UT_Cot_s102[15]   4		
12, CoSpurVernierLUT, Cot., \$16(2)[5]   2		
T2_CoSpuv/emerUT_Cnt_st82[IS]		
12. CoSpurVement.UT_Cnt_s16[2][9] 9 17. CoSpurVement.UT_Cnt_s16[2][7] 7 17. CoSpurVement.UT_Cnt_s16[2][9] 5 17. CoSpurVement.UT_Cnt_s16[2][9] 3 17. CoSpurVement.UT_Cnt_s16[2][10] 1 17. CoSpurVement.UT_Cnt_s16[2][10] 1 17. CoSpurVement.UT_Cnt_s16[2][11] 10 17. CoSpurVement.UT_Cnt_s16[2][12] 8 17. CoSpurVement.UT_Cnt_s16[2][12] 8 17. CoSpurVement.UT_Cnt_s16[2][13] 6 17. CoSpurVement.UT_Cnt_s16[2][14] 4 17. CoSpurVement.UT_Cnt_s16[2][15] 2 17. CoSpurVement.UT_Cnt_s16[2][16] 10 17. CoSpurVement.UT_Cnt_s16[2][16] 10 17. CoSpurVement.UT_Cnt_s16[2][16] 11 17. CoSpurVement.UT_Cnt_s16[2][16] 15 17. CoSpurVement.UT_Cnt_s16[2][16] 15 17. CoSpurVement.UT_Cnt_s16[2][16] 15 17. CoSpurVement.UT_Cnt_s16[2][17] 12 17. CoSpurVement.UT_Cnt_s16[2][17] 13 17. CoSpurVement.UT_Cnt_s16[2][17] 14 18. CoSpu		
17. CoSpurVemerLUT_Cnt_s16[2][7] 7. CoSpurVemerLUT_Cnt_s16[2][8] 7. CoSpurVemerLUT_Cnt_s16[2][8] 7. CoSpurVemerLUT_Cnt_s16[2][9] 7. CoSpurVemerLUT_Cnt_s16[2][1] 7. CoSpurVemerLUT_Cnt_s16[2][		
17. ColSput/emierLUT_Cnt_s16[2] 8    5     7. ColSput/emierLUT_Cnt_s16[2] 8    3     7. ColSput/emierLUT_Cnt_s16[2] 10    1     7. ColSput/emierLUT_Cnt_s16[2] 11    10     7. ColSput/emierLUT_Cnt_s16[2] 12    8     8     8     7. ColSput/emierLUT_Cnt_s16[2] 12    8     8     7. ColSput/emierLUT_Cnt_s16[2] 13    6     7. ColSput/emierLUT_Cnt_s16[2] 14    4     8     9     9     9     9     9     10     10     10     11     12     12     13     14     15     15     16     17     18     18     18     18     19     19     10     10     10     10     10     10     10     10     10     10     10     10     10     10     11     12     12     13     14     15     15     16     17     18     18     18     18     19     19     10     10     10     10     10     11     12     12     13     14     15     15     16     16     17     18     18     18     19     19     10     10     10     11     12     13     14     15     15     16     17     18     18     18     18     19     19     10     10     10     11     12     13     14     15     15     16     17     18     18     18     19     19     10     10     10     10     10     10     10     10     10     10     10     10     10     10     10     10     11     12     13     14     15     15     16     16     17     18     18     18     19     19     10     1		
T2_ColSpurVemierLUT_Cnt_s162[10]   1   1   1   1   1   1   1   1   1	olSpurVernierLUT_Cnt_s16[2][7]	
12_ColSpurVerniet.UT_Cnt_s16[2][10]   1   17_ColSpurVerniet.UT_Cnt_s16[2][11]   10   17_ColSpurVerniet.UT_Cnt_s16[2][12]   8   8   17_ColSpurVerniet.UT_Cnt_s16[2][13]   6   6   17_ColSpurVerniet.UT_Cnt_s16[2][14]   4   17_ColSpurVerniet.UT_Cnt_s16[2][14]   4   17_ColSpurVerniet.UT_Cnt_s16[2][15]   2   17_ColSpurVerniet.UT_Cnt_s16[2][16]   10   17_ColSpurVerniet.UT_Cnt_s16[2][16]   10   17_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   14   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   15   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   16   18_ColSpurVerniet.UT_Cnt_s16[3][1]   17   18_ColSpurVerniet.UT_Cnt_s16[3][1]   19   18_ColSpurVerniet	olSpurVernierLUT_Cnt_s16[2][8]	5
12, ColSpurVement.UT_Cnt_s16[2][10]   1   10   10   10   10   10   10	olSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVemierLUT_Cnt_st02[]11]   10	olSpurVernierLUT Cnt s16[2][10]	1
12 ColSpurVemiet.UT_Cnt_s16(2)[12]   8   72 ColSpurVemiet.UT_Cnt_s16(2)[15]   6   6   72 ColSpurVemiet.UT_Cnt_s16(2)[15]   4   4   72 ColSpurVemiet.UT_Cnt_s16(2)[15]   2   72 ColSpurVemiet.UT_Cnt_s16(2)[16]   10   72 ColSpurVemiet.UT_Cnt_s16(2)[16]   10   72 ColSpurVemiet.UT_Cnt_s16(2)[16]   11   72 ColSpurVemiet.UT_Cnt_s16(2)[17]   14   72 ColSpurVemiet.UT_Cnt_s16(2)[17]   14   72 ColSpurVemiet.UT_Cnt_s16(2)[17]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   8   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   8   73 ColSpurVemiet.UT_Cnt_s16(2)[18]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   15   72 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   17 ColSpurVemiet.UT_Cnt_s16(2)[18]   18 ColSpurVemiet.UT_Cnt_s16(2)[18]   19 ColSpurVemiet.UT_Cnt_s16(2)[18]   18 ColSpurVemiet.UT_Cnt_s16(2)[18]   19 Col		
12		
T2_ColSpurVemietUT_Cnt_st6[2][14]		
T2_ColSpurVernierLUT_Cnt_st6[2][16]   10		
T2_ColSpurVernierLUT_Cnt_st6[3][16]		
T2_ColSpurVernierLUT_Cnt_st6[3][0]   1   14   12_ColSpurVernierLUT_Cnt_st6[3][1]   14   14   12_ColSpurVernierLUT_Cnt_st6[3][3]   11   12_ColSpurVernierLUT_Cnt_st6[3][3]   8   12_ColSpurVernierLUT_Cnt_st6[3][4]   5   12_ColSpurVernierLUT_Cnt_st6[3][6]   2   12_ColSpurVernierLUT_Cnt_st6[3][6]   2   12_ColSpurVernierLUT_Cnt_st6[3][7]   12_ColSpurVernierLUT_Cnt_st6[3][7]   12_ColSpurVernierLUT_Cnt_st6[3][7]   12_ColSpurVernierLUT_Cnt_st6[3][8]   9   12_ColSpurVernierLUT_Cnt_st6[3][9]   6   12_ColSpurVernierLUT_Cnt_st6[3][9]   6   12_ColSpurVernierLUT_Cnt_st6[3][10]   3   12_ColSpurVernierLUT_Cnt_st6[3][10]   16_ColSpurVernierLUT_Cnt_st6[3][10]   16_ColSpurVernierLUT_Cnt_st6[3][10]   16_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   17_ColSpurVernierLUT_Cnt_st6[3][10]   18_ColSpurVernierLUT_Cnt_st6[3][10]   18_ColSpurVernierLUT_Cnt_		
T2		
T2_ColSpurVernierLUT_Cnt_s16[3][2]   11   12   20   20   20   20   20   20		
T2_ColSpurVernierLUT_Cnt_s16[3][4]   5   5   7   2   ColSpurVernierLUT_Cnt_s16[3][4]   5   7   2   ColSpurVernierLUT_Cnt_s16[3][6]   15   7   2   ColSpurVernierLUT_Cnt_s16[3][6]   15   7   7   7   7   7   7   7   7   7	olSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_st6[3][4] 5 T2_ColSpurVernierLUT_Cnt_st6[3][5] 2 T2_ColSpurVernierLUT_Cnt_st6[3][7] 15 T2_ColSpurVernierLUT_Cnt_st6[3][7] 12 T2_ColSpurVernierLUT_Cnt_st6[3][7] 12 T2_ColSpurVernierLUT_Cnt_st6[3][8] 9 T2_ColSpurVernierLUT_Cnt_st6[3][9] 6 T2_ColSpurVernierLUT_Cnt_st6[3][10] 3 T2_ColSpurVernierLUT_Cnt_st6[3][11] 16 T2_ColSpurVernierLUT_Cnt_st6[3][11] 16 T2_ColSpurVernierLUT_Cnt_st6[3][12] 13 T2_ColSpurVernierLUT_Cnt_st6[3][13] 10 T2_ColSpurVernierLUT_Cnt_st6[3][14] 7 T2_ColSpurVernierLUT_Cnt_st6[3][16] 17 T2_ColSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 17 T2_DualSpurVernierLUT_Cnt_st6[3][16] 19 T2_DualSpurVernierLUT_Cnt_st6[3][1	olSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][5]   15   15   15   15   15   15   15	olSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][6]	olSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16(3)[7] T2_ColSpurVernierLUT_Cnt_s16(3)[8] 9 T2_ColSpurVernierLUT_Cnt_s16(3)[9] 6 T2_ColSpurVernierLUT_Cnt_s16(3)[10] 3 T2_ColSpurVernierLUT_Cnt_s16(3)[11] 12_ColSpurVernierLUT_Cnt_s16(3)[11] 13_ColSpurVernierLUT_Cnt_s16(3)[12] 13_ColSpurVernierLUT_Cnt_s16(3)[12] 13_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[14] 17_ColSpurVernierLUT_Cnt_s16(3)[15] 4 T2_ColSpurVernierLUT_Cnt_s16(3)[16] 17_ColSpurVernierLUT_Cnt_s16(3)[16] 17_ColSpurVernierLUT_Cnt_s16(3)[16] 17_DualSpurVernierLUT_Cnt_s16(0)[0] 12_DualSpurVernierLUT_Cnt_s16(0)[1] 12_DualSpurVernierLUT_Cnt_s16(0)[2] 12_DualSpurVernierLUT_Cnt_s16(0)[3] 12_DualSpurVernierLUT_Cnt_s16(0)[6] 12_DualSpurVernierLUT_Cnt_s16(0)[10] 12_DualSpurVernierLUT_Cnt_s16(0)[10	olSpurVernierLUT_Cnt_s16[3][5]	2
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T2_DualSpurVernierLUT_Cnt_s16[0][1] -360  T2_DualSpurVernierLUT_Cnt_s16[0][2] -324  T2_DualSpurVernierLUT_Cnt_s16[0][3] -288  T2_DualSpurVernierLUT_Cnt_s16[0][4] -252  T2_DualSpurVernierLUT_Cnt_s16[0][5] -216  T2_DualSpurVernierLUT_Cnt_s16[0][6] -180  T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][11] 7  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		-396
T2_DualSpurVernierLUT_Cnt_s16[0][2] -324  T2_DualSpurVernierLUT_Cnt_s16[0][3] -288  T2_DualSpurVernierLUT_Cnt_s16[0][4] -252  T2_DualSpurVernierLUT_Cnt_s16[0][5] -216  T2_DualSpurVernierLUT_Cnt_s16[0][6] -180  T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288  T2_DualSpurVernierLUT_Cnt_s16[0][4] -252  T2_DualSpurVernierLUT_Cnt_s16[0][5] -216  T2_DualSpurVernierLUT_Cnt_s16[0][6] -180  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][6] -180  T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	ualSpurVernierLUT_Cnt_s16[0][9]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72         T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216	ualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	ualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	ualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][14]       108         T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][15]       144         T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216		
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216		
T2 DualSpur/ornigrI LT Cnt c16(0)(19)		
	ualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288		
T2_DualSpurVernierLUT_Cnt_s16[0][20] 324	ualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360	ualSpurVernierLUT_Cnt_s16[0][21]	360





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][41]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T3_DualSpurVernierLUT_Cnt_s16[3][42]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T0_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	160
k_SkipStepErrDiag_Cnt_str.PStep	23
k_SkipStepErrDiag_Cnt_str.NStep	16
k_VernCorrErrorDiag_Cnt_str.Threshold	82
k_VernCorrErrorDiag_Cnt_str.PStep	43

DigColPs\_Per2

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Input Value k\_VernCorrErrorThresh\_Deg\_f32 16.35241604 k\_VernOORangeThresh\_Deg\_f32 1800 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 76.6514684 336.2350776 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 3059 tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum$ tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL tgt\_Pim\_DigColPsEOL

19C-110C-04_5190011 011 1111_5190011 02-02	tgt_:b.goo 0202	19.25.1966 62-62			
Name	Actual Value	Expected Value	Result		
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	<b>✓</b>		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	<b>✓</b>		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>		
DigColPs_PrevColPos_Deg_M_f32	982.951477	982.9514684 ± 0.0001220703125	<b>✓</b>		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	<b>✓</b>		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>		
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>~</b>		
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>		
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>~</b>		
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~		
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	82.9514771	82.9514684 ± 0.00009	<b>✓</b>		
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>~</b>		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>~</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.83 (Repeat Count = 1)	<u></u>
Name	Input Value
DigColPsInt_GetCustData()	128
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	177.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	10005
DigColPs_I2CHwColAngle_Deg_M_f32	152.7639936
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29915
DigColPs_I2CHwSpurAngle_Deg_M_f32	89.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	89.4
DigColPs_TrimCompStatic_Cnt_M_u16	2860
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

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N	Invest Malice
Name	Input Value 32
T2_ColSpurVernierLUT_Cnt_s16[0][6] T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9] T0_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12] T3_ColSpurVernierLUT_Cnt_s16[1][13]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1 14
T2_ColSpurVernierLUT_Cnt_s16[3][1]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2] T3_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	
	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-180 -144 -108
	-180 -144

DigColPs\_Per2





Input Value
0
36
72
108
144
180
216
252
288
324
360 9
0
1
2
3
4
5
6
7
8
9
0
1
2
3
4
5
6
7
8
9
0
0
1
2
3 4
5
6
7
8
9
10
0
1
2
3
4
5
6
7
8
9
10
22
2
4
6
8
10
12
14 16
16
18 20
1
1
3
3 5
3 5 7
3 5





Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	0			
k_SkipStepErrDiag_Cnt_str.Threshold	125			
k_SkipStepErrDiag_Cnt_str.PStep	10			
k_SkipStepErrDiag_Cnt_str.NStep	38			
k_VernCorrErrorDiag_Cnt_str.Threshold	64			
k_VernCorrErrorDiag_Cnt_str.PStep	8			
k_VernCorrErrorDiag_Cnt_str.NStep	11			
k_VernCorrErrorThresh_Deg_f32	78.40277648			
k_VernOORangeThresh_Deg_f32	547.3349351			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	152.7639936			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.24033874			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsl	PosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	~	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	<b>✓</b>	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~	
DigColPs_PrevColPos_Deg_M_f32	334.963989	334.9639936 ± 0.0001220703125	~	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	~	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	<b>✓</b>	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-572.727295	-572.7272727 ± 0.0009	<b>✓</b>	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.84 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_ColTrimStatic_Deg_M_f32	190.1	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	58760	
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972	
DigColPs_I2CHwSpurAngle_Deg_M_f32	92.7	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	421.9525396	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	
DigColPs_SpurParityError_Cnt_M_lgc	0	





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	92.7
DigColPs_TrimCompStatic_Cnt_M_u16	2968
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLOT_Cnt_s10[0][10] T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLOT_Cht_s16[1][0] T2_ColSpurVernierLUT_Cht_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2 ColSpurVernierLUT Cnt s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
	1
T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duanopui veitiieleu i Ott 510[3][3]	10

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			= 10510
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.143258		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPe	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPe	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	-
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	-
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	-
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	647.932129	647.9321395 ± 0.0001220703125	-
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	-
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	-

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

-252.067871

0

-252.0678605 ± 0.0009

0

Test Step 2.85 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	194.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432

DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

 $\label{tgt_digColPs_Per2_I2CHwAbsPos_HwDeg_f32.value} $$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value $$ tgt_Dig$ 





Name	Input Value
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	93.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	93.8
DigColPs_TrimCompStatic_Cnt_M_u16	3004
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2 ColSpurVernierLUT Cnt s16[0][6]	32
T2 ColSpurVernierLUT Cnt s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
	196
T2_ColSpurVernierLUT_Cnt_s16[0][11]	229
T2_ColSpurVernierLUT_Cnt_s16[0][12]	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2 ColSpurVernierLUT Cnt s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_\$16[2][3] T2_ColSpurVernierLUT_Cnt_\$16[2][4]	2
	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2]	11

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2 DualSpurVernierLUT Cnt s16[0][9]	-106 -72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2 DualSpurVernierLUT Cnt s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][14]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2_DualSpurVernierLUT_Cnt_s16[1][18]	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
	l'





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt Pim DigColPsEOL.ColTrim Deg f32	274.3637406		
tgt Pim DigColPsEOL.SpurTrim Deg f32	88.88743997		
tgt Pim DigColPsEOL.TrimComp Cnt u16	797		
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid Cnt lgc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt DigColPs Per2 MecState Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Ci	_	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	Nesul
DigColPs I2CHwColAngleForTrim Deg M f32	1145.45447	1145.454545 ± 0.00048828125	
DigColPs I2CHwColAngleFolThiil_Deg_in_i32  DigColPs I2CHwTrimTransCnts Uls M u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1160.1637	1160.163741 ± 0.0001220703125	
DIGOUIT S_TIEVOUITUS_DEG_IVI_I32	1100.1037	1100.103741 ± 0.0001220703125	

9	-9-C		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1160.1637	1160.163741 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	260.163696	260.1637406 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~



Test Step 2.86 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs ColSensorFaultAcc Cnt M u16	101
DigColPs_ColTrimStatic_Deg_M_f32	198.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.4548138
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	94.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	94.9
DigColPs_TrimCompStatic_Cnt_M_u16	3040
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0] T3_ColSpurVernierLUT_Cnt_s16[2][1]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1] T3_ColSpurVernierLUT_Cnt_s18[2][2]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s16[2][2]	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	0 36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T3_DualSpurVernierLUT_Cnt_s16[1][11]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][13] T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][14]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
	3   4

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10 22		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][1]			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_S16[3][6]	12		
T2_DualSpurVernierLUT_Crit_\$10[3][0] T2_DualSpurVernierLUT_Crit_\$16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.1102527		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwI		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enur		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1450.4314	1450.431485 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value			

550.431396

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_Igc.value

550.4314854 ± 0.0009



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.87 (Repeat Count = 1)	
Name	Input Value
	124
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	-360
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	96
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	96
DigColPs_TrimCompStatic_Cnt_M_u16	3076
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
	98
T2_ColSpurVernierLUT_Cnt_s16[0][8]	130
T2_ColSpurVernierLUT_Cnt_s16[0][9]	
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
12_00/0put vernierE01_0nt_5 ro[1][14]	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 17:31:16+0530





Input Value	
T2_DualSpurVerniert.UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert.UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert.UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert.UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert.UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert.UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert.UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert.UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert.UT_Cnt_s16[2][1] 1 T2_DualSpurVerniert.UT_Cnt_s16[2][2] 2 T2_DualSpurVerniert.UT_Cnt_s16[2][3] 3 T2_DualSpurVerniert.UT_Cnt_s16[2][4] 4 T2_DualSpurVerniert.UT_Cnt_s16[2][6] 5 T2_DualSpurVerniert.UT_Cnt_s16[2][6] 6 T2_DualSpurVerniert.UT_Cnt_s16[2][6] 7 T2_DualSpurVerniert.UT_Cnt_s16[2][9] 9 T2_DualSpurVerniert.UT_Cnt_s16[2][9] 9 T2_DualSpurVerniert.UT_Cnt_s16[2][9] 9 T2_DualSpurVerniert.UT_Cnt_s16[2][10] 10 T2_DualSpurVerniert.UT_Cnt_s16[2][11] 0 T2_DualSpurVerniert.UT_Cnt_s16[2][11] 0 T2_DualSpurVerniert.UT_Cnt_s16[2][11] 0 T2_DualSpurVerniert.UT_Cnt_s16[2][12] 1 T2_DualSpurVerniert.UT_Cnt_s16[2][12] 1 T2_DualSpurVerniert.UT_Cnt_s16[2][13] 2	
T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][8] 9 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1	
T2_DualSpurVernierLUT_Cnt_s16[1][19]       8         T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[1][20]       9         T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[1][21]       0         T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][0]       0         T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][1]       1         T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][2]       2         T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][3]       3         T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][4]       4         T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][5]       5         T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][6]       6         T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][7]       7         T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][8]       8         T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][9]       9         T2_DualSpurVernierLUT_Cnt_s16[2][10]       10         T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][11]       0         T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][12]       1         T2_DualSpurVernierLUT_Cnt_s16[2][13]       2	
T2_DualSpurVernierLUT_Cnt_s16[2][13] 2	
T2 DualSpurVernierLUT Cnt s16[2][14] 3	
T2_DualSpurVernierLUT_Cnt_s16[2][15] 4	
T2_DualSpurVernierLUT_Cnt_s16[2][16] 5	
T2_DualSpurVernierLUT_Cnt_s16[2][17] 6	
T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	
T2_DualSpurVernierLUT_Cnt_s16[2][21] 9  T0_DualSpurVernierLUT_Cnt_s16[2][21] 10	
T2_DualSpurVernierLUT_Cnt_s16[3][0] 22	
T2_DualSpurVernierLUT_Cnt_s16[3][1] 2	
T2_DualSpurVernierLUT_Cnt_s16[3][2] 4	
T2_DualSpurVernierLUT_Cnt_s16[3][3] 6	
T2_DualSpurVernierLUT_Cnt_s16[3][4] 8	
T2_DualSpurVernierLUT_Cnt_s16[3][5] 10	
T2_DualSpurVernierLUT_Cnt_s16[3][6] 12	
T2_DualSpurVernierLUT_Cnt_s16[3][7] 14	
T2_DualSpurVernierLUT_Cnt_s16[3][8] 16	
T2_DualSpurVernierLUT_Cnt_s16[3][9] 18	
T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T3_DualSpurVernierLUT_Cnt_s16[3][10] 4	
T2_DualSpurVernierLUT_Cnt_s16[3][11] 1  T3_DualSpurVernierLUT_Cnt_s16[3][11] 2	
T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5	
T2_DualSpurVernierLUT_Cnt_s16[3][14] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	
T2 DualSpurVernierLUT Cnt s16[3][16] 11	
T2_DualSpurVernierLUT_Cnt_s16[3][17] 13	
T2_DualSpurVernierLUT_Cnt_s16[3][18] 15	
T2_DualSpurVernierLUT_Cnt_s16[3][19] 17	
T2_DualSpurVernierLUT_Cnt_s16[3][20] 19	
T2_DualSpurVernierLUT_Cnt_s16[3][21] 21	
k_SelectFromColumn_Cnt_lgc 1	
k_SkipStepErrDiag_Cnt_str.Threshold 160	
k_SkipStepErrDiag_Cnt_str.PStep 23	
k_SkipStepErrDiag_Cnt_str.NStep 16	
k_VernCorrErrorDiag_Cnt_str.Threshold 82	
k_VernCorrErrorDiag_Cnt_str.PStep 43 k_VernCorrErrorDiag_Cnt_str.NStep 12	
k_VernCorrErrorDiag_Cnt_str.NStep 12 k_VernCorrErrorThresh_Deg_f32 16.35241604	
k_VernOORangeThresh_Deg_f32 16.35241604 k_VernOORangeThresh_Deg_f32 106.1935596	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 347.8614647	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 210.7976598	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 3059	
tgt_Rte_Inst_Sa_DigColPs.DigColPs.Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL	
Name Actual Value Expected Value	
DigColPs_HwAVernCorrFault_Cnt_M_lgc 0	Result

1636.36353

2

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

 ${\tt DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08}$ 

1636.363636 ± 0.00048828125

2





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1787.86145	1787.861465 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	887.86145	887.8614647 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Name	Input Value
DigColPsInt_GetCustData()	127
	1
DigColPs_ColParityError_Cnt_M_lgc DigColPs ColSensorFaultAcc Cnt M u16	105
bigColPs_ColTrimStatic_Deg_M_f32	360
igColPs_Continustatic_Deg_ivi_is2 igColPs_HwAVernCorrFault_Cnt_M_lgc	0
igColPs_HwAverncorrFault_Crit_in_igc	0
ligColPs_I2CHwColAngle_Cnt_M_u16	14286
igColPs_I2CHwColAngle_Cnt_iv_u16	298.7894
igColPs_I2CHwDataType_Cnt_M_u08	2
igColPs I2CHwSpurAngle Cnt M u16	18921
igColPs I2CHwSpurAngle Deg M f32	97.1
igColPs_I2CHwTrimTransCnts_Uls_M_u08	4
igColPs_I2CSensCommFlts_Cnt_M_u08	13
igColPs I2CSpurSensorFault Cnt M Igc	1
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
igColPs PrevColPos Deg M f32	814.3879313
igColPs_PrevVernierLevelNo_Cnt_M_u08	3
igColPs SkipStepFltDetectAcc Cnt M u16	13
igColPs_SpurParityError_Cnt_M_lgc	0
igColPs_SpurSensorFaultAcc_Cnt_M_u16	127
igColPs SpurTrimStatic Deg M f32	97.1
igColPs TrimCompStatic Cnt M u16	3112
igColPs_VernCorrDetectAcc_Cnt_M_u16	12
igColPs_VernierAngleOORange_Cnt_M_lgc	1
te_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2 ColSpurVernierLUT Cnt s16[0][3]	-66
2 ColSpurVernierLUT Cnt s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2 ColSpurVernierLUT Cnt s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2 ColSpurVernierLUT Cnt s16[0][14]	294
2 ColSpurVernierLUT Cnt s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
z_colspurVernierL0T_cnt_s16[0][10] 2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_S16[1][1] 2 ColSpurVernierLUT Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][2]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][7]	
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
	12
T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
	-252
T2_DualSpurVernierLUT_Cnt_s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
Lit Thiopship (orminal III Cet of 6[0][20]	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	360 9 0
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	360 9





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3
T2_DualSpurVernierLUT_Cnt_s16[2][14]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	125
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	38
k_VernCorrErrorDiag_Cnt_str.Threshold	64
k_VernCorrErrorDiag_Cnt_str.PStep	8
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32	78.40277648
k_VernOORangeThresh_Deg_f32	547.3349351
tat DiaColDo Dor? MooState Cat onum value	0
tgt_DigColPs_Per2_MecState_Cnt_enum.value	





Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	658.789429	658.7894 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

Test Step 2.89 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	106
DigColPs_ColTrimStatic_Deg_M_f32	180.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	29294
DigColPs_I2CHwColAngle_Deg_M_f32	199.9994296
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49318
DigColPs_I2CHwSpurAngle_Deg_M_f32	98.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1048.767936
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	98.2
DigColPs_TrimCompStatic_Cnt_M_u16	3148
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2 ColSpurVernierLUT Cnt s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T0_ColOpurVernierLUT_Cnt_s16[3][1]	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
	0 36 72

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T. D. Designation and LTJ. Cell. 5 (1915)   144   148   149   14	Namo	Input Value
12_DasSparvemental_Cot_stoligits	Name	Input Value
T. DuaSquiverment_T. Co., 149(07)   26		
T2. DusSporkerentU. Cot. 3100179 T3. DusSporkerentU. Cot. 3100179 T4. DusSporkerentU. Cot. 3100179 T4. DusSporkerentU. Cot. 3100179 T5. DusSporkerentU. Cot. 3100179		
T2_Dustport/emed_TU_Crt_s100[19]   288    -T2_Dustport/emed_TU_Crt_s100[29]   294    -T2_Dustport/emed_TU_Crt_s100[29]   294    -T2_Dustport/emed_TU_Crt_s100[29]   300    -T2_Dustport/emed_TU_Crt_s100[29]   9    -T2_Dustport/emed_TU_Crt_s100[29]   9    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   1    -T2_Dustport/emed_TU_Crt_s100[29]   5    -T2_Dustport/emed_TU_Crt_s100[29]   5    -T2_Dustport/emed_TU_Crt_s100[29]   7    -T2_Dustport/emed_TU_Crt_s100[29]   7    -T2_Dustport/emed_TU_Crt_s100[29]   7    -T2_Dustport/emed_TU_Crt_s100[29]   9    -T2_Dustport/emed_TU_Crt_s10		
T2_Dustport/emicUT_OL_st 90(91)		
T2_DusSpurVermed.U_Dus_14(0)[23]   T2_DusSpurVermed.U_Dus_14(0)[23]   T2_DusSpurVermed.U_Dus_14(0)[3]   T2		
T2_DusSprivement_U_Cnt_stqUp		
12_Dustpar/ment U_Cnt_sto[1]		
T2. DuslSpurVerneUT. Cnt.; 16(1)[2] 17. DuslSpurVerneUT. Cnt.; 16(1)[2] 17. DuslSpurVerneUT. Cnt.; 16(1)[3] 17. DuslSpurVerneUT. Cnt.; 16(1)[4] 18. DuslSpurVerneUT. Cnt.; 16(1)[4] 19. DuslSpurVerneUT. Cnt.; 16(1)[6] 19. DuslSpurVerneUT. Cnt.; 16(	T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DusSparvementU_Cor_16(9)  3   2   72_DusSparvementU_Cor_16(9)  3   2   72_DusSparvementU_Cor_16(9)  3   3   72_DusSparvementU_Cor_16(9)  4   3   72_DusSparvementU_Cor_16(9)  6   72_DusSparvementU_Cor_16(9)  6   72_DusSparvementU_Cor_16(9)  7   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DasSgov/Period UT_Ord_19(1)	T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
TP_DasSpawYene(UT_CM_16(1)):  TP_Das	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DusSpurVermeUT_Cnt_st0[19]	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T. Q. DuaSgout/vernetU. Cm. 1 sticl 191   5   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   2. DuaSgout/vernetU. Cm. 1 sticl 191   9   7   7   7   7   7   7   7   7   7	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DasSparVerminUT_Cnt_s10[17] 6 T2_DasSparVerminUT_Cnt_s10[18] 7 T2_DasSparVerminUT_Cnt_s10[18] 9 T2_DasSparVerminUT_Cnt_s10[110] 9 T2_DasSparVerminUT_Cnt_s10[110] 9 T2_DasSparVerminUT_Cnt_s10[111] 10 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[112] 1 T2_DasSparVerminUT_Cnt_s10[114] 2 T2_DasSparVerminUT_Cnt_s10[114] 3 T2_DasSparVerminUT_Cnt_s10[116] 1 T2_DasSp	T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DusSprivement_UT_Cett_\$16(19)	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DusSparVermeUT_Cnt_stig1190 9 172_DusSparVermeUT_Cnt_stig1191 9 172_DusSparVermeUT_Cnt_stig1191 172_DusSparVermeUT_Cnt_stig	T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
12_Dust   Description   15   15   15   15   15   15   15   1	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2, DuniSpurVernict U. Fort, s16[115] 172, DuniSpurVernict U. Fort, s16[115] 172, DuniSpurVernict U. Fort, s16[115] 172, DuniSpurVernict U. Fort, s16[116] 172, DuniSpurVernict U. Fort, s16[116] 173, DuniSpurVernict U. Fort, s16[116] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 172, DuniSpurVernict U. Fort, s16[117] 173, DuniSpurVernict U. Fort, s16[117] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 172, DuniSpurVernict U. Fort, s16[117] 173, DuniSpurVernict U. Fort, s16[117] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 170, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 171, DuniSpurVernict U. Fort, s16[117] 172, DuniSpurVernict U. Fort, s16[117] 173, DuniSpurVernict U. Fort, s16[117] 174, DuniSpurVernict U. Fort, s16[117] 175, DuniSpurVernict U. Fort, s16[117] 176, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 177, DuniSpurVernict U. Fort, s16[117] 178, DuniSpurVernict U. Fort, s16[117] 179, DuniSpurVernic		8
17. DuaSgnufvemet.U. F. Cot. 19(1)(12) 12. DuaSgnufvemet.U. F. Cot. 19(1)(19) 12. DuaSgnufvemet.U. F. Cot. 19(1)(19) 13. DuaSgnufvemet.U. F. Cot. 19(1)(19) 14. DuaSgnufvemet.U. F. Cot. 19(1)(19) 15. DuaSgnufvemet.U. F. Cot. 19(1)(19) 16. T. DuaSgnufvemet.U. F. Cot. 19(1)(19) 17. DuaSgnufvemet.U. F. Cot. 19(1)(19) 18. T. DuaSgnufvemet.U. F. Cot. 19(1)(19) 19. DuaSgnuf	T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVermetU_T_Cnt_st0[1]2] 12_DualSpurVermetU_T_Cnt_st0[1]10] 12_DualSpurVermet	T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[115]  12_DualSpurVermetU_T_Cnt_st0[117]  12_DualSpurVerme		
12_DuaSpar/emetLUT_Crt_s16[1]14    12_DuaSpar/emetLUT_Crt_s16[1]17    12_DuaSpar/emetLUT_Crt_s16[1]17    12_DuaSpar/emetLUT_Crt_s16[1]17    12_DuaSpar/emetLUT_Crt_s16[1]17    13_DuaSpar/emetLUT_Crt_s16[1]18    7_DuaSpar/emetLUT_Crt_s16[1]18    7_DuaSpar/emetLUT_Crt_s16[1]28    13_DuaSpar/emetLUT_Crt_s16[1]28    14_DuaSpar/emetLUT_Crt_s16[1]28    15_DuaSpar/emetLUT_Crt_s16[1]28    16_DuaSpar/emetLUT_Crt_s16[1]28    17_DuaSpar/emetLUT_Crt_s16[2]8    18_DuaSpar/emetLUT_Crt_s16[2]8    19_DuaSpar/emetLUT_Crt_s16[2]8    10_DuaSpar/emetLUT_Crt_s16[2]8    10_DuaSpar/emetLUT_Crt_s16[2]8    11_DuaSpar/emetLUT_Crt_s16[2]8    12_DuaSpar/emetLUT_Crt_s16[2]8    12_DuaSpar/emetLUT_Crt_s16[2		
12. DualSpurVernetLUT. Cnt. 518(1)15) 12. DualSpurVernetLUT. Cnt. 518(1)17) 13. DualSpurVernetLUT. Cnt. 518(1)17) 14. DualSpurVernetLUT. Cnt. 518(1)17) 15. DualSpurVernetLUT. Cnt. 518(1)19) 18. T. DualSpurVernetLUT. Cnt. 518(1)19) 18. T. DualSpurVernetLUT. Cnt. 518(1)19) 19. T. DualSpurVernetLUT. Cnt. 518(1)21) 19. DualSpurVernetLUT. Cnt. 518(1)21) 10. DualSpurVernetLUT. Cnt. 518(1)21) 11. DualSpurVernetLUT. Cnt. 518(1)21) 12. DualSpurVernetLUT. Cnt. 518(1)21) 12. DualSpurVernetLUT. Cnt. 518(1)21) 12. DualSpurVernetLUT. Cnt. 518(1)21) 13. DualSpurVernetLUT. Cnt. 518(1)21) 14. DualSpurVernetLUT. Cnt. 518(1)21) 15. DualSpurVernetLUT. Cnt. 518(1)21) 16. DualSpurVernetLUT. Cnt. 518(1)21) 17. DualSpurVernetLUT. Cnt. 518(1)21) 18. DualSpurVernetLUT. Cnt. 518(1)21) 19. DualSpurVernetLUT. Cnt. 518(1)211 19. DualSpurVernetLUT. Cnt. 518(1)21 19. DualSpurVernetLUT. Cnt. 5		
12_DusSpurVermetUT_Cnt_st@[1]16    12_DusSpurVerm		
12_DusSpurVemetLUT_Cut_s16(1)17		
T2_DusSpurVemietUT_Cnt_st6[1]18   7   7   7   7   7   7   7   7   7		
12   DuaSpurVernictUT Cnt		
12_DuaSpurVernietUT_Cnt_s16(1) 20    12_DuaSpurVernietUT_Cnt_s16(1) 21    12_DuaSpurVernietUT_Cnt_s16(1) 21    13_DuaSpurVernietUT_Cnt_s16(2) 11    14_DuaSpurVernietUT_Cnt_s16(2) 11    15_DuaSpurVernietUT_Cnt_s16(2) 12    12_DuaSpurVernietUT_Cnt_s16(2) 13    13_DuaSpurVernietUT_Cnt_s16(2) 13    14_DuaSpurVernietUT_Cnt_s16(2) 15    15_DuaSpurVernietUT_Cnt_s16(2) 15    15_DuaSpurVernietUT_Cnt_s16(2) 17    17_DuaSpurVernietUT_Cnt_s16(2) 17    17_DuaSpurVernietUT_Cnt_s16(2) 17    18_DuaSpurVernietUT_Cnt_s16(2) 19    19_DuaSpurVernietUT_Cnt_s16(2) 19    19_DuaSpurVernietUT_Cnt_s16(2) 19    10_DuaSpurVernietUT_Cnt_s16(2) 11    10_DuaSpurVernietUT_Cnt_s16(2) 11    10_DuaSpurVernietUT_Cnt_s16(2) 11    10_DuaSpurVernietUT_Cnt_s16(2) 11    11_DuaSpurVernietUT_Cnt_s16(2) 11    12_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 13    13_DuaSpurVernietUT_Cnt_s16(2) 13    14_DuaSpurVernietUT_Cnt_s16(2) 13    15_DuaSpurVernietUT_Cnt_s16(2) 13    16_DuaSpurVernietUT_Cnt_s16(2) 13    17_DuaSpurVernietUT_Cnt_s16(2) 13    18_DuaSpurVernietUT_Cnt_s16(2) 13    19_DuaSpurVernietUT_Cnt_s16(2) 13    10_DuaSpurVernietUT_Cnt_s16(2) 13    10_DuaSpurVernietUT_Cnt_s16(2) 13    11_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 13    13_DuaSpurVernietUT_Cnt_s16(2) 13    14_DuaSpurVernietUT_Cnt_s16(2) 13    15_DuaSpurVernietUT_Cnt_s16(2) 13    16_DuaSpurVernietUT_Cnt_s16(2) 13    17_DuaSpurVernietUT_Cnt_s16(2) 13    18_DuaSpurVernietUT_Cnt_s16(2) 13    19_DuaSpurVernietUT_Cnt_s16(2) 13    10_DuaSpurVernietUT_Cnt_s16(2) 13    11_DuaSpurVernietUT_Cnt_s16(2) 13    12_DuaSpurVernietUT_Cnt_s16(2) 13    13_DuaSpurVernietUT_Cnt_s16(2) 13    14_DuaSpurVernietUT_Cnt_s16(2) 13    15_DuaSpurVernietUT_Cnt_s16(2) 13    16_DuaSpurVernietUT_Cnt_s16(2) 13    17_DuaSpurVernietUT_Cnt_s16(2) 13    18_DuaSpurVernietUT_Cnt_s16(2) 13    19_DuaSpurVernietUT_Cnt_s16(2) 13    10_DuaSpurVernietUT_Cnt_s16(2) 13    11_DuaSpu		
12		
T2_DualSpurVernierLUT_Cnt_s16[2][0]   0   1   1   1   1   1   1   1   1   1		
T2		
T2_DualSpurVemierLUT_Cnt_s16[2][3]   2   2   2   2   2   2   2   2   2		
T2 DualSpurVermierLUT_Cnt_s16[2][4]   4		
T2   DualSpurVernierLUT_Cnt_s16[2][4]   5   5   5   5   5   5   5   5   5		
T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][8] 9 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][16] 4 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 2 T2_DualSpurVernierLUT_Cnt_s16[3][16] 10 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11		
T2 DualSpurVernierLUT_Cnt_st6[2][5]   6   6   7   2 DualSpurVernierLUT_Cnt_st6[2][7]   7   7   7   7   7   7   7   7   7		
T2_DualSpurVemierLUT_Cnt_s16[2][7] T2_DualSpurVemierLUT_Cnt_s16[2][8] 8 8 7 12_DualSpurVemierLUT_Cnt_s16[2][9] 9 72_DualSpurVemierLUT_Cnt_s16[2][10] 10 72_DualSpurVemierLUT_Cnt_s16[2][11] 0 72_DualSpurVemierLUT_Cnt_s16[2][12] 1 72_DualSpurVemierLUT_Cnt_s16[2][13] 1 72_DualSpurVemierLUT_Cnt_s16[2][13] 1 72_DualSpurVemierLUT_Cnt_s16[2][14] 1 72_DualSpurVemierLUT_Cnt_s16[2][16] 1 72_DualSpurVemierLUT_Cnt_s16[2][17] 1 8 72_DualSpurVemierLUT_Cnt_s16[2][17] 1 8 72_DualSpurVemierLUT_Cnt_s16[2][17] 1 8 72_DualSpurVemierLUT_Cnt_s16[2][18] 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	T2_DualSpurVernierLUT_Cnt_s16[2][5]	
T2_DualSpurVemierLUT_Cnt_s16[2][8] 8 T2_DualSpurVemierLUT_Cnt_s16[2][9] 9 T2_DualSpurVemierLUT_Cnt_s16[2][10] 10 T2_DualSpurVemierLUT_Cnt_s16[2][11] 0 T2_DualSpurVemierLUT_Cnt_s16[2][11] 0 T2_DualSpurVemierLUT_Cnt_s16[2][12] 1 T2_DualSpurVemierLUT_Cnt_s16[2][13] 2 T2_DualSpurVemierLUT_Cnt_s16[2][14] 3 T2_DualSpurVemierLUT_Cnt_s16[2][15] 4 T2_DualSpurVemierLUT_Cnt_s16[2][16] 5 T2_DualSpurVemierLUT_Cnt_s16[2][17] 6 T2_DualSpurVemierLUT_Cnt_s16[2][18] 7 T2_DualSpurVemierLUT_Cnt_s16[2][18] 7 T2_DualSpurVemierLUT_Cnt_s16[2][19] 8 T2_DualSpurVemierLUT_Cnt_s16[2][19] 8 T2_DualSpurVemierLUT_Cnt_s16[2][19] 8 T2_DualSpurVemierLUT_Cnt_s16[2][19] 10 T2_DualSpurVemierLUT_Cnt_s16[2][19] 10 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 2 T2_DualSpurVemierLUT_Cnt_s16[3][1] 10 T2_DualSpurVemierLUT_Cnt_s16[3][1] 11	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVerni	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
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T2_DualSpurVernierLUT_Cnt_s16[2][19]       8         T2_DualSpurVernierLUT_Cnt_s16[2][20]       9         T2_DualSpurVernierLUT_Cnt_s16[2][1]       10         T2_DualSpurVernierLUT_Cnt_s16[3][0]       22         T2_DualSpurVernierLUT_Cnt_s16[3][1]       2         T2_DualSpurVernierLUT_Cnt_s16[3][2]       4         T2_DualSpurVernierLUT_Cnt_s16[3][3]       6         T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][6]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11 <tr< td=""><td></td><td></td></tr<>		
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T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13		
T2_DualSpurVerniert.UT_Cnt_s16[3][0]       22         T2_DualSpurVerniert.UT_Cnt_s16[3][1]       2         T2_DualSpurVerniert.UT_Cnt_s16[3][2]       4         T2_DualSpurVerniert.UT_Cnt_s16[3][3]       6         T2_DualSpurVerniert.UT_Cnt_s16[3][4]       8         T2_DualSpurVerniert.UT_Cnt_s16[3][5]       10         T2_DualSpurVerniert.UT_Cnt_s16[3][6]       12         T2_DualSpurVerniert.UT_Cnt_s16[3][7]       14         T2_DualSpurVerniert.UT_Cnt_s16[3][8]       16         T2_DualSpurVerniert.UT_Cnt_s16[3][9]       18         T2_DualSpurVerniert.UT_Cnt_s16[3][10]       20         T2_DualSpurVerniert.UT_Cnt_s16[3][11]       1         T2_DualSpurVerniert.UT_Cnt_s16[3][12]       3         T2_DualSpurVerniert.UT_Cnt_s16[3][13]       5         T2_DualSpurVerniert.UT_Cnt_s16[3][14]       7         T2_DualSpurVerniert.UT_Cnt_s16[3][16]       11         T2_DualSpurVerniert.UT_Cnt_s16[3][16]       11         T2_DualSpurVerniert.UT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][1]       2         T2_DualSpurVernierLUT_Cnt_s16[3][2]       4         T2_DualSpurVernierLUT_Cnt_s16[3][3]       6         T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][6]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
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T2_DualSpurVernierLUT_Cnt_s16[3][3]       6         T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][5]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][5]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
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T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13		
T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13	T2_DualSpurVernierLUT_Cnt_s16[3][13]	
T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13	T2_DualSpurVernierLUT_Cnt_s16[3][14]	
T2_DualSpurVernierLUT_Cnt_s16[3][17] 13	T2_DualSpurVernierLUT_Cnt_s16[3][15]	
	T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
TO Discloud/amical LIT Oct -44001401	T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
12_DualSpurvernierLU1_Ght_s16[3][18]	T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] 17	T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] 19	T2_DualSpurVernierLUT_Cnt_s16[3][20]	19





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	191		
k_SkipStepErrDiag_Cnt_str.PStep	16		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	24		
k_VernCorrErrorDiag_Cnt_str.PStep	21		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	67.6606307		
k_VernOORangeThresh_Deg_f32	664.4244195		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	199.9994296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	301.9312882		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2922		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1459.39941	1459.39943 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	559.399414	559.3994296 ± 0.0009	•
tgt DigColPs Per2 TrimComp Cnt Igc.value	0	0	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

T4 04 0 00 (D4 04 )		-4
Test Step 2.90 (Repeat Count = 1)		•
Name	Input Value	
DigColPsInt_GetCustData()	124	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	
DigColPs_ColTrimStatic_Deg_M_f32	-82.29	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	50517	
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908	
DigColPs_I2CHwSpurAngle_Deg_M_f32	96	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	25	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	1680.342175	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124	
DigColPs_SpurTrimStatic_Deg_M_f32	96	
DigColPs_TrimCompStatic_Cnt_M_u16	3076	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13	

2014-10-14, 17:31:16+0530



Input Value
0
tgt_Rte_Inst_Sa_DigColPs
-163
-131
-99
-66 -33
-33
32
65
98
130
163
196
229
261
294
327
359
0
4
3
2
1
0
4
3
2
1
0
4
3
2
1
0
4
0
8
6
4
2
0
9
7
5
3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10 7
7
7 4
7 4 17
7 4





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T0_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpur\oracicrt LT_Cnt_s16[2][12] T3_DualSpur\oracicrt LT_Cnt_s16[2][12]	1
T2_DualSpur/ornierLUT_Cnt_s16[2][13] T2_DualSpur/ornierLUT_Cnt_s16[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
	18





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	<u>f</u> 32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M Igc	0	0	~

0	0 = 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1150.15149	1150.151465 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	250.151489	250.1514647 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Name	Input Value	
DigColPsInt GetCustData()	127	
DigColPs ColParityError Cnt M Igc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	14286	
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921	
DigColPs_I2CHwSpurAngle_Deg_M_f32	97.1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	





Name	Input Value
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	814.3879313 3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	97.1
DigColPs_TrimCompStatic_Cnt_M_u16	3112
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163 -131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5] T3_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T3_ColSpurVernierLUT_Cnt_s16[3][3]	11 8
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12





T2_ColSpurVernierLUT_Cnt_s16[3][9] T2_ColSpurVernierLUT_Cnt_s16[3][10] T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	Input Value 6 3 16 13 10 7 4 17 -396
T2_ColSpurVernierLUT_Cnt_s16[3][10] T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	3 16 13 10 7 4
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	16 13 10 7 4 17
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	13 10 7 4 17
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	10 7 4 17
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	7 4 17
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	4 17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	
	-396
T2 DualSpurVernierI LIT Cpt e16/01/11	
12_DaaroparvernierE01_Ont_510[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2 DualSpurVernierLUT Cnt s16[0][11]	0
	36
	72
	108
	144
	180
	216
	252
	288
	324
	360
	9
	0
	1
	2
	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2 DualSpurVernierLUT Cnt s16[1][17]	6
_ ' '	7
	8
	9
	0
	0
	1
	2
	3
	4
	5
	6
	7
	8
	9
	10
	0
	1
	2
	3
	4
	5
	6
	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9

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DigColPs\_Per2 Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[2][21] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][0] 22 T2\_DualSpurVernierLUT\_Cnt\_s16[3][1] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[3][2] 4 T2 DualSpurVernierLUT\_Cnt\_s16[3][3] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[3][4] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[3][5] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][6] 12 T2\_DualSpurVernierLUT\_Cnt\_s16[3][7] 14 16 T2\_DualSpurVernierLUT\_Cnt\_s16[3][8] T2\_DualSpurVernierLUT\_Cnt\_s16[3][9] 18 T2\_DualSpurVernierLUT\_Cnt\_s16[3][10] 20 T2\_DualSpurVernierLUT\_Cnt\_s16[3][11] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[3][12] T2\_DualSpurVernierLUT\_Cnt\_s16[3][13] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[3][15] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[3][16] 11 T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21 k\_SelectFromColumn\_Cnt\_lgc 0  $k\_SkipStepErrDiag\_Cnt\_str.Threshold$ 125 k\_SkipStepErrDiag\_Cnt\_str.PStep 10 k\_SkipStepErrDiag\_Cnt\_str.NStep 38  $k\_VernCorrErrorDiag\_Cnt\_str.Threshold$ 64 k\_VernCorrErrorDiag\_Cnt\_str.PStep 8  $k\_VernCorrErrorDiag\_Cnt\_str.NStep$ 11 k VernCorrErrorThresh Deg f32 78.40277648 k\_VernOORangeThresh\_Deg\_f32 547.3349351 tgt DigColPs Per2 MecState Cnt enum.value 0 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 298.7894 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 103.8339644  $tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16$ 491  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32$ tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL tgt\_Pim\_DigColPsEOL

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	658.789429	658.7894 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	<b>✓</b>
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	



Test Step 2.92 (Repeat Count = 1)	v
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	-360
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte Inst Sa DigColPs	1
T2_ColSpurVernierLUT_Cnt_s16[0][0]	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Cnt_S16[3][6] T2_ColSpurVernierLUT_Cnt_S16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18] T3_DualSpurVernierLUT_Cnt_s16[1][10]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9 0
T2 DualSnur\/arniarl LIT Cnt e16(1)(21)	
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	10
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	1 2 3
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14] T3_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.552634		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313	V. 1. 0. 1.	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cr	_	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_C tgt_Pim_DigColPsEOL	an_igc	
		Expected Value	Dage
Name  DigColPs HwAVernCorrEquit Cnt M Igs	Actual Value	Expected Value	Resul
DigCoIPs_HwAVernCorrFault_Cnt_M_lgc DigCoIPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	
DigColPs_I2CHwColAngleForTilli_Deg_M_i32  DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	
DigColPs PrevAngleDataAvailable Cnt M lgc	0	0	
DigColPs PrevColPos Deg M f32	4.37533569	4.375334609 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	1	1	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-895.624634	-895.6246654 ± 0.0009	•
tot DigColPs Per2 TrimComp Cnt lgc value	0	0	

0

0

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value



Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Γest Step 2.93 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
0igColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
ligColPs_I2CHwDataType_Cnt_M_u08	1
igColPs_I2CHwSpurAngle_Cnt_M_u16	53866
igColPs_I2CHwSpurAngle_Deg_M_f32	360
igColPs_I2CHwTrimTransCnts_Uls_M_u08	0
igColPs_I2CSensCommFlts_Cnt_M_u08	22
igColPs_I2CSpurSensorFault_Cnt_M_Igc	0
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
igColPs_PrevColPos_Deg_M_f32	321.3070593
igColPs_PrevVernierLevelNo_Cnt_M_u08	16
ligColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
igColPs_SpurParityError_Cnt_M_lgc	1
higColPs_SpurSensorFaultAcc_Cnt_M_u16	124
igColPs_SpurTrimStatic_Deg_M_f32	360
higColPs_TrimCompStatic_Cnt_M_u16	3220
igColPs_VernCorrDetectAcc_Cnt_M_u16	8
igColPs_VernierAngleOORange_Cnt_M_lgc	0
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2 ColSpurVernierLUT Cnt s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2 ColSpurVernierLUT Cnt s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2 ColSpurVernierLUT Cnt s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
	· · · · · · · · · · · · · · · · · · ·
2_ColSpurVernierLUT_Cnt_s16[1][1] 2 ColSpurVernierLUT Cnt s16[1][2]	3
_ :	2
2_ColSpurVernierLUT_Cnt_s16[1][3]	1
2_ColSpurVernierLUT_Cnt_s16[1][4]	
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
2_ColSpurVernierLUT_Cnt_s16[1][12]	3
2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2 ColSpurVernierLUT Cnt s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T3_DualSpurVernierLUT_Cnt_s16[1][2]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1423		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	nt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	<del></del>	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Pagult
		The state of the s	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•

1636.36353

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

1636.363636 ± 0.00048828125

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1649.86719	1649.867139 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>✓</b>

Manage 1	Inner Walter
Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	222.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	325.6206695
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	180.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	180.6
DigColPs_TrimCompStatic_Cnt_M_u16	3256
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
[2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2 ColSpurVernierLUT Cnt s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
72 ColSpurVernierLUT Cnt s16[0][4]	-33
Γ2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
F2_ColSpurVernierLUT_Cnt_s16[0][7]	65
F2_ColSpurVernierLUT_Cnt_s16[0][8]	98
Γ2_ColSpurVernierLUT_Cnt_s16[0][9]	130
Γ2_ColSpurVernierLUT_Cnt_s16[0][10]	163
F2_ColSpurVernierLUT_Cnt_s16[0][11]	196
[2_ColSpurVernierLUT_Cnt_s16[0][12]	229
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[0][13]	261
<sup>7</sup> 2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2 ColSpurVernierLUT Cnt s16[1][2]	3

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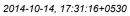


Nama	Input Value
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T3_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2 DualSpurVernierLUT Cnt s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
12 DUGIODUI VOITIOLEO I OTIL 3 IUI I II I	•

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5] T2_DualSpurVernierLUT_Cnt_s16[2][6]	5   6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2]	2 4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2 DualSpurVernierLUT Cnt s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cnt_s16[3][21]	19 21
T2_DualSpurVernierLUT_Cnt_s16[3][21] k_SelectFromColumn_Cnt_lgc	21
k_SkipStepErrDiag_Cnt_str.Threshold	99
k_SkipStepErrDiag_Ont_str.PStep	3
k_SkipStepErrDiag_Cnt_str.NStep	13
k_VernCorrErrorDiag_Cnt_str.Threshold	74
k_VernCorrErrorDiag_Cnt_str.PStep	33
k_VernCorrErrorDiag_Cnt_str.NStep	6
k_VernCorrErrorThresh_Deg_f32	78.75594592
k_VernOORangeThresh_Deg_f32	1151.771932
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2





Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwD	eg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	ı	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	822.720703	822.7206695 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-77.2792969	-77.27933046 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 2.95 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	-82.29
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	112





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpur/orpicsLUT_Cnt_s16[2][18]	
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	8 9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17



DigColPs_Per2	14, 11.51.16.0000		Razorcat
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.552634		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	364.677246	364.6772727 ± 0.00048828125	<b>✓</b>
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	364.375336	364.3753346 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigCoIPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-535.624634	-535.6246654 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

Test Step 2.96 (Repeat Count = 1)	range in the second
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	3220

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Name	Input Value
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-66 -66
T2_ColSpurVernierLUT_Cnt_s16[0][3] T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2 ColSpurVernierLUT Cnt s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T3_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3] T3_ColSpurVernierLUT_Cnt_s16[3][4]	8 5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_S16[3][7]	12
T2_ColSpurVernierLUT_Cnt_S16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2 DualSpurVernierLUT Cnt s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
	-72
T2_DualSpurVernierLUT_Cnt_s16[0][9]	
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[1][8]	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2 DualSpurVernierLUT Cnt s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][8]	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
	12
12 DuaispurvernierLOT Citt STotsilot	
T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	14 16





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1423		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHw/	AbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHw/	AbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecSta	te_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimCo	mp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M lgc	1	1	

tgr_rtte_mor_eu_bigeen en m_bigeen eller	19(_1 111_2190011 0202		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1649.86719	1649.867139 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.97 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
DigColPsInt_GetCustData()	127	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175	
DigColPs_ColTrimStatic_Deg_M_f32	227	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129	
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.1	





Name	Input Value
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9 7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	297.1
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T3_ColSpurVernierLUT_Cnt_s16[2][40]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	-
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15] T3_ColSpurVernierLUT_Cnt_s16[3][46]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	17 -396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2 DualSpurVernierLUT Cnt s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T0_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8

DigColPs\_Per2

2014-10-14, 17:31:16+0530



Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[2][20] T2\_DualSpurVernierLUT\_Cnt\_s16[2][21] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][0] 22 T2\_DualSpurVernierLUT\_Cnt\_s16[3][1] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[3][2] 4 T2\_DualSpurVernierLUT\_Cnt\_s16[3][3] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[3][4] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[3][5] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][6] 12 T2 DualSpurVernierLUT Cnt s16[3][7] 14 T2\_DualSpurVernierLUT\_Cnt\_s16[3][8] 16 T2\_DualSpurVernierLUT\_Cnt\_s16[3][9] 18 T2\_DualSpurVernierLUT\_Cnt\_s16[3][10] 20 T2 DualSpurVernierLUT Cnt s16[3][11] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[3][12] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[3][13] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[3][15] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[3][16] 11 T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21 k\_SelectFromColumn\_Cnt\_lgc 0  $k\_SkipStepErrDiag\_Cnt\_str.Threshold$ 70 k\_SkipStepErrDiag\_Cnt\_str.PStep 47 k\_SkipStepErrDiag\_Cnt\_str.NStep 44  $k\_VernCorrErrorDiag\_Cnt\_str.Threshold$ 88  $k\_VernCorrErrorDiag\_Cnt\_str.PStep$ 0 k VernCorrErrorDiag Cnt str.NStep 38 78 63725519 k\_VernCorrErrorThresh\_Deg\_f32 k VernOORangeThresh Deg f32 1720.30508 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value 115.010748 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 0.980068922 tgt Pim DigColPsEOL.SpurTrim Deg f32 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc$ tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum tot DigColPs Per2 MecState Cnt enum  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc  $tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL$ tgt\_Pim\_DigColPsEOL **Actual Value Expected Value** Result DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32 981.818176 981.8181818 ± 0.00048828125  $DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08$ DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc 0 0 DigColPs\_PrevColPos\_Deg\_M\_f32 968 010742 968.010748 ± 0.0001220703125 DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08 10  $DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08$ 4 4 DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16 0 0 DigColPs VernCorrDetectAcc Cnt M u16 0 0 DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc 1

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0

81.81818182 ± 0.00009

0

0

81.8181763

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value



Test Step 2.98 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	231.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25526
DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwDataType_Cnt_M_u08	216.7759984
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.2
DigColPs I2CHwTrimTransCnts UIs M u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	298.2
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18 0
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14] T2_ColSpurVernierLUT_Cnt_s16[0][15]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
	7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][8]	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s46[2][42]	2		
T2_DualSpur/craierLUT_Cnt_s16[2][13]	3		
T2_DualSpur/crierLUT_Cnt_s16[2][14]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2 DualSpurVernierLUT Cnt s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	99		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.5857018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn	ıt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1785.67603	1785.675998 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	885.676025	885.6759984 ± 0.0009	•
tat DiaColPs Per2 TrimComp Cnt lac value	10	10	I

0

0

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.99 (Repeat Count = 1)	
Name	Input Value
	241
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	99.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs VernierAngleOORange Cnt M lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
_ : :	229
T2_ColSpurVernierLUT_Cnt_s16[0][12]	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

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News	Insuré Value
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2 DualSpurVernierLUT Cnt s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2 DualSpurVernierLUT Cnt s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
	180
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18]	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
saa.spa. voilloite i_oiit_o i o[i][iii]	

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0] T3_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5		
T2 DualSpurVernierLUT Cnt s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20] T0_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10 22		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18] T3_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	19 21		
	0		
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k SkipStepErrDiag_Cnt_str.PStep	17		
k VernCorrErrorDiag Cnt str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k VernCorrErrorDiag Cnt str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwA	bsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecStat		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimCon	np_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	-
DigColPs I2CHwTrimTransCnts Uls M u08	3	3	<b>✓</b>

3

3

 ${\tt DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08}$ 

DigColPs\_Per2



Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	340.411285	340.4112897 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-572.727295	-572.7272727 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Name	Input Value
DigColPsInt_GetCustData()	149
ligColPs_ColParityError_Cnt_M_lgc	0
igColPs ColSensorFaultAcc Cnt M u16	103
igColPs ColTrimStatic Deg M f32	214.7
igColPs HwAVernCorrFault Cnt M lgc	0
igColPs I2CColSensorFault Cnt M Igc	0
igColPs_I2CHwColAngle_Cnt_M_u16	15468
igColPs_I2CHwColAngle_Deg_M_f32	0
igColPs_I2CHwDataType_Cnt_M_u08	1
igColPs_I2CHwSpurAngle_Cnt_M_u16	58410
igColPs I2CHwSpurAngle Deg M f32	297.1
gCoIPs I2CHwTrimTransCnts UIs M u08	5
igColPs I2CSensCommFlts Cnt M u08	23
igColPs I2CSpurSensorFault Cnt M Igc	1
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
igColPs PrevColPos Deg M f32	569.7636028
igColPs_PrevVernierLevelNo_Cnt_M_u08	11
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
igColPs_SpurParityError_Cnt_M_lgc	0
igColPs_SpurSensorFaultAcc_Cnt_M_u16	149
igColPs SpurTrimStatic Deg M f32	0
igColPs TrimCompStatic Cnt M u16	3184
igColPs_VernCorrDetectAcc_Cnt_M_u16	19
igColPs_VernierAngleOORange_Cnt_M_lgc	1
te_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2 ColSpurVernierLUT Cnt s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2 ColSpurVernierLUT Cnt s16[1][3]	2

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][7]	
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
	12
T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
	-252
T2_DualSpurVernierLUT_Cnt_s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
Lit Thioly bury (ormior) III Cat of 6[0][20]	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	360 9 0
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	360 9





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2 DualSpurVernierLUT Cnt s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T3_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10] T3_DualSpurVernierLUT_Cnt_s16[3][11]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	35
k_SkipStepErrDiag_Cnt_str.PStep	2
k_SkipStepErrDiag_Cnt_str.NStep	28
k_VernCorrErrorDiag_Cnt_str.Threshold	42
k_VernCorrErrorDiag_Cnt_str.PStep k_VernCorrErrorDiag_Cnt_str.NStep	16 18
	92.41026139
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	1413.552634
k_VernOORangeThresh_Deg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum.value	1413.552634

tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

DigColPs\_Per2

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-754.7 ± 0.0009

0

Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPer2_	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	135.045456	135.0454545 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	145.300003	145.3 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

-754.700012

0

Test Step 2.101 (Repeat Count = 1)	<u></u>
Name	Input Value
DigColPsInt GetCustData()	124
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	151
DigColPs ColTrimStatic Deg M f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	3220
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	112





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpur/orpicsLUT_Cnt_s16[2][18]	
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	8 9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17

tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc

DigColPs\_Per2

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	116
k_SkipStepErrDiag_Cnt_str.PStep	3
k_SkipStepErrDiag_Cnt_str.NStep	6
k_VernCorrErrorDiag_Cnt_str.Threshold	37
k_VernCorrErrorDiag_Cnt_str.PStep	8
k_VernCorrErrorDiag_Cnt_str.NStep	7
k_VernCorrErrorThresh_Deg_f32	84.34178925
k_VernOORangeThresh_Deg_f32	1712.165488
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt DigColPs Per2 MecState Cnt enum

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1358.22327	1358.223274 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1371.33289	1371.332861 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	458.223267	458.2232736 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	<b>✓</b>

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.102 (Repeat Count = 1)	<b>√</b>
Name	Input Value
DigColPsInt GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	0
	165
DigColPs_ColSensorFaultAcc_Cnt_M_u16	111
DigColPs_ColTrimStatic_Deg_M_f32	222.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	60.482
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	99.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	180.6
DigColPs_TrimCompStatic_Cnt_M_u16	3256
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7

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Input Value
0
tgt_Rte_Inst_Sa_DigColPs
-163
-131
-99
-66 -33
-33
32
65
98
130
163
196
229
261
294
327
359
0
4
3
2
1
0
4
3
2
1
0
4
3
2
1
0
4
0
8
6
4
2
0
9
7
5
3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10 7
7
7 4
7 4 17
7 4

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0 36
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T0_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][14]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13] T3_DualSpurVernierLUT_Cnt_s18[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
	40
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	99		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	13		
k_VernCorrErrorDiag_Cnt_str.Threshold	74		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	78.75594592		
k_VernOORangeThresh_Deg_f32	1151.771932		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosV	/alid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_	HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_	enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1272.13635	1272.136364 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•

<u> </u>	0 = 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1272.13635	1272.136364 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1277.58203	1277.582 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	377.582031	377.582 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	~

Test Step Call Trace   ✓				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.103 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	127	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175	
DigColPs_ColTrimStatic_Deg_M_f32	227	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	



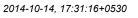


Name	Input Value
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9 7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	297.1
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2_ColSpurVernierLUT_Cnt_s16[0][1]	-163 -131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130 163
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2] T3_ColSpurVernierLUT_Cnt_s16[1][2]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3] T3_ColSpurVernierLUT_Cnt_s16[2][4]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T3_ColSpurVernierLUT_Cnt_s16[2][14]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4 17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpur/ornierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5] T3_DualSpurVernierLUT_Cnt_s18[2][6]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	0
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2 DualSpurVernierLUT Cnt s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	70		
k_SkipStepErrDiag_Cnt_str.PStep	47		
k_SkipStepErrDiag_Cnt_str.NStep	44		
k_VernCorrErrorDiag_Cnt_str.Threshold	88		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	38		
k_VernCorrErrorThresh_Deg_f32	78.63725519		
k_VernOORangeThresh_Deg_f32	1720.30508		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	115.010748		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1337.68176	1337.681818 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1328.01074	1328.010748 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	437.681763	437.6818182 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	•
Status	0x01	0x01	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>~</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~



Test Step 2.104 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	231.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25526
DigColPs_I2CHwColAngle_Deg_M_f32	216.7759984
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	298.2
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc  Pto_Inst_Sa_DigColPs	0 tat Pto Inst Sa DigColDs
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65 98
T2_ColSpurVernierLUT_Cnt_s16[0][8]	
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130 163
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2 ColSpurVernierLUT Cnt s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1

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Nama	Input Value
Name T2_ColSpurVernierLUT_Cnt_s16[2][12]	Input Value 8
T2_ColSpurVernierLUT_Cnt_S10[2][12] T2_ColSpurVernierLUT_Cnt_S10[2][13]	6
	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
12_buaiopui veriilereo 1_ont_s 10[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
	<b>4</b> 5

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DigColPs\_Per2

DigColPs_Per2			Cital
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	99		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.5857018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp	_Cnt_igc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	<b>-</b>	-
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	355.363617	355.3636364 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	345.675995	345.6759984 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-554.323975	-554.3240016 ± 0.0009	•
tgt DigColPs Per2 TrimComp Cnt Igc.value	0	0	· · · · · · · · · · · · · · · · · · ·

0

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

0



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.105 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_12CColSensorFault_Cnt_M_lgc	0
	56399
DigColPs_I2CHwColAngle_Cnt_M_u16 DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
	2 13.0112697
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	
DigColPs_I2CHwSpurAngle_Deg_M_f32	60.482
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
	163
T2_ColSpurVernierLUT_Cnt_s16[0][10]	
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Name		
12. CodesyAmment   Ch. 1991   6	Name	Input Value
12. CodesyAmment   Ch. 1991   6	T2 ColSpurVernierLUT Cnt s16[1][15]	0
17_COSQAVermentUT_CRI_STREET    0		
TCOS_AVVANISH_UTOTSTREETS		
T. Codes/whemetutt. Cet. 1908[27]   5   T. Codes/whemetutt. Cet. 1908[28]   2   T. Codes/whemetutt. Cet. 1908[28]   2   T. Codes/whemetutt. Cet. 1908[28]   9   T. Codes/whemetutt. Cet. 1908[28]   9   T. Codes/whemetutt. Cet. 1908[28]   9   T. Codes/whemetutt. Cet. 1908[28]   5   T. Codes/whemetutt. Cet. 1908[28]   7   T. C		
T_COSSAVVeneut   Cet   1903	T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
TE, COSSO/Wheeld, COL, 198294 2 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 11, Colspan/Wheeld, COL, 198294 0 12, Colspan/Wheeld, COL, 198294 0 13, Colspan/Wheeld, COL, 198294 0 19, Colspan/Wheeld, COL, 198294 0 19, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 11, Colspan/Wheeld, COL, 198294 0 12, Colspan/Wheeld, COL, 198294 0 12, Colspan/Wheeld, COL, 198294 0 13, Colspan/Wheeld, COL, 198294 0 14, Colspan/Wheeld, COL, 198294 0 15, Colspan/Wheeld, COL, 198294 0 16, Colspan/Wheeld, COL, 198294 0 17, Colspan/Wheeld, COL, 198294 0 18, Colspan/Wheeld, COL, 198294 0 19, Cols	T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
TE, COSSO/Wheeld, COL, 198294 2 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 11, Colspan/Wheeld, COL, 198294 0 12, Colspan/Wheeld, COL, 198294 0 13, Colspan/Wheeld, COL, 198294 0 19, Colspan/Wheeld, COL, 198294 0 19, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 10, Colspan/Wheeld, COL, 198294 0 11, Colspan/Wheeld, COL, 198294 0 12, Colspan/Wheeld, COL, 198294 0 12, Colspan/Wheeld, COL, 198294 0 13, Colspan/Wheeld, COL, 198294 0 14, Colspan/Wheeld, COL, 198294 0 15, Colspan/Wheeld, COL, 198294 0 16, Colspan/Wheeld, COL, 198294 0 17, Colspan/Wheeld, COL, 198294 0 18, Colspan/Wheeld, COL, 198294 0 19, Cols	T2 ColSpurVernierLUT Cnt s16[2][3]	4
P_COSSAVVentUT_CN_S192 3 4   P_COSSAVVentUT_CN_S192 7 7   P_COSSAVVentUT		
TR. CORRENTMENT OUT SERGIFF  12. CORRENTMENT OUT SERGIFF  13. CORRENTMENT OUT SERGIFF  14. CORRENTMENT OUT SERGIFF  15. CORRENTMENT OUT SERGIFF  16. CORRENTMENT OUT SERGIFF  17. CORRENTMENT OUT SERGIFF  18. CORRENTMENT OUT SERGIFF  19. CORRENTMENT OUT SERGIFF  10. CORRENTMENT OUT SERGIFF  11. CORRENTMENT OUT SERGIFF  12. CORRENTMENT OUT SERGIFF  13. CORRENTMENT OUT SERGIFF  14. CORRENTMENT OUT SERGIFF  15. CORRENTMENT OUT SERGIFF  16. CORRENTMENT OUT SERGIFF  17. CORRENTMENT OUT SERGIFF  18. CORRENTMENT OUT SERGIFF  19. CORRENTMENT		
T2_CoSignativement_U_Cot_stip[2]  T2_CoSignativement_U_Cot_stip[2]  T2_CoSignativement_U_Cot_stip[2]  T2_CoSignativement_U_Cot_stip[2]  T2_CoSignativement_U_Cot_stip[2]  T3_CoSignativement_U_Cot_stip[2]  T3_CoSignativement_U_Cot_stip[2]  T4_CoSignativement_U_Cot_stip[2]  T5_CoSignativement_U_Cot_stip[2]  T6_CoSignativement_U_Cot_stip[2]  T7_CoSignativement_U_Cot_stip[2]  T8_CoSignativement_U_Cot_stip[2]  T9_CoSignativement_U_Cot_stip[2]  T9_CoSignativement_U_Cot_stip[2]  T9_CoSignativement_U_Cot_stip[2]  T0_CoSignativement_U_Cot_stip[2]  T0_CoS		
17. CoSquar/memork U. Cot., shippilis	T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
To Cossaviewent U. Cit. 410(19) 12. Cossaviewent U. Cit. 410(19) 13. Cossaviewent U. Cit. 410(19) 14. Cossaviewent U. Cit. 410(19) 15. Cossaviewent U. Cit. 410(19) 16. Cossaviewent U. Cit. 410(19) 17. Cossaviewent U. Cit. 410(19) 18. Cossaviewent U. Cit. 410(19) 19. Cossaviewent U. Cit. 410(19) 1		7
T2_CoSpulvement_T_Det_stip[19]		
T2_Colspar/memal L1_Col_s (190] 11   10   10   10   10   10   10   10		
T2_Colspar/winest_U_Cot_stign[11]		
T2_CoSput/weinst_UT_Cnt_sing[15]	T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_COSQUAYMENT_C_C_N_1902[14]	T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_COSQUAYMENT_C_C_N_1902[14]	T2 ColSpurVernierLUT Cnt s16[2][12]	8
TZ_COSPOLYMENTUL_COL_150[15]  TZ_COS		
T2_COSQUAYMENT_CT_CS_15(2)[19] 12_COSQUAYMENT_CT_CS_15(2)[19] 12_COSQUAYMENT_CT_CS_15(2)[19] 14_T2_COSQUAYMENT_CS_15(2)[19] 15_COSQUAYMENT_CS_15(2)[19] 16_COSQUAYMENT_CS_15(2)[19] 17_COSQUAYMENT_CS_15(2)[19] 18_COSQUAYMENT_CS_15(2)[19] 18_COSQUAYMENT_CS_15(2)[19] 19_COSQUAYMENT_CS_15(2)[19] 19_COSQUAYMENT_CS_		
T2_Colspow/ment_UT_Col_ssig_10    T2_C		
12, CoSport/veneUU, Cnt, 16/321) 14, CoSport/veneUU, Cnt, 16/321) 15, CoSport/veneUU, Cnt, 16/321) 16, CoSport/veneUU, Cnt, 16/321) 17, CoSport/veneUU, Cnt, 16/321) 18, CoSport/veneUU, Cnt, 16/3210 19, DuaSport/veneUU, Cnt, 16/3210	T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
12 CoSpulvementUT Cet. \$16300   14 12 CoSpulvementUT Cet. \$16300   14 12 CoSpulvementUT Cet. \$16300   16 12 CoSpulvementUT Cet. \$16300   18 12 CoSpulvementUT Cet. \$16300   15 12 CoSpulvementUT Cet. \$16300   16 12 CoSpulvementUT Cet. \$16000   17 13 CoSpulvementUT Cet. \$16000   17 14 CoSpulvementUT Cet. \$16000   17 15 CoSpulvementUT Cet. \$16000   17 16 CoSpulvementUT Cet. \$16000   17 17 CoSpulvementUT Cet. \$16000   17 18 CoSpulvementUT Cet. \$16000   17 19 CoSpulvementU	T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
12, Colsput/emenUT Cot.; 1903 2    14		1
17_CoSpulvementUT_Cnt_1 stigster		
17_Colsput/emerUT_Ort, 16/316    17_Colsput/emerUT_Ort, 16/316    17_Colsput/emerUT_Ort, 16/316    17_Colsput/emerUT_Ort, 16/316    18_Colsput/emerUT_Ort, 16/316		
T2_CoSparVerment_UT_Cnt_st@3[4] 5 T2_CoSparVerment_UT_Cnt_st@3[5] 2 T2_CoSparVerment_UT_Cnt_st@3[5] 15 T2_CoSparVerment_UT_Cnt_st@3[7] 12 T2_CoSparVerment_UT_Cnt_st@3[7] 12 T2_CoSparVerment_UT_Cnt_st@3[7] 12 T2_CoSparVerment_UT_Cnt_st@3[7] 13 T2_CoSparVerment_UT_Cnt_st@3[7] 17 T2_CosparVerment_U	· · · · · · · · · · · ·	
T2_CoSparVerment_UT_Cnt_st@3[4] 5 T2_CoSparVerment_UT_Cnt_st@3[5] 2 T2_CoSparVerment_UT_Cnt_st@3[5] 15 T2_CoSparVerment_UT_Cnt_st@3[7] 12 T2_CoSparVerment_UT_Cnt_st@3[7] 12 T2_CoSparVerment_UT_Cnt_st@3[7] 12 T2_CoSparVerment_UT_Cnt_st@3[7] 13 T2_CoSparVerment_UT_Cnt_st@3[7] 17 T2_CosparVerment_U	T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
12_Colsput/emieLUT_Cnt_s169  5    15    2_Colsput/emieLUT_Cnt_s169  7    12    2_Colsput/emieLUT_Cnt_s169  7    12    2_Colsput/emieLUT_Cnt_s169  7    12    2_Colsput/emieLUT_Cnt_s169  7    13    2_Colsput/emieLUT_Cnt_s169  7    16    2_Colsput/emieLUT_Cnt_s169  7    16    2_Colsput/emieLUT_Cnt_s169  7    16    2_Colsput/emieLUT_Cnt_s169  7    17    2_Colsput/emieLUT_Cnt_s169  7    17    2_Colsput/emieLUT_Cnt_s169  7    17    2_Colsput/emieLUT_Cnt_s169  7    17    2_Colsput/emieLUT_Cnt_s169  7    7    2_Colsput/emieLUT		5
12, Osbgw/venicUT, Cnt, s163 81   15		
12. CoSpurVernict U.T. Cnt. s16[0][7] 12. CoSpurVernict U.T. Cnt. s16[0][8] 9 12. CoSpurVernict U.T. Cnt. s16[0][8] 9 12. CoSpurVernict U.T. Cnt. s16[0][11] 13. T2. CoSpurVernict U.T. Cnt. s16[0][11] 16. T2. CoSpurVernict U.T. Cnt. s16[0][11] 17. CoSpurVernict U.T. Cnt. s16[0][12] 18. T2. CoSpurVernict U.T. Cnt. s16[0][13] 19. CoSpurVernict U.T. Cnt. s16[0][14] 19. CoSpurVernict U.T. Cnt. s16[0][15] 10. CoSpurVernict U.T. Cnt. s16[0][16] 19. C		
12. ColsparVement UT. Cnt. 18(3)(8) 9 12. ColsparVement UT. Cnt. 18(3)(1) 6 12. ColsparVement UT. Cnt. 18(3)(1) 1 13. 12. ColsparVement UT. Cnt. 18(3)(1) 1 14. ColsparVement UT. Cnt. 18(3)(1) 1 15. ColsparVement UT. Cnt. 18(3)(1) 1 17. ColsparVement UT. Cnt. 18(3)(1) 1 18. ColsparVement UT. Cnt. 18(3)(1) 1 19. DualSparVement UT. Cnt. 18(3)(1) 1 19. DualSparVement UT. Cnt. 18(3)(1) 1 19. DualSparVement UT. Cnt. 18(3)(1) 1 19. ColsparVement UT. Cnt. 18(1)(1) 1 19. ColsparVement UT. Cnt. 18(1)(1) 1 19. ColsparVement UT. Cnt. 18(1)(1) 1 19. ColsparVement UT. Cnt		
12, CoSpurVement.UT_Cnt_s16(3) 10    3   3   3   3   3   3   3   3   3	T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
12, CoSpurVermet.UT_Cnt_s16[3][11] 12, CoSpurVermet.UT_Cnt_s16[3][11] 13 172, CoSpurVermet.UT_Cnt_s16[3][12] 13 172, CoSpurVermet.UT_Cnt_s16[3][13] 10 172, CoSpurVermet.UT_Cnt_s16[3][14] 17 172, CoSpurVermet.UT_Cnt_s16[3][16] 17 17, CoSpurVermet.UT_Cnt_s16[3][16] 17 17, CoSpurVermet.UT_Cnt_s16[3][16] 17 182, DuSSpurVermet.UT_Cnt_s16[3][16] 17 182, DuSSpurVermet.UT_Cnt_s16[3][16] 182, DuSSpurVermet.UT_Cnt_s16[3][16] 182, DuSSpurVermet.UT_Cnt_s16[3][16] 182, DuSSpurVermet.UT_Cnt_s16[3][16] 182, DuSSpurVermet.UT_Cnt_s16[3][16] 183, DuSSpurVermet.UT_Cnt_s16[3][16] 184, DuSSpurVermet.UT_Cnt_s16[3][16] 185, DuSSpurVermet.UT_Cnt_s16[3][16] 186, DuSSpurVermet.UT_Cnt_s16[3][16] 187, DuSSpurVermet.UT_Cnt_s16[3][16] 189, DuSSpurVermet.UT_Cnt_s16[3][16] 189, DuSSpurVermet.UT_Cnt_s16[3][16] 189, DuSSpurVermet.UT_Cnt_s16[3][16] 189, DuSSpurVermet.UT_Cnt_s16[3][16] 190, DuSSpurVermet.UT_Cnt_s16[3][16] 191, DuSSpurVermet.UT_Cnt_s16[3][16] 192, DuSSpurVermet.UT_Cnt_s16[3][16] 193, DuSSpurVermet.UT_Cnt_s16[3][16] 194, DuSSpurVermet.UT_Cnt_s16[3][16] 195, DuSSpurVermet.UT_Cnt_s16[3][16] 196, DuSSpurVermet.UT_Cnt_s16[3][16] 197, DuSSpurVermet.UT_Cnt_s16[3][16] 198, DuSSpurVermet.UT_Cnt_s16[3][16] 199, DuSSpurVermet.UT_Cnt_s16[3][16] 199, DuSSpurVermet.UT_Cnt_s16[3][16] 199, DuSSpurVermet.UT_Cnt_s16[3][16] 199, DuSSpurVermet.UT_Cnt_s16[3][16] 190, DuSSpurVermet.UT_Cnt_s16[3][16] 190, DuSSpurVermet.UT_Cnt_s16[3][16] 190, DuSSpurVermet.UT_Cnt_s16[3][16] 191, DuSSpurVermet.UT_Cnt_s16[3][16] 192, DuSSpurVermet.UT_Cnt_s16[3][16] 193, DuSSpurVermet.UT_Cnt_s16[3][16] 194, DuSSpurVermet.UT_Cnt_s16[3][16] 195, DuSSpurVermet.UT_Cnt_s16[3][16] 196, DuSSpurVermet.UT_Cnt_s16[3][16] 197, DuSSpurVermet.UT_Cnt_s16[3][16] 198, DuSSpurVermet.UT_Cnt_s16[3][16] 199, DuSSpurVermet.UT_Cnt_s16[3][16] 190, DuSSpurVermet.UT_Cnt_s16[3][16]	T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
12. ColspurVermict UT. Cnt. 15(3)(10) 12. ColspurVermict UT. Cnt. 15(3)(11) 16 17. ColspurVermict UT. Cnt. 15(3)(12) 17. ColspurVermict UT. Cnt. 15(3)(12) 18. ColspurVermict UT. Cnt. 15(3)(13) 19. ColspurVermict UT. Cnt. 15(3)(14) 19. ColspurVermict UT. Cnt. 15(3)(14) 19. ColspurVermict UT. Cnt. 15(3)(15) 19. ColspurVermict UT. Cnt. 15(3)(15) 19. ColspurVermict UT. Cnt. 15(3)(15) 19. ColspurVermict UT. Cnt. 15(3)(16) 19. ColspurVermict UT. Cnt. 15(3)(17) 19. ColspurVermict UT. Cnt. 15(3)(18) 19. ColspurVermict UT. Cnt. 1	T2 ColSpurVernierLUT Cnt s16[3][9]	6
12 CoSpurVemiet.UT_Cnt_s16(3)[12]   13   13   13   13   13   13   13   1		
12 ColSpurVemet.UT Cnt_s16[3]112] 12 ColSpurVemet.UT Cnt_s16[3]113] 11 ColSpurVemet.UT Cnt_s16[3]114] 12 ColSpurVemet.UT Cnt_s16[3]115] 4 ColSpurVemet.UT Cnt_s16[3]116] 4 ColSpurVemet.UT Cnt_s16[3]116] 4 ColSpurVemet.UT Cnt_s16[3]116] 17 ColSpurVemet.UT Cnt_s16[3]116] 17 ColSpurVemet.UT Cnt_s16[3]116] 18 ColSpurVemet.UT Cnt_s16[3]116] 19 ColSpurVemet.UT Cnt_s16[3]116] 19 ColSpurVemet.UT Cnt_s16[3]116] 10 ColSpurVemet.UT Cnt_s16[3]116] 11 ColSpurVemet.UT Cnt_s16[3]116] 12 ColSpurVemet.UT Cnt_s16[3]116] 12 ColSpurVemet.UT Cnt_s16[3]116] 12 ColSpurVemet.UT Cnt_s16[3]116] 13 ColSpurVemet.UT Cnt_s16[3]116] 14 ColSpurVemet.UT Cnt_s16[3]116] 15 ColSpurVemet.UT Cnt_s16[3]116] 16 ColSpurVemet.UT Cnt_s16[3]116] 17 ColSpurVemet.UT Cnt_s16[3]116] 18 ColSpurVemet.UT Cnt_s16[3]116] 19 ColSpurVemet.UT Cnt_s16[3]116] 10 ColSpurVemet.UT Cnt_s16[3]116] 11 ColSpurVemet.UT Cnt_s16[3]116] 12 ColSpurVemet.UT Cnt_s16[3]116] 13 ColSpurVemet.UT Cnt_s16[3]116] 14 ColSpurVemet.UT Cnt_s16[3]116] 15 ColSpurVemet.UT Cnt_s16[3]116] 16 ColSpurVemet.UT Cnt_s16[3]116] 17 ColSpurVemet.UT Cnt_s16[3]116] 18 ColSpurVemet.UT Cnt_s16[3]116] 19 ColSpurVemet.UT Cnt_s16[3]116] 10 ColSpurVemet.UT Cnt_s16[3]116] 11 ColSpurVemet.UT Cnt_s16[3]116] 12 ColSpurVemet.UT Cnt_s16[3]116] 13 ColSpurVemet.UT Cnt_s16[3]116] 14 ColSpurVemet.UT Cnt_s16[3]116] 15 ColSpurVemet.UT Cnt_s16[3]116] 16 ColSpurVemet.UT Cnt_s16[3]116] 17 ColSpurVemet.UT Cnt_s16[3]116] 18 ColSpurVemet.UT Cnt_s16[3]116] 19 ColSpurVemet.UT Cnt_s16[3]116] 11 ColSpurVemet.UT Cnt_s16[3]116] 11 ColSpurVemet.UT Cnt_s16[3]1176] 11 ColSpurVemet.UT Cnt_s16[3]1176] 12 ColSpurVemet.UT Cnt_s16[3]1176] 13 ColSpurVemet.UT Cnt_s16[3]1176] 14 ColSpurVemet.UT Cnt_s16[3]1176] 15 ColSpurVemet.UT Cnt_s16[3]1176] 16 ColSpurVemet.UT Cnt_s16[3]1176] 17 ColSpurVemet.UT Cnt_s16[3]1176] 18 ColSpurVemet.UT Cnt_s16[3]1176] 19 ColSpurVemet.UT Cnt_s16[3]1176] 11 ColSpurVemet.UT Cnt_s16[3]1176] 11 ColSpurVemet.UT Cnt_s16[3]1176] 11 ColSpurVemet.UT Cnt_s16[3]1176] 12 ColSpurVemet.UT Cnt_s16[3]1176] 12 ColSpurVemet.UT Cnt_s16[3]		
12, ColSpurVemert.UT_Cnt_s16(3)[14]   7     12, ColSpurVemert.UT_Cnt_s16(3)[14]   7     12, ColSpurVemert.UT_Cnt_s16(3)[16]   17     12, DoulSpurVemert.UT_Cnt_s16(3)[16]   17     12, DoulSpurVemert.UT_Cnt_s16(3)[16]   396     12, DoulSpurVemert.UT_Cnt_s16(3)[17]   380     12, DoulSpurVemert.UT_Cnt_s16(3)[17]   380     12, DoulSpurVemert.UT_Cnt_s16(3)[17]   380     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   288     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   288     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     13, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     14, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     15, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     16, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     17, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     18, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     19, DoulSpurVemert.UT_Cnt_s16(3)[18]   281     19, DoulSpurVemert.UT_Cnt_s16(3)[18]   386     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   386     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   386     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     13, DoulSpurVemert.UT_Cnt_s16(3)[18]   382     14, DoulSpurVemert.UT_Cnt_s16(3)[18]   382     15, DoulSpurVemert.UT_Cnt_s16(3)[18]   382     16, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     17, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     18, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     19, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     10, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     11, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     12, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     13, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     14, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     15, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     16, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     17, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     18, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     19, DoulSpurVemert.UT_Cnt_s16(3)[18]   381     10, DoulSpurVemert.UT_Cnt_s16(3)		
12 ColSpurVemietUT_Cnt_st6[3][14]   7   7   2 ColSpurVemietUT_Cnt_st6[3][15]   4   7   7   7   2 ColSpurVemietUT_Cnt_st6[3][15]   17   7   7   7   7   7   7   7   7	T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
12 ColSpurVemieLUT_Cnt_st6[3][15]   4     12 ColSpurVemieLUT_Cnt_st6[3][15]   17     12 DualSpurVemieLUT_Cnt_st6[0][10]   366     13 DualSpurVemieLUT_Cnt_st6[0][1]   360     14 DualSpurVemieLUT_Cnt_st6[0][1]   360     15 DualSpurVemieLUT_Cnt_st6[0][3]   324     17 DualSpurVemieLUT_Cnt_st6[0][3]   288     17 DualSpurVemieLUT_Cnt_st6[0][4]   252     17 DualSpurVemieLUT_Cnt_st6[0][6]   360     18 DualSpurVemieLUT_Cnt_st6[0][6]   360     19 DualSpurVemieLUT_Cnt_st6[0][6]   360     19 DualSpurVemieLUT_Cnt_st6[0][6]   360     10 DualSpurVemieLUT_Cnt_st6[0][6]   360     10 DualSpurVemieLUT_Cnt_st6[0][6]   360     10 DualSpurVemieLUT_Cnt_st6[0][6]   360     12 DualSpurVemieLUT_Cnt_st6[0][6]   360     12 DualSpurVemieLUT_Cnt_st6[0][1]   360     13 DualSpurVemieLUT_Cnt_st6[0][1]   360     14 DualSpurVemieLUT_Cnt_st6[0][1]   360     15 DualSpurVemieLUT_Cnt_st6[0][1]   360     16 DualSpurVemieLUT_Cnt_st6[0][1]   360     17 DualSpurVemieLUT_Cnt_st6[0][1]   360     18 DualSpurVemieLUT_Cnt_st6[0][1]   360     18 DualSpurVemieLUT_Cnt_st6[0][1]   360     19 DualSpurVemieLUT_Cnt_st6[0][1]   360     10 DualSpurVemieLUT_Cnt_st6[0][1]   360     10 DualSpurVemieLUT_Cnt_st6[0][1]   360     11 DualSpurVemieLUT_Cnt_st6[0][1]   370     12 DualSpurVemieLUT_Cnt_st6[0][1]   370     12 DualSpurVemieLUT_Cnt_st6[0][1]   370     13 DualSpurVemieLUT_Cnt_st6[0][1]   370     14 DualSpurVemieLUT_Cnt_st6[0][1]   370     15 DualSpurVemieLUT_Cnt_st6[0][1]   370     16 DualSpurVemieLUT_Cnt_st6[0][1]   370     17 DualSpurVemieLUT_Cnt_st6[0][1]   370     18 DualSpurVemieLUT_Cnt_st6[0][1]   370     18 DualSpurVemieLUT_Cnt_st6[0][1]   370     18 DualSpurVemieLUT_Cnt_st6[0][1]   370     18 DualSpurVemieLUT_Cn	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2 CoSpurVemieLUT_Cnt_s16[3]15  4	T2 ColSpurVernierLUT Cnt s16[3][14]	7
TZ_ColSpurVemierLUT_Cnt_s16[3][16] 17 TZ_DualSpurVemierLUT_Cnt_s16[0][1] -396 TZ_DualSpurVemierLUT_Cnt_s16[0][1] -396 TZ_DualSpurVemierLUT_Cnt_s16[0][2] -324 TZ_DualSpurVemierLUT_Cnt_s16[0][3] -328 TZ_DualSpurVemierLUT_Cnt_s16[0][4] -252 TZ_DualSpurVemierLUT_Cnt_s16[0][5] -216 TZ_DualSpurVemierLUT_Cnt_s16[0][5] -216 TZ_DualSpurVemierLUT_Cnt_s16[0][6] -180 TZ_DualSpurVemierLUT_Cnt_s16[0][9] -180 TZ_DualSpurVemierLUT_Cnt_s16[0][9] -172 TZ_DualSpurVemierLUT_Cnt_s16[0][9] -72 TZ_DualSpurVemierLUT_Cnt_s16[0][9] -72 TZ_DualSpurVemierLUT_Cnt_s16[0][1] -73 TZ_DualSpurVemierLUT_Cnt_s16[0][1] -73 TZ_DualSpurVemierLUT_Cnt_s16[0][1] -74 TZ_DualSp		
12 DualSpurVernierLUT_Cnt_st6[0][1]   396     12 DualSpurVernierLUT_Cnt_st6[0][1]   386     13 DualSpurVernierLUT_Cnt_st6[0][2]   324     14 DualSpurVernierLUT_Cnt_st6[0][3]   288     15 DualSpurVernierLUT_Cnt_st6[0][3]   288     15 DualSpurVernierLUT_Cnt_st6[0][6]   252     17 DualSpurVernierLUT_Cnt_st6[0][6]   480     18 DualSpurVernierLUT_Cnt_st6[0][6]   480     19 DualSpurVernierLUT_Cnt_st6[0][7]   444     19 DualSpurVernierLUT_Cnt_st6[0][8]   490     10 DualSpurVernierLUT_Cnt_st6[0][9]   472     12 DualSpurVernierLUT_Cnt_st6[0][9]   472     12 DualSpurVernierLUT_Cnt_st6[0][1]   36     12 DualSpurVernierLUT_Cnt_st6[0][1]   36     12 DualSpurVernierLUT_Cnt_st6[0][1]   36     12 DualSpurVernierLUT_Cnt_st6[0][1]   36     12 DualSpurVernierLUT_Cnt_st6[0][1]   44     12 DualSpurVernierLUT_Cnt_st6[0][1]   48     12 DualSpurVernierLUT_Cnt_st6[0][1]   59     12 DualSpurVernierLUT_Cnt_st6[0][1]   69     12 DualSpurVernierLUT_Cnt_st6[0][1]   69     12 DualSpurVernierLUT_Cnt_st6[0][1]   69     12 DualSpurVernierLUT_Cnt_st6[0][1]   79     12 DualSpurVernierLUT_Cnt_st6[0][1]   79     12 DualSpurVernierLUT_Cnt_st6[0][1]   79     13 DualSpurVernierLUT_Cnt_st6[0][1]   79     14 DualSpurVernierLUT_Cnt_st6[0][1]   79     15 DualSpurVernierLUT_Cnt_st6[0][1]   79     16 DualSpurVernierLUT_Cnt_st6[0][1]   79     17 DualSpurVernierLUT_Cnt_st6[0][1]   79     18 DualSpurVernierLUT_Cnt_st6[0][1]   79     19 DualSpurVernierLUT_Cnt_st6[0][1]   79     10 DualSpurVernierLUT_Cnt_st6[0][1]   70     10 DualSpurVernierLUT_Cnt_st6[0][1]   70     11 DualSpurVernierLUT_Cnt_st6[0][1]   70     12 DualSpurVernierLUT_Cnt_st6[0][1]   70     13 DualSpurVernierLUT_Cnt_st6[0][1]   70     14 DualSpurVernierLUT_Cnt_st6[0][1]   70     15 DualSpurVernierLUT_Cnt_st6[0][1]   70     16 DualSpurVernierLUT_Cnt_st6[0][1]   70     17 Dual		
T2   DualSpurVernierLUT_Cnt_st60[12]   -324		
T2_DualSpurVermert.UT_Cnt_st 6[0] 2    -324     T2_DualSpurVermert.UT_Cnt_st 6[0] 3    -288     T2_DualSpurVermiert.UT_Cnt_st 6[0] 6    -252     T2_DualSpurVermiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVermiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVermiert.UT_Cnt_st 6[0] 7    -144     T2_DualSpurVermiert.UT_Cnt_st 6[0] 8    -108     T2_DualSpurVermiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVermiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVermiert.UT_Cnt_st 6[0] 10    -36     T2_DualSpurVermiert.UT_Cnt_st 6[0] 11    0     T2_DualSpurVermiert.UT_Cnt_st 6[0] 12    36     T2_DualSpurVermiert.UT_Cnt_st 6[0] 13    72     T2_DualSpurVermiert.UT_Cnt_st 6[0] 14    108     T2_DualSpurVermiert.UT_Cnt_st 6[0] 15    144     T2_DualSpurVermiert.UT_Cnt_st 6[0] 16    120     T2_DualSpurVermiert.UT_Cnt_st 6[0] 16    120     T2_DualSpurVermiert.UT_Cnt_st 6[0] 17    216     T2_DualSpurVermiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVermiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVermiert.UT_Cnt_st 6[0] 20    324     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    372     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    373     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    374     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    374     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    374     T2_DualSpurVermiert.UT_Cnt_st 6[0] 21    374     T2_DualSpurVermiert.UT_Cnt_st 6[0] 3    74     T2_DualSpurVermiert.UT_Cnt_st 6[0] 6    74     T2_DualSpurVermiert.	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
12 DualSpurVemiet.UT_Cnt_st6[0][2]   324     72 DualSpurVemiet.UT_Cnt_st6[0][3]   -288     72 DualSpurVemiet.UT_Cnt_st6[0][5]   -216     72 DualSpurVemiet.UT_Cnt_st6[0][6]   -180     73 DualSpurVemiet.UT_Cnt_st6[0][7]   -144     74 DualSpurVemiet.UT_Cnt_st6[0][8]   -108     75 DualSpurVemiet.UT_Cnt_st6[0][8]   -108     76 DualSpurVemiet.UT_Cnt_st6[0][9]   -72     72 DualSpurVemiet.UT_Cnt_st6[0][10]   -36     73 DualSpurVemiet.UT_Cnt_st6[0][10]   -36     74 DualSpurVemiet.UT_Cnt_st6[0][11]   0     75 DualSpurVemiet.UT_Cnt_st6[0][12]   36     75 DualSpurVemiet.UT_Cnt_st6[0][13]   72     75 DualSpurVemiet.UT_Cnt_st6[0][13]   72     76 DualSpurVemiet.UT_Cnt_st6[0][14]   108     77 DualSpurVemiet.UT_Cnt_st6[0][16]   180     78 DualSpurVemiet.UT_Cnt_st6[0][17]   216     79 DualSpurVemiet.UT_Cnt_st6[0][18]   252     70 DualSpurVemiet.UT_Cnt_st6[0][19]   288     70 DualSpurVemiet.UT_Cnt_st6[0][19]   288     70 DualSpurVemiet.UT_Cnt_st6[0][19]   324     70 DualSpurVemiet.UT_Cnt_st6[0][19]   360     70 DualSpurVemiet.UT_Cnt_st6[0][19]   3     70 DualSpurVemiet.UT_Cnt_st6[0][19]   3     71 DualSpurVemiet.UT_Cnt_st6[0][19]   3     72 DualSpurVemiet.UT_Cnt_st6[0][19]   3     73 DualSpurVemiet.UT_Cnt_st6[0][19]   3     74 DualSpurVemiet.UT_Cnt_st6[0][19]   3     75 DualSpurVemiet.UT_Cnt_st6[0][19]   3     76 DualSpurVemiet.UT_Cnt_st6[0][19]   3     77 DualSpurVemiet.UT_Cnt_st6[0][19]   5     78 DualSpurVemiet.UT_Cnt_st6[0][19]   6     79 DualSpurVemiet.UT_Cnt_st6[0][19]   7     70 DualSpurVemiet.UT_Cnt_st6[0][19]   8     70 DualSpurVemiet.UT_Cnt_st6[0][19]   8     71 DualSpurVemiet.UT_Cnt_st6[0][19]   8     72 DualSpurVemiet.UT_Cnt_st6[0][19]   8     73 DualSpurVemiet.UT_Cnt_st6[0][19]   8     74 DualSpurVemiet.UT_Cnt_st6[0][19]   8     75 DualSpurVemiet.UT_Cnt_st6[0][19]   8     76 DualSpurVemiet.UT_Cnt_st6[0][19]   8     77 DualSpurVemiet.UT_Cnt_st6[0][19]   8     78 DualSpurVemiet.UT_Cnt_st6[0][19]   9     79 DualSpurVemiet.UT_Cnt_st6[0][19]   9     70 DualSpurVemiet.UT_Cnt_st6[0][19]   9     70 DualSpurVemiet.UT_Cnt_st	T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
12   DualSpurVemiet.UT_Cnt_st6[0][4]   -252   -216   -21		-324
12_DualSpurVernietLUT_Cnt_s16(0)[4]   -252     12_DualSpurVernietLUT_Cnt_s16(0)[5]   -216     12_DualSpurVernietLUT_Cnt_s16(0)[7]   -144     12_DualSpurVernietLUT_Cnt_s16(0)[8]   -108     12_DualSpurVernietLUT_Cnt_s16(0)[8]   -108     12_DualSpurVernietLUT_Cnt_s16(0)[9]   -72     12_DualSpurVernietLUT_Cnt_s16(0)[10]   -36     12_DualSpurVernietLUT_Cnt_s16(0)[10]   -36     12_DualSpurVernietLUT_Cnt_s16(0)[11]   0     12_DualSpurVernietLUT_Cnt_s16(0)[12]   36     12_DualSpurVernietLUT_Cnt_s16(0)[13]   72     12_DualSpurVernietLUT_Cnt_s16(0)[15]   144     12_DualSpurVernietLUT_Cnt_s16(0)[16]   180     12_DualSpurVernietLUT_Cnt_s16(0)[16]   180     12_DualSpurVernietLUT_Cnt_s16(0)[18]   252     12_DualSpurVernietLUT_Cnt_s16(0)[19]   288     12_DualSpurVernietLUT_Cnt_s16(0)[19]   288     12_DualSpurVernietLUT_Cnt_s16(0)[19]   360     12_DualSpurVernietLUT_Cnt_s1		
T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][18] 258 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[0][11] 360 T2_		
T2   DualSpurVernierLUT Cnt_s16(0)[6]   -180   -180     -184   -184   -184     -184     -184     -184     -184     -184     -184     -184     -184     -184     -184     -184     -184     -184     -18		
T2_DualSpurVernierLUT_Cnt_s16(0)[7] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[11] 0 T2_DualSpurVernierLUT_Cnt_s16(0)[12] 36 T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[14] T2_DualSpurVernierLUT_Cnt_s16(0)[15] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[17] T2_DualSpurVernierLUT_Cnt_s16(0)[17] T2_DualSpurVernierLUT_Cnt_s16(0)[18] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSp	T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][18] 288 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 6 T2_DualSpurVernierLUT_Cnt_s16[1][6] 6 T2_DualSpurVernierLUT_Cnt_s16[1][6] 6 T2_DualSpurVernierLUT_Cnt_s16[1][6] 7 T2_DualSpurVernierLUT_Cnt_s16[1][6] 8 T2_DualSpurVernierLUT_Cnt_s16[1][6] 9 T2_DualSpurVernierLUT_Cnt_s16[1][6] 11	T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][18] 288 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 6 T2_DualSpurVernierLUT_Cnt_s16[1][6] 6 T2_DualSpurVernierLUT_Cnt_s16[1][6] 6 T2_DualSpurVernierLUT_Cnt_s16[1][6] 7 T2_DualSpurVernierLUT_Cnt_s16[1][6] 8 T2_DualSpurVernierLUT_Cnt_s16[1][6] 9 T2_DualSpurVernierLUT_Cnt_s16[1][6] 11	T2 DualSpurVernierLUT Cnt s16[0][7]	-144
T2_DualSpurVerniert_UT_Cnt_s16[0][0]   .72   .		
T2_DualSpurVemierLUT_Cnt_s16[0][10]   36     T2_DualSpurVemierLUT_Cnt_s16[0][11]   0     T2_DualSpurVemierLUT_Cnt_s16[0][12]   36     T2_DualSpurVemierLUT_Cnt_s16[0][13]   72     T2_DualSpurVemierLUT_Cnt_s16[0][14]   108     T2_DualSpurVemierLUT_Cnt_s16[0][15]   144     T2_DualSpurVemierLUT_Cnt_s16[0][16]   180     T2_DualSpurVemierLUT_Cnt_s16[0][17]   216     T2_DualSpurVemierLUT_Cnt_s16[0][18]   252     T2_DualSpurVemierLUT_Cnt_s16[0][19]   288     T2_DualSpurVemierLUT_Cnt_s16[0][20]   324     T2_DualSpurVemierLUT_Cnt_s16[0][21]   360     T2_DualSpurVemierLUT_Cnt_s16[1][0]   9     T2_DualSpurVemierLUT_Cnt_s16[1][1]   0     T2_DualSpurVemierLUT_Cnt_s16[1][2]   1     T2_DualSpurVemierLUT_Cnt_s16[1][3]   2     T2_DualSpurVemierLUT_Cnt_s16[1][4]   3     T2_DualSpurVemierLUT_Cnt_s16[1][6]   5     T2_DualSpurVemierLUT_Cnt_s16[1][6]   5     T2_DualSpurVemierLUT_Cnt_s16[1][6]   5     T2_DualSpurVemierLUT_Cnt_s16[1][6]   7     T2_DualSpurVemierLUT_Cnt_s16[1][6]   7     T2_DualSpurVemierLUT_Cnt_s16[1][6]   7     T2_DualSpurVemierLUT_Cnt_s16[1][6]   9     T2_DualSpurVemierLUT_Cnt_s16[1][6]   1     T2_DualSpurVemierLUT_Cnt_s16[1][6]		
T2_DualSpurVemierLUT_Cnt_s16[0][11]  T2_DualSpurVemierLUT_Cnt_s16[0][12]  T2_DualSpurVemierLUT_Cnt_s16[0][13]  T2_DualSpurVemierLUT_Cnt_s16[0][14]  T2_DualSpurVemierLUT_Cnt_s16[0][15]  T2_DualSpurVemierLUT_Cnt_s16[0][15]  T2_DualSpurVemierLUT_Cnt_s16[0][16]  T2_DualSpurVemierLUT_Cnt_s16[0][17]  T2_DualSpurVemierLUT_Cnt_s16[0][18]  T2_DualSpurVemierLUT_Cnt_s16[0][19]  T2_DualSpurVemierLUT_Cnt_s16[0][19]  T2_DualSpurVemierLUT_Cnt_s16[0][21]  T2_DualSpurVemierLUT_Cnt_s16[0][21]  T2_DualSpurVemierLUT_Cnt_s16[0][21]  T2_DualSpurVemierLUT_Cnt_s16[1][1]  T2_DualSpurVemierLUT_Cnt_s16[1][1]  T2_DualSpurVemierLUT_Cnt_s16[1][3]  T2_DualSpurVemierLUT_Cnt_s16[1][4]  T2_DualSpurVemierLUT_Cnt_s16[1][4]  T2_DualSpurVemierLUT_Cnt_s16[1][6]  T2_DualSpurVemierLUT_Cnt_s16[1][1]  D1_DualSpurVemierLUT_Cnt_s16[1][1]		
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17] DualSpurVernierLUT_Cnt_s16[0][18] T2_DualSpurVernierLUT_Cnt_s16[0][19] DualSpurVernierLUT_Cnt_s16[0][20] DualSpurVernierLUT_Cnt_s16[0][20] DualSpurVernierLUT_Cnt_s16[0][21] DualSpurVernierLUT_Cnt_s16[0][21] DualSpurVernierLUT_Cnt_s16[1][0] DualSpurVernierLUT_Cnt_s16[1][1]	T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][12]   36     T2_DualSpurVernierLUT_Cnt_s16[0][14]   108     T2_DualSpurVernierLUT_Cnt_s16[0][15]   144     T2_DualSpurVernierLUT_Cnt_s16[0][16]   180     T2_DualSpurVernierLUT_Cnt_s16[0][16]   180     T2_DualSpurVernierLUT_Cnt_s16[0][17]   216     T2_DualSpurVernierLUT_Cnt_s16[0][18]   252     T2_DualSpurVernierLUT_Cnt_s16[0][19]   288     T2_DualSpurVernierLUT_Cnt_s16[0][20]   324     T2_DualSpurVernierLUT_Cnt_s16[0][21]   360     T2_DualSpurVernierLUT_Cnt_s16[0][21]   360     T2_DualSpurVernierLUT_Cnt_s16[1][1]   0     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1][2]   1     T2_DualSpurVernierLUT_Cnt_s16[1][3]   2     T2_DualSpurVernierLUT_Cnt_s16[1][4]   3     T2_DualSpurVernierLUT_Cnt_s16[1][6]   5     T2_DualSpurVernierLUT_Cnt_s16[1][6]   5     T2_DualSpurVernierLUT_Cnt_s16[1][6]   5     T2_DualSpurVernierLUT_Cnt_s16[1][6]   6     T2_DualSpurVernierLUT_Cnt_s16[1][6]   7     T2_DualSpurVernierLUT_Cnt_s16[1][6]   7     T2_DualSpurVernierLUT_Cnt_s16[1][6]   9     T2_DualSpurVernierLUT_Cnt_s16[1][1]   0     T2_DualSpurVernierLUT_Cnt_s16[1][1]   1     T2_DualSpurVernierLUT_Cnt_s16[1]	T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][18] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][6] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][9] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0		36
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][0]  12_DualSpurVernierLUT_Cnt_s16[1][1]  0  T2_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][4]  3  T2_DualSpurVernierLUT_Cnt_s16[1][6]  4  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][7]  6  T2_DualSpurVernierLUT_Cnt_s16[1][8]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][1]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  10_DualSpurVernierLUT_Cnt_s16[1][1]  11_DualSpurVernierLUT_Cnt_s16[1][1]  12_DualSpurVernierLUT_Cnt_s16[1][1]  13_DualSpurVernierLUT_Cnt_s16[1][1]  14_DualSpurVernierLUT_Cnt_s16[1][1]  15_DualSpurVernierLUT_Cnt_s16[1][1]  16_DualSpurVernierLUT_Cnt_s16[1][1]  17_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpurVernierLUT_Cnt_s16[1][1]  18_DualSpur		
T2_DualSpurVerniert.UT_Cnt_st6[0][15] 144  T2_DualSpurVerniert.UT_Cnt_st6[0][16] 180  T2_DualSpurVerniert.UT_Cnt_st6[0][17] 216  T2_DualSpurVerniert.UT_Cnt_st6[0][18] 252  T2_DualSpurVerniert.UT_Cnt_st6[0][19] 288  T2_DualSpurVerniert.UT_Cnt_st6[0][20] 324  T2_DualSpurVerniert.UT_Cnt_st6[0][21] 360  T2_DualSpurVerniert.UT_Cnt_st6[0][21] 9  T2_DualSpurVerniert.UT_Cnt_st6[1][1] 0  T2_DualSpurVerniert.UT_Cnt_st6[1][1] 10  T2_DualSpurVerniert.UT_Cnt_st6[1][2] 1  T2_DualSpurVerniert.UT_Cnt_st6[1][3] 2  T2_DualSpurVerniert.UT_Cnt_st6[1][4] 3  T2_DualSpurVerniert.UT_Cnt_st6[1][6] 4  T2_DualSpurVerniert.UT_Cnt_st6[1][6] 5  T2_DualSpurVerniert.UT_Cnt_st6[1][6] 5  T2_DualSpurVerniert.UT_Cnt_st6[1][6] 7  T2_DualSpurVerniert.UT_Cnt_st6[1][8] 7  T2_DualSpurVerniert.UT_Cnt_st6[1][9] 8  T2_DualSpurVerniert.UT_Cnt_st6[1][1] 9  T2_DualSpurVerniert.UT_Cnt_st6[1][1] 9  T2_DualSpurVerniert.UT_Cnt_st6[1][11] 0		
T2_DualSpurVernierLUT_Cnt_s16[0][16]       180         T2_DualSpurVernierLUT_Cnt_s16[0][17]       216         T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0         T2_DualSpurVernierLUT_Cnt_s16[1][10]       0 <td></td> <td></td>		
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0	T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0	T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[0][19]       288         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][1]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       1         T2_DualSpurVernierLUT_Cnt_s16[1][1]       1         T2_DualSpurVernierLUT_Cnt_s16[1][1]       1         T2_DualSpurVernierLUT_Cnt_s16[1][1]       2		
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][1]       1         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0		
T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2	T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2	T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][13] 2		
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		3
T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2	T2_DualSpurVernierLUT_Cnt_s16[1][7]	
T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2	T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][12]       1         T2_DualSpurVernierLUT_Cnt_s16[1][13]       2		
T2_DualSpurVernierLUT_Cnt_s16[1][13] 2		
	T2_DualSpurVernierLUT_Cnt_s16[1][12]	
	T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
12 Duaiopui veriiei LUT Unt Stoft [14]	T2_DualSpurVernierLUT_Cnt_s16[1][14]	3

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2 DualSpurVernierLUT Cnt s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2 DualSpurVernierLUT Cnt s16[1][19]	8		
T2 DualSpurVernierLUT Cnt s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2 DualSpurVernierLUT Cnt s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k SkipStepErrDiag Cnt str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k VernCorrErrorDiag Cnt str.PStep	26		
k VernCorrErrorDiag Cnt str.NStep	9		
k VernCorrErrorThresh Deg f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	J
D. O. ID. LOOM O. IA. J. E. T. D. M. 190	4700.05547	4700 055455 + 0 0004000405	1

1782.35547

2

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08

1782.355455 ± 0.00048828125

2





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1780.41125	1780.41129 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	882.355469	882.355455 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Took Ston 2 406 (Pomost Count - 4)	
Test Step 2.106 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2 ColSpurVernierLUT Cnt s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2 ColSpurVernierLUT Cnt s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
12 ColSpurvernieri UT Cnt \$1611131	

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lame 2_ColSpurVernierLUT_Cnt_s16[1][4] 2_ColSpurVernierLUT_Cnt_s16[1][5]	Input Value
2_ColSpurVernierLUT_Cnt_s16[1][4]	•
	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2 ColSpurVernierLUT Cnt s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
2_ColSpurVernierLUT_Cnt_s16[1][12]	3
2_ColSpurVernierLUT_Cnt_s16[1][13]	2
2_ColSpurVernierLUT_Cnt_s16[1][14]	1
2 ColSpurVernierLUT Cnt s16[1][15]	0
2_ColSpurVernierLUT_Cnt_s16[1][16]	4
2_ColSpurVernierLUT_Cnt_s16[2][0]	0
2_ColSpurVernierLUT_Cnt_s16[2][1]	8
2_ColSpurVernierLUT_Cnt_s16[2][2]	6
2_ColSpurVernierLUT_Cnt_s16[2][3]	4
2_ColSpurVernierLUT_Cnt_s16[2][4]	2
2_ColSpurVernierLUT_Cnt_s16[2][5]	0
2_ColSpurVernierLUT_Cnt_s16[2][6]	9
2_ColSpurVernierLUT_Cnt_s16[2][7]	7
2 ColSpurVernierLUT Cnt s16[2][8]	5
2_ColSpurVernierLUT_Cnt_s16[2][9]	3
2_ColSpurVernierLUT_Cnt_s16[2][9] 2 ColSpurVernierLUT_Cnt_s16[2][10]	1
2_ColSpurVernierLUT_Cnt_s16[2][11]	10
2_ColSpurVernierLUT_Cnt_s16[2][11]	8
2_ColSpurVernierLUT_Cnt_s16[2][13]	6
2_ColSpurVernierLUT_Cnt_s16[2][14]	4
2_ColSpurVernierLUT_Cnt_s16[2][15]	2
2_ColSpurVernierLUT_Cnt_s16[2][16]	10
	1
2_ColSpurVernierLUT_Cnt_s16[3][0]	14
2_ColSpurVernierLUT_Cnt_s16[3][1]	
2_ColSpurVernierLUT_Cnt_s16[3][2]	11
2_ColSpurVernierLUT_Cnt_s16[3][3]	8
2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
2_ColSpurVernierLUT_Cnt_s16[3][5]	
2_ColSpurVernierLUT_Cnt_s16[3][6]	15
2_ColSpurVernierLUT_Cnt_s16[3][7]	12
2_ColSpurVernierLUT_Cnt_s16[3][8]	9
2_ColSpurVernierLUT_Cnt_s16[3][9]	6
2_ColSpurVernierLUT_Cnt_s16[3][10]	3
2_ColSpurVernierLUT_Cnt_s16[3][11]	16
2_ColSpurVernierLUT_Cnt_s16[3][12]	13
2_ColSpurVernierLUT_Cnt_s16[3][13]	10
2_ColSpurVernierLUT_Cnt_s16[3][14]	7
2_ColSpurVernierLUT_Cnt_s16[3][15]	4
2_ColSpurVernierLUT_Cnt_s16[3][16]	17
2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
2_DualSpurVernierLUT_Cnt_s16[0][11]	0
2_DualSpurVernierLUT_Cnt_s16[0][12]	36
2_DualSpurVernierLUT_Cnt_s16[0][13]	72
2_DualSpurVernierLUT_Cnt_s16[0][14]	108
2_DualSpurVernierLUT_Cnt_s16[0][15]	144
2_DualSpurVernierLUT_Cnt_s16[0][16]	180
2_DualSpurVernierLUT_Cnt_s16[0][17]	216
2_DualSpurVernierLUT_Cnt_s16[0][18]	252
2_DualSpurVernierLUT_Cnt_s16[0][19]	288
2_DualSpurVernierLUT_Cnt_s16[0][20]	324
2_DualSpurVernierLUT_Cnt_s16[0][21]	360
	9
2_DualSpurVernierLUT_Cnt_s16[1][0]	
2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1]	0
	0 1

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2 3
T2_DualSpurVernierLUT_Cnt_s16[1][14] T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T3_DualSpurVernierLUT_Cnt_s16[2][10]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	167 27
k_SkipStepErrDiag_Cnt_str.PStep	33
k_SkipStepErrDiag_Cnt_str.NStep k_VernCorrErrorDiag_Cnt_str.Threshold	97
k_VernCorrErrorDiag_Cnt_str.   Trieshold k_VernCorrErrorDiag_Cnt_str.PStep	13
k_VernCorrErrorDiag_Cnt_str.PStep	3
K_YSTIOOTIETIOIDIAG_OTIC_Str.Motep	82.93280101
k VernCorrErrorThresh Deg f32	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum.value	1028.14



Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	g_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0C	0x0C	<b>✓</b>
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.107 (Repeat Count = 1)	<b>√</b>
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0





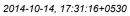
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][11]	
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2 ColSpurVernierLUT Cnt s16[3][0]	1
T2 ColSpurVernierLUT Cnt s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierE01_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
	6
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][10] T3_ColSpurVernierLUT_Cnt_s16[3][11]	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36

DigColPs\_Per2





Input Value
0
36
72
108
144
180
216
252
288
324
360 9
0
1
2
3
4
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8
9
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1
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22
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12
14 16
16
18 20
1
1
3
3 5
3 5 7
3 5





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	-
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
VernierLookup	1	VernierLookup	1	~	
DiagnosticThreshold	1	DiagnosticThreshold	1	~	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~	

Test Step 2.108 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetCustData()	241		
DigColPs_ColParityError_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195		
DigColPs_ColTrimStatic_Deg_M_f32	235.2		
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	56399		
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	60.482		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3		
DigColPs_I2CSensCommFlts_Cnt_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0		
DigColPs_PrevColPos_Deg_M_f32	812.7722371		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4		
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12		
DigColPs_SpurParityError_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241		





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_S16[0][10] T2_ColSpurVernierLUT_Cnt_S16[1][0]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][1]	
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	11





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5]	-292
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	2 3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][5]	5
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwA	hbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwA	bsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecSta	te_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimCo	mp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M lgc	0	0	•

90.000.000.000.000.000.000.000.000.000.	195 9 - 1		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1782.35547	1782.355455 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1780.41125	1780.41129 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	882.355469	882.3554545 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	<b>✓</b>	
ConstrainOneRev	2	ConstrainOneRev	2	~	
VernierLookup	1	VernierLookup	1	•	
DiagnosticThreshold	1	DiagnosticThreshold	1	•	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•	

Test Step 2.109 (Repeat Count = 1)		<b>√</b>
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_ColTrimStatic_Deg_M_f32	4.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	58760	
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395	
DigColPs_I2CHwDataType_Cnt_M_u08	0	





Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2 ColSpurVernierLUT Cnt s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s46[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_S10[2][0] T2_ColSpurVernierLUT_Cnt_S10[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3 16
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2 DualSpurVernierLUT Cnt s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T3_DualSpurVernierLUT_Cnt_s16[1][11]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	0 1
T2 DualSpurVernierLUT Cnt s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
TO D. 10 . W	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	5 6

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.14		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc	tgt_DigColPs_Per2_I2CHwAb	sPosValid Cnt lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAb		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState		
tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc	tgt_DigColPs_Per2_TrimComp		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
		Expected Value	Pocul
Name DigCoIPs HwAVernCorrFault Cnt M Igc	Actual Value	Expected Value	Resul
DigCoIPs_HwAvernCorrFault_Cnt_M_igc DigCoIPs_I2CHwColAngleForTrim_Deg_M_f32			
	818.181763	818.1818182 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	•
Status	0x01	0x01	

0x01

Status

0x01



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
VernierLookup	1	VernierLookup	1	•	
DiagnosticThreshold	1	DiagnosticThreshold	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~	

Test Step 2.110 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs I2CHwColAngle Deg M f32	274.3637406
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs 12CHwTrimTransCnts Uls M u08	3
	18
DigColPs_I2CSensCommFlts_Cnt_M_u08	1
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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12_Configenment   Grag   19    1   1   1   1   1   1   1   1		I
T. Codeys/mental Cost 1981 10	Name	Input Value
12. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. CoSportwenset UT_Del. 1981[910] 10. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981	T2_ColSpurVernierLUT_Cnt_s16[1][14]	
17. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(2) 3. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 5. Costaya/ment.U. Cut. 1902(3) 6. Costaya/ment.U. Cut. 1902(3) 7. Costaya/ment.U. Cut. 1902(	T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
17_0085pvvrinstrut _Cot_st@  0	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
17_CORSE/vernetLT_CR_15[2]  6	T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
P. Collego-Vernicut   Ces. 1982    6	T2 ColSpurVernierLUT Cnt s16[2][1]	8
TE, COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 13. COSSA/VerineLUT, Cri.; 195(20) 15. COSSA/VerineLUT, Cri.; 195(20) 16. COSSA/VerineLUT, Cri.; 195(20) 17. COSSA/VerineLUT, Cri.; 195(20) 18. COSSA/VerineLUT, Cri.; 195(20) 19. COSSA/VerineLUT, Cri.; 195(20) 10. COSSA/VerineLUT, Cri.; 195(20) 11. COSSA/VerineLUT, Cri.; 195(20) 12. COSSA/VerineLUT, Cri.; 195(20) 13. COSSA/VerineLUT, Cri.; 195(20) 14. COSSA/VerineLUT, Cri.; 195(20) 15. COSSA/VerineLUT, Cri.; 195(20) 17. COSSA/VerineLUT, Cri.; 195(20) 18. COSSA/VerineLUT, Cri.; 195(20) 19. COSSA/Verine		6
PCOS_AVENNELT_CR_15[0]  2   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  0   PCOS_AVENNELT_CR_15[0]  7   PC		
12 COSSAVVenneLUT Cut = 1619[15]   0		
12_Colspan/woment_Col_statigned   0		
T2_CuSignaviernet_UT_Cut_stip[15] 2_CuSignaviernet_UT_Cut_stip[15] 3_CuSignaviernet_UT_Cut_stip[15] 4_CuSignaviernet_UT_Cut_stip[15] 1_CuSignaviernet_UT_Cut_stip[15]		
T2_Colsput/emetU_Cot_stq09  5		
T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1919 1 T2_CoSpur/emed.U_Cnt_stQ1911		
T. C. Colsput/ment U. Crit. st 1921   1		
T2_CoSpur'ementU_Cot_stQ111	T2_ColSpurVernierLUT_Cnt_s16[2][9]	
T. Colling/months   T. Colling   T. Collin	T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_CoSpur/emetUT_Cnt_sto[0]16  4 72_CoSpur/emetUT_Cnt_sto[0]16  172_CoSpur/emetUT_Cnt_sto[0]16  182_CoSpur/emetUT_Cnt_sto[0]16	T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   13_CoSput/venietU_Cot_s100214   1   14_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   1   15_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   17_CoSput/venietU_Cot_s100216   0   18_CoSput/venietU_Cot_s100216   0   18_Cosput/venietU_Cot_s10	T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   2   12_CoSput/venietU_Cot_s1002145   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   12_CoSput/venietU_Cot_s100214   1   13_CoSput/venietU_Cot_s100214   1   14_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100214   1   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   2   15_CoSput/venietU_Cot_s100216   1   15_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   16_CoSput/venietU_Cot_s100216   0   17_CoSput/venietU_Cot_s100216   0   18_CoSput/venietU_Cot_s100216   0   18_Cosput/venietU_Cot_s10	T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T. Colspur/emicAUT Cnt. srie[]16   10   12   Colspur/emicAUT Cnt. srie[]16   10   12   Colspur/emicAUT Cnt. srie[]16   11   12   Colspur/emicAUT Cnt. srie[]16   14   12   Colspur/emicAUT Cnt. srie[]17   14   12   Colspur/emicAUT Cnt. srie[]18   14   12   Colspur/emicAUT Cnt. srie[]18   15   Colspur/emicAUT Cnt. srie[]18   16   Colspur/emicAUT Cnt. srie[]18   16   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   18   Colspur/emicAUT Cnt. srie[]18   19   Colspur/emicAUT Cnt. srie[]18   19   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   17   Colspur/emicAUT Cnt. srie[]18   18   Colspur/emicAUT Cnt. srie[]18   19   17   Colspur/emicAUT Cnt		4
12, CoSparVeneUT, Cot. 918(319) 12, CoSparVeneUT, Cot. 918(319) 13, CoSparVeneUT, Cot. 918(319) 14, CoSparVeneUT, Cot. 918(319) 15, CoSparVeneUT, Cot. 918(319) 16, CoSparVeneUT, Cot. 918(319) 17, CoSparVeneUT, Cot. 918(319) 18, CoSparVeneUT, Cot. 918(319) 18, CoSparVeneUT, Cot. 918(319) 19, CoSparVeneUT, Cot. 918(319		
17_CoSput/venicUT_Cot_1 1613[1] 17_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 11_CoSput/venicUT_Cot_1 1613[2] 12_CoSput/venicUT_Cot_1 1613[2] 13_CoSput/venicUT_Cot_1 1613[2] 14_CoSput/venicUT_Cot_1 1613[2] 15_CoSput/venicUT_Cot_1 1613[2] 16_CoSput/venicUT_Cot_1 1613[2] 17_CoSput/venicUT_Cot_1 1613[2		
12, CoSppt/vermicHUT_Cnt, 18(3)[2]   11   12, CoSppt/vermicHUT_Cnt, 18(3)[2]   11   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   8   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   8   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   15   12, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   19   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   19   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   16   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   10   17, CoSppt/vermicHUT_Cnt, 18(3)[3]   17, CoSppt		
17_CoSput/venieUT_Cnt_180S S S S S S S S S S S S S S S S S S S		
T2_CoSput/vemet.UT_Cnt_s160  16   5     T2_CoSput/vemet.UT_Cnt_s160  16   5     T2_CoSput/vemet.UT_Cnt_s160  16   15     T2_CoSput/vemet.UT_Cnt_s160  16   15     T2_CoSput/vemet.UT_Cnt_s160  16   16     T2_CoSput/vemet.UT_Cnt_s160  17   16     T2_CoSput/vemet.UT_Cnt_s160  17   16     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  17   18     T2_CoSput/vemet.UT_Cnt_s160  18   17     T2_DusSput/vemet.UT_Cnt_s160  18   18     T2_DusSput/vem		
12, CoSparVermentUT, Cnt. \$163[15] 2, CoSparVermentUT, Cnt. \$163[15] 2, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 10, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 10, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 13, CoSparVermentUT, Cnt. \$163[15] 14, CoSparVermentUT, Cnt. \$163[15] 15, CoSparVermentUT, Cnt. \$163[15] 16, CoSparVermentUT, Cnt. \$163[15] 17, CoSparVermentUT, Cnt. \$163[15] 18, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 19, CoSparVermentUT, Cnt. \$163[15] 10, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentUT, Cnt. \$163[15] 12, CoSparVermentUT, Cnt. \$163[15] 11, CoSparVermentU		
12_CobSparVement_UT_Cnt_steQt  5    2_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  7    12_CobSparVement_UT_Cnt_steQt  5    12_CobSparVement_UT_Cnt_steQt  5    13_CobSparVement_UT_Cnt_steQt  5    14_CobSparVement_UT_Cnt_steQt  5    15_CobSparVement_UT_Cnt_steQt  5    16_CobSparVement_UT_Cnt_steQt  5    17_CobSparVement_UT_Cnt_steQt  5    18_CobSparVement_UT_Cnt_steQt  5    19_CobSparVement_UT_Cnt_steQt  6    19_CobSparVement_UT_Cnt_steQt		
T2_CoSpuVerinetUT_Cnt_s160]10   15   T2_CoSpuVerinetUT_Cnt_s160]11   12   T2_CoSpuVerinetUT_Cnt_s160]10   0   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   16   T2_CoSpuVerinetUT_Cnt_s160]11   18   T2_CoSpuVerinetUT_Cnt_s160]12   13   T2_CoSpuVerinetUT_Cnt_s160]13   10   T2_CoSpuVerinetUT_Cnt_s160]14   7   T2_CoSpuVerinetUT_Cnt_s160]15   4   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]10   17   T2_CoSpuVerinetUT_Cnt_s160]10   360   T2_CoSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuV		5
T2_CoSpuVerinetUT_Cnt_s160]10   15   T2_CoSpuVerinetUT_Cnt_s160]11   12   T2_CoSpuVerinetUT_Cnt_s160]10   0   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   3   T2_CoSpuVerinetUT_Cnt_s160]10   16   T2_CoSpuVerinetUT_Cnt_s160]11   18   T2_CoSpuVerinetUT_Cnt_s160]12   13   T2_CoSpuVerinetUT_Cnt_s160]13   10   T2_CoSpuVerinetUT_Cnt_s160]14   7   T2_CoSpuVerinetUT_Cnt_s160]15   4   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]16   17   T2_CoSpuVerinetUT_Cnt_s160]10   17   T2_CoSpuVerinetUT_Cnt_s160]10   360   T2_CoSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuVerinetUT_Cnt_s160]11   360   T2_CospuSpuV	T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
12. CoSpurVemierLUT_Cnt_s16(3)[8] 9 17. CoSpurVemierLUT_Cnt_s16(3)[8] 9 17. CoSpurVemierLUT_Cnt_s16(3)[10] 13 17. CoSpurVemierLUT_Cnt_s16(3)[11] 16 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[12] 13 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[14] 7 17. CoSpurVemierLUT_Cnt_s16(3)[16] 17 17. CospurVemierLUT_Cnt_s16(3)[16] 18 18. CospurVemierLUT_Cnt_s16(3)[16] 18 1		15
12. ColspurVement.UT. Cnt. 15(8)[9] 6 12. ColspurVement.UT. Cnt. 15(8)[11] 16 12. ColspurVement.UT. Cnt. 15(8)[12] 13 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[14] 7 12. ColspurVement.UT. Cnt. 15(8)[15] 19 12. ColspurVement.UT. Cnt. 15(8)[16] 19 13. ColspurVement.UT. Cnt. 15(8)[16] 19 14. ColspurVement.UT. Cnt. 15(8)[16] 19 15. DualspurVement.UT. Cnt. 15(8)[16] 19 16. DualspurVement.UT. Cnt. 15(8)[16] 19 17. DualspurVement.UT. Cnt. 15(8)[17] 144 18. DualspurVement.UT. Cnt. 15(8)[18] 19 18. DualspurVement.UT. Cnt. 15(8)[19] 19 18. DualspurV	T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
12. ColspurVement.UT. Cnt. 15(8)[9] 6 12. ColspurVement.UT. Cnt. 15(8)[11] 16 12. ColspurVement.UT. Cnt. 15(8)[12] 13 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[13] 19 12. ColspurVement.UT. Cnt. 15(8)[14] 7 12. ColspurVement.UT. Cnt. 15(8)[15] 19 12. ColspurVement.UT. Cnt. 15(8)[16] 19 13. ColspurVement.UT. Cnt. 15(8)[16] 19 14. ColspurVement.UT. Cnt. 15(8)[16] 19 15. DualspurVement.UT. Cnt. 15(8)[16] 19 16. DualspurVement.UT. Cnt. 15(8)[16] 19 17. DualspurVement.UT. Cnt. 15(8)[17] 144 18. DualspurVement.UT. Cnt. 15(8)[18] 19 18. DualspurVement.UT. Cnt. 15(8)[19] 19 18. DualspurV		9
12 ColSpurVemiet.UT_Cnt_s16[3][10] 12 ColSpurVemiet.UT_Cnt_s16[3][11] 16 17 ColSpurVemiet.UT_Cnt_s16[3][12] 13 17 ColSpurVemiet.UT_Cnt_s16[3][13] 10 10 ColSpurVemiet.UT_Cnt_s16[3][14] 17 12 ColSpurVemiet.UT_Cnt_s16[3][16] 18 19 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 10 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][16] 12 DualSpurVemiet.UT_Cnt_s16[3][16] 13 ColSpurVemiet.UT_Cnt_s16[3][16] 14 ColSpurVemiet.UT_Cnt_s16[3][16] 15 ColSpurVemiet.UT_Cnt_s16[3][16] 16 ColSpurVemiet.UT_Cnt_s16[3][16] 17 ColSpurVemiet.UT_Cnt_s16[3][16] 18 ColSpurVemiet.UT_Cnt_s16[3][16] 19 ColSpurVemiet.UT_Cnt_s16[3][16] 10 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][16] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 13 ColSpurVemiet.UT_Cnt_s16[3][17] 14 ColSpurVemiet.UT_Cnt_s16[3][17] 15 ColSpurVemiet.UT_Cnt_s16[3][17] 16 ColSpurVemiet.UT_Cnt_s16[3][17] 17 ColSpurVemiet.UT_Cnt_s16[3][17] 18 ColSpurVemiet.UT_Cnt_s16[3][17] 19 ColSpurVemiet.UT_Cnt_s16[3][17] 10 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 13 ColSpurVemiet.UT_Cnt_s16[3][17] 14 ColSpurVemiet.UT_Cnt_s16[3][17] 15 ColSpurVemiet.UT_Cnt_s16[3][17] 16 ColSpurVemiet.UT_Cnt_s16[3][17] 17 ColSpurVemiet.UT_Cnt_s16[3][17] 18 ColSpurVemiet.UT_Cnt_s16[3][17] 19 ColSpurVemiet.UT_Cnt_s16[3][17] 10 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 11 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 12 ColSpurVemiet.UT_Cnt_s16[3][17] 13 ColSpurVemiet.UT_Cnt_s16[3][17] 14 ColSpurVemiet.UT_Cnt_s16[3][17] 15 ColSpurVemiet.UT_Cnt_s16[3][17] 16 ColSpurVemiet.UT_Cnt_s16[3][17] 17 ColSpurVemiet.UT_Cnt_s16[3][17] 18 ColSpurVemiet.UT_Cnt_s16[3][17] 19 ColSpurVemiet.UT_Cnt_s16[3][17] 10 ColSpurVemiet.UT_Cnt_s16[3][17]		
12 ColSpurVemiet.UT_Cnt_s16[3][11]   16   17 ColSpurVemiet.UT_Cnt_s16[3][12]   13   18   18   18   18   18   18   18		
12		
12 CoSparVement.UT_Cnt_s16[3]1:3]   10   7   12 CoSparVement.UT_Cnt_s16[3]1:4  7   7   12 CoSparVement.UT_Cnt_s16[3]1:6  4   7   7   7   7   7   7   7   7   7		
12 ColSpurVemietLUT_Cnt_st6[3][14]   7   7   2 ColSpurVemietLUT_Cnt_st6[3][15]   4   7   7   2 ColSpurVemietLUT_Cnt_st6[3][15]   4   7   7   7   7   7   7   7   7   7		
T2_ColSpurVemierLUT_Cnt_s16[3]15		
T2   DualspurVemierLUT_Cnt_s16(0)(1)   396		
T2 DualSpurVemierLUT_Cnt_st6[0][0]   396     T2 DualSpurVemierLUT_Cnt_st6[0][1]   360     T2 DualSpurVemierLUT_Cnt_st6[0][2]   324     T2 DualSpurVemierLUT_Cnt_st6[0][3]   286     T2 DualSpurVemierLUT_Cnt_st6[0][3]   286     T2 DualSpurVemierLUT_Cnt_st6[0][6]   252     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][6]   180     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -108     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -72     T2 DualSpurVemierLUT_Cnt_st6[0][9]   -72     T2 DualSpurVemierLUT_Cnt_st6[0][1]   -73     T2 DualSpurVemierLUT_Cnt_st6[0][1]   -74     T2 DualSpurVemierL	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_DualSpurVemiert.UT_Cnt_st6[0][1]   360     T2_DualSpurVemiert.UT_Cnt_st6[0][2]   324     T2_DualSpurVemiert.UT_Cnt_st6[0][4]   252     T2_DualSpurVemiert.UT_Cnt_st6[0][5]   216     T2_DualSpurVemiert.UT_Cnt_st6[0][6]   -180     T2_DualSpurVemiert.UT_Cnt_st6[0][7]   -144     T2_DualSpurVemiert.UT_Cnt_st6[0][8]   -108     T2_DualSpurVemiert.UT_Cnt_st6[0][9]   -72     T2_DualSpurVemiert.UT_Cnt_st6[0][9]   -72     T2_DualSpurVemiert.UT_Cnt_st6[0][1]   -73     T2_DualSpurVemiert.UT_Cnt_st6[0][1]   -74     T2_DualSpurVemier	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVemiert.UT_Cnt_st 6[0] 2    324     T2_DualSpurVemiert.UT_Cnt_st 6[0] 3    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    252     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 6    -180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 8    -108     T2_DualSpurVemiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 9    -72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 10    -36     T2_DualSpurVemiert.UT_Cnt_st 6[0] 11    0     T2_DualSpurVemiert.UT_Cnt_st 6[0] 12    36     T2_DualSpurVemiert.UT_Cnt_st 6[0] 13    72     T2_DualSpurVemiert.UT_Cnt_st 6[0] 14    108     T2_DualSpurVemiert.UT_Cnt_st 6[0] 15    144     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 16    180     T2_DualSpurVemiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 19    288     T2_DualSpurVemiert.UT_Cnt_st 6[0] 20    324     T2_DualSpurVemiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVemiert.UT_Cnt_st 6[0] 21    360     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    5     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    8     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    8     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    7     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    9     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1     T2_DualSpurVemiert.UT_Cnt_st 6[1] 2    1	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVerniertUT_Cnt_s16[0][3]	T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][4]   2552   216   12_DualSpurVernierLUT_Cnt_s16[0][5]   2-16   180	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][4]   2552   216   12_DualSpurVernierLUT_Cnt_s16[0][5]   2-16   180		-288
T2_DualSpurVernierLUT_Cnt_s16(0)[5] T2_DualSpurVernierLUT_Cnt_s16(0)[6] T2_DualSpurVernierLUT_Cnt_s16(0)[6] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[8] T2_DualSpurVernierLUT_Cnt_s16(0)[9] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[10] T2_DualSpurVernierLUT_Cnt_s16(0)[11] T2_DualSpurVernierLUT_Cnt_s16(0)[12] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[13] T2_DualSpurVernierLUT_Cnt_s16(0)[15] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[16] T2_DualSpurVernierLUT_Cnt_s16(0)[17] T2_DualSpurVernierLUT_Cnt_s16(0)[18] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(0)[19] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[1] T2_DualSpurVernierLUT_Cnt_s16(1)[19] T2_DualSpurVernierLU		
T2_DualSpurVernierLUT_Cnt_s16(0)[6]   -180   -144		
12   DualSpurVernierLUT_Cnt_s16[0][7]   -144         12   DualSpurVernierLUT_Cnt_s16[0][8]   -108       12   DualSpurVernierLUT_Cnt_s16[0][10]   -36       12   DualSpurVernierLUT_Cnt_s16[0][11]   0       12   DualSpurVernierLUT_Cnt_s16[0][12]   36       12   DualSpurVernierLUT_Cnt_s16[0][13]   72       12   DualSpurVernierLUT_Cnt_s16[0][14]   108       12   DualSpurVernierLUT_Cnt_s16[0][15]   144       12   DualSpurVernierLUT_Cnt_s16[0][16]   180       12   DualSpurVernierLUT_Cnt_s16[0][17]   216       12   DualSpurVernierLUT_Cnt_s16[0][17]   216       12   DualSpurVernierLUT_Cnt_s16[0][19]   288       12   DualSpurVernierLUT_Cnt_s16[0][19]   288       12   DualSpurVernierLUT_Cnt_s16[0][21]   360       12   DualSpurVernierLUT_Cnt_s16[0][21]   360       12   DualSpurVernierLUT_Cnt_s16[1][0]   9       12   DualSpurVernierLUT_Cnt_s16[1][1]   0       12   DualSpurVernierLUT_Cnt_s16[1][2]   1       12   DualSpurVernierLUT_Cnt_s16[1][3]   2       12   DualSpurVernierLUT_Cnt_s16[1][4]   3       12   DualSpurVernierLUT_Cnt_s16[1][6]   5       12   DualSpurVernierLUT_Cnt_s16[1][6]   6       13   DualSpurVernierLUT_Cnt_s16[1][6]   7       14   12   DualSpurVernierLUT_Cnt_s16[1][6]   6       15   DualSpurVernierLUT_Cnt_s16[1][6]   7       16   DualSpurVernierLUT_Cnt_s16[1][6]   9       17   DualSpurVernierLUT_Cnt_s16[1][6]   9       18   DualSpurVernierLUT_Cnt_s16[1][6]   9       19   DualSpurVernierLUT_Cnt_s16[1][6]   9       10   DualSpurVernierLUT_Cnt_s16[1][6]   9       11   DualSpurVernierLUT_Cnt_s16[1][6]   9       12   DualSpurVernierLUT_Cnt_s16[1][6]   9         13   DualSpurVernierLUT_Cnt_s16[1][6]   9		
T2_DualSpurVerniertLUT_Cnt_s16[0][8]   -108     -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72       -72		
T2_DualSpurVernierLUT_Cnt_s16[0][9]		
T2_DualSpurVernierLUT_Cnt_s16[0][10]  72_DualSpurVernierLUT_Cnt_s16[0][11]  72_DualSpurVernierLUT_Cnt_s16[0][12]  72_DualSpurVernierLUT_Cnt_s16[0][13]  72_DualSpurVernierLUT_Cnt_s16[0][14]  72_DualSpurVernierLUT_Cnt_s16[0][15]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][16]  72_DualSpurVernierLUT_Cnt_s16[0][17]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][18]  72_DualSpurVernierLUT_Cnt_s16[0][19]  72_DualSpurVernierLUT_Cnt_s16[0][20]  72_DualSpurVernierLUT_Cnt_s16[0][21]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][0]  72_DualSpurVernierLUT_Cnt_s16[1][1]  72_DualSpurVernierLUT_Cnt_s16[1][2]  72_DualSpurVernierLUT_Cnt_s16[1][3]  72_DualSpurVernierLUT_Cnt_s16[1][4]  72_DualSpurVernierLUT_Cnt_s16[1][6]  73_DualSpurVernierLUT_Cnt_s16[1][6]  74_DualSpurVernierLUT_Cnt_s16[1][6]  75_DualSpurVernierLUT_Cnt_s16[1][6]  76_DualSpurVernierLUT_Cnt_s16[1][6]  77_DualSpurVernierLUT_Cnt_s16[1][6]  78_DualSpurVernierLUT_Cnt_s16[1][6]  79_DualSpurVernierLUT_Cnt_s16[1][6]  70_DualSpurVernierLUT_Cnt_s16[1][6]  71_DualSpurVernierLUT_Cnt_s16[1][6]		
T2_DualSpurVernierLUT_Cnt_s16[0][11]  T2_DualSpurVernierLUT_Cnt_s16[0][12]  T2_DualSpurVernierLUT_Cnt_s16[0][13]  T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  T2_DualSpurVernierLUT_Cnt_s16[0][19]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[0][21]  T2_DualSpurVernierLUT_Cnt_s16[1][0]  T2_DualSpurVernierLUT_Cnt_s16[1][1]  T3_DualSpurVernierLUT_Cnt_s16[1][1]		
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 12_DualSpurVernierLUT_Cnt_s16[0][17] 12_DualSpurVernierLUT_Cnt_s16[0][19] 12_DualSpurVernierLUT_Cnt_s16[0][19] 12_DualSpurVernierLUT_Cnt_s16[0][20] 12_DualSpurVernierLUT_Cnt_s16[0][21] 1360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 12_DualSpurVernierLUT_Cnt_s16[1][0] 12_DualSpurVernierLUT_Cnt_s16[1][1] 12_DualSpurVernierLUT_Cnt_s16[1][1] 12_DualSpurVernierLUT_Cnt_s16[1][2] 12_DualSpurVernierLUT_Cnt_s16[1][3] 12_DualSpurVernierLUT_Cnt_s16[1][4] 13_DualSpurVernierLUT_Cnt_s16[1][5] 14_DualSpurVernierLUT_Cnt_s16[1][6] 15_DualSpurVernierLUT_Cnt_s16[1][6] 16_DualSpurVernierLUT_Cnt_s16[1][6] 17_DualSpurVernierLUT_Cnt_s16[1][6] 18_DualSpurVernierLUT_Cnt_s16[1][6] 10_DualSpurVernierLUT_Cnt_s16[1][6] 10_DualSpurVernierLUT_Cnt_s16[1][11] 10_DualSpurVernierLUT_Cnt_s16[1][11] 10_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12] 11_DualSpurVernierLUT_Cnt_s16[1][12]		
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72  T2_DualSpurVernierLUT_Cnt_s16[0][14] 108  T2_DualSpurVernierLUT_Cnt_s16[0][15] 144  T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[0][21] 9  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0		0
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  0  T2_DualSpurVernierLUT_Cnt_s16[1][1]  12_DualSpurVernierLUT_Cnt_s16[1][2]  12_DualSpurVernierLUT_Cnt_s16[1][3]  2  T2_DualSpurVernierLUT_Cnt_s16[1][4]  3  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  7  T2_DualSpurVernierLUT_Cnt_s16[1][9]  8  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9  T2_DualSpurVernierLUT_Cnt_s16[1][11]  0  T2_DualSpurVernierLUT_Cnt_s16[1][11]  10	T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][14]  T2_DualSpurVernierLUT_Cnt_s16[0][15]  T2_DualSpurVernierLUT_Cnt_s16[0][16]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][17]  T2_DualSpurVernierLUT_Cnt_s16[0][18]  252  T2_DualSpurVernierLUT_Cnt_s16[0][19]  288  T2_DualSpurVernierLUT_Cnt_s16[0][20]  324  T2_DualSpurVernierLUT_Cnt_s16[0][21]  360  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][0]  9  T2_DualSpurVernierLUT_Cnt_s16[1][1]  10  T2_DualSpurVernierLUT_Cnt_s16[1][1]  11  T2_DualSpurVernierLUT_Cnt_s16[1][3]  21  T2_DualSpurVernierLUT_Cnt_s16[1][4]  32  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][6]  5  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][9]  T2_DualSpurVernierLUT_Cnt_s16[1][10]  9	T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][15]		108
T2_DualSpurVernierLUT_Cnt_s16[0][16] 180  T2_DualSpurVernierLUT_Cnt_s16[0][17] 216  T2_DualSpurVernierLUT_Cnt_s16[0][18] 252  T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 10		144
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10		
T2_DualSpurVernierLUT_Cnt_s16[0][18]       252         T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0		
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288  T2_DualSpurVernierLUT_Cnt_s16[0][20] 324  T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][6] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][9] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][11] 1		
T2_DualSpurVernierLUT_Cnt_s16[0][20]       324         T2_DualSpurVernierLUT_Cnt_s16[0][21]       360         T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360  T2_DualSpurVernierLUT_Cnt_s16[1][0] 9  T2_DualSpurVernierLUT_Cnt_s16[1][1] 0  T2_DualSpurVernierLUT_Cnt_s16[1][2] 1  T2_DualSpurVernierLUT_Cnt_s16[1][3] 2  T2_DualSpurVernierLUT_Cnt_s16[1][4] 3  T2_DualSpurVernierLUT_Cnt_s16[1][5] 4  T2_DualSpurVernierLUT_Cnt_s16[1][6] 5  T2_DualSpurVernierLUT_Cnt_s16[1][7] 6  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][8] 7  T2_DualSpurVernierLUT_Cnt_s16[1][9] 8  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][10] 9  T2_DualSpurVernierLUT_Cnt_s16[1][11] 0  T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][0]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][1]       0         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][2]       1         T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][3]       2         T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][4]       3         T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][5]       4         T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][6]       5         T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1	T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][7]       6         T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][8]       7         T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][9]       8         T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][10]       9         T2_DualSpurVernierLUT_Cnt_s16[1][11]       0         T2_DualSpurVernierLUT_Cnt_s16[1][12]       1		
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2 DualSpur/erniert LT_Cnt_s16[1][13]		
	T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2 DualSpurVernierLUT Cnt s16[1][18]	7		
T2 DualSpurVernierLUT Cnt s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16] T0_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0] T3_DualSpurVernierLUT_Cst_s46[3][4]	22 2		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name			
	Actual Value	Expected Value	
DigColPs_HwAVernCorrFault_Cnt_M_lgc DigColPs_I2CHwColAngleForTrim_Deg_M_f32		Expected Value  1  981.8181818 ± 0.00048828125	Result

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

DigColPs\_Per2

2014-10-14, 17:31:16+0530



Test Case 3: Path Test

2014-10-14, 17:31:16+0530

DigColPs\_Per2



#### Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

12132.00 Cycles
5985.00 Cycles
5985.00 Cycles
5985.00 Cycles
5935.00 Cycles
5935.00 Cycles
5906.00 Cycles
5921.00 Cycles
2806.00 Cycles
2658.00 Cycles
5773.00 Cycles
8897.00 Cycles
8897.00 Cycles
8863.00 Cycles
8856.00 Cycles
8856.00 Cycles
8856.00 Cycles
8856.00 Cycles
8854.00 Cycles
8854.00 Cycles
8854.00 Cycles
5822.00 Cycles
5822.00 Cycles
12189.00 Cycles
12185.00 Cycles
12185.00 Cycles
3039.00 Cycles
3039.00 Cycles TS3.1 TS3.2 TS3.3 TS3.4 TS3.5 TS3.6 TS3.6 TS3.7 TS3.8 TS3.10 TS3.11 TS3.11 TS3.12 TS3.13 TS3.14 TS3.15 TS3.17 TS3.18 TS3.19 TS3.20 TS3.20 TS3.22 TS3.23



#### VECTOR DESCRIPTION: Description

```
TS3.1 "if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>FALSE if (12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>False && (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>False && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>False && (TrimCompleteEOL_Cnt_T_lgc == TRUE)=>False if (k_SelectFromColumn_Cnt_lgc == TRUE)=>False if (k_SelectFromColumn_Cnt_lgc == TRUE)=>False if ((AbsVernLevelDiff_Cnt_T_u08 > 1U)=>True&& (AngleDataAvailable_Cnt_T_lgc == TRUE)=>False && (DigColPs_PrevAngleDataAvailable_Cnt_T_lgc == TRUE)=>False && (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE)=>False if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) == TRUE)=>False
(AngieUataAvailable_Cnt__|gc == IRUE|=>False &&
(DigCoIPS_PrevAngleDataAvailable_Cnt_M_gc == TRUE)=>False
if (DiagFailed_m([DigCoIPS_SkipStepFltDetectAcc_Cnt_M_u16 + DigCoIPS_VernCorrDetectAcc_Cnt_M_u16), k_SkipSte;
If ((AbsCoIPosDiff_Deg_T_32 > k_VernOORangeThresh_Deg_f32) &&
(AngieDataAvailable_Cnt_T_lgc == TRUE) &&
(DigCoIPS_PrevAngleDataAvailable_Cnt_M_igc == TRUE) |=>False
if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(DigCoIPS_VernCorrDetectAcc_Cnt_M_u16 == 0U) && (DigCoIPS_SkipStepFltDetectAcc_Cnt_M_u16 == 0U) ||
(1)(2CcIPS_SkipStepFltDetect_Cnt_M_u16 == 0U) ||
(1)(2CcIPS_SkipStepFltDetect_
"TS3.4 "if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
(I2CColSensorFault_Cnt_T_lgc == FALSE)=>False &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
(I7ImCompleteOL_Cnt_T_lgc == TRUE)=False"

TS3.5 "if ((ColParityError_Cnt_T_lgc == TRUE) ||
(SpurParityError_Cnt_T_lgc == TRUE)=>True ||
(I2CSensCommFits_Cnt_T_u08 != 0U) )=>True"

TS3.6 "if ((AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
(AngleDataAvailable_Cnt_T_lgc == TRUE) =>False
if (I2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>False
(ParityOrCommFault_Cnt_T_lgc == FALSE)=>False &&
(I2CColSensorFault_Cnt_T_lgc == TRUE)=>False
TS3.7 if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U)=>True&& (DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 == 0U))=>False
TS3.8 if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U)=>True &&
(ParityOrCommFault_Cnt_T_lgc == TRUE)=>True
TS3.9 "else if (((I2CColSensorFault_Cnt_T_lgc == TRUE)=>True)||
TS3.10 if ((MSVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32)=>True && (AngleDataAvailable_Cnt_T_lgc ==
TRUE)=>False)=>False
TS3.11 "if ((I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
(I2CCOlSensorFault_Cnt_T_lgc == D_ANGLEDATA_CNT_U08)=>True
TS3.10 'if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32)=>True && (AngleDataAvailable_Cnt_T_lgc == TRUE)=>False)=>False
TS3.11 " if ((12CHWDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True && (12CColSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (17mCompleteCOL_Cnt_T_lgc = True)=>False"
TS3.12 " if (12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True&& (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>False && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CColSensorFault_Cnt_T_lgc == FALSE)=>True && (12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True && (12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True && (12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == TRUE)=>True && (12CSpurS
```



```
if ((AbsColPosDiff_Deg_T_i32 > k_VernOORangeThresh_Deg_f32) &&
    (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
    (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) >> True"

TS3.17 "DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 = DiagPStep_m(DigColPs_SkipStepFitDetectAcc_Cnt_M_u16, k_SkipStepErrDiag_Cnt_str) => False
    if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) == TRUE) => True
    if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
        (SkipStepFitDetect_Cnt_T_lgc == TRUE) => True||
        (SkipStepFitDetect_
```

Test Step 3.1 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	20
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
Γ2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3





Nama	Input Value
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T3_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2 DualSpurVernierLUT Cnt s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216 400
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
12 DUGIODUI VOITIOLEO I OTIL 3 IUI I II I	•





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T3_DualSpurVernierLUT_Cnt_s46[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	9   11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	10
k_SkipStepErrDiag_Cnt_str.PStep	0
k_SkipStepErrDiag_Cnt_str.NStep	0
k_VernCorrErrorDiag_Cnt_str.Threshold	0
k_VernCorrErrorDiag_Cnt_str.PStep	0
k_VernCorrErrorDiag_Cnt_str.NStep	0
k_VernCorrErrorThresh_Deg_f32	1
	400
k_VernOORangeThresh_Deg_f32	100



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cri	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	Ō	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	<b>✓</b>
Param	0x00	0x00	<b>✓</b>
Status	0x00	0x00	~
NTC	0x6E	0x6E	~
Param	0x00	0x00	~
Status	0x00	0x00	~
NTC	0x6F	0x6F	<b>✓</b>
Param	0x00	0x00	~
Status	0x00	0x00	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 3.2 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
DigColPsInt_GetCustData()	414
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	10
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	100
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1

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Name	Input Value
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2 ColSpurVernierLUT Cnt s16[0][8]	98
T2 ColSpurVernierLUT Cnt s16[0][9]	130
T2 ColSpurVernierLUT Cnt s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSputVernierLUT_Cnt_s16[0][13]	261
	294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	
	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
_ <del></del>	
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-396 -360

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
	324
T2_DualSpurVernierLUT_Cnt_s16[0][20]	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
	8
T2 DualSpurVernierl IIT Cnt s16(3)(4)	U
T2_DualSpurVernierLUT_Cnt_s16[3][4]	10
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	12 14
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	12 14





Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	1			
k_SkipStepErrDiag_Cnt_str.Threshold	255			
k_SkipStepErrDiag_Cnt_str.PStep	50			
k_SkipStepErrDiag_Cnt_str.NStep	50			
k_VernCorrErrorDiag_Cnt_str.Threshold	60			
k_VernCorrErrorDiag_Cnt_str.PStep	50			
k_VernCorrErrorDiag_Cnt_str.NStep	50			
k_VernCorrErrorThresh_Deg_f32	1			
k_VernOORangeThresh_Deg_f32	1800			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	

tgt_Rte_filst_3a_bigColFs.Filft_bigColFsEOL	IGI_FIIII_DIGCOIFSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1427.27271	1427.272727 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1430	1430 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	131	131	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	60	60	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	530	530 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	~
Param	0x0D	0x0D	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)		<b>√</b>
Name	Input Value	
DigColPsInt_GetCustData()	12	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	212	
DigColPs_ColTrimStatic_Deg_M_f32	4.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	58760	
DigColPs_I2CHwColAngle_Deg_M_f32	118.032	
DigColPs_I2CHwDataType_Cnt_M_u08	0	

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Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.308
	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs SkipStepFltDetectAcc Cnt M u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
	5.8
DigColPs_SpurTrimStatic_Deg_M_f32	
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2 ColSpurVernierLUT Cnt s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2 ColSpurVernierLUT Cnt s16[2][0]	0
	8
T2_ColSpurVernierLUT_Cnt_s16[2][1]	
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10] T3_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2

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	I
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
	-108
T2_DualSpurVernierLUT_Cnt_s16[0][8]	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
LA THISISOHIVARDIAN III I DE CIBIANIA	
T2_DualSpurVernierLUT_Cnt_s16[2][12]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.143258		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPe	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	840.685425	840.6854545 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	833.432007	833.432 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5679932	-66.568 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•
		000	
NTC	0x6C	0x6C	

0x04

0x01

0x04

0x01

Param

Status



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetCustData()	24
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs ColTrimStatic Deg M f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs I2CHwColAngle Deg M f32	274.36
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	88.88
DigColPs I2CHwTrimTransCnts Uls M u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs SkipStepFltDetectAcc Cnt M u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs TrimCompStatic Cnt M u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
0	1-

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Input Value  1 0 4 0
4
0
8
6
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5 3
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14
11
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5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
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6
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8
9
0
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2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	. Kosul

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Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1339.56006	1339.56 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	439.560059	439.56 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	
ConstrainOneRev	2	ConstrainOneRev	2	•	
VernierLookup	1	VernierLookup	1	•	
DiagnosticThreshold	1	DiagnosticThreshold	1	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~	

Test Step 3.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	62
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	25
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.45
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	143.95
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	80
DigColPs_TrimCompStatic_Cnt_M_u16	196
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





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Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	10		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.1102527		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2646		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

igi_rite_mat_da_bigdoii 3.1 im_bigdoii 3EOE	tgt_i iii_bigooii acoc		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	192.704544	192.7045455 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	201.449997	201.45 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-707.295471	-707.2954545 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace   ✓				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	<b>~</b>

Test Step 3.6 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt_GetCustData()	100
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.86
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	210.79
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	327 359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2 0
T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2 ColSpurVernierLUT Cnt s16[2][7]	7
T2_ColSpurVernierEUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2 ColSpurVernierLUT Cnt s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T3_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2 DualSpurVernierLUT Cnt s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T3_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Crit_S16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3



DigColPs\_Per2

		•	1000
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	34		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cri	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1400.76807	1400.768182 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1392.65991	1392.66 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	•

Param		0x00 0x00		<b>~</b>
Status		0x00 0x00		~
Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	<b>✓</b>

Rte\_Call\_DigColPs\_Per2\_CP1\_CheckpointReached

0

0

0

492.659912

0x6C

0

0

0

0x6C

492.66 ± 0.0009

Test Step 3.7 (Repeat Count = 1)		V
Name	Input Value	
DigColPsInt_GetCustData()	138	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152	
DigColPs_ColTrimStatic_Deg_M_f32	76	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	57565	
DigColPs_I2CHwColAngle_Deg_M_f32	68.667	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866	
DigColPs_I2CHwSpurAngle_Deg_M_f32	190.108	

DigColPs\_Reql2CSnsrDataType\_Cnt\_M\_u08 DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16

DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16

NTC

DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value

 $\label{tgt_digColPs_Per2_12CHwAbsPos_HwDeg_f32.value} $$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value $$ tgt_Dig$ 

Rte\_Call\_DigColPs\_Per2\_CP1\_CheckpointReached





Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1 0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs PrevColPos Deg M f32	321.3070593
DigColPs_PrevColPos_Deg_M_I32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs SpurSensorFaultAcc Cnt M u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	13.5
DigColPs_TrimCompStatic_Cnt_M_u16	376
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98 130
T2_ColSpurVernierLUT_Cnt_s16[0][9] T2_ColSpurVernierLUT_Cnt_s16[0][10]	130
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	Ō
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	10 8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T3_DualSpurVernierLUT_Cst_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	5
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	0

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			10-10
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2 DualSpurVernierLUT Cnt s16[3][1]	2		
T2 DualSpurVernierLUT Cnt s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3	3	
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	48		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3501		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs I2CHwColAngleForTrim Deg M f32	1062.09448	1062.094545 ± 0.000488281	25
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1072.66699	1072.667 ± 0.000122070312	.5 <b>•</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs SkipStepFltDetectAcc Cnt M u16	10	10	•
DigColPs VernCorrDetectAcc Cnt M u16	0	0	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value	0	0	•
tot DigColPs Per? I2CHwAhsPos HwDeg f32 value	162 094482	162 0945455 + 0 0009	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	•
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V

162.094482

0

162.0945455 ± 0.0009

0

 $\label{tgt_digColPs_Per2_I2CHwAbsPos_HwDeg_f32.value} $$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value $$ $$$ 





Test Step 3.8 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
DigColPsInt_GetCustData()	2
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	116.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.61
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	58.784
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	17.9
DigColPs_TrimCompStatic_Cnt_M_u16	520
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs T3_CalSauri/carried LIT_Cat_ad6(0)(0)	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T3_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][2]	- <del></del>
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	7
LZ CONSULVERNIER III CON STRIZIBI	5
	9
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
	3 1 10

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2 DualSpurVernierLUT Cnt s16[2][17]	6		
T2 DualSpurVernierLUT Cnt s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7		
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067 2579		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16		col/olid Cat Inc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP tgt_DigColPs_Per2_I2CHwAbsP		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_(	_	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs HwAVernCorrFault Cnt M Igc	0	0	Resul
DigColPs_12CHwColAngleForTrim_Deg_M_f32	1491.31091	1491.3109 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1489.40503	1489.405 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	18	18	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4	4	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	4	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	591.310913	591.31091 ± 0.0009	•
tgt DigColPs Per2 TrimComp Cnt Igc.value	0	0	· · · · · · · · · · · · · · · · · · ·

0

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

0



Test Step Call Trace	Test Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.9 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
DigColPsInt_GetCustData()	214
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	241
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.48
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196 229
T2_ColSpurVernierLUT_Cnt_s16[0][12]	261
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

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	1 (1)		
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
	0		
T2_DualSpurVernierLUT_Cnt_s16[2][11]			
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
	8		
T2_DualSpurVernierLUT_Cnt_s16[3][4]			
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2 DualSpurVernierLUT Cnt s16[3][19]	17		
T2 DualSpurVernierLUT Cnt s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	42		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2109		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	<b>~</b>
Discoside 1001 by College Indian Design No. 100	4570 44540	4570 445455 + 0.00040000405	. 4

1573.44543

5

DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32

DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08

1573.445455 ± 0.00048828125

5





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	1581.19995	1581.2 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	673.445435	673.4454545 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace	Test Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.10 (Repeat Count = 1)	Innuit Value
	Input Value
DigColPsInt_GetCustData()	252
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.877
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T3_DualSpurVernierLUT_Cnt_s16[0][10]	-72 36
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][41]	10 0
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T3_DualSpurVernierLUT_Cnt_s16[3][42]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][43]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	175
k_SkipStepErrDiag_Cnt_str.PStep	12
k_SkipStepErrDiag_Cnt_str.NStep	41
k_VernCorrErrorDiag_Cnt_str.Threshold	48
k_VernCorrErrorDiag_Cnt_str.PStep	12

DigColPs\_Per2

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Input Value k\_VernCorrErrorThresh\_Deg\_f32 78.9135704 k\_VernOORangeThresh\_Deg\_f32 1722.743855 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 350.8777566 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 0 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 2056 tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_MecState\_Cnt\_enum$ tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$  $tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL tgt Pim DigColPsEOL

tgt_Rte_inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_PIm_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	476.772736	476.7727273 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	471.677002	471.677 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-428.322998	-428.323 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	<b>✓</b>
NTC	0x6C	0x6C	<b>✓</b>
Param	0x00	0x00	<b>✓</b>
Status	0x00	0x00	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.11 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	290
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2 DualSpurVernierLUT Cnt s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpur/craierLUT_Cnt_s16[2][12]	
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2 DualSpurt/ornight LT Cot of 6(2)(42)	3
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5





Dig 0011 0_1 012		• • •	
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4242		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	447.151367	447.1513636 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	460.399994	460.4 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	<b>✓</b>

-452.848633

0x6C

0x0C

0x01

0x6F

0x00

0x00

-452.8486364 ± 0.0009

0x6C

0x0C

0x01

0x6F

0x00

0x00

Test Step 3.12 (Repeat Count = 1)		🗸
Name	Input Value	
DigColPsInt_GetCustData()	3	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185	
DigColPs_ColTrimStatic_Deg_M_f32	22	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	16067	
DigColPs_I2CHwColAngle_Deg_M_f32	272.64	
DigColPs_I2CHwDataType_Cnt_M_u08	1	

DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value

 $tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value$ 

NTC Param

Status

Param

Status

NTC

 $tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value$ 





Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16937
DigColPs_I2CHwSpurAngle_Deg_M_f32	19.172
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs I2CSensCommFlts Cnt M u08	15
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	O .
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1733.007516
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	47.6
DigColPs_TrimCompStatic_Cnt_M_u16	1492
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs T3_ColSpur/conied_LIT_Cot_o160000	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3] T3_ColSpurVernierLUT_Cnt_s46[4][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
.2_00.0pd. 10	0
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2





	I
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
	-108
T2_DualSpurVernierLUT_Cnt_s16[0][8]	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
LA THISISOHIVARDIAN III I DE CIBIANIA	
T2_DualSpurVernierLUT_Cnt_s16[2][12]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3

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DigColPs\_Per2 Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[2][18] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[2][19] T2\_DualSpurVernierLUT\_Cnt\_s16[2][20] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[2][21] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][0] 22 T2\_DualSpurVernierLUT\_Cnt\_s16[3][1] 2 T2\_DualSpurVernierLUT\_Cnt\_s16[3][2] 4 T2\_DualSpurVernierLUT\_Cnt\_s16[3][3] 6 T2\_DualSpurVernierLUT\_Cnt\_s16[3][4] 8 T2\_DualSpurVernierLUT\_Cnt\_s16[3][5] 10 T2\_DualSpurVernierLUT\_Cnt\_s16[3][6] 12 T2\_DualSpurVernierLUT\_Cnt\_s16[3][7] 14 T2\_DualSpurVernierLUT\_Cnt\_s16[3][8] 16 T2 DualSpurVernierLUT Cnt s16[3][9] 18 T2\_DualSpurVernierLUT\_Cnt\_s16[3][10] 20 T2\_DualSpurVernierLUT\_Cnt\_s16[3][11] 1 T2\_DualSpurVernierLUT\_Cnt\_s16[3][12] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[3][13] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[3][15] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[3][16] 11 T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17 19 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20] T2\_DualSpurVernierLUT\_Cnt\_s16[3][21] 21 k\_SelectFromColumn\_Cnt\_lgc 1  $k\_SkipStepErrDiag\_Cnt\_str.Threshold$ 80 k\_SkipStepErrDiag\_Cnt\_str.PStep 43 k\_SkipStepErrDiag\_Cnt\_str.NStep 7 k VernCorrErrorDiag Cnt str.Threshold 6  $k\_VernCorrErrorDiag\_Cnt\_str.PStep$ 27 k VernCorrErrorDiag Cnt str.NStep 49 k\_VernCorrErrorThresh\_Deg\_f32 86.69760323 k\_VernOORangeThresh\_Deg\_f32 1173.76136 tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 272.6490288  $tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32$ 19.17228091 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 621 tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32$ tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32 tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum tgt DigColPs Per2 MecState Cnt enum  $tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$  $tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc$ 

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	<b>✓</b>
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	968.896362	968.8963636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	970.640015	970.64 ± 0.0001220703125	<b>✓</b>
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	70.6400146	70.64 ± 0.00009	✓
tat DiaColPs Per2 TrimComp Cnt lac.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	<b>~</b>
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~



Test Step 3.13 (Repeat Count = 1)	V
Name	Input Value
DigColPsInt_GetCustData()	7
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigCoIPs_I2CHwColAngle_Cnt_M_u16 DigCoIPs_I2CHwColAngle_Deg_M_f32	46069 360
DigColPs_12CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	224.1625181 7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	1 tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8]	65 98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	327 359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4-
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6] T3_ColSpurVernierLUT_Cnt_s16[2][7]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

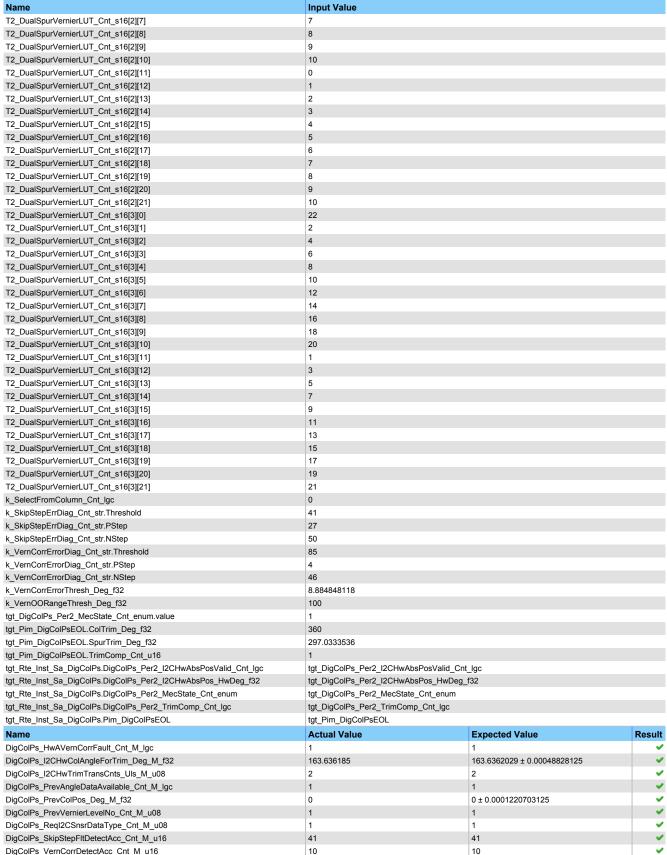
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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

DigColPs\_Per2





0 0	0 = 0	32 2 3 4 4 4			
Name	Actual Value	Expected Value	Result		
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	<b>✓</b>		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	<b>~</b>		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~		
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	•		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~		
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	41	41	~		
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	~		
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~		
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~		
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	~		
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~		
NTC	0x6C	0x6C	~		
Param	0x0E	0x0E	~		
Status	0x01	0x01	~		
NTC	0x6F	0x6F	~		





Name	Actual Value	Expected Value	Result
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	54
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs I2CHwSpurAngle Deg M f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0

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Name	
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[	
T2_ColSpurVernierLUT_Cnt_s16{  st }   1	
T2_ColSpurVernierLUT_Cnt_s16[1][14]  T2_ColSpurVernierLUT_Cnt_s16[1][16]  T2_ColSpurVernierLUT_Cnt_s16[1][16]  T2_ColSpurVernierLUT_Cnt_s16[2][0]  T2_ColSpurVernierLUT_Cnt_s16[2][1]  T2_ColSpurVernierLUT_Cnt_s16[2][1]  T2_ColSpurVernierLUT_Cnt_s16[2][1]  T2_ColSpurVernierLUT_Cnt_s16[2][2]  T2_ColSpurVernierLUT_Cnt_s16[2][3]  T2_ColSpurVernierLUT_Cnt_s16[2][4]  T2_ColSpurVernierLUT_Cnt_s16[2][6]  T2_ColSpurVernierLUT_Cnt_s16[2][6]  T2_ColSpurVernierLUT_Cnt_s16[2][7]  T3_ColSpurVernierLUT_Cnt_s16[2][7]  T4_ColSpurVernierLUT_Cnt_s16[2][8]  T5_ColSpurVernierLUT_Cnt_s16[2][8]  T2_ColSpurVernierLUT_Cnt_s16[2][10]  T2_ColSpurVernierLUT_Cnt_s16[2][11]  T2_ColSpurVernierLUT_Cnt_s16[2][12]  T2_ColSpurVernierLUT_Cnt_s16[2][13]  T2_ColSpurVernierLUT_Cnt_s16[2][14]  T2_ColSpurVernierLUT_Cnt_s16[2][15]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][6]  T3_ColSpurVernierL	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2] T1_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][1]  12_ColSpurVernierLUT_Cnt_s16[2][2]  6  T2_ColSpurVernierLUT_Cnt_s16[2][3]  4  T2_ColSpurVernierLUT_Cnt_s16[2][4]  2  T2_ColSpurVernierLUT_Cnt_s16[2][5]  0  T2_ColSpurVernierLUT_Cnt_s16[2][6]  9  T2_ColSpurVernierLUT_Cnt_s16[2][7]  7  T2_ColSpurVernierLUT_Cnt_s16[2][8]  5  T2_ColSpurVernierLUT_Cnt_s16[2][9]  3  T2_ColSpurVernierLUT_Cnt_s16[2][9]  3  T2_ColSpurVernierLUT_Cnt_s16[2][10]  1  T2_ColSpurVernierLUT_Cnt_s16[2][11]  10  T2_ColSpurVernierLUT_Cnt_s16[2][12]  8  T2_ColSpurVernierLUT_Cnt_s16[2][13]  6  T2_ColSpurVernierLUT_Cnt_s16[2][14]  4  T2_ColSpurVernierLUT_Cnt_s16[2][15]  72_ColSpurVernierLUT_Cnt_s16[2][16]  10  T2_ColSpurVernierLUT_Cnt_s16[3][0]  11  T2_ColSpurVernierLUT_Cnt_s16[3][0]  12_ColSpurVernierLUT_Cnt_s16[3][1]  14  T2_ColSpurVernierLUT_Cnt_s16[3][2]  11  T2_ColSpurVernierLUT_Cnt_s16[3][3]  8  T2_ColSpurVernierLUT_Cnt_s16[3][4]  5  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  16  T2_ColSpurVernierLUT_Cnt_s16[3][6]  17  T2_ColSpurVernierLUT_Cnt_s16[3][6]  18  T2_ColSpurVernierLUT_Cnt_s16[3][6]  19  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  11  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12  T2_ColSpurVernierLUT_Cnt_s16[3][6]  13	
T2_ColSpurVemierLUT_Cnt_s16[2][2]  T2_ColSpurVemierLUT_Cnt_s16[2][3]  T2_ColSpurVemierLUT_Cnt_s16[2][4]  2_ColSpurVemierLUT_Cnt_s16[2][5]  T2_ColSpurVemierLUT_Cnt_s16[2][6]  T2_ColSpurVemierLUT_Cnt_s16[2][7]  T2_ColSpurVemierLUT_Cnt_s16[2][7]  T2_ColSpurVemierLUT_Cnt_s16[2][8]  T2_ColSpurVemierLUT_Cnt_s16[2][8]  T2_ColSpurVemierLUT_Cnt_s16[2][9]  T2_ColSpurVemierLUT_Cnt_s16[2][10]  T2_ColSpurVemierLUT_Cnt_s16[2][11]  T2_ColSpurVemierLUT_Cnt_s16[2][12]  8_ColSpurVemierLUT_Cnt_s16[2][12]  8_ColSpurVemierLUT_Cnt_s16[2][13]  6_ColSpurVemierLUT_Cnt_s16[2][14]  4_ColSpurVemierLUT_Cnt_s16[2][16]  T2_ColSpurVemierLUT_Cnt_s16[2][16]  T2_ColSpurVemierLUT_Cnt_s16[3][0]  T2_ColSpurVemierLUT_Cnt_s16[3][0]  T2_ColSpurVemierLUT_Cnt_s16[3][1]  T2_ColSpurVemierLUT_Cnt_s16[3][1]  T2_ColSpurVemierLUT_Cnt_s16[3][4]  T2_ColSpurVemierLUT_Cnt_s16[3][6]	
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T2_ColSpurVernierLUT_Cnt_s16[2][12]       8         T2_ColSpurVernierLUT_Cnt_s16[2][13]       6         T2_ColSpurVernierLUT_Cnt_s16[2][14]       4         T2_ColSpurVernierLUT_Cnt_s16[2][15]       2         T2_ColSpurVernierLUT_Cnt_s16[2][16]       10         T2_ColSpurVernierLUT_Cnt_s16[3][0]       1         T2_ColSpurVernierLUT_Cnt_s16[3][1]       14         T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[2][13]       6         T2_ColSpurVernierLUT_Cnt_s16[2][14]       4         T2_ColSpurVernierLUT_Cnt_s16[2][15]       2         T2_ColSpurVernierLUT_Cnt_s16[2][16]       10         T2_ColSpurVernierLUT_Cnt_s16[3][0]       1         T2_ColSpurVernierLUT_Cnt_s16[3][1]       14         T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[2][14]       4         T2_ColSpurVernierLUT_Cnt_s16[2][15]       2         T2_ColSpurVernierLUT_Cnt_s16[2][16]       10         T2_ColSpurVernierLUT_Cnt_s16[3][0]       1         T2_ColSpurVernierLUT_Cnt_s16[3][1]       14         T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[2][15]       2         T2_ColSpurVernierLUT_Cnt_s16[2][16]       10         T2_ColSpurVernierLUT_Cnt_s16[3][0]       1         T2_ColSpurVernierLUT_Cnt_s16[3][1]       14         T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][6]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[2][16]       10         T2_ColSpurVernierLUT_Cnt_s16[3][0]       1         T2_ColSpurVernierLUT_Cnt_s16[3][1]       14         T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][1] 14  T2_ColSpurVernierLUT_Cnt_s16[3][2] 11  T2_ColSpurVernierLUT_Cnt_s16[3][3] 8  T2_ColSpurVernierLUT_Cnt_s16[3][4] 5  T2_ColSpurVernierLUT_Cnt_s16[3][5] 2  T2_ColSpurVernierLUT_Cnt_s16[3][6] 15  T2_ColSpurVernierLUT_Cnt_s16[3][7] 12  T2_ColSpurVernierLUT_Cnt_s16[3][8] 9  T2_ColSpurVernierLUT_Cnt_s16[3][9] 6  T2_ColSpurVernierLUT_Cnt_s16[3][9] 6  T2_ColSpurVernierLUT_Cnt_s16[3][10] 3  T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7	
T2_ColSpurVernierLUT_Cnt_s16[3][15] 4	
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17	
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396	
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360	
T2_DualSpurVernierLUT_Cnt_s16[0][2] -324	
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288	
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252	
T2 DualSpurVernierLUT Cnt s16[0][5] -216	
T2_DualSpurVernierLUT_Cnt_s16[0][6] -180	
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144	
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72	
T2_DualSpurVernierLUT_Cnt_s16[0][10] -36	
T2_DualSpurVernierLUT_Cnt_s16[0][11] 0	
T2_DualSpurVernierLUT_Cnt_s16[0][12] 36	
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	
T2_DualSpurVernierLUT_Cnt_s16[0][16] 180	
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	
T2_DualSpurVernierLUT_Cnt_s16[0][18] 252	
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288	
T2_DualSpurVernierLUT_Cnt_s16[0][20] 324	
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360	
T2_DualSpurVernierLUT_Cnt_s16[1][0] 9	
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0	
T2_DualSpurVernierLUT_Cnt_s16[1][2] 1	
T2_DualSpurVernierLUT_Cnt_s16[1][3] 2	
T2_DualSpurVernierLUT_Cnt_s16[1][4] 3	
T2_DualSpurVernierLUT_Cnt_s16[1][5] 4	
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5	
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6	
T2_DualSpurVernierLUT_Cnt_s16[1][8] 7	
T2_DualSpurVernierLUT_Cnt_s16[1][9] 8	
T2_DualSpurVernierLUT_Cnt_s16[1][10] 9	

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Input Value
0
1
2
3
5
6
7
8
9
0
0
1 2
3
4
5
6
7
8
9
10
0 1
2
3
4
5
6
7
8
9
10 22
2
4
6
8
10
12
14
16
18 20
1
3
5
7
9
11
13
15
17 19
21
0
41
27
50
85
4
46 9 99/9/9/119
8.884848118 1087.934204
0
360
297.0333536
1
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_TrimComp_Cnt_lgc

DigColPs\_Per2

Param

Status

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**Actual Value Expected Value** DigColPs\_HwAVernCorrFault\_Cnt\_M\_lgc DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32 163.636185 163.6362029 ± 0.00048828125 DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 3 DigColPs\_PrevAngleDataAvailable\_Cnt\_M\_lgc DigColPs\_PrevColPos\_Deg\_M\_f32 0 ± 0.0001220703125 0 DigColPs\_PrevVernierLevelNo\_Cnt\_M\_u08 1 DigColPs\_ReqI2CSnsrDataType\_Cnt\_M\_u08 1 DigColPs\_SkipStepFltDetectAcc\_Cnt\_M\_u16 10 10 DigColPs\_VernCorrDetectAcc\_Cnt\_M\_u16 10 10 DigColPs\_VernierAngleOORange\_Cnt\_M\_lgc 1 1 tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value 0 -736.363831 tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value -736.3637971 ± 0.0009 tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value 0x6C NTC 0x6C Param 0x0C 0x0C Status 0x01 0x01 NTC 0x6F 0x6F

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x00

0x00

0x00

0x00

Test Step 3.15 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.03
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2 ColSpurVernierLUT Cnt s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
	-396 -360
T2_DualSpurVernierLUT_Cnt_s16[0][1] T3_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][2] T3_DualSpurVernierLUT_Cnt_s16[0][3]	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252 -216
T2_DualSpurVernierLUT_Cnt_s16[0][5]	
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144

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Section   Sect	Nome	Input Value
12 Design/cremental   Cos. 189 01   202	Name To Due locate for late LITE Costs add(SVIAC)	Input Value
12_DasSpacement_D_Cot_stepsime    252		
T2_DusSpurinment_U_Cot_stroppy  289		
12_Daspyriment U_OL_15(192)		
17_Despoyment UT, Ord, 19(19)   9   17_Despoyment UT, Ord, 19(19)   9   17_Despoyment UT, Ord, 19(19)   1   1   1   1   1   1   1   1   1		
12_DasSprivement U. Cort. 51(19)		
12_DasSportmentU_CR_1519[13]   0   12_DasSportmentU_CR_1519[13]   1   12_DasSportmentU_CR_1519[13]   2   12_DasSportmentU_CR_1519[13]   3   12_DasSportmentU_CR_1519[13]   3   12_DasSportmentU_CR_1519[13]   5   12_DasSportmentU_CR_1519[13]   5   12_DasSportmentU_CR_1519[13]   7   12_DasSportmentU_CR_1519[13]   7   12_DasSportmentU_CR_1519[13]   7   12_DasSportmentU_CR_1519[13]   7   12_DasSportmentU_CR_1519[13]   7   12_DasSportmentU_CR_1519[13]   7   12_DasSportmentU_CR_1519[13]   1   12_DasSportmen		
T2_DusSpurVennetU_Ot_1 stq173    1		
T2_DusSpurVerneLUT_CM_110[13] 3 12_DusSpurVerneLUT_CM_110[13] 13_DusSpurVerneLUT_CM_110[13] 14_DusSpurVerneLUT_CM_110[13] 15_DusSpurVerneLUT_CM_110[13] 16_DusSpurVerneLUT_CM_110[13] 17_DusSpurVerneLUT_CM_110[13] 17_DusSpurVerneLUT_CM_110[13] 17_DusSpurVerneLUT_CM_110[13] 17_DusSpurVerneLUT_CM_110[13] 17_DusSpurVerneLUT_CM_110[13] 18_DusSpurVerneLUT_CM_110[13] 18_DusSpurVerneLUT_CM_110[13] 19_DusSpurVerneLUT_CM_110[13] 19_DusSpurVerneLUT_CM_110[13] 10_DusSpurVerneLUT_CM_110[13] 11_DusSpurVerneLUT_CM_110[13] 11_D	T2_DualSpurVernierLUT_Cnt_s16[1][1]	
T2_Dasign/venerU_Cot_10(1)2) 4 12_Dasign/venerU_Cot_10(1)2) 4 12_Dasign/venerU_Cot_10(1)2) 4 12_Dasign/venerU_Cot_10(1)2) 5 12_Dasign/venerU_Cot_10(1)2) 7	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DasSquvVernetU_Crt_10(1)[0] 6 T2_DasSquvVernetU_Crt_10(1)[0] 7 T2_DasSquvVernetU_Crt_10(1)[0][12] 7 T2_DasSquvVernetU_Crt_10(1)[12] 7 T2_DasSquvV	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T. DusSpurVenietUT Cet. 310  10  10	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2. Duslipu/vernetUT. Crt.; 19(1)[9] 72. Duslipu/vernetUT. Crt.; 19(1)[9] 73. Duslipu/vernetUT. Crt.; 19(1)[9] 74. Duslipu/vernetUT. Crt.; 19(1)[10] 75. Duslipu/vernetUT. Crt.; 19(1)[10] 76. Duslipu/vernetUT. Crt.; 19(1)[12] 76. Duslipu/vernetUT. Crt.; 19(1)[13] 77. Duslipu/vernetUT. Crt.; 19(1)[14] 78. Duslipu/vernetUT. Crt.; 19(1)[14] 79. Duslipu/vernetUT. Crt.; 19(1)[16] 70. Duslipu/vernetUT. Crt.; 19(1)[16] 71. Duslipu/vernetUT. Crt.; 19(1)[16] 71. Duslipu/vernetUT. Crt.; 19(1)[16] 72. Duslipu/vernetUT. Crt.; 19(1)[16] 73. Duslipu/vernetUT. Crt.; 19(1)[16] 74. Duslipu/vernetUT. Crt.; 19(1)[16] 75. Duslipu/vernetUT. Crt.; 19(1)[16] 76. Duslipu/vernetUT. Crt.; 19(1)[16] 77. Duslipu/vernetUT. Crt.; 19(1)[17] 78. Duslipu/vernetUT. Crt.; 19(1)[17] 79. Duslipu/vernetUT. Crt.; 19(1)[17] 70. Duslipu/vernetUT. Crt.; 19(1)[17] 70. Duslipu/vernetUT. Crt.; 19(1)[17] 71. Duslipu/vernetUT. Crt.; 19(1)[17] 72. Duslipu/vernetUT. Crt.; 19(1)[17] 73. Duslipu/vernetUT. Crt.; 19(1)[17] 74. Duslipu/vernetUT. Crt.; 19(1)[17] 75. Duslipu/vernetUT. Crt.; 19(1)[17] 76. Duslipu/vernetUT. Crt.; 19(1)[17] 77. Duslipu/vernetUT. Crt.; 19(1)[17] 78. Duslipu/vernetUT. Crt.; 19(1)[17] 79. Duslipu/vernetUT. Crt.; 19(1)[17] 70. Duslipu/vernetUT. Crt.; 19(1)[17] 71. Duslipu/vernetUT. Crt.; 19(1)[17] 72. Duslipu/vernetUT. Crt.; 19(1)[17] 73. Duslipu/vernetUT. Crt.; 19(1)[17] 74. Duslipu/vernetUT. Crt.; 19(1)[17] 75. Duslipu/vernetUT. Crt.; 19(1)[17] 76. Duslipu/vernetUT. Crt.; 19(1)[17] 77. Duslipu/vernetUT. Crt.; 19(1)[1	T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T. DualSpurVermoRUL Cent. 19(1) 9  8   8   17. DualSpurVermoRUL Cent. 19(1) 9  8   8   17. DualSpurVermoRUL Cent. 19(1) 19  9   9   17. DualSpurVermoRUL Cent. 19(1) 19  9   17. DualSpurVermoRUL Cent. 19(1) 19  9   17. DualSpurVermoRUL Cent. 19(1) 19  1   17. DualSpurVermoRUL Cent.	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DusSparVermeUT_Cen_1 28(3)191 9 172_DusSparVermeUT_Cen_1 28(3)191 172_DusSparVermeUT_Cen_1 28(3)192 172_DusSparVermeUT_Cen_1 28(3)192 172_DusSparVermeUT_Cen_1 28(3)191 172_D	T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
72. DuaSport/venietUT_Cet_ s10(1910) 172. DuaSport/venietUT_Cet_ s10(1911) 173. DuaSport/venietUT_Cet_ s10(1911) 173. DuaSport/venietUT_Cet_ s10(1911) 174. DuaSport/venietUT_Cet_ s10(1911) 175. DuaSport/venietUT_Cet_ s10(1911) 176. DuaSport/venietUT_Cet_ s10(1911) 177. DuaSport/venietUT_Cet_ s10(1911) 177. DuaSport/venietUT_Cet_ s10(1911) 178. DuaSport/venietUT_Cet_ s10(1911) 179. DuaSport/venietUT_Cet_ s10(1911) 179. DuaSport/venietUT_Cet_ s10(1911) 179. DuaSport/venietUT_Cet_ s10(1911) 179. DuaSport/venietUT_Cet_ s10(1912) 170. DuaSport/venietUT_Cet_ s10(1912) 171. DuaSport/venietUT_Cet_ s10(1912) 171. DuaSport/venietUT_Cet_ s10(1912) 171. DuaSport/venietUT_Cet_ s10(1912) 172. DuaSport/venietUT_Cet_ s10(1912) 173. DuaSport/venietUT_Cet_ s10(1912) 174. DuaSport/venietUT_Cet_ s10(1912) 175. DuaSport/venietUT_Cet_ s10(1912) 176. DuaSport/venietUT_Cet_ s10(1912) 177. DuaSport/venietUT_Cet_ s10(1	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
12 Qualigos/viemeLUC ent still 1911 1	T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DusBpu/VermeUT_CR_1 strip[112]   1   1   1   1   1   1   1   1   1	T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DasSparVernictU_Cot_s10[114] 2		0
12, DasiSpurVernictU, Fott, 510(115) 4 12, DasiSpurVernictU, Fott, 510(115) 4 12, DasiSpurVernictU, Fott, 510(115) 5 12, DasiSpurVernictU, Fott, 510(116) 7 13, DasiSpurVernictU, Fott, 510(116) 7 14, DasiSpurVernictU, Fott, 510(116) 7 15, DasiSpurVernictU, Fott, 510(116) 7 16, DasiSpurVernictU, Fott, 510(116) 7 17, DasiSpurVernictU, Fott, 510(116) 7 18, DasiSpurVernictU, Fott, 510(116) 7 19, DasiSpurVernictU, Fott, 510(116) 7 10, DasiSpurVernictU, Fott, 510(116) 7 11, DasiSpurVernictU, Fott, 510(116) 7 12, DasiSpurVernictU, Fott, 510(116) 7 13, DasiSpurVernictU, Fott, 510(116) 7 14, DasiSpurVernictU, Fott, 510(116) 7 15, DasiSpurVernictU, Fott, 510(116) 7 16, DasiSpurVernictU, Fott, 510(116) 7 17, DasiSpurVernic	T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
17. DualSparVermentUT. Cnt. 15(0)[15] 12. DualSparVermentUT. Cnt. 15(0)[15] 13. DualSparVermentUT. Cnt. 15(0)[16] 15. DualSparVermentUT. Cnt. 15(0)[16] 17. DualSparVermentUT. Cnt. 15(0)[17] 17. DualSparVermentUT. Cnt. 15(0		2
T2, DuaSpurVermetUT. Cnt. s16(1):16  T2, DuaSpurVermetUT. Cnt. s16(1):17  T2, DuaSpurVermetUT. Cnt. s16(1):18  T2, DuaSpurVermetUT. Cnt. s16(1):19  T2, DuaSpur		
T. D. Dual SparVerment U. F. Cit.   1610   15		
12. Dual SpurVermick UT. Cot.; 19(1):17) 12. Dual SpurVermick UT. Cot.; 19(1):19) 13. Dual SpurVermick UT. Cot.; 19(1):19) 14. Dual SpurVermick UT. Cot.; 19(1):20) 15. Dual SpurVermick UT. Cot.; 19(1):20) 17. Dual SpurVermick UT. Cot.; 19(1):20) 17. Dual SpurVermick UT. Cot.; 19(2):11) 18. Dual SpurVermick UT. Cot.; 19(2):11) 19. Dual SpurVermick UT. Cot.; 19(2):11) 19. Dual SpurVermick UT. Cot.; 19(2):11) 19. Dual SpurVermick UT. Cot.; 19(2):12) 19. Dual SpurVermick UT. Cot.; 19(2):13) 19. Dual SpurVermick UT. Cot.; 19(2):13) 19. Dual SpurVermick UT. Cot.; 19(2):14) 19. Dual SpurVermick UT. Cot.; 19(2):19 19. Dual SpurVermick UT. Cot.; 1		
12_Dus SpurVerment_UT_Cot_s16[1]19  8     12_Dus SpurVerment_UT_Cot_s16[1]20  9     12_Dus SpurVerment_UT_Cot_s16[1]20  9     12_Dus SpurVerment_UT_Cot_s16[1]20  9     12_Dus SpurVerment_UT_Cot_s16[1]20  0     12_Dus SpurVerment_UT_Cot_s16[2]0  0     12_Dus SpurVerment_UT_Cot_s16[2]0  1     12_Dus SpurVerment_UT_Cot_s16[2]0  1     12_Dus SpurVerment_UT_Cot_s16[2]0  3     12_Dus SpurVerment_UT_Cot_s16[2]0  3     12_Dus SpurVerment_UT_Cot_s16[2]0  4     12_Dus SpurVerment_UT_Cot_s16[2]0  6     12_Dus SpurVerment_UT_Cot_s16[2]0  6     12_Dus SpurVerment_UT_Cot_s16[2]0  7     12_Dus SpurVerment_UT_Cot_s16[2]0  9     12_Dus SpurVerment_UT_Cot_s16[2]0  9     12_Dus SpurVerment_UT_Cot_s16[2]0  9     12_Dus SpurVerment_UT_Cot_s16[2]0  10		
17_DusSpurVernetUT_Crt_15(1)[10]   8		
T2_DusSpurVernictUT_Cnt_st(P[120]   9		
12. DualSpurVemiet.UT_Cnt_s16[1][1] 12. DualSpurVemiet.UT_Cnt_s16[2][1] 13. DualSpurVemiet.UT_Cnt_s16[2][1] 14. DualSpurVemiet.UT_Cnt_s16[2][2] 15. DualSpurVemiet.UT_Cnt_s16[2][3] 16. DualSpurVemiet.UT_Cnt_s16[2][3] 17. DualSpurVemiet.UT_Cnt_s16[2][3] 18. DualSpurVemiet.UT_Cnt_s16[2][5] 19. DualSpurVemiet.UT_Cnt_s16[2][6] 19. DualSpurVemiet.UT_Cnt_s16[2][6] 19. DualSpurVemiet.UT_Cnt_s16[2][7] 17. DualSpurVemiet.UT_Cnt_s16[2][8] 18. T2. DualSpurVemiet.UT_Cnt_s16[2][8] 19. DualSpurVemiet.UT_Cnt_s16[2][8] 19. DualSpurVemiet.UT_Cnt_s16[2][9] 19. DualSpurVemiet.UT_Cnt_s16[2][9] 10. DualSpurVemiet.UT_Cnt_s16[2][9] 10. DualSpurVemiet.UT_Cnt_s16[2][9] 11. DualSpurVemiet.UT_Cnt_s16[2][1] 12. DualSpurVemiet.UT_Cnt_s16[2][1] 12. DualSpurVemiet.UT_Cnt_s16[2][1] 12. DualSpurVemiet.UT_Cnt_s16[2][1] 13. DualSpurVemiet.UT_Cnt_s16[2][1] 14. DualSpurVemiet.UT_Cnt_s16[2][1] 15. DualSpurVemiet.UT_Cnt_s16[2][1] 16. DualSpurVemiet.UT_Cnt_s16[2][1] 17. DualSpurVemiet.UT_Cnt_s16[2][1] 18. DualSpurVemiet.UT_Cnt_s16[2][1] 19. DualSpurVemiet.UT_Cnt_s16[2][1] 10. DualSpurVemiet.UT_Cnt_s16[2][1] 11. DualSpurVemiet.UT_Cnt_s16[2][1] 12. DualSpurVemiet.UT_Cnt_s16[2][1] 13. DualSpurVemiet.UT_Cnt_s16[2][1] 14. DualSpurVemiet.UT_Cnt_s16[2][1] 15. DualSpurVemiet.UT_Cnt_s16[2][1] 16. DualSpurVemiet.UT_Cnt_s16[2][1] 17. DualSpurVemiet.UT_Cnt_s16[2][1] 18. DualSpurVemiet.UT_Cnt_		
12. DualSpurVement.UT_Cnt_stig[1]   1   17. DualSpurVement.UT_Cnt_stig[2]   2   12. DualSpurVement.UT_Cnt_stig[2]   2   12. DualSpurVement.UT_Cnt_stig[2]   3   3   13. DualSpurVement.UT_Cnt_stig[2]   4   4   12. DualSpurVement.UT_Cnt_stig[2]   5   12. DualSpurVement.UT_Cnt_stig[2]   6   12. DualSpurVement.UT_Cnt_stig[2]   6   12. DualSpurVement.UT_Cnt_stig[2]   7   13. DualSpurVement.UT_Cnt_stig[2]   7   14. DualSpurVement.UT_Cnt_stig[2]   7   15. DualSpurVement.UT_Cnt_stig[2]   9   16. DualSpurVement.UT_Cnt_stig[2]   9   17. DualSpurVement.UT_Cnt_stig[2]   10   18. DualSpurVement.UT_Cnt_stig[2]   10   19. DualSpurVement.UT_Cnt_stig[2]   11   19. DualSpurVement.UT_Cnt_stig[2]   11   19. DualSpurVement.UT_Cnt_stig[2]   12   12. DualSpurVement.UT_Cnt_stig[2]   14   14. DualSpurVement.UT_Cnt_stig[2]   16   17. DualSpurVement.UT_Cnt_stig[2]   17   18. DualSpurVement.UT_Cnt_stig[2]   17   18. DualSpurVement.UT_Cnt_stig[2]   18   18. DualSpurVement.UT_Cnt_stig[2]   19   19. DualSpurVement.UT_Cnt_stig[2]   19   19. DualSpurVement.UT_Cnt_stig[2]   10   19. DualSpurVement.UT_Cnt_stig[3]   11		
12		
12. DualSpurVermierLUT_Cnt_st6[2][15]   2   7   2   DualSpurVermierLUT_Cnt_st6[2][15]   3   3   3   3   3   3   3   3   3		
T2_DualSpurVemierLUT_Cnt_s16[2][4] 4 72_DualSpurVemierLUT_Cnt_s16[2][4] 4 72_DualSpurVemierLUT_Cnt_s16[2][6] 6 72_DualSpurVemierLUT_Cnt_s16[2][6] 6 72_DualSpurVemierLUT_Cnt_s16[2][6] 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7		
12 Dua SpurVermierLUT_Cnt_s16 2 14    4     12 Dua SpurVermierLUT_Cnt_s16 2 15    5     13 Dua SpurVermierLUT_Cnt_s16 2 15    6     14 Dua SpurVermierLUT_Cnt_s16 2 17    7     15 Dua SpurVermierLUT_Cnt_s16 2 18    8     15 Dua SpurVermierLUT_Cnt_s16 2 19    9     17 Dua SpurVermierLUT_Cnt_s16 2 19    9     18 Dua SpurVermierLUT_Cnt_s16 2 11    0     19 Dua SpurVermierLUT_Cnt_s16 2 11    0     10 Dua SpurVermierLUT_Cnt_s16 2 11    1   0     12 Dua SpurVermierLUT_Cnt_s16 2 11    1   0     12 Dua SpurVermierLUT_Cnt_s16 2 11    1   2   Dua SpurVermierLUT_Cnt_s16 2 11    3   2     15 Dua SpurVermierLUT_Cnt_s16 2 11    3   4   4   4   4   4   4   4     15 Dua SpurVermierLUT_Cnt_s16 2 11    5   7   7   7   7   7   7   7   7		
T2 DuaiSpurVermierLUT_Cnt_s16[2][5]   6     T2 DuaiSpurVermierLUT_Cnt_s16[2][6]   6     T2 DuaiSpurVermierLUT_Cnt_s16[2][7]   7     T3 DuaiSpurVermierLUT_Cnt_s16[2][8]   8     T2 DuaiSpurVermierLUT_Cnt_s16[2][8]   9     T2 DuaiSpurVermierLUT_Cnt_s16[2][10]   10     T2 DuaiSpurVermierLUT_Cnt_s16[2][11]   0     T2 DuaiSpurVermierLUT_Cnt_s16[2][11]   1     T2 DuaiSpurVermierLUT_Cnt_s16[2][12]   1     T2 DuaiSpurVermierLUT_Cnt_s16[2][13]   2     T2 DuaiSpurVermierLUT_Cnt_s16[2][14]   3     T2 DuaiSpurVermierLUT_Cnt_s16[2][16]   4     T2 DuaiSpurVermierLUT_Cnt_s16[2][16]   5     T2 DuaiSpurVermierLUT_Cnt_s16[2][17]   6     T3 DuaiSpurVermierLUT_Cnt_s16[2][18]   7     T2 DuaiSpurVermierLUT_Cnt_s16[2][18]   7     T2 DuaiSpurVermierLUT_Cnt_s16[2][2]   8     T2 DuaiSpurVermierLUT_Cnt_s16[2][2]   8     T2 DuaiSpurVermierLUT_Cnt_s16[2][2]   9     T2 DuaiSpurVermierLUT_Cnt_s16[2][2]   9     T2 DuaiSpurVermierLUT_Cnt_s16[2][2]   10     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   2     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   2     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   2     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   2     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   4     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   8     T3 DuaiSpurVermierLUT_Cnt_s16[3][1]   10     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   10     T2 DuaiSpurVermierLUT_Cnt_s16[3][1]   10     T3 DuaiSpurVermierLUT_Cnt_s16[3][1]   11     T4 DuaiSpurVermierLUT_Cnt_s16[3][1]   11     T5 DuaiSpurVermierLUT_Cnt_s16[3][		
12 Dua SpurVernierLUT_Cnt_s16[2] 6    6     12 Dua SpurVernierLUT_Cnt_s16[2] 8    8     13 Dua SpurVernierLUT_Cnt_s16[2] 8    8     14 Dua SpurVernierLUT_Cnt_s16[2] 8    9     15 Dua SpurVernierLUT_Cnt_s16[2] 8    9     16 Dua SpurVernierLUT_Cnt_s16[2] 11    0     17 Dua SpurVernierLUT_Cnt_s16[2] 11    0     18 Dua SpurVernierLUT_Cnt_s16[2] 12    1     19 Dua SpurVernierLUT_Cnt_s16[2] 13    2     10 Dua SpurVernierLUT_Cnt_s16[2] 14    3     12 Dua SpurVernierLUT_Cnt_s16[2] 15    4     12 Dua SpurVernierLUT_Cnt_s16[2] 16    5     13 Dua SpurVernierLUT_Cnt_s16[2] 17    6     14 Dua SpurVernierLUT_Cnt_s16[2] 17    6     15 Dua SpurVernierLUT_Cnt_s16[2] 19    8     17 Dua SpurVernierLUT_Cnt_s16[2] 20    9     18 Dua SpurVernierLUT_Cnt_s16[2] 20    9     19 Dua SpurVernierLUT_Cnt_s16[2] 20    9     10 Dua SpurVernierLUT_Cnt_s16[3] 3   6     10 Dua SpurVernierLUT_Cnt_s16[3] 3   6     11 Dua SpurVernierLUT_Cnt_s16[3] 3   6     12 Dua SpurVernierLUT_Cnt_s16[3] 3   6     13 Dua SpurVernierLUT_Cnt_s16[3] 3   6     14 Dua SpurVernierLUT_Cnt_s16[3] 3   6     15 Dua SpurVernierLUT_Cnt_s16[3] 3   6     16 Dua SpurVernierLUT_Cnt_s16[3] 3   6     17 Dua SpurVernierLUT_Cnt_s16[3] 3   6     18 Dua SpurVernierLUT_Cnt_s16[3] 3   6     19 Dua SpurVernierLUT_Cnt_s16[3] 3   6     10 Dua SpurVernierLUT_Cnt_s16[3] 3   6     11 Dua SpurVernierLUT_Cnt_s16[3] 3   7     12 Dua SpurVernierLUT_Cnt_s16[3] 4   7     13 Dua SpurVernierLUT_Cnt_s16[3] 4   7     14 Dua SpurVernierLUT_Cnt_s16[3] 4   7     15 Dua SpurVernierLUT_Cnt_s16[3] 4   7     17 Dua SpurVernierLUT_Cnt_s16[3] 4   7     18 Dua SpurVernierLUT_Cnt_s16[3] 4   7     19 Dua SpurVernierLUT_Cnt_s16[3] 4   7     10 Dua SpurVernierLUT_Cnt_s16[3] 4   7     11 Dua SpurVernierLUT_Cnt_s16[3] 4   7     12 Dua SpurVernierLUT_Cnt_s16[3] 4   7     13 Dua SpurVernierLUT_Cnt_s16[3] 4   7     14 Dua SpurVernierLUT_Cnt_s16[3] 4		
T2 DualSpurVernierLUT_Cnt_st6[2][7]   7   7   7   7   7   7   7   7   7		
T2_DualSpurVemierLUT_Cnt_st6[2][8]   8   8   7   DualSpurVemierLUT_Cnt_st6[2][9]   9   9   9   9   9   9   9   9   9		
T2_DualSpurVermierLUT_Cnt_s16[2][9] T2_DualSpurVermierLUT_Cnt_s16[2][10] T2_DualSpurVermierLUT_Cnt_s16[2][11] T2_DualSpurVermierLUT_Cnt_s16[2][12] T2_DualSpurVermierLUT_Cnt_s16[2][13] T2_DualSpurVermierLUT_Cnt_s16[2][14] T2_DualSpurVermierLUT_Cnt_s16[2][16] T2_DualSpurVermierLUT_Cnt_s16[2][16] T2_DualSpurVermierLUT_Cnt_s16[2][16] T2_DualSpurVermierLUT_Cnt_s16[2][16] T2_DualSpurVermierLUT_Cnt_s16[2][17] E6 T2_DualSpurVermierLUT_Cnt_s16[2][18] T2_DualSpurVermierLUT_Cnt_s16[2][18] T2_DualSpurVermierLUT_Cnt_s16[2][20] E7_DualSpurVermierLUT_Cnt_s16[2][20] E7_DualSpurVermierLUT_Cnt_s16[2][20] E7_DualSpurVermierLUT_Cnt_s16[3][0] E7_DualSpurVermierLUT_Cnt_s16[3][10] E7_DualSpurVermierLUT_Cnt		
T2_DualSpurVermiert.UT_Cnt_st6[2][10]   10   10   12_DualSpurVermiert.UT_Cnt_st6[2][11]   0   12_DualSpurVermiert.UT_Cnt_st6[2][12]   1   12_DualSpurVermiert.UT_Cnt_st6[2][13]   2   12_DualSpurVermiert.UT_Cnt_st6[2][14]   3   3   12_DualSpurVermiert.UT_Cnt_st6[2][16]   4   12_DualSpurVermiert.UT_Cnt_st6[2][16]   5   14_DualSpurVermiert.UT_Cnt_st6[2][16]   5   14_DualSpurVermiert.UT_Cnt_st6[2][16]   5   14_DualSpurVermiert.UT_Cnt_st6[2][16]   7   12_DualSpurVermiert.UT_Cnt_st6[2][18]   7   12_DualSpurVermiert.UT_Cnt_st6[2][19]   8   14_DualSpurVermiert.UT_Cnt_st6[2][19]   8   14_DualSpurVermiert.UT_Cnt_st6[2][19]   8   14_DualSpurVermiert.UT_Cnt_st6[2][19]   10   12_DualSpurVermiert.UT_Cnt_st6[2][1]   10   12_DualSpurVermiert.UT_Cnt_st6[2][1]   10   12_DualSpurVermiert.UT_Cnt_st6[3][0]   12_DualSpurVermiert.UT_Cnt_st6[3][0]   12_DualSpurVermiert.UT_Cnt_st6[3][1]   10_DualSpurVermiert.UT_Cnt_st6[3][1]		
T2_DualSpurVerniertUT_Cnt_s16[2][11] T2_DualSpurVerniertUT_Cnt_s16[2][12] T2_DualSpurVerniertUT_Cnt_s16[2][13] T2_DualSpurVerniertUT_Cnt_s16[2][14] T2_DualSpurVerniertUT_Cnt_s16[2][15] T2_DualSpurVerniertUT_Cnt_s16[2][15] T2_DualSpurVerniertUT_Cnt_s16[2][16] T2_DualSpurVerniertUT_Cnt_s16[2][17] F2_DualSpurVerniertUT_Cnt_s16[2][17] F3_DualSpurVerniertUT_Cnt_s16[2][18] F4_DualSpurVerniertUT_Cnt_s16[2][19] F4_DualSpurVerniertUT_Cnt_s16[2][19] F5_DualSpurVerniertUT_Cnt_s16[2][19] F5_DualSpurVerniertUT_Cnt_s16[2][19] F5_DualSpurVerniertUT_Cnt_s16[3][1] F6_DualSpurVerniertUT_Cnt_s16[3][1] F6_DualSpurVerni	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVerniertUT_Cnt_s16[2][12] T2_DualSpurVerniertUT_Cnt_s16[2][13] T2_DualSpurVerniertUT_Cnt_s16[2][14] T2_DualSpurVerniertUT_Cnt_s16[2][15] T2_DualSpurVerniertUT_Cnt_s16[2][15] T2_DualSpurVerniertUT_Cnt_s16[2][16] T2_DualSpurVerniertUT_Cnt_s16[2][18] T2_DualSpurVerniertUT_Cnt_s16[2][18] T2_DualSpurVerniertUT_Cnt_s16[2][19] T2_DualSpurVerniertUT_Cnt_s16[2][19] T2_DualSpurVerniertUT_Cnt_s16[2][19] T2_DualSpurVerniertUT_Cnt_s16[2][19] T2_DualSpurVerniertUT_Cnt_s16[2][1] T2_DualSpurVerniertUT_Cnt_s16[3][0] T2_DualSpurVerniertUT_Cnt_s16[3][0] T2_DualSpurVerniertUT_Cnt_s16[3][1] T2_DualSpurVerniertUT_Cnt_s16[3][1] T2_DualSpurVerniertUT_Cnt_s16[3][1] T2_DualSpurVerniertUT_Cnt_s16[3][1] T2_DualSpurVerniertUT_Cnt_s16[3][6] T2_DualSpurVernier	T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][13]  12_DualSpurVernierLUT_Cnt_s16[2][14]  13_DualSpurVernierLUT_Cnt_s16[2][16]  12_DualSpurVernierLUT_Cnt_s16[2][16]  12_DualSpurVernierLUT_Cnt_s16[2][17]  12_DualSpurVernierLUT_Cnt_s16[2][18]  12_DualSpurVernierLUT_Cnt_s16[2][19]  13_DualSpurVernierLUT_Cnt_s16[2][20]  14_DualSpurVernierLUT_Cnt_s16[2][21]  10_DualSpurVernierLUT_Cnt_s16[2][21]  10_DualSpurVernierLUT_Cnt_s16[2][21]  10_DualSpurVernierLUT_Cnt_s16[3][1]  12_DualSpurVernierLUT_Cnt_s16[3][1]  12_DualSpurVernierLUT_Cnt_s16[3][2]  14_DualSpurVernierLUT_Cnt_s16[3][3]  15_DualSpurVernierLUT_Cnt_s16[3][4]  16_DualSpurVernierLUT_Cnt_s16[3][6]  17_DualSpurVernierLUT_Cnt_s16[3][6]  18_DualSpurVernierLUT_Cnt_s16[3][6]  19_DualSpurVernierLUT_Cnt_s16[3][6]  10_DualSpurVernierLUT_Cnt_s16[3][6]  10_DualSpurVernierLUT_Cnt_s16[3][6]  11_DualSpurVernierLUT_Cnt_s16[3][6]  12_DualSpurVernierLUT_Cnt_s16[3][6]  13_DualSpurVernierLUT_Cnt_s16[3][6]  14_DualSpurVernierLUT_Cnt_s16[3][6]  15_DualSpurVernierLUT_Cnt_s16[3][6]  16_DualSpurVernierLUT_Cnt_s16[3][6]  17_DualSpurVernierLUT_Cnt_s16[3][6]  18_DualSpurVernierLUT_Cnt_s16[3][6]  19_DualSpurVernierLUT_Cnt_s16[3][6]  10_DualSpurVernierLUT_Cnt_s16[3][6]  11_DualSpurVernierLUT_Cnt_s16[3][6]  12_DualSpurVernierLUT_Cnt_s16[3][6]  13_DualSpurVernierLUT_Cnt_s16[3][6]  14_DualSpurVernierLUT_Cnt_s16[3][6]  15_DualSpurVernierLUT_Cnt_s16[3][6]  16_DualSpurVernierLUT_Cnt_s16[3][6]  17_DualSpurVernierLUT_Cnt_s16[3][6]  18_DualSpurVernierLUT_Cnt_s16[3][6]  19_DualSpurVernierLUT_Cnt_s16[3][6]  10_DualSpurVernierLUT_Cnt_s16[3][6]  11_DualSpurVernierLUT_Cnt_s16[3][6]  12_DualSpurVernierLUT_Cnt_s16[3][6]  13_DualSpurVernierLUT_Cnt_s16[3][6]	T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][14]  T2_DualSpurVernierLUT_Cnt_s16[2][15]  4  T2_DualSpurVernierLUT_Cnt_s16[2][17]  6  T2_DualSpurVernierLUT_Cnt_s16[2][17]  6  T2_DualSpurVernierLUT_Cnt_s16[2][19]  7  T2_DualSpurVernierLUT_Cnt_s16[2][19]  8  T2_DualSpurVernierLUT_Cnt_s16[2][20]  9  T2_DualSpurVernierLUT_Cnt_s16[2][21]  10  T2_DualSpurVernierLUT_Cnt_s16[3][0]  22  T2_DualSpurVernierLUT_Cnt_s16[3][0]  22  T2_DualSpurVernierLUT_Cnt_s16[3][0]  22  T2_DualSpurVernierLUT_Cnt_s16[3][0]  4  T2_DualSpurVernierLUT_Cnt_s16[3][0]  6  T2_DualSpurVernierLUT_Cnt_s16[3][0]  72_DualSpurVernierLUT_Cnt_s16[3][0]  72_DualSpurVernierLUT_Cnt_s16[3][1]  72_DualSpurVernierLUT_Cnt_s16[3][1]  73_DualSpurVernierLUT_Cnt_s16[3][1]  74_DualSpurVernierLUT_Cnt_s16[3][1]  75_DualSpurVernierLUT_Cnt_s16[3][1]  76_DualSpurVernierLUT_Cnt_s16[3][1]  77_DualSpurVernierLUT_Cnt_s16[3][1]  78_DualSpurVernierLUT_Cnt_s16[3][1]  79_DualSpurVernierLUT_Cnt_s16[3][1]  70_DualSpurVernierLUT_Cnt_s16[3][1]  71_DualSpurVernierLUT_Cnt_s16[3][1]  72_DualSpurVernierLUT_Cnt_s16[3][1]  73_DualSpurVernierLUT_Cnt_s16[3][1]  74_DualSpurVernierLUT_Cnt_s16[3][1]  75_DualSpurVernierLUT_Cnt_s16[3][1]  76_DualSpurVernierLUT_Cnt_s16[3][1]  77_DualSpurVernierLUT_Cnt_s16[3][1]  78_DualSpurVernierLUT_Cnt_s16[3][1]  79_DualSpurVernierLUT_Cnt_s16[3][1]  70_DualSpurVernierLUT_Cnt_s16[3][1]  71_DualSpurVernierLUT_Cnt_s16[3][1]  72_DualSpurVernierLUT_Cnt_s16[3][1]  73_DualSpurVernierLUT_Cnt_s16[3][1]  74_DualSpurVernierLUT_Cnt_s16[3][1]  75_DualSpurVernierLUT_Cnt_s16[3][1]  76_DualSpurVernierLUT_Cnt_s16[3][1]	T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][15]	T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVerniert.UT_Cnt_s16[2][16] 5 T2_DualSpurVerniert.UT_Cnt_s16[2][17] 6 T2_DualSpurVerniert.UT_Cnt_s16[2][18] 7 T2_DualSpurVerniert.UT_Cnt_s16[2][19] 8 T2_DualSpurVerniert.UT_Cnt_s16[2][20] 9 T2_DualSpurVerniert.UT_Cnt_s16[2][21] 10 T2_DualSpurVerniert.UT_Cnt_s16[3][1] 2 T2_DualSpurVerniert.UT_Cnt_s16[3][1] 2 T2_DualSpurVerniert.UT_Cnt_s16[3][1] 2 T2_DualSpurVerniert.UT_Cnt_s16[3][2] 4 T2_DualSpurVerniert.UT_Cnt_s16[3][2] 4 T2_DualSpurVerniert.UT_Cnt_s16[3][3] 6 T2_DualSpurVerniert.UT_Cnt_s16[3][6] 10 T2_DualSpurVerniert.UT_Cnt_s16[3][6] 12 T2_DualSpurVerniert.UT_Cnt_s16[3][6] 12 T2_DualSpurVerniert.UT_Cnt_s16[3][8] 16 T2_DualSpurVerniert.UT_Cnt_s16[3][8] 16 T2_DualSpurVerniert.UT_Cnt_s16[3][8] 16 T2_DualSpurVerniert.UT_Cnt_s16[3][11] 11 T2_DualSpurVerniert.UT_Cnt_s16[3][11] 11 T2_DualSpurVerniert.UT_Cnt_s16[3][12] 3 T2_DualSpurVerniert.UT_Cnt_s16[3][13] 5 T2_DualSpurVerniert.UT_Cnt_s16[3][14] 7 T2_DualSpurVerniert.UT_Cnt_s16[3][15] 19 T2_DualSpurVerniert.UT_Cnt_s16[3][16] 11	T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVemierLUT_Cnt_s16[2][17]  72_DualSpurVemierLUT_Cnt_s16[2][18]  7  72_DualSpurVemierLUT_Cnt_s16[2][19]  8  7  72_DualSpurVemierLUT_Cnt_s16[2][20]  9  72_DualSpurVemierLUT_Cnt_s16[2][21]  10  72_DualSpurVemierLUT_Cnt_s16[3][0]  22  72_DualSpurVemierLUT_Cnt_s16[3][2]  72_DualSpurVemierLUT_Cnt_s16[3][2]  72_DualSpurVemierLUT_Cnt_s16[3][2]  72_DualSpurVemierLUT_Cnt_s16[3][3]  6  72_DualSpurVemierLUT_Cnt_s16[3][4]  8  72_DualSpurVemierLUT_Cnt_s16[3][6]  72_DualSpurVemierLUT_Cnt_s16[3][6]  72_DualSpurVemierLUT_Cnt_s16[3][6]  72_DualSpurVemierLUT_Cnt_s16[3][7]  72_DualSpurVemierLUT_Cnt_s16[3][8]  72_DualSpurVemierLUT_Cnt_s16[3][8]  73_DualSpurVemierLUT_Cnt_s16[3][8]  74_DualSpurVemierLUT_Cnt_s16[3][8]  75_DualSpurVemierLUT_Cnt_s16[3][10]  70_DualSpurVemierLUT_Cnt_s16[3][10]  71_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  73_DualSpurVemierLUT_Cnt_s16[3][10]  74_DualSpurVemierLUT_Cnt_s16[3][10]  75_DualSpurVemierLUT_Cnt_s16[3][10]  76_DualSpurVemierLUT_Cnt_s16[3][10]  77_DualSpurVemierLUT_Cnt_s16[3][10]  78_DualSpurVemierLUT_Cnt_s16[3][10]  79_DualSpurVemierLUT_Cnt_s16[3][10]  70_DualSpurVemierLUT_Cnt_s16[3][10]  71_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  73_DualSpurVemierLUT_Cnt_s16[3][10]  74_DualSpurVemierLUT_Cnt_s16[3][10]  75_DualSpurVemierLUT_Cnt_s16[3][10]  76_DualSpurVemierLUT_Cnt_s16[3][10]  77_DualSpurVemierLUT_Cnt_s16[3][10]  78_DualSpurVemierLUT_Cnt_s16[3][10]  79_DualSpurVemierLUT_Cnt_s16[3][10]  70_DualSpurVemierLUT_Cnt_s16[3][10]  70_DualSpurVemierLUT_Cnt_s16[3][10]  70_DualSpurVemierLUT_Cnt_s16[3][10]  71_DualSpurVemierLUT_Cnt_s16[3][10]  72_DualSpurVemierLUT_Cnt_s16[3][10]  73_DualSpurVemierLUT_Cnt_s16[3][10]	T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
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T2_DualSpurVernierLUT_Cnt_s16[3][2]		
T2_DualSpurVernierLUT_Cnt_s16[3][3]       6         T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][5]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][4]       8         T2_DualSpurVernierLUT_Cnt_s16[3][6]       10         T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][5] 10  T2_DualSpurVernierLUT_Cnt_s16[3][6] 12  T2_DualSpurVernierLUT_Cnt_s16[3][7] 14  T2_DualSpurVernierLUT_Cnt_s16[3][8] 16  T2_DualSpurVernierLUT_Cnt_s16[3][9] 18  T2_DualSpurVernierLUT_Cnt_s16[3][10] 20  T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 1  T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 3  T2_DualSpurVernierLUT_Cnt_s16[3][13] 5  T2_DualSpurVernierLUT_Cnt_s16[3][14] 7  T2_DualSpurVernierLUT_Cnt_s16[3][15] 9  T2_DualSpurVernierLUT_Cnt_s16[3][16] 11  T2_DualSpurVernierLUT_Cnt_s16[3][16] 11  T2_DualSpurVernierLUT_Cnt_s16[3][16] 11  T2_DualSpurVernierLUT_Cnt_s16[3][17] 13  T2_DualSpurVernierLUT_Cnt_s16[3][18] 15  T2_DualSpurVernierLUT_Cnt_s16[3][19] 17  T2_DualSpurVernierLUT_Cnt_s16[3][19] 17  T2_DualSpurVernierLUT_Cnt_s16[3][19] 17		
T2_DualSpurVernierLUT_Cnt_s16[3][6]       12         T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][7]       14         T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][8]       16         T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][9]       18         T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][10]       20         T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][11]       1         T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][12]       3         T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][13]       5         T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][14]       7         T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][15]       9         T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][16]       11         T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19		
T2_DualSpurVernierLUT_Cnt_s16[3][17]       13         T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19	T2_DualSpurVernierLUT_Cnt_s16[3][15]	
T2_DualSpurVernierLUT_Cnt_s16[3][18]       15         T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19	T2_DualSpurVernierLUT_Cnt_s16[3][16]	
T2_DualSpurVernierLUT_Cnt_s16[3][19]       17         T2_DualSpurVernierLUT_Cnt_s16[3][20]       19	T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][20] 19	T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
	T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][21] 21	T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
	T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc 0	k_SelectFromColumn_Cnt_lgc	0

DigColPs\_Per2

Status

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Name	Input Value		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	3		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.634827	163.6348393 ± 0.00048828125	<b>✓</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	41	41	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	<b>✓</b>
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.365173	-736.3651607 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	<b>✓</b>
NTC	0x6C	0x6C	<b>✓</b>
Param	0x0E	0x0E	<b>✓</b>
Status	0x01	0x01	~
NTC	0x6F	0x6F	~
Param	0x00	0x00	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x00

0x00

Test Step 3.16 (Repeat Count = 1)		~
Name	Input Value	
DigColPsInt_GetCustData()	148	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	
DigColPs_ColTrimStatic_Deg_M_f32	259.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	46069	
DigColPs_I2CHwColAngle_Deg_M_f32	360	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552	
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	224.1625181	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4	

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Name	Input Value
DigColPs_SpurParityError_Cnt_M_lgc	o
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs TrimCompStatic Cnt M u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs VernierAngleOORange Cnt M Igc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[1][0]	359
	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	5
T2_ColSpurVernierLUT_Cnt_s16[2][8]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	
	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	13 10 7





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2 ColSpurVernierLUT Cnt s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2 DualSpurVernierLUT Cnt s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	
	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2 DualSpurVernierLUT Cnt s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwD		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	1	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	<b>~</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>~</b>
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	<b>~</b>
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	41	41	<b>V</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	1	1	
NTC	0x6C	0x6C	
Param	0x0E	0x0E	
Status	0x01	0x01	-
NTC		0x6F	
			<b>*</b>
NTC Param Status	0x6F 0x00 0x00	0x6F 0x00 0x00	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	<b>~</b>
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	<b>~</b>
VernierLookup	1	VernierLookup	1	<b>✓</b>
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	<b>✓</b>
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_



Test Step 3.17 (Repeat Count = 1)	🗸
Name	Input Value
DigColPsInt_GetCustData()	195
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552 297.033
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2_ColSpurVernierLUT_Cnt_s16[0][1]	-163 -131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5] T3_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	3 1

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt Pim DigColPsEOL.ColTrim Deg f32	360		
tgt Pim DigColPsEOL.SpurTrim Deg f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt_DigColPs_Per2_MecState_Cnt_enum	_	
tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	- Toodit
DigColPs I2CHwColAngleForTrim Deg M f32	163.636185	163.6362029 ± 0.00048828125	
DigColPs 12CHwTrimTransCnts Uls M u08	0	0	
DigColPs_I2CHW1HH1HalisChis_ois_w_uoo  DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	1	1	•
DigColPs_PrevVernierLeverno_Cnt_M_u08  DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	41	41	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	
	10	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	Ž
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value		· ·	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	
NTC	0x6C	0x6C	~
Param	0x06	0x06	~
Status	0x01	0x01	
NTC	0x6F	0x6F	





Name	Actual Value	Expected Value	Result
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.18 (Repeat Count = 1)	· ·
	Innut Value
Name	Input Value
DigColPsInt_GetCustData()	242
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_12CColSensorFault_Cnt_M_lgc	
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2 ColSpurVernierLUT Cnt s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0





Name	
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[	
T2_ColSpurVernierLUT_Cnt_s16{  st }   1	
T2_ColSpurVernierLUT_Cnt_s16[1][14]  T2_ColSpurVernierLUT_Cnt_s16[1][16]  T2_ColSpurVernierLUT_Cnt_s16[1][16]  T2_ColSpurVernierLUT_Cnt_s16[2][0]  T2_ColSpurVernierLUT_Cnt_s16[2][1]  T2_ColSpurVernierLUT_Cnt_s16[2][1]  T2_ColSpurVernierLUT_Cnt_s16[2][1]  T2_ColSpurVernierLUT_Cnt_s16[2][2]  T2_ColSpurVernierLUT_Cnt_s16[2][3]  T2_ColSpurVernierLUT_Cnt_s16[2][4]  T2_ColSpurVernierLUT_Cnt_s16[2][6]  T2_ColSpurVernierLUT_Cnt_s16[2][6]  T2_ColSpurVernierLUT_Cnt_s16[2][7]  T3_ColSpurVernierLUT_Cnt_s16[2][7]  T4_ColSpurVernierLUT_Cnt_s16[2][8]  T5_ColSpurVernierLUT_Cnt_s16[2][8]  T2_ColSpurVernierLUT_Cnt_s16[2][10]  T2_ColSpurVernierLUT_Cnt_s16[2][11]  T2_ColSpurVernierLUT_Cnt_s16[2][12]  T2_ColSpurVernierLUT_Cnt_s16[2][13]  T2_ColSpurVernierLUT_Cnt_s16[2][14]  T2_ColSpurVernierLUT_Cnt_s16[2][15]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][1]  T2_ColSpurVernierLUT_Cnt_s16[3][6]  T3_ColSpurVernierL	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][1]  12_ColSpurVernierLUT_Cnt_s16[2][2]  6  T2_ColSpurVernierLUT_Cnt_s16[2][3]  4  T2_ColSpurVernierLUT_Cnt_s16[2][4]  2  T2_ColSpurVernierLUT_Cnt_s16[2][5]  0  T2_ColSpurVernierLUT_Cnt_s16[2][6]  9  T2_ColSpurVernierLUT_Cnt_s16[2][7]  7  T2_ColSpurVernierLUT_Cnt_s16[2][8]  5  T2_ColSpurVernierLUT_Cnt_s16[2][9]  3  T2_ColSpurVernierLUT_Cnt_s16[2][9]  3  T2_ColSpurVernierLUT_Cnt_s16[2][10]  1  T2_ColSpurVernierLUT_Cnt_s16[2][11]  10  T2_ColSpurVernierLUT_Cnt_s16[2][12]  8  T2_ColSpurVernierLUT_Cnt_s16[2][13]  6  T2_ColSpurVernierLUT_Cnt_s16[2][14]  4  T2_ColSpurVernierLUT_Cnt_s16[2][15]  72_ColSpurVernierLUT_Cnt_s16[2][16]  10  T2_ColSpurVernierLUT_Cnt_s16[3][0]  11  T2_ColSpurVernierLUT_Cnt_s16[3][0]  12_ColSpurVernierLUT_Cnt_s16[3][1]  14  T2_ColSpurVernierLUT_Cnt_s16[3][2]  11  T2_ColSpurVernierLUT_Cnt_s16[3][3]  8  T2_ColSpurVernierLUT_Cnt_s16[3][4]  5  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  16  T2_ColSpurVernierLUT_Cnt_s16[3][6]  17  T2_ColSpurVernierLUT_Cnt_s16[3][6]  18  T2_ColSpurVernierLUT_Cnt_s16[3][6]  19  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  11  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12  T2_ColSpurVernierLUT_Cnt_s16[3][6]  13	
T2_ColSpurVemierLUT_Cnt_s16[2][2]  T2_ColSpurVemierLUT_Cnt_s16[2][3]  T2_ColSpurVemierLUT_Cnt_s16[2][4]  2_ColSpurVemierLUT_Cnt_s16[2][5]  T2_ColSpurVemierLUT_Cnt_s16[2][6]  T2_ColSpurVemierLUT_Cnt_s16[2][7]  T2_ColSpurVemierLUT_Cnt_s16[2][7]  T2_ColSpurVemierLUT_Cnt_s16[2][8]  T2_ColSpurVemierLUT_Cnt_s16[2][8]  5_T2_ColSpurVemierLUT_Cnt_s16[2][9]  3_ColSpurVemierLUT_Cnt_s16[2][10]  T2_ColSpurVemierLUT_Cnt_s16[2][11]  10  T2_ColSpurVemierLUT_Cnt_s16[2][12]  8_ColSpurVemierLUT_Cnt_s16[2][12]  8_ColSpurVemierLUT_Cnt_s16[2][13]  6_ColSpurVemierLUT_Cnt_s16[2][14]  4_ColSpurVemierLUT_Cnt_s16[2][16]  1_ColSpurVemierLUT_Cnt_s16[2][16]  1_ColSpurVemierLUT_Cnt_s16[2][16]  1_ColSpurVemierLUT_Cnt_s16[3][0]  1_ColSpurVemierLUT_Cnt_s16[3][0]  1_ColSpurVemierLUT_Cnt_s16[3][1]  1_ColSpurVemierLUT_Cnt_s16[3][1]  1_ColSpurVemierLUT_Cnt_s16[3][1]  1_ColSpurVemierLUT_Cnt_s16[3][1]  1_ColSpurVemierLUT_Cnt_s16[3][6]	
T2_ColSpurVernierLUT_Cnt_s16[2][2]  T2_ColSpurVernierLUT_Cnt_s16[2][3]  4  T2_ColSpurVernierLUT_Cnt_s16[2][4]  2  T2_ColSpurVernierLUT_Cnt_s16[2][5]  0  T2_ColSpurVernierLUT_Cnt_s16[2][6]  9  T2_ColSpurVernierLUT_Cnt_s16[2][7]  7  T2_ColSpurVernierLUT_Cnt_s16[2][8]  5  T2_ColSpurVernierLUT_Cnt_s16[2][9]  3  T2_ColSpurVernierLUT_Cnt_s16[2][9]  3  T2_ColSpurVernierLUT_Cnt_s16[2][10]  1  T2_ColSpurVernierLUT_Cnt_s16[2][11]  10  T2_ColSpurVernierLUT_Cnt_s16[2][12]  8  T2_ColSpurVernierLUT_Cnt_s16[2][13]  6  T2_ColSpurVernierLUT_Cnt_s16[2][14]  4  T2_ColSpurVernierLUT_Cnt_s16[2][15]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  10  T2_ColSpurVernierLUT_Cnt_s16[3][0]  1  T2_ColSpurVernierLUT_Cnt_s16[3][0]  1  T2_ColSpurVernierLUT_Cnt_s16[3][1]  14  T2_ColSpurVernierLUT_Cnt_s16[3][1]  15  T2_ColSpurVernierLUT_Cnt_s16[3][4]  5  T2_ColSpurVernierLUT_Cnt_s16[3][4]  5  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  16  T2_ColSpurVernierLUT_Cnt_s16[3][6]  17  T2_ColSpurVernierLUT_Cnt_s16[3][6]  18  T2_ColSpurVernierLUT_Cnt_s16[3][6]  19  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  11  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  16  T2_ColSpurVernierLUT_Cnt_s16[3][6]  17  T2_ColSpurVernierLUT_Cnt_s16[3][6]  18  T2_ColSpurVernierLUT_Cnt_s16[3][6]  19  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  11  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12  T2_ColSpurVernierLUT_Cnt_s16[3][6]  13	
T2_ColSpurVernierLUT_Cnt_s16[2][3]  12_ColSpurVernierLUT_Cnt_s16[2][6]  12_ColSpurVernierLUT_Cnt_s16[2][6]  12_ColSpurVernierLUT_Cnt_s16[2][6]  12_ColSpurVernierLUT_Cnt_s16[2][7]  7  T2_ColSpurVernierLUT_Cnt_s16[2][8]  5  T2_ColSpurVernierLUT_Cnt_s16[2][8]  5  T2_ColSpurVernierLUT_Cnt_s16[2][10]  10  T2_ColSpurVernierLUT_Cnt_s16[2][11]  10  T2_ColSpurVernierLUT_Cnt_s16[2][11]  10  T2_ColSpurVernierLUT_Cnt_s16[2][12]  8  T2_ColSpurVernierLUT_Cnt_s16[2][13]  6  T2_ColSpurVernierLUT_Cnt_s16[2][13]  6  T2_ColSpurVernierLUT_Cnt_s16[2][15]  T2_ColSpurVernierLUT_Cnt_s16[2][16]  10  T2_ColSpurVernierLUT_Cnt_s16[3][0]  11  T2_ColSpurVernierLUT_Cnt_s16[3][1]  14  T2_ColSpurVernierLUT_Cnt_s16[3][1]  14  T2_ColSpurVernierLUT_Cnt_s16[3][4]  5  T2_ColSpurVernierLUT_Cnt_s16[3][4]  5  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  16  T2_ColSpurVernierLUT_Cnt_s16[3][6]  17  T2_ColSpurVernierLUT_Cnt_s16[3][6]  18  T2_ColSpurVernierLUT_Cnt_s16[3][6]  19  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  11  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12  T2_ColSpurVernierLUT_Cnt_s16[3][6]  13  T2_ColSpurVernierLUT_Cnt_s16[3][6]  14  T2_ColSpurVernierLUT_Cnt_s16[3][6]  15  T2_ColSpurVernierLUT_Cnt_s16[3][6]  16  T2_ColSpurVernierLUT_Cnt_s16[3][6]  17  T2_ColSpurVernierLUT_Cnt_s16[3][6]  18  T2_ColSpurVernierLUT_Cnt_s16[3][6]  19  T2_ColSpurVernierLUT_Cnt_s16[3][6]  10  T2_ColSpurVernierLUT_Cnt_s16[3][6]  11  T2_ColSpurVernierLUT_Cnt_s16[3][6]  12  T2_ColSpurVernierLUT_Cnt_s16[3][6]  13	
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T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[2][16]       10         T2_ColSpurVernierLUT_Cnt_s16[3][0]       1         T2_ColSpurVernierLUT_Cnt_s16[3][1]       14         T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][1] 14  T2_ColSpurVernierLUT_Cnt_s16[3][2] 11  T2_ColSpurVernierLUT_Cnt_s16[3][3] 8  T2_ColSpurVernierLUT_Cnt_s16[3][4] 5  T2_ColSpurVernierLUT_Cnt_s16[3][5] 2  T2_ColSpurVernierLUT_Cnt_s16[3][6] 15  T2_ColSpurVernierLUT_Cnt_s16[3][7] 12  T2_ColSpurVernierLUT_Cnt_s16[3][8] 9  T2_ColSpurVernierLUT_Cnt_s16[3][9] 6  T2_ColSpurVernierLUT_Cnt_s16[3][9] 6  T2_ColSpurVernierLUT_Cnt_s16[3][10] 3  T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][2]       11         T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][3]       8         T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][5]       2         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16	
T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7	
T2_ColSpurVernierLUT_Cnt_s16[3][15] 4	
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17	
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396	
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360	
T2_DualSpurVernierLUT_Cnt_s16[0][2] -324	
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288	
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252	
T2 DualSpurVernierLUT Cnt s16[0][5] -216	
T2_DualSpurVernierLUT_Cnt_s16[0][6] -180	
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144	
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72	
T2_DualSpurVernierLUT_Cnt_s16[0][10] -36	
T2_DualSpurVernierLUT_Cnt_s16[0][11] 0	
T2_DualSpurVernierLUT_Cnt_s16[0][12] 36	
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	
T2_DualSpurVernierLUT_Cnt_s16[0][16] 180	
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	
T2_DualSpurVernierLUT_Cnt_s16[0][18] 252	
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288	
T2_DualSpurVernierLUT_Cnt_s16[0][20] 324	
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360	
T2_DualSpurVernierLUT_Cnt_s16[1][0] 9	
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0	
T2_DualSpurVernierLUT_Cnt_s16[1][2] 1	
T2_DualSpurVernierLUT_Cnt_s16[1][3] 2	
T2_DualSpurVernierLUT_Cnt_s16[1][4] 3	
T2_DualSpurVernierLUT_Cnt_s16[1][5] 4	
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5	
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6	
T2_DualSpurVernierLUT_Cnt_s16[1][8] 7	
T2_DualSpurVernierLUT_Cnt_s16[1][9] 8	
T2_DualSpurVernierLUT_Cnt_s16[1][10] 9	





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][11]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T3_DualSpurVernierLUT_Cnt_s16[2][12]	0 1
T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][14]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	41
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	50
k_VernCorrErrorDiag_Cnt_str.Threshold	85
k_VernCorrErrorDiag_Cnt_str.PStep	4
k_VernCorrErrorDiag_Cnt_str.NStep	46
k_VernCorrErrorThresh_Deg_f32	3
k_VernOORangeThresh_Deg_f32	1087.934204
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	





Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	<b>✓</b>
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	<b>✓</b>
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	24	24	<b>✓</b>
DigColPs_VernCorrDetectAcc_Cnt_M_u16	24	24	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	<b>✓</b>
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	<b>✓</b>
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	•
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~
NTC	0x6F	0x6F	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	<b>✓</b>
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.19 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
DigColPsInt GetCustData()	289
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.48
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163

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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2 ColSpurVernierLUT Cnt s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	6
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s16[3][40]	3
T2_ColSpurVernierLUT_Cnt_s16[3][10] T2_ColSpurVernierLUT_Cnt_s16[3][11]	3   16
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
	108
T2_DualSpurVernierLUT_Cnt_s16[0][14]	100





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324 360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_bualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2] T3_DualSpurVernierLUT_Cst_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8
T2_DualSpurVernierLUT_Cnt_s16[3][4] T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0

DigColPs\_Per2



Name	Input Value		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	42		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2109		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwA	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwA	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Namo	Actual Value	Expected Value	Pocult

tgt_Rte_inst_3a_bigCoiFs.Filli_bigCoiFsEOL	tgt_Filli_DigColFSEOL	tgt_Filli_bigColFsEOL	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	98.7181778	98.71818182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	<b>✓</b>
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	100.399994	100.4 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-801.281799	-801.2818182 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	<b>✓</b>
Status	0x01	0x01	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 3.20 (Repeat Count = 1)		V
Name	Input Value	
DigColPsInt_GetCustData()	336	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	0	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	0	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	
DigColPs_SpurTrimStatic_Deg_M_f32	0	





Name	Input Value
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_Igc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
	-99
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2 ColSpurVernierLUT Cnt s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2 ColSpurVernierLUT Cnt s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2 ColSpurVernierLUT Cnt s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2 DualSpurVernierLUT Cnt s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0 1
T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

DigColPs\_Per2

2014-10-14, 17:31:16+0530



Input Value T2\_DualSpurVernierLUT\_Cnt\_s16[3][8] 16 T2\_DualSpurVernierLUT\_Cnt\_s16[3][9] 18 20 T2\_DualSpurVernierLUT\_Cnt\_s16[3][10] T2\_DualSpurVernierLUT\_Cnt\_s16[3][11] T2\_DualSpurVernierLUT\_Cnt\_s16[3][12] 3 T2\_DualSpurVernierLUT\_Cnt\_s16[3][13] 5 T2\_DualSpurVernierLUT\_Cnt\_s16[3][14] 7 T2\_DualSpurVernierLUT\_Cnt\_s16[3][15] 9 T2\_DualSpurVernierLUT\_Cnt\_s16[3][16] 11 T2\_DualSpurVernierLUT\_Cnt\_s16[3][17] 13 T2\_DualSpurVernierLUT\_Cnt\_s16[3][18] 15 T2\_DualSpurVernierLUT\_Cnt\_s16[3][19] 17

 T2\_DualSpurVernierLUT\_Cnt\_s16[3][20]
 19

 T2\_DualSpurVernierLUT\_Cnt\_s16[3][21]
 21

 k\_SelectFromColumn\_Cnt\_lgc
 0

 k\_SkipStepErrDiag\_Cnt\_str.Threshold
 10

 k\_SkipStepErrDiag\_Cnt\_str.PStep
 0

 k\_SkipStepErrDiag\_Cnt\_str.NStep
 0

 k\_VernCorrErrorDiag\_Cnt\_str.Threshold
 0

 k\_VernCorrErrorDiag\_Cnt\_str.PStep
 0

 k\_VernCorrErrorDiag\_Cnt\_str.NStep
 0

 k\_VernCorrErrorThresh\_Deg\_f32
 1

k\_VernOORangeThresh\_Deg\_f32

tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum.value

tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32

tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32

tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16
tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc
tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32

 $\label{thm:continuous} $$ tgt_Re_inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum $$ tg_Re_inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc $$ tgt_Rte_inst_Sa_DigColPs.Pim_DigColPsEOL $$$ 

tgt\_DigColPs\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc tgt\_DigColPs\_Per2\_I2CHwAbsPos\_HwDeg\_f32

tgt\_DigColPs\_Per2\_MecState\_Cnt\_enum

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc

tgt\_Pim\_DigColPsEOL

100

0

0

0

0

Actual Value	Expected Value	Result
0	0	~
0	0 ± 0.00048828125	✓
2	2	~
0	0	✓
0	0 ± 0.0001220703125	~
2	2	✓
4	4	~
0	0	✓
0	0	~
0	0	✓
0	0	~
-900	-900 ± 0.0009	✓
0	0	~
0x6C	0x6C	✓
0x00	0x00	~
0x00	0x00	✓
0x6E	0x6E	✓
0x00	0x00	✓
0x01	0x01	✓
0x6F	0x6F	✓
0x00	0x00	~
0x00	0x00	✓
	Actual Value  0 0 0 2 0 0 2 0 0 0 2 4 0 0 0 0 0 0 0	Actual Value         Expected Value           0         0           0         0 ± 0.00048828125           2         2           0         0 ± 0.0001220703125           2         2           4         4           0         0           0         0           0         0           0         0           -900         -900 ± 0.0009           0         0           0x6C         0x6C           0x00         0x00           0x00         0x00           0x6E         0x6E           0x00         0x00           0x01         0x01           0x6F         0x6F           0x00         0x00

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_



Test Step 3.21 (Repeat Count = 1)	V
Name	Input Value
DigColPsInt_GetCustData()	383
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigCoIPs_I2CHwColAngle_Cnt_M_u16 DigCoIPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_12CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs T3_ColCourt (orginal LIT_Cont_od0/07/07)	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9] T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0] T3_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	IU





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	





Namo	Input Value		
Name T2_DualSpurVernierLUT_Cnt_s16[2][7]	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2 DualSpurVernierLUT Cnt s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k SkipStepErrDiag Cnt str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k VernCorrErrorDiag Cnt str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_ ·	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	<u>f</u> 32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	Form and all Malana	D
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	- 4
DigColPs_12CHwColAngleForTrim_Deg_M_f32	3	0 ± 0.00048828125	<b>V</b>
DigColPs_I2CHwTrimTransCnts_Uls_M_u08 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	-
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	-
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value		0	~
	0	1 *	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	900	-900 ± 0.0009	<b>✓</b>
tgt_DigCoIPs_Per2_IzCHWAGSPGs_HWDeg_t3z.value tgt_DigCoIPs_Per2_TrimComp_Cnt_Igc.value			~
	-900	-900 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	-900 0	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value NTC	-900 0 0x6C	-900 ± 0.0009 0 0x6C	· ·
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value NTC Param	-900 0 0x6C 0x00	-900 ± 0.0009 0 0x6C 0x00	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \





Name	Actual Value	Expected Value	Result
Param	0x00	0x00	<b>✓</b>
Status	0x00	0x00	<b>✓</b>
NTC	0x6F	0x6F	~
Param	0x00	0x00	~
Status	0x01	0x01	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Name	Input Value
DigColPsInt_GetCustData()	430
DigColPs_ColParityError_Cnt_M_lgc	0
DigCoIPs_CoISensorFaultAcc_Cnt_M_u16	103
DigColPs ColTrimStatic Deg M f32	214.7
DigCoIPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
	15468
DigColPs_I2CHwColAngle_Cnt_M_u16	
higColPs_I2CHwColAngle_Deg_M_f32	219.075
DigColPs_I2CHwDataType_Cnt_M_u08	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	324.208
bigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
higColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
ligColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
ligColPs_PrevColPos_Deg_M_f32	569.7636028
ligColPs_PrevVernierLevelNo_Cnt_M_u08	11
higColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
ligColPs_SpurSensorFaultAcc_Cnt_M_u16	149
igColPs_SpurTrimStatic_Deg_M_f32	0
igColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
igColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2 ColSpurVernierLUT Cnt s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
<sup>2</sup> _ColSpurVernierLUT_Cnt_s16[1][6]	4
2_coopurVernierEUT_cnt_s16[1][7]	3





Input Value 2 1
1
0
4
3
2
1
0
4
0
8
6
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0
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7
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2
10
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14
11
8
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2
15
12
9
6 3
16
13
10
7
4
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216 252
288
324
360
9
0
1
2
3
4
5





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3 4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_S16[1][19]	8
_ :	9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2 DualSpurVernierLUT Cnt s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2 DualSpurVernierLUT Cnt s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	35
k_SkipStepErrDiag_Cnt_str.PStep	2
k_SkipStepErrDiag_Cnt_str.NStep	28
k_VernCorrErrorDiag_Cnt_str.Threshold	42
k_VernCorrErrorDiag_Cnt_str.PStep	16
k_VernCorrErrorDiag_Cnt_str.NStep	31
k_VernCorrErrorThresh_Deg_f32	92.41026139
k_VernOORangeThresh_Deg_f32	1413.552634
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
19(_1 110_1101_04_5 190011 015190011 0_1 012_1201 1111 1101 00 44114_011_190	



Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1456.45813	1456.458182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1444.375	1444.375 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	544.375	544.375 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.23 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	64
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.86
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	210.79
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130

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12. CoSquarement   Cn.   1909	News	Invest Value
12.0059/wemeULC On s100[11]   186   180	Name T3 ColSput/ordiod LIT Cot of6(0)(40)	Input Value
12 CodeywheelUT Chr   1901 15    229		
12_ColspacewinsCut_Dot_strop(1)	_ :	
12.0038/wrinestuff Oct   500  14    294   12.0038/wrinestuff Oct   500  14    297   12.0038/wrinestuff Oct   500  16    398   12.0038/wrinestuff Oct   500  16    398   12.0038/wrinestuff Oct   500  16    398   12.0038/wrinestuff Oct   500  16    4   12.0038/wrinestuff Oct   500  16    5   12.0038/wrinestuff Oct   500  16		
12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  13_colspa/encecluf_On_stroligits  14_colspa/encecluf_On_stroligits  15_colspa/encecluf_On_stroligits  16_colspa/encecluf_On_stroligits  17_colspa/encecluf_On_stroligits  17_colspa/encecluf_On_stroligits  18_colspa/encecluf_On_stroligits  19_colspa/encecluf_On_stroligits  19_colspa/encecluf_On_stroligits  10_colspa/encecluf_On_stroligits  10_colspa/encecluf_On_stroligits  10_colspa/encecluf_On_stroligits  10_colspa/encecluf_On_stroligits  11_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  13_colspa/encecluf_On_stroligits  14_colspa/encecluf_On_stroligits  15_colspa/encecluf_On_stroligits  16_colspa/encecluf_On_stroligits  17_colspa/encecluf_On_stroligits  18_colspa/encecluf_On_stroligits  18_colspa/encecluf_On_stroligits  19_colspa/encecluf_On_stroligits  11_colspa/encecluf_On_stroligits  12_colspa/encecluf_On_stroligits  13_col		
12. CoSpar/weinesCUT Cut   1910  10   12. CoSpar/weinesCUT Cut   1910  10   12. CoSpar/weinesCUT Cut   1910  10   12. CoSpar/weinesCUT Cut   1910  11   13. CoSpar/weinesCUT Cut   1910  11   13. CoSpar/weinesCUT Cut   1910  11   14. CoSpar/weinesCUT Cut   1910  11   14. CoSpar/weinesCUT Cut   1910  11   17. CoSpar/weinesCUT Cut   191		
12_CoSquirmentU_Co_Sqt[ij]    12_CoSquirmentU_Co_Sqt[ij]    12_CoSquirmentU_Co_Sqt[ij]    13_CoSquirmentU_Co_Sqt[ij]    14_CoSquirmentU_Co_Sqt[ij]    15_CoSquirmentU_Co_Sqt[ij]    17_CoSquirmentU_Co_Sqt[ij]    17_CoSquirmentU_Co_Sqt[ij]    18_CoSquirmentU_Co_Sqt[ij]    19_CoSquirmentU_Co_Sqt[ij]    19_C	_ :	
12. CoSsa/vermentUT_Ost_StripTi0   4 12. CoSsa/vermentUT_Ost_StripTi0   5 12. CoSsa/vermentUT_Ost_StripTi0   5 12. CoSsa/vermentUT_Ost_StripTi0   1 12. CoSsa/vermentUT_Ost_StripTi0   0 13. CoSsa/vermentUT_Ost_StripTi0   0 14. CoSsa/vermentUT_Ost_StripTi0   0 14. CoSsa/vermentUT_Ost_StripTi0   0 15. CoSsa/vermentUT_Ost_StripTi0   0 17. CoSsa/v		
12_CoSpar/wromeUT_On_Set[ID]   2   2_CoSpar/wromeUT_On_Set[ID]   2   2_CoSpar/wromeUT_On_Set[ID]   3   12_CoSpar/wromeUT_On_Set[ID]   1   12_CoSpar/wromeUT_On_Set[ID]   2   12_CoSpar/wromeUT_On_Set[ID]   3   12_CoSpar/wromeUT_On_Set[ID]   3   12_CoSpar/wromeUT_On_Set[ID]   1   12_COSpar/wromeU		4
12_Colspar/weinstrill_Col_strill_B    12_Colspar/weinstrill_Col_strill_B    13_Colspar/weinstrill_Col_strill_B    14_Colspar/weinstrill_Col_strill_B    15_Colspar/weinstrill_Col_strill_B    16_Colspar/weinstrill_Col_strill_B    17_Colspar/weinstrill_Col_strill_B    18_Colspar/weinstrill_Col_strill_B    18_Colspar/weinstrill_Col_stri	T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
12 CoSpa/wemstLD   Cit   still   18		2
12_CoSparVermontU_Cot_s01[9] 13_COSparVermontU_Cot_s01[9] 14_COSparVermontU_Cot_s01[9] 15_COSparVermontU_Cot_s01[9] 16_COSparVermontU_Cot_s01[9] 17_CoSparVermontU_Cot_s01[9] 18_COSparVermontU_Cot_s01[9] 19_COSparVermontU_Cot_s01[9] 10_COSparVermontU_Cot_s01[9] 11_COSparVermontU_Cot_s01[9] 12_COSparVermontU_Cot_s01[9] 12_COSparVermont	T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
12_Colspat/weinetU_Col_1st(I]T)		0
12. CoSpavement UT. Ont. #18[19] 12. CoSpavement UT. Ont. #18[19] 13. CoSpavement UT. Ont. #18[19] 14. CoSpavement UT. Ont. #18[19] 15. CoSpavement UT. Ont. #18[19] 16. CoSpavement UT. Ont. #18[19] 17. CoSpavement UT. Ont. #18[19] 18. CoSpavement UT. Ont. #18[19] 19. CoSpavement UT. Ont. #18[20] 10. CoSpavement UT. Ont. #18[20] 10. CoSpavement UT. Ont. #18[20] 11. CoSpavement UT. Ont. #18[20] 12. CoSpavement UT. Ont. #18[20] 12. CoSpavement UT. Ont. #18[20] 12. CoSpavement UT. Ont. #18[20] 13. CoSpavement UT. Ont. #18[20] 14. CoSpavement UT. Ont. #18[20] 15. CoSpavement UT. Ont. #18[20] 17. CoSpavement UT. Ont. #18[20] 18. CoSpavement UT. Ont. #18[20] 19. CoSpavement UT. Ont. #18[20]	T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
12 CoSpar/weinetU_Ont_stSt[1]   1	T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
12. CoSayurVenet U. Fort, s16(11)   13. CoSayurVenet U. Fort, s16(11)   14. CoSayurVenet U. Fort, s16(11)   15. CoSayurVenet U. Fort, s16(11)   16. CoSayurVenet U. Fort, s16(11)   17. CoSayurVenet U. Fort, s16(11)   18. CoSayurVenet U. Fort, s16(11)   19. CoSayurVenet U. Fort, s16(	T2_ColSpurVernierLUT_Cnt_s16[1][8]	
12, CoSpa/Wemel LU, Cnt. s10[11]   1	T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
12. Collogua/Yeamed LU. Cost. #16[11/2]         2           12. Collogua/Yeamed LU. Cost. #16[11/4]         1           12. Collogua/Yeamed LU. Cost. #16[11/4]         0           12. Collogua/Yeamed LU. Cost. #16[11/4]         4           12. Collogua/Yeamed LU. Cost. #16[11/4]         4           12. Collogua/Yeamed LU. Cost. #16[11/4]         8           12. Collogua/Yeamed LU. Cost. #16[11/4]         9           12. Collogua/Yeamed LU. Cost. #16[11/4]         10           12. Collogua/Yeamed LU. Cost. #16[11/4]         11           12. Collogua/Yeamed LU.	T2_ColSpurVernierLUT_Cnt_s16[1][10]	
12_ColSput/ment_UT_Cnt_st[0] 14    12_ColSput/ment_UT_Cnt_st[0] 14    12_ColSput/ment_UT_Cnt_st[0] 14    12_ColSput/ment_UT_Cnt_st[0] 16    12_ColSput/men	T2_ColSpurVernierLUT_Cnt_s16[1][11]	
12, CoSport/mentUT, Cnt, 16(1)(14) 12, CoSport/mentUT, Cnt, 16(1)(16) 12, CoSport/mentUT, Cnt, 16(1)(16) 13, CoSport/mentUT, Cnt, 16(2)(10) 14, CoSport/mentUT, Cnt, 16(2)(10) 17, CoSport/mentUT, Cnt, 16(2)(10) 18, CoSport/mentUT, Cnt, 16(2)(10) 19, CoSport/mentUT, Cnt, 16(3)(10) 11, CoSport/ment	T2_ColSpurVernierLUT_Cnt_s16[1][12]	
12_Colsput/mentUT_Cnt_st[1][16]		
12. Colspur/emieLUT_Cnt_s161[16] 12. Colspur/emieLUT_Cnt_s161[21] 12. Colspur/emieLUT_Cnt_s161[21] 13. Colspur/emieLUT_Cnt_s161[21] 14. Colspur/emieLUT_Cnt_s161[21] 15. Colspur/emieLUT_Cnt_s161[21] 16. Colspur/emieLUT_Cnt_s161[21] 17. Colspur/emieLUT_Cnt_s161[21] 18. Colspur/emieLUT_Cnt_s161[21] 18. Colspur/emieLUT_Cnt_s161[21] 19. Colspur/emieLUT_Cnt_s161[21] 19. Colspur/emieLUT_Cnt_s161[21] 19. Colspur/emieLUT_Cnt_s161[21] 19. Colspur/emieLUT_Cnt_s161[21] 10. Colspur/emieLUT_Cnt_s161[21] 11. Colspur/emieLUT_Cnt_s161[21] 11. Colspur/emieLUT_Cnt_s161[21] 12. Colspur/emieLUT_Cnt_s161[21] 13. Colspur/emieLUT_Cnt_s161[21] 14. Colspur/emieLUT_Cnt_s161[21] 16. Colspur/emieLUT_Cnt_s161[21] 17. Colspur/emieLUT_Cnt_s161[21] 18. Colspur/emieLUT_Cnt_s161[21] 19. Colspur/emieLUT_Cnt_s161[21]		
12 ColSput/vennetUT_Cnt_st@2[1]   8		
12, CoSpurVemieLUT_Cnt_s16[21] 13, CoSpurVemieLUT_Cnt_s16[21] 14, CoSpurVemieLUT_Cnt_s16[21] 15, CoSpurVemieLUT_Cnt_s16[21] 16, CoSpurVemieLUT_Cnt_s16[21] 17, CoSpurVemieLUT_Cnt_s16[21] 18, CoSpurVemieLUT_Cnt_s16[21] 19, CoSpurVemieLUT_Cnt_s16[21] 10, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 12, CoSpurVemieLUT_Cnt_s16[21] 13, CoSpurVemieLUT_Cnt_s16[21] 14, CoSpurVemieLUT_Cnt_s16[21] 16, CoSpurVemieLUT_Cnt_s16[21] 17, CoSpurVemieLUT_Cnt_s16[21] 18, CoSpurVemieLUT_Cnt_s16[21] 19, CoSpurVemieLUT_Cnt_s16[21] 10, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 12, CoSpurVemieLUT_Cnt_s16[21] 13, CoSpurVemieLUT_Cnt_s16[21] 14, CoSpurVemieLUT_Cnt_s16[21] 15, CoSpurVemieLUT_Cnt_s16[21] 16, CoSpurVemieLUT_Cnt_s16[21] 17, CoSpurVemieLUT_Cnt_s16[21] 18, CoSpurVemieLUT_Cnt_s16[21] 19, CoSpurVemieLUT_Cnt_s16[21] 10, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 12, CoSpurVemieLUT_Cnt_s16[21] 13, CoSpurVemieLUT_Cnt_s16[21] 14, CoSpurVemieLUT_Cnt_s16[21] 15, CoSpurVemieLUT_Cnt_s16[21] 16, CoSpurVemieLUT_Cnt_s16[21] 17, CoSpurVemieLUT_Cnt_s16[21] 18, CoSpurVemieLUT_Cnt_s16[21] 19, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 12, CoSpurVemieLUT_Cnt_s16[21] 13, CoSpurVemieLUT_Cnt_s16[21] 14, CoSpurVemieLUT_Cnt_s16[21] 15, CoSpurVemieLUT_Cnt_s16[21] 16, CoSpurVemieLUT_Cnt_s16[21] 17, CoSpurVemieLUT_Cnt_s16[21] 18, CoSpurVemieLUT_Cnt_s16[21] 19, CoSpurVemieLUT_Cnt_s16[21] 10, CoSpurVemieLUT_Cnt_s16[21] 11, CoSpurVemieLUT_Cnt_s16[21] 12, CoSpurVemieLUT_Cnt_s16[21] 13, CoSpurVemieLUT_Cnt_s16[21] 14, CoSpurVemieLUT_Cnt_s16[21] 15, CoSpurVemieLUT_Cnt_s16[21] 16, CoSpurVemieLUT_Cnt_s16[21] 17, CoSpurVemieLUT_Cnt_s16[21]		
T2_Colspur/emieLUT_Cnt_stig2  3    4     T2_Colspur/emieLUT_Cnt_stig2  3    4     T2_Colspur/emieLUT_Cnt_stig2  3    4     T2_Colspur/emieLUT_Cnt_stig2  3    9     T2_Colspur/emieLUT_Cnt_stig2  3    9     T2_Colspur/emieLUT_Cnt_stig2  3    9     T2_Colspur/emieLUT_Cnt_stig2  3    9     T2_Colspur/emieLUT_Cnt_stig2  3    5     T2_Colspur/emieLUT_Cnt_stig2  3    5     T2_Colspur/emieLUT_Cnt_stig2  3    5     T2_Colspur/emieLUT_Cnt_stig2  4    10     T2_Colspur/emieLUT_Cnt_stig3  4    10		
12. ColspurVerniert UT, Cnt. s162[3]   4     12. ColspurVerniert UT, Cnt. s162[4]   2     12. ColspurVerniert UT, Cnt. s162[4]   0     12. ColspurVerniert UT, Cnt. s162[4]   0     12. ColspurVerniert UT, Cnt. s162[4]   7     12. ColspurVerniert UT, Cnt. s162[4]   7     12. ColspurVerniert UT, Cnt. s162[4]   1     13. ColspurVerniert UT, Cnt. s162[4]   1     14. ColspurVerniert UT, Cnt. s162[4]   1     15. ColspurVerniert UT, Cnt. s162[4]   1     16. ColspurVerniert UT, Cnt. s162[4]   1     17. ColspurVerniert UT, Cnt. s162[4]   1     18. ColspurVerniert UT, Cnt. s162[4]   1     19. ColspurVerniert UT, Cnt. s162[4]   5     19. ColspurVerniert UT, Cnt. s162[4]   6     19. ColspurVerniert UT, Cnt. s162[4]   7     19. ColspurVerniert		
17. ColSpurVermieLUT_Cnt_s16[2][5]   0     17. ColSpurVermieLUT_Cnt_s16[2][5]   0     17. ColSpurVermieLUT_Cnt_s16[2][6]   9     17. ColSpurVermieLUT_Cnt_s16[2][6]   9     17. ColSpurVermieLUT_Cnt_s16[2][6]   5     17. ColSpurVermieLUT_Cnt_s16[2][6]   5     17. ColSpurVermieLUT_Cnt_s16[2][6]   1     18. ColSpurVermieLUT_Cnt_s16[2][6]   1     19. ColSpurVermieLUT_Cnt_s16[2][1]   1     10. ColSpurVermieLUT_Cnt_s16[2][1]   1     10. ColSpurVermieLUT_Cnt_s16[2][1]   1     12. ColSpurVermieLUT_Cnt_s16[2][1]   8     17. ColSpurVermieLUT_Cnt_s16[2][1]   1     18. ColSpurVermieLUT_Cnt_s16[2][1]   1     18. ColSpurVermieLUT_Cnt_s16[2][1]   1     19. ColSpurVermieLUT_Cn		
17. ColSpurVernietUT_Cnt_st62[5]   0   1   2   2   2   3   3   3   3   3   3   3		
12. ColSpurVerniet.UT_Cnt_s16[2][5]   9     7. ColSpurVerniet.UT_Cnt_s16[2][7]   7     7. ColSpurVerniet.UT_Cnt_s16[2][8]   5     7. ColSpurVerniet.UT_Cnt_s16[2][9]   3     7. ColSpurVerniet.UT_Cnt_s16[2][10]   1     7. ColSpurVerniet.UT_Cnt_s16[2][11]   10     7. ColSpurVerniet.UT_Cnt_s16[2][11]   10     7. ColSpurVerniet.UT_Cnt_s16[2][12]   8     8     7. ColSpurVerniet.UT_Cnt_s16[2][13]   6     7. ColSpurVerniet.UT_Cnt_s16[2][14]   4     8     7. ColSpurVerniet.UT_Cnt_s16[2][15]   2     7. ColSpurVerniet.UT_Cnt_s16[2][16]   10     7. ColSpurVerniet.UT_Cnt_s16[2][16]   10     7. ColSpurVerniet.UT_Cnt_s16[2][16]   11     7. ColSpurVerniet.UT_Cnt_s16[2][16]   11     7. ColSpurVerniet.UT_Cnt_s16[2][16]   12     7. ColSpurVerniet.UT_Cnt_s16[2][16]   13     7. ColSpurVerniet.UT_Cnt_s16[2][16]   14     7. ColSpurVerniet.UT_Cnt_s16[2][16]   15     7. ColSpurVerniet.UT_Cnt_s16[2][16]   16     7. ColSpurVerniet.UT_Cnt_s16[2][16]   17     7. ColSpurVerniet.UT_Cnt_s16[2][16]   18     7. ColSpurVerniet.U		
T2_ColSput/vemierLUT_Cnt_s16[2][7]   7   7   7   7   7   7   7   7   7		
12_CoSpurVerniet.UT_Cnt_st@2  9    3   3   3   3   3   3   3   3   3		
T.Z. ColSpurVemiet.UT_Cnt_st62[9]   3   1   2   2   2   3   3   3   3   3   3   3		
TZ_ColSpurVemiet.UT_Cnt_st62[[10]   1   TZ_ColSpurVemiet.UT_Cnt_st62[[11]   10   TZ_ColSpurVemiet.UT_Cnt_st62[[12]   8   TZ_ColSpurVemiet.UT_Cnt_st62[[14]   4   TZ_ColSpurVemiet.UT_Cnt_st62[[14]   4   TZ_ColSpurVemiet.UT_Cnt_st62[[14]   4   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   10   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   10   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   10   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   11   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   11   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   11   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   11   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   11   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   15   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   15   TZ_ColSpurVemiet.UT_Cnt_st62[[16]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[17]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[17]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[17]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[17]   12   TZ_ColSpurVemiet.UT_Cnt_st62[[18]   13   TZ_ColSpurVemiet.UT_Cnt_st62[[18]   13   TZ_ColSpurVemiet.UT_Cnt_st62[[18]   13   TZ_ColSpurVemiet.UT_Cnt_st62[[18]   13   TZ_ColSpurVemiet.UT_Cnt_st62[[18]   17		
12 ColSpurVemierLUT_Cnt_st6[2][11]		
T2_ColSpurVemierLUT_Cnt_s16[2][12]   8     T2_ColSpurVemierLUT_Cnt_s16[2][14]   4     T2_ColSpurVemierLUT_Cnt_s16[2][16]   2     T2_ColSpurVemierLUT_Cnt_s16[2][16]   10     T2_ColSpurVemierLUT_Cnt_s16[2][16]   10     T2_ColSpurVemierLUT_Cnt_s16[3][1]   14     T2_ColSpurVemierLUT_Cnt_s16[3][1]   14     T2_ColSpurVemierLUT_Cnt_s16[3][1]   14     T2_ColSpurVemierLUT_Cnt_s16[3][1]   18     T2_ColSpurVemierLUT_Cnt_s16[3][1]   18     T2_ColSpurVemierLUT_Cnt_s16[3][1]   19     T2_ColSpurVemierLUT_C		
T2_ColSpurVemierLUT_Cnt_st6[2][14]		
T2_ColSpurVernierLUT_Cnt_s16[2][14]		
T2_ColSpurVemierLUT_Cnt_st6[2][16]		
T2_ColSpurVemierLUT_Cnt_sti6[3][10]	T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVermierLUT_Cnt_s16[3][1]		10
T2_ColSpurVemierLUT_Cnt_s16[3][2] 11 T2_ColSpurVemierLUT_Cnt_s16[3][3] 8 T2_ColSpurVemierLUT_Cnt_s16[3][5] 12_ColSpurVemierLUT_Cnt_s16[3][6] 15 T2_ColSpurVemierLUT_Cnt_s16[3][6] 15 T2_ColSpurVemierLUT_Cnt_s16[3][7] 12_ColSpurVemierLUT_Cnt_s16[3][7] 12_ColSpurVemierLUT_Cnt_s16[3][7] 12_ColSpurVemierLUT_Cnt_s16[3][8] 9 T2_ColSpurVemierLUT_Cnt_s16[3][9] 6 T2_ColSpurVemierLUT_Cnt_s16[3][9] 6 T2_ColSpurVemierLUT_Cnt_s16[3][10] 13 T2_ColSpurVemierLUT_Cnt_s16[3][11] 16 T2_ColSpurVemierLUT_Cnt_s16[3][12] 13 T2_ColSpurVemierLUT_Cnt_s16[3][13] 10 T2_ColSpurVemierLUT_Cnt_s16[3][14] 7 T2_ColSpurVemierLUT_Cnt_s16[3][15] 14 T2_ColSpurVemierLUT_Cnt_s16[3][16] 17 T2_ColSpurVemierLUT_Cnt_s16[3][16] 17 T2_DualSpurVemierLUT_Cnt_s16[3][16] 17 T2_DualSpurVemierLUT_Cnt_s16[0][1] 12_DualSpurVemierLUT_Cnt_s16[0][1] 12_DualSpurVemierLUT_Cnt_s16[0][1] 12_DualSpurVemierLUT_Cnt_s16[0][2] 12_DualSpurVemierLUT_Cnt_s16[0][3] 12_DualSpurVemierLUT_Cnt_s16[0][6] 12_DualSpurVemierLUT_Cnt_s16[0][6] 12_DualSpurVemierLUT_Cnt_s16[0][6] 12_DualSpurVemierLUT_Cnt_s16[0][6] 12_DualSpurVemierLUT_Cnt_s16[0][6] 12_DualSpurVemierLUT_Cnt_s16[0][7] 144 12_DualSpurVemierLUT_Cnt_s16[0][7] 144 12_DualSpurVemierLUT_Cnt_s16[0][7] 144 12_DualSpurVemierLUT_Cnt_s16[0][6] 13_DualSpurVemierLUT_Cnt_s16[0][6] 140 140 140 140 140 140 140 140 140 140	T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][4]   5     T2_ColSpurVernierLUT_Cnt_s16[3][6]   2     T2_ColSpurVernierLUT_Cnt_s16[3][6]   15     T2_ColSpurVernierLUT_Cnt_s16[3][6]   15     T2_ColSpurVernierLUT_Cnt_s16[3][7]   12     T2_ColSpurVernierLUT_Cnt_s16[3][8]   9     T2_ColSpurVernierLUT_Cnt_s16[3][9]   6     T2_ColSpurVernierLUT_Cnt_s16[3][10]   3     T2_ColSpurVernierLUT_Cnt_s16[3][10]   3     T2_ColSpurVernierLUT_Cnt_s16[3][11]   16     T2_ColSpurVernierLUT_Cnt_s16[3][12]   13     T2_ColSpurVernierLUT_Cnt_s16[3][13]   10     T2_ColSpurVernierLUT_Cnt_s16[3][14]   7     T2_ColSpurVernierLUT_Cnt_s16[3][15]   4     T2_ColSpurVernierLUT_Cnt_s16[3][16]   17     T2_DualSpurVernierLUT_Cnt_s16[0][0]   396     T2_DualSpurVernierLUT_Cnt_s16[0][1]   380     T2_DualSpurVernierLUT_Cnt_s16[0][2]   324     T2_DualSpurVernierLUT_Cnt_s16[0][3]   288     T2_DualSpurVernierLUT_Cnt_s16[0][6]   180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   190     T2_D	T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][4]       5         T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][9]       6         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -0         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -0	T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][5]   2     T2_ColSpurVernierLUT_Cnt_s16[3][7]   12     T2_ColSpurVernierLUT_Cnt_s16[3][8]   9     T2_ColSpurVernierLUT_Cnt_s16[3][9]   6     T2_ColSpurVernierLUT_Cnt_s16[3][9]   6     T2_ColSpurVernierLUT_Cnt_s16[3][10]   3     T2_ColSpurVernierLUT_Cnt_s16[3][11]   16     T2_ColSpurVernierLUT_Cnt_s16[3][12]   13     T2_ColSpurVernierLUT_Cnt_s16[3][13]   10     T2_ColSpurVernierLUT_Cnt_s16[3][14]   7     T2_ColSpurVernierLUT_Cnt_s16[3][16]   17     T2_ColSpurVernierLUT_Cnt_s16[3][16]   17     T2_DualSpurVernierLUT_Cnt_s16[0][0]   -396     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -360     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -324     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -252     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -190     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -190     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -190     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -190     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -190     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -190     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -72     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -72     DualSpurVernierLUT_Cnt_s16[0][1]   -72     DualSpurVernierLUT	T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][6]       15         T2_ColSpurVernierLUT_Cnt_s16[3][7]       12         T2_ColSpurVernierLUT_Cnt_s16[3][8]       9         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36	T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3] T7   12   12   12   12   13   12   12   13   12   13   12   13   13	T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][8]   9     T2_ColSpurVernierLUT_Cnt_s16[3][9]   6     T2_ColSpurVernierLUT_Cnt_s16[3][10]   3     T2_ColSpurVernierLUT_Cnt_s16[3][11]   16     T2_ColSpurVernierLUT_Cnt_s16[3][12]   13     T2_ColSpurVernierLUT_Cnt_s16[3][12]   13     T2_ColSpurVernierLUT_Cnt_s16[3][14]   7     T2_ColSpurVernierLUT_Cnt_s16[3][15]   4     T2_ColSpurVernierLUT_Cnt_s16[3][16]   17     T2_DualSpurVernierLUT_Cnt_s16[0][0]   -396     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -360     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -324     T2_DualSpurVernierLUT_Cnt_s16[0][2]   -324     T2_DualSpurVernierLUT_Cnt_s16[0][3]   -288     T2_DualSpurVernierLUT_Cnt_s16[0][5]   -216     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -180     T2_DualSpurVernierLUT_Cnt_s16[0][6]   -180     T2_DualSpurVernierLUT_Cnt_s16[0][7]   -144     T2_DualSpurVernierLUT_Cnt_s16[0][9]   -72     T2_DualSpurVernierLUT_Cnt_s16[0][1]   -72     T2_DualSpurVernierLUT_Cnt_s	T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][12] 10 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] 396 T2_DualSpurVernierLUT_Cnt_s16[0][1] 360 T2_DualSpurVernierLUT_Cnt_s16[0][2] 324 T2_DualSpurVernierLUT_Cnt_s16[0][3] 288 T2_DualSpurVernierLUT_Cnt_s16[0][4] 252 T2_DualSpurVernierLUT_Cnt_s16[0][5] 216 T2_DualSpurVernierLUT_Cnt_s16[0][6] 180 T2_DualSpurVernierLUT_Cnt_s16[0][6] 180 T2_DualSpurVernierLUT_Cnt_s16[0][7] 144 T2_DualSpurVernierLUT_Cnt_s16[0][8] 108 T2_DualSpurVernierLUT_Cnt_s16[0][9] 36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 36 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72		
T2_ColSpurVernierLUT_Cnt_s16[3][10]       3         T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][1]       0         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_ColSpurVernierLUT_Cnt_s16[3][11]       16         T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][5]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_ColSpurVernierLUT_Cnt_s16[3][12]       13         T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_ColSpurVernierLUT_Cnt_s16[3][13]       10         T2_ColSpurVernierLUT_Cnt_s16[3][14]       7         T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72          T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72	_ :	
T2_ColSpurVernierLUT_Cnt_s16[3][15]       4         T2_ColSpurVernierLUT_Cnt_s16[3][16]       17         T2_DualSpurVernierLUT_Cnt_s16[0][0]       -396         T2_DualSpurVernierLUT_Cnt_s16[0][1]       -360         T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][5]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17  T2_DualSpurVernierLUT_Cnt_s16[0][0] -396  T2_DualSpurVernierLUT_Cnt_s16[0][1] -360  T2_DualSpurVernierLUT_Cnt_s16[0][2] -324  T2_DualSpurVernierLUT_Cnt_s16[0][3] -288  T2_DualSpurVernierLUT_Cnt_s16[0][4] -252  T2_DualSpurVernierLUT_Cnt_s16[0][5] -216  T2_DualSpurVernierLUT_Cnt_s16[0][6] -180  T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72		
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][6] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] -36 T2_DualSpurVernierLUT_Cnt_s16[0][12] -36 T2_DualSpurVernierLUT_Cnt_s16[0][13] -72		
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360  T2_DualSpurVernierLUT_Cnt_s16[0][2] -324  T2_DualSpurVernierLUT_Cnt_s16[0][3] -288  T2_DualSpurVernierLUT_Cnt_s16[0][4] -252  T2_DualSpurVernierLUT_Cnt_s16[0][6] -216  T2_DualSpurVernierLUT_Cnt_s16[0][6] -180  T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72		
T2_DualSpurVernierLUT_Cnt_s16[0][2]       -324         T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][5]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][3]       -288         T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][5]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][4]       -252         T2_DualSpurVernierLUT_Cnt_s16[0][5]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][5]       -216         T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][6]       -180         T2_DualSpurVernierLUT_Cnt_s16[0][7]       -144         T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144  T2_DualSpurVernierLUT_Cnt_s16[0][8] -108  T2_DualSpurVernierLUT_Cnt_s16[0][9] -72  T2_DualSpurVernierLUT_Cnt_s16[0][10] -36  T2_DualSpurVernierLUT_Cnt_s16[0][11] 0  T2_DualSpurVernierLUT_Cnt_s16[0][12] 36  T2_DualSpurVernierLUT_Cnt_s16[0][13] 72		
T2_DualSpurVernierLUT_Cnt_s16[0][8]       -108         T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][9]       -72         T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][10]       -36         T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][11]       0         T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][12]       36         T2_DualSpurVernierLUT_Cnt_s16[0][13]       72		
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72		
T2_DualSpurVernierLUT_Cnt_s16[0][14] 108		

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2 DualSpurVernierLUT Cnt s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2 DualSpurVernierLUT Cnt s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2 DualSpurVernierLUT Cnt s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2 DualSpurVernierLUT Cnt s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2 DualSpurVernierLUT Cnt s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
	11
T2_DualSpurVernierLUT_Cnt_s16[3][16] T3_DualSpurVernierLUT_Cnt_s16[3][17]	
T2_DualSpurVernierLUT_Cnt_s16[3][17] T3_DualSpurVernierLUT_Cnt_s16[3][18]	13 15
T2_DualSpurVernierLUT_Cnt_s16[3][18] T3_DualSpurVernierLUT_Cnt_s16[3][10]	17
T2_DualSpurVernierLUT_Cnt_s16[3][19]	
T2 DualSpurVernierl LT_Cnt_c46(2)(20)	
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	19 21

tgt\_DigColPs\_Per2\_TrimComp\_Cnt\_lgc.value

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		• "	
Name	Input Value		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	34		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1400.76807	1400.768182 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	1392.65991	1392.66 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	492.659912	492.66 ± 0.0009	•
tot DigColDo Dog2 TrimComp Cot Jac value	4	4	

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.24 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	53	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	
DigColPs_I2CHwColAngle_Deg_M_f32	360	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535	
DigColPs_I2CHwSpurAngle_Deg_M_f32	360	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	31	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1800	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255	
DigColPs_SpurTrimStatic_Deg_M_f32	360	
DigColPs_TrimCompStatic_Cnt_M_u16	4488	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20	

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Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2 ColSpurVernierLUT Cnt s16[0][8]	98
T2 ColSpurVernierLUT Cnt s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][5] T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T3_ColSpurVernierLUT_Cnt_s46[4][412]	
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1 0
T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2 DualSpurVernierLUT Cnt s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][10]	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
	10
	1 10
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	1	
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
$tgt\_Rte\_Inst\_Sa\_DigColPs.DigColPs\_Per2\_TrimComp\_Cnt\_lgc$	tgt_DigColPs_Per2_TrimComp	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrEquit Cnt M Igc	1	1	

tgt_Rte_inst_Sa_bigColPs:Pilli_bigColPsEOL	tgt_Pim_bigColPSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1800	1800 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	900	900 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	<b>✓</b>
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	<b>✓</b>
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

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Project	DigColPs
Module	DigColPs
Test Object	VernierLookup

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
indule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro "Redundant_Format_1_m" to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac_" "

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg</pre>
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 358.00 Cycles Longest Execution Path TS1.2 16.00 Cycles Shortest Execution Path

#### VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path =>
If (Level\_Deg\_T\_f32 <= T2\_VernierLUT\_Cnt\_u16(D\_VERNIERLEVEL\_CNT\_U08, 0))=>FALSE
For Search\_Cnt\_T\_u08 = 0 To (TableSize\_Cnt\_T\_u08 - 2)=>TRUE
If (MatchFound\_Cnt\_T\_lgc = False)=>TRUE
If (Level\_Deg\_T\_f32 < T2\_VernierLUT\_Cnt\_u16(D\_VERNIERLEVEL\_CNT\_U08, (Search\_Cnt\_T\_u08 + 1)))=>TRUE
If (Level\_Deg\_T\_f32 < Middle\_Cnt\_T\_f32)=>FALSE
If (MatchFound\_Cnt\_T\_lgc = False)=>FALSE''
TS1.2 "Shortest Execution Path =>
If (Level\_Deg\_T\_f32 <= T2\_VernierLUT\_Cnt\_u16(D\_VERNIERLEVEL\_CNT\_U08, 0)) =>TRUE''

Test Step 1.1 (Repeat Count = 1)	✓
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr Cnt T u08	tgt SpurRevPtr Cnt T u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	3
VernierLUT_Cnt_T_s16[36]	4
VernierLUT_Cnt_T_s16[37]	5
VernierLUT_Cnt_T_s16[38] VernierLUT_Cnt_T_s16[39]	6
	7
VernierLUT_Cnt_T_s16[40] VernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[41]	9
VernierLUT_Cnt_T_s16[42] VernierLUT_Cnt_T_s16[43]	0
VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4
V6/11/0/12/01/_1_0/10[T0]	*

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u	08	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	<b>~</b>
tgt_SpurRevPtr_Cnt_T_u08	0	0	<b>~</b>
tgt_VernierLevelNo_Cnt_T_u08	1	1	_
<del></del>			

Test Step 1.2 (Repeat Count = 1)	<u>la companya di managan di managan</u>
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[20]	2		
VernierLUT_Cnt_T_s16[21]	1		
VernierLUT_Cnt_T_s16[22]	0		
VernierLUT_Cnt_T_s16[23]	4		
VernierLUT_Cnt_T_s16[24]	3		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	1		
VernierLUT_Cnt_T_s16[27]	0		
VernierLUT_Cnt_T_s16[28] VernierLUT_Cnt_T_s16[29]	3		
VernierLUT_Cnt_T_s16[30]	2		
VernierLUT_Cnt_T_s16[31]	1		
VernierLUT_Cnt_T_s16[32]	0		
VernierLUT_Cnt_T_s16[33]	4		
VernierLUT_Cnt_T_s16[34]	0		
VernierLUT_Cnt_T_s16[35]	8		
VernierLUT_Cnt_T_s16[36]	6		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	0		
VernierLUT_Cnt_T_s16[39] VernierLUT_Cnt_T_s16[40]	9		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[42]	5		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50] VernierLUT_Cnt_T_s16[51]	10		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60] VernierLUT_Cnt_T_s16[61]	6 3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70] VernierLUT_Cnt_T_s16[71]	-99 -66		
VernierLUT_Cnt_T_s16[71] VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[72]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261 294		
VernierLUT_Cnt_T_s16[82] VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_	T_u08	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	~
tgt VernierLevelNo Cnt T u08	1	1	<b>✓</b>

tgt\_VernierLevelNo\_Cnt\_T\_u08



# 

Test Step 2.1 (Repeat Count = 1)	Inc. of Value
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1.
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT Cnt T s16[40]	9
VernierLUT Cnt T s16[41]	7

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VernierLookup

Name	Input Value		
VernierLUT_Cnt_T_s16[42]	5		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT Cnt T s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u0	08	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	
tgt_SpurRevPtr_Cnt_T_u08	0	0	
tgt_VernierLevelNo_Cnt_T_u08	1	1	

Test Step 2.2 (Repeat Count = 1)		✓
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	360	
LookupTableXSize_Cnt_T_u08	22	
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
VernierLUT_Cnt_T_s16[0]	-396	
VernierLUT_Cnt_T_s16[1]	-360	
VernierLUT_Cnt_T_s16[2]	-324	
VernierLUT_Cnt_T_s16[3]	-288	
VernierLUT_Cnt_T_s16[4]	-252	
VernierLUT_Cnt_T_s16[5]	-216	
VernierLUT_Cnt_T_s16[6]	-180	
VernierLUT_Cnt_T_s16[7]	-144	
VernierLUT_Cnt_T_s16[8]	-108	
VernierLUT_Cnt_T_s16[9]	-72	
VernierLUT_Cnt_T_s16[10]	-36	
VernierLUT_Cnt_T_s16[11]	0	
VernierLUT_Cnt_T_s16[12]	36	

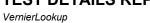
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VernierLookup

VernierLookup	TOACIO
Name	Input Value
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
/ernierLUT_Cnt_T_s16[31]	8
/ernierLUT_Cnt_T_s16[32]	9
/ernierLUT_Cnt_T_s16[33]	0
/ernierLUT_Cnt_T_s16[34]	1
/ernierLUT_Cnt_T_s16[35]	2
/ernierLUT_Cnt_T_s16[36]	3
/ernierLUT_Cnt_T_s16[37]	4
/ernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
/ernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[42]	9
/ernierLUT_Cnt_T_s16[43]	0
/ernierLUT_Cnt_T_s16[44]	0
/ernierLUT_Cnt_T_s16[45]	1
/ernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	5
VernierLUT_Cnt_T_s16[50]	6
VernierLUT_Cnt_T_s16[51]	7
VernierLUT_Cnt_T_s16[52]	8
VernierLUT_Cnt_T_s16[53]	9
VernierLUT_Cnt_T_s16[54]	10
VernierLUT_Cnt_T_s16[55]	0
/ernierLUT_Cnt_T_s16[56]	1
/ernierLUT_Cnt_T_s16[57]	2
/ernierLUT_Cnt_T_s16[58]	3
/ernierLUT_Cnt_T_s16[59]	4
/ernierLUT_Cnt_T_s16[60]	5
/ernierLUT_Cnt_T_s16[61]	6
/ernierLUT_Cnt_T_s16[62]	7
/ernierLUT_Cnt_T_s16[63]	8
/ernierLUT_Cnt_T_s16[64]	9
/ernierLUT_Cnt_T_s16[65]	10
/ernierLUT_Cnt_T_s16[66]	22
/ernierLUT_Cnt_T_s16[67]	2
/ernierLUT_Cnt_T_s16[68]	4
/ernierLUT_Cnt_T_s16[69]	6
/ernierLUT_Cnt_T_s16[70]	8
/ernierLUT_Cnt_T_s16[71]	10
/ernierLUT_Cnt_T_s16[72]	12
/ernierLUT_Cnt_T_s16[73]	14
/ernierLUT_Cnt_T_s16[74]	16
/ernierLUT_Cnt_T_s16[75]	18
/ernierLUT_Cnt_T_s16[76]	20
/ernierLUT_Cnt_T_s16[77]	1
/ernierLUT_Cnt_T_s16[78]	3
/ernierLUT_Cnt_T_s16[79]	5
VernierLUT_Cnt_T_s16[80]	7
VernierLUT_Cnt_T_s16[81]	9
VernierLUT_Cnt_T_s16[82]	11
	13
/ernierLUT_Cnt_T_s16[83]	13
VernierLUT_Cnt_T_s16[83] VernierLUT_Cnt_T_s16[84]	15

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Name	Input Value	Input Value		
VernierLUT_Cnt_T_s16[86]	19			
VernierLUT_Cnt_T_s16[87]	21			
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result	
tgt_ColRevPtr_Cnt_T_u08	0	0	~	
tgt_SpurRevPtr_Cnt_T_u08	10	10	<b>✓</b>	
tgt_VernierLevelNo_Cnt_T_u08	21	21	<b>✓</b>	

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level Deg T f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr Cnt T u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6 4
VernierLUT_Cnt_T_s16[37]	
VernierLUT_Cnt_T_s16[38] VernierLUT_Cnt_T_s16[39]	2 0
	9
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41] VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_1_s16[42] VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[43] VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[44]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT Cnt T s16[47]	6
VernierLUT_Cnt_T_s16[47] VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2
	<u> </u>

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Name	Input Value		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	-
tgt_SpurRevPtr_Cnt_T_u08	0	0	~
tgt_VernierLevelNo_Cnt_T_u08	2	2	~

Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
ColRevPtr Cnt T u08	tgt ColRevPtr Cnt T u08
Level Deg T f32	245.2
LookupTableXSize Cnt T u08	22
SpurRevPtr Cnt T u08	tgt SpurRevPtr Cnt T u08
VernierLUT Cnt T s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4

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vernier Lookup		
Name	Input Value	
VernierLUT_Cnt_T_s16[28]	5	
VernierLUT_Cnt_T_s16[29]	6	
VernierLUT_Cnt_T_s16[30]	7	
VernierLUT_Cnt_T_s16[31]	8	
VernierLUT_Cnt_T_s16[32]	9	
VernierLUT_Cnt_T_s16[33] VernierLUT_Cnt_T_s16[34]	1	
VernierLUT_Cnt_T_s16[35]	2	
VernierLUT_Cnt_T_s16[36]	3	
VernierLUT_Cnt_T_s16[37]	4	
VernierLUT_Cnt_T_s16[38]	5	
VernierLUT_Cnt_T_s16[39]	6	
VernierLUT_Cnt_T_s16[40]	7	
VernierLUT_Cnt_T_s16[41]	8	
VernierLUT_Cnt_T_s16[42]	9	
VernierLUT_Cnt_T_s16[43]	0	
VernierLUT_Cnt_T_s16[44]	0	
VernierLUT_Cnt_T_s16[45]	1	
VernierLUT_Cnt_T_s16[46]	2 3	
VernierLUT_Cnt_T_s16[47]	4	
VernierLUT_Cnt_T_s16[48] VernierLUT_Cnt_T_s16[49]	5	
VernierLUT_Cnt_T_s16[50]	6	
VernierLUT_Cnt_T_s16[51]	7	
VernierLUT_Cnt_T_s16[52]	8	
VernierLUT_Cnt_T_s16[53]	9	
VernierLUT_Cnt_T_s16[54]	10	
VernierLUT_Cnt_T_s16[55]	0	
VernierLUT_Cnt_T_s16[56]	1	
VernierLUT_Cnt_T_s16[57]	2	
VernierLUT_Cnt_T_s16[58]	3	
VernierLUT_Cnt_T_s16[59]	4	
VernierLUT_Cnt_T_s16[60]	5	
VernierLUT_Cnt_T_s16[61]	6	
VernierLUT_Cnt_T_s16[62]	7	
VernierLUT_Cnt_T_s16[63]	8	
VernierLUT_Cnt_T_s16[64] VernierLUT_Cnt_T_s16[65]	10	
VernierLUT_Cnt_T_s16[66]	22	
VernierLUT_Cnt_T_s16[67]	2	
VernierLUT_Cnt_T_s16[68]	4	
VernierLUT_Cnt_T_s16[69]	6	
VernierLUT_Cnt_T_s16[70]	8	
VernierLUT_Cnt_T_s16[71]	10	
VernierLUT_Cnt_T_s16[72]	12	
VernierLUT_Cnt_T_s16[73]	14	
VernierLUT_Cnt_T_s16[74]	16	
VernierLUT_Cnt_T_s16[75]	18	
VernierLUT_Cnt_T_s16[76]	20	
VernierLUT_Cnt_T_s16[77]	1	
VernierLUT_Cnt_T_s16[78]	3 5	
VernierLUT_Cnt_T_s16[79] VernierLUT_Cnt_T_s16[80]	7	
VernierLUT_Cnt_T_s16[81]	9	
VernierLUT_Cnt_T_s16[82]	11	
VernierLUT_Cnt_T_s16[83]	13	
VernierLUT_Cnt_T_s16[84]	15	
VernierLUT_Cnt_T_s16[85]	17	
VernierLUT_Cnt_T_s16[86]	19	
VernierLUT_Cnt_T_s16[87]	21	
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08	
Name	Actual Value Expo	ected Value Resul
tgt_ColRevPtr_Cnt_T_u08	7 7	•
tgt_SpurRevPtr_Cnt_T_u08	7	•
tgt_VernierLevelNo_Cnt_T_u08	15 15	

Test Step 2.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-456.8
LookupTableXSize_Cnt_T_u08	17

VernierLookup

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Input Value tgt\_SpurRevPtr\_Cnt\_T\_u08 SpurRevPtr\_Cnt\_T\_u08 VernierLUT\_Cnt\_T\_s16[0] -163 -131 VernierLUT\_Cnt\_T\_s16[1] VernierLUT\_Cnt\_T\_s16[2] -99 VernierLUT\_Cnt\_T\_s16[3] -66 VernierLUT\_Cnt\_T\_s16[4] -33 VernierLUT\_Cnt\_T\_s16[5] 0 VernierLUT\_Cnt\_T\_s16[6] 32 VernierLUT\_Cnt\_T\_s16[7] 65 VernierLUT\_Cnt\_T\_s16[8] 98 VernierLUT\_Cnt\_T\_s16[9] 130 VernierLUT\_Cnt\_T\_s16[10] 163 VernierLUT\_Cnt\_T\_s16[11] 196 VernierLUT Cnt T s16[12] 229 VernierLUT\_Cnt\_T\_s16[13] 261 VernierLUT\_Cnt\_T\_s16[14] 294 VernierLUT\_Cnt\_T\_s16[15] 327 VernierLUT\_Cnt\_T\_s16[16] 359 VernierLUT\_Cnt\_T\_s16[17] 0 VernierLUT\_Cnt\_T\_s16[18] 3 VernierLUT\_Cnt\_T\_s16[19] VernierLUT\_Cnt\_T\_s16[20] 2 VernierLUT\_Cnt\_T\_s16[21] 1 VernierLUT\_Cnt\_T\_s16[22] 0 VernierLUT\_Cnt\_T\_s16[23] 4 VernierLUT\_Cnt\_T\_s16[24] 3 VernierLUT\_Cnt\_T\_s16[25] 2 VernierLUT\_Cnt\_T\_s16[26] VernierLUT\_Cnt\_T\_s16[27] 0 VernierLUT\_Cnt\_T\_s16[28] 4 VernierLUT\_Cnt\_T\_s16[29] 3 VernierLUT\_Cnt\_T\_s16[30] 2 VernierLUT Cnt T s16[31] 1 VernierLUT\_Cnt\_T\_s16[32] 0 VernierLUT\_Cnt\_T\_s16[33] 4 VernierLUT Cnt T s16[34] 0 VernierLUT\_Cnt\_T\_s16[35] 8 VernierLUT\_Cnt\_T\_s16[36] 6 VernierLUT\_Cnt\_T\_s16[37] 4 VernierLUT Cnt T s16[38] 2 VernierLUT\_Cnt\_T\_s16[39] 0 VernierLUT\_Cnt\_T\_s16[40] 9 VernierLUT\_Cnt\_T\_s16[41] 7 VernierLUT\_Cnt\_T\_s16[42] 5 VernierLUT\_Cnt\_T\_s16[43] 3 VernierLUT\_Cnt\_T\_s16[44] VernierLUT\_Cnt\_T\_s16[45] 10 VernierLUT\_Cnt\_T\_s16[46] 8 6 VernierLUT\_Cnt\_T\_s16[47] VernierLUT\_Cnt\_T\_s16[48] 4 2 VernierLUT\_Cnt\_T\_s16[49] VernierLUT\_Cnt\_T\_s16[50] 10 VernierLUT\_Cnt\_T\_s16[51] 1 VernierLUT\_Cnt\_T\_s16[52] 14 VernierLUT\_Cnt\_T\_s16[53] 11 VernierLUT\_Cnt\_T\_s16[54] 8 VernierLUT\_Cnt\_T\_s16[55] 5 VernierLUT\_Cnt\_T\_s16[56] 2 VernierLUT\_Cnt\_T\_s16[57] 15 VernierLUT\_Cnt\_T\_s16[58] 12 VernierLUT\_Cnt\_T\_s16[59] 9 VernierLUT Cnt T s16[60] 6 VernierLUT\_Cnt\_T\_s16[61] 3 VernierLUT\_Cnt\_T\_s16[62] 16 VernierLUT\_Cnt\_T\_s16[63] 13 VernierLUT Cnt T s16[64] 10 VernierLUT\_Cnt\_T\_s16[65] 7 VernierLUT Cnt T s16[66] 17 VernierLUT\_Cnt\_T\_s16[67] VernierLUT\_Cnt\_T\_s16[68] -163 -131 VernierLUT\_Cnt\_T\_s16[69] VernierLUT\_Cnt\_T\_s16[70] -99 VernierLUT\_Cnt\_T\_s16[71] -66

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Name	Input Value		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	<b>→</b>
tgt_SpurRevPtr_Cnt_T_u08	0	0	<b>✓</b>
tgt_VernierLevelNo_Cnt_T_u08	1	1	<b>✓</b>

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
/ernierLUT_Cnt_T_s16[2]	-99
/ernierLUT_Cnt_T_s16[3]	-66
/ernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
/ernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
/ernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
/ernierLUT_Cnt_T_s16[13]	261
/ernierLUT_Cnt_T_s16[14]	294
/ernierLUT_Cnt_T_s16[15]	327
/ernierLUT_Cnt_T_s16[16]	359
/ernierLUT_Cnt_T_s16[17]	0
/ernierLUT_Cnt_T_s16[18]	4
/ernierLUT_Cnt_T_s16[19]	3
/ernierLUT_Cnt_T_s16[20]	2
/ernierLUT_Cnt_T_s16[21]	1
/ernierLUT_Cnt_T_s16[22]	0
/ernierLUT_Cnt_T_s16[23]	4
/ernierLUT_Cnt_T_s16[24]	3
/ernierLUT_Cnt_T_s16[25]	2
/ernierLUT_Cnt_T_s16[26]	1
/ernierLUT_Cnt_T_s16[27]	0
/ernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
/ernierLUT_Cnt_T_s16[31]	1
/ernierLUT_Cnt_T_s16[32]	0
/ernierLUT_Cnt_T_s16[33]	4
/ernierLUT_Cnt_T_s16[34]	0
/ernierLUT_Cnt_T_s16[35]	8
/ernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[40] VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[41] VernierLUT_Cnt_T_s16[42]	5

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Vernier Lookup			
Name	Input Value		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	~
tgt_VernierLevelNo_Cnt_T_u08	2	2	•

Test Step 2.7 (Repeat Count = 1)	✓
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-7
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72

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VernierLookup		MACILAL
Name	Input Value	
VernierLUT_Cnt_T_s16[14]	108	
VernierLUT_Cnt_T_s16[15]	144	
VernierLUT_Cnt_T_s16[16]	180	
VernierLUT_Cnt_T_s16[17]	216	
VernierLUT_Cnt_T_s16[18]	252	
VernierLUT_Cnt_T_s16[19]	288	
VernierLUT_Cnt_T_s16[20]	324	
VernierLUT_Cnt_T_s16[21]	360	
VernierLUT_Cnt_T_s16[22]	9	
VernierLUT_Cnt_T_s16[23]	0	
VernierLUT_Cnt_T_s16[24]	1	
VernierLUT_Cnt_T_s16[25]	2	
VernierLUT_Cnt_T_s16[26]	3	
VernierLUT_Cnt_T_s16[27]	4	
VernierLUT_Cnt_T_s16[28]	5	
VernierLUT_Cnt_T_s16[29]	6	
VernierLUT_Cnt_T_s16[30]	7	
VernierLUT_Cnt_T_s16[31]	8	
VernierLUT_Cnt_T_s16[32]	9	
VernierLUT_Cnt_T_s16[33]	0	
VernierLUT_Cnt_T_s16[34]	1	
VernierLUT_Cnt_T_s16[35]	2	
VernierLUT_Cnt_T_s16[36]	3	
VernierLUT_Cnt_T_s16[37]	4	
VernierLUT_Cnt_T_s16[38]	5	
VernierLUT_Cnt_T_s16[39]	6	
VernierLUT_Cnt_T_s16[40]	7	
VernierLUT_Cnt_T_s16[41]	8	
VernierLUT_Cnt_T_s16[42]	9	
VernierLUT_Cnt_T_s16[43]	0	
VernierLUT_Cnt_T_s16[44]	0	
VernierLUT_Cnt_T_s16[45]	1	
VernierLUT_Cnt_T_s16[46]	2	
VernierLUT_Cnt_T_s16[47]	3	
VernierLUT_Cnt_T_s16[48]	4	
VernierLUT_Cnt_T_s16[49]	5	
VernierLUT_Cnt_T_s16[50]	6	
VernierLUT_Cnt_T_s16[51]	7	
VernierLUT_Cnt_T_s16[52]	8	
VernierLUT_Cnt_T_s16[53]	9	
VernierLUT_Cnt_T_s16[54]	10	
VernierLUT_Cnt_T_s16[55]	0	
VernierLUT_Cnt_T_s16[56]	1	
VernierLUT_Cnt_T_s16[57]	2	
VernierLUT_Cnt_T_s16[58]	3	
VernierLUT_Cnt_T_s16[59]	4	
VernierLUT_Cnt_T_s16[60]	5	
VernierLUT_Cnt_T_s16[61]	6	
VernierLUT_Cnt_T_s16[62]	7	
VernierLUT_Cnt_T_s16[63]	8	
VernierLUT_Cnt_T_s16[64]	9	
VernierLUT_Cnt_T_s16[65]	10	
VernierLUT_Cnt_T_s16[66]	22	
VernierLUT_Cnt_T_s16[67]	2	
VernierLUT_Cnt_T_s16[68]	4	
VernierLUT_Cnt_T_s16[69]	6	
VernierLUT_Cnt_T_s16[70]	8	
VernierLUT_Cnt_T_s16[71]	10	
VernierLUT_Cnt_T_s16[72]	12	
VernierLUT_Cnt_T_s16[73]	14	
VernierLUT_Cnt_T_s16[74]	16	
VernierLUT_Cnt_T_s16[75]	18	
VernierLUT_Cnt_T_s16[76]	20	
VernierLUT_Cnt_T_s16[77]	1	
VernierLUT_Cnt_T_s16[78]	3	
VernierLUT_Cnt_T_s16[79]	5	
VernierLUT_Cnt_T_s16[80]	7	
VernierLUT_Cnt_T_s16[81]	9	
VernierLUT_Cnt_T_s16[82]	11	
VernierLUT_Cnt_T_s16[83]	13	
VernierLUT_Cnt_T_s16[84]	15	
VernierLUT_Cnt_T_s16[85]	17	
VernierLUT_Cnt_T_s16[86]	19	

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Name	Input Value			
VernierLUT_Cnt_T_s16[87]	21			
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result	
tgt_ColRevPtr_Cnt_T_u08	0	0	~	
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓	
tgt_VernierLevelNo_Cnt_T_u08	1	1	~	

est Step 2.8 (Repeat Count = 1)	Invest Webse	
lame	Input Value	
colRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
evel_Deg_T_f32	-792 47	
ookupTableXSize_Cnt_T_u08	17	
purRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
ernierLUT_Cnt_T_s16[0]	-163	
ernierLUT_Cnt_T_s16[1]	-131	
ernierLUT_Cnt_T_s16[2]	-99	
ernierLUT_Cnt_T_s16[3]	-66	
ernierLUT_Cnt_T_s16[4]	-33	
ernierLUT_Cnt_T_s16[5]	0	
ernierLUT_Cnt_T_s16[6]	32	
ernierLUT_Cnt_T_s16[7]	65	
ernierLUT_Cnt_T_s16[8]	98	
ernierLUT_Cnt_T_s16[9]	130	
ernierLUT_Cnt_T_s16[10]	163	
ernierLUT_Cnt_T_s16[11]	196	
ernierLUT_Cnt_T_s16[12]	229	
ernierLUT_Cnt_T_s16[13]	261	
ernierLUT_Cnt_T_s16[14]	294	
ernierLUT_Cnt_T_s16[15]	327	
ernierLUT_Cnt_T_s16[16]	359	
ernierLUT_Cnt_T_s16[17]	0	
ernierLUT_Cnt_T_s16[18]	4	
ernierLUT_Cnt_T_s16[19]	3	
ernierLUT_Cnt_T_s16[20]	2	
ernierLUT_Cnt_T_s16[21]	1	
ernierLUT_Cnt_T_s16[22]	0	
ernierLUT_Cnt_T_s16[23]	4	
ernierLUT_Cnt_T_s16[24]	3	
ernierLUT_Cnt_T_s16[25]	2	
ernierLUT_Cnt_T_s16[26]	1	
ernierLUT_Cnt_T_s16[27]	0	
ernierLUT_Cnt_T_s16[28]	4	
ernierLUT_Cnt_T_s16[29]	3	
ernierLUT_Cnt_T_s16[30]	2	
ernierLUT_Cnt_T_s16[31]	1	
ernierLUT_Cnt_T_s16[32]	0	
ernierLUT_Cnt_T_s16[33]	4	
ernierLUT_Cnt_T_s16[34]	0	
ernierLUT_Cnt_T_s16[35]	8	
ernierLUT_Cnt_T_s16[36]	6	
ernierLUT Cnt T s16[37]	4	
ernierLUT_Cnt_T_s16[38]	2	
ernierLUT_Cnt_T_s16[39]	0	
ernierLUT_Cnt_T_s16[40]	9	
	7	
ernierLUT_Cnt_T_s16[41]		
ernierLUT_Cnt_T_s16[42]	5	
ernierLUT_Cnt_T_s16[43]	3	
ernierLUT_Cnt_T_s16[44]	1	
ernierLUT_Cnt_T_s16[45]	10	
ernierLUT_Cnt_T_s16[46]	8	
ernierLUT_Cnt_T_s16[47]	6	
ernierLUT_Cnt_T_s16[48]	4	
ernierLUT_Cnt_T_s16[49]	2	
ernierLUT_Cnt_T_s16[50]	10	
ernierLUT_Cnt_T_s16[51]	1	
ernierLUT_Cnt_T_s16[52]	14	
ernierLUT_Cnt_T_s16[53]	11	
ernierLUT_Cnt_T_s16[54]	8	
ernierLUT_Cnt_T_s16[55]	5	
ernierLUT_Cnt_T_s16[56]	2	
ernierLUT_Cnt_T_s16[57]	15	

VernierLookup

tgt\_VernierLevelNo\_Cnt\_T\_u08





·			
Name	Input Value		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08	3	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	<b>~</b>

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Test Step 2.9 (Repeat Count = 1)		<b>~</b>
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	360	
LookupTableXSize_Cnt_T_u08	22	
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
VernierLUT_Cnt_T_s16[0]	-396	
VernierLUT_Cnt_T_s16[1]	-360	
VernierLUT_Cnt_T_s16[2]	-324	
VernierLUT_Cnt_T_s16[3]	-288	
VernierLUT_Cnt_T_s16[4]	-252	
VernierLUT_Cnt_T_s16[5]	-216	
VernierLUT_Cnt_T_s16[6]	-180	
VernierLUT_Cnt_T_s16[7]	-144	
VernierLUT_Cnt_T_s16[8]	-108	
VernierLUT_Cnt_T_s16[9]	-72	
VernierLUT_Cnt_T_s16[10]	-36	
VernierLUT_Cnt_T_s16[11]	0	
VernierLUT_Cnt_T_s16[12]	36	
VernierLUT_Cnt_T_s16[13]	72	
VernierLUT_Cnt_T_s16[14]	108	
VernierLUT_Cnt_T_s16[15]	144	
VernierLUT_Cnt_T_s16[16]	180	
VernierLUT_Cnt_T_s16[17]	216	
VernierLUT_Cnt_T_s16[18]	252	
VernierLUT_Cnt_T_s16[19]	288	
VernierLUT_Cnt_T_s16[20]	324	
VernierLUT_Cnt_T_s16[21]	360	
VernierLUT_Cnt_T_s16[22]	9	
VernierLUT_Cnt_T_s16[23]	0	
VernierLUT_Cnt_T_s16[24]	1	
VernierLUT_Cnt_T_s16[25]	2	
VernierLUT_Cnt_T_s16[26]	3	
VernierLUT_Cnt_T_s16[27]	4	
VernierLUT_Cnt_T_s16[28]	5	

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Name	Input Value		
VernierLUT_Cnt_T_s16[29]	6		
VernierLUT_Cnt_T_s16[30]	7		
VernierLUT_Cnt_T_s16[31]	8		
VernierLUT_Cnt_T_s16[32]	9		
VernierLUT_Cnt_T_s16[33]	0		
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	2		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
	5		
VernierLUT_Cnt_T_s16[60] VernierLUT_Cnt_T_s16[61]	6		
	7		
VernierLUT_Cnt_T_s16[62] VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
	2		
VernierLUT_Cnt_T_s16[67] VernierLUT_Cnt_T_s16[68]	4		
	6		
VernierLUT_Cnt_T_s16[69]			
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	10	10	•
tgt_VernierLevelNo_Cnt_T_u08	21	21	~
			-



### **Test Case 3: Path Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 16.00 Cycles TS3.2 378.00 Cycles TS3.3 203.00 Cycles TS3.4 358.00 Cycles

Description

VECTOR DESCRIPTION:

 $TS3.1 \quad \text{If (Level\_Deg\_T\_f32} <= T2\_VernierLUT0\_Cnt\_u16(0)) => True \\ TS3.2 \quad \text{"If (Level\_Deg\_T\_f32} <= T2\_VernierLUT0\_Cnt\_u16(0)) => False \\ For Search\_Cnt\_T\_u08 = 0 To (TableSize\_Cnt\_T\_u08 - 1) => True \\ If (MatchFound\_Cnt\_T\_lgc = False) => True \\ If (Level\_Deg\_T\_f32 < T2\_VernierLUT0\_Cnt\_u16(Search\_Cnt_T\_u08 + 1)) => False \\ If (MatchFound\_Cnt\_T\_lgc = False) => True" \\ TS3.3 \quad \text{"If (Level\_Deg\_T\_f32} < T2\_VernierLUT0\_Cnt\_u16(Search\_Cnt_T\_u08 + 1)) => True \\ If (Level\_Deg\_T\_f32 < Middle\_Cnt_T\_f32) => True" \\ TS3.4 \quad If (Level\_Deg\_T\_f32 < Middle\_Cnt_T\_f32) => False \\ \label{eq:table_cnt_T}$ 

Test Step 3.1 (Repeat Count = 1)	✓
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4

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		`	
Name	Input Value		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u	08	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	- Trooun
tgt_SpurRevPtr_Cnt_T_u08	0	0	_
tgt_VernierLevelNo_Cnt_T_u08	1	1	
3 1 00. 1 0 0 1 _ 1 _ u 0 0	'	'	· · · · · · · · · · · · · · · · · · ·

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	360
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288

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Namo	Innut Value		
Name VernierLUT_Cnt_T_s16[20]	Input Value 324		
VernierLUT_Cnt_T_s16[20] VernierLUT_Cnt_T_s16[21]	360		
VernierLUT_Cnt_T_s16[22]	9		
VernierLUT_Cnt_T_s16[23]	0		
VernierLUT_Cnt_T_s16[24]	1		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	3		
VernierLUT_Cnt_T_s16[27]	4		
VernierLUT_Cnt_T_s16[28]	5		
VernierLUT_Cnt_T_s16[29] VernierLUT_Cnt_T_s16[30]	6 7		
VernierLUT_Cnt_T_s16[31]	8		
VernierLUT_Cnt_T_s16[32]	9		
VernierLUT_Cnt_T_s16[33]	0		
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	2		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42] VernierLUT_Cnt_T_s16[43]	9		
VernierLUT_Cnt_1_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	0		
VernierLUT_Cnt_T_s16[55] VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67] VernierLUT_Cnt_T_s16[68]	2		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79] VernierLUT_Cnt_T_s16[80]	5 7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_		
Name	Actual Value	Expected Va	
tgt_ColRevPtr_Cnt_T_u08	0	0	•
tgt_SpurRevPtr_Cnt_T_u08	10	10	~

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tgt\_VernierLevelNo\_Cnt\_T\_u08



Test Step 3.3 (Repeat Count = 1)		
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	0	
LookupTableXSize_Cnt_T_u08	17	
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
VernierLUT_Cnt_T_s16[0]	-163	
VernierLUT_Cnt_T_s16[1]	-131	
VernierLUT_Cnt_T_s16[2]	-99	
VernierLUT_Cnt_T_s16[3]	-66	
VernierLUT_Cnt_T_s16[4]	-33	
VernierLUT_Cnt_T_s16[5]	0	
VernierLUT_Cnt_T_s16[6]	32 65	
VernierLUT_Cnt_T_s16[7] VernierLUT_Cnt_T_s16[8]	98	
VernierLUT_Cnt_T_s16[9]	130	
VernierLUT_Cnt_T_s16[10]	163	
VernierLUT_Cnt_T_s16[10]	196	
VernierLUT_Cnt_T_s16[12]	229	
VernierLUT_Cnt_T_s16[13]	261	
VernierLUT_Cnt_T_s16[14]	294	
VernierLUT_Cnt_T_s16[15]	327	
VernierLUT_Cnt_T_s16[16]	359	
VernierLUT_Cnt_T_s16[17]	0	
VernierLUT_Cnt_T_s16[18]	4	
VernierLUT_Cnt_T_s16[19]	3	
VernierLUT_Cnt_T_s16[20]	2	
VernierLUT_Cnt_T_s16[21]	1	
VernierLUT_Cnt_T_s16[22]	0	
VernierLUT_Cnt_T_s16[23]	4	
VernierLUT_Cnt_T_s16[24]	3	
VernierLUT_Cnt_T_s16[25]	2	
VernierLUT_Cnt_T_s16[26]	1	
VernierLUT_Cnt_T_s16[27]	0	
VernierLUT_Cnt_T_s16[28]	4	
VernierLUT_Cnt_T_s16[29]	3	
VernierLUT_Cnt_T_s16[30]	2	
VernierLUT_Cnt_T_s16[31]	1	
VernierLUT_Cnt_T_s16[32]	0	
VernierLUT_Cnt_T_s16[33]	4	
VernierLUT_Cnt_T_s16[34]	0	
VernierLUT_Cnt_T_s16[35]	8	
VernierLUT_Cnt_T_s16[36]	6	
VernierLUT_Cnt_T_s16[37]	4	
VernierLUT_Cnt_T_s16[38]	2	
VernierLUT_Cnt_T_s16[39]	0	
VernierLUT_Cnt_T_s16[40]	9	
VernierLUT_Cnt_T_s16[41]	7	
VernierLUT_Cnt_T_s16[42]	5	
VernierLUT_Cnt_T_s16[43]	3	
VernierLUT_Cnt_T_s16[44]		
VernierLUT_Cnt_T_s16[45]	10 8	
VernierLUT_Cnt_T_s16[46]	8	
VernierLUT_Cnt_T_s16[47] VernierLUT_Cnt_T_s16[48]	4	
	2	
VernierLUT_Cnt_T_s16[49] VernierLUT_Cnt_T_s16[50]	10	
VernierLUT_Cnt_T_s16[50] VernierLUT_Cnt_T_s16[51]	1	
VernierLUT_Cnt_T_s16[51]	14	
VernierLUT_Cnt_T_s16[52]	11	
VernierLUT_Cnt_T_s16[54]	8	
VernierLUT_Cnt_T_s16[55]	5	
VernierLUT_Cnt_T_s16[56]	2	
VernierLUT_Cnt_T_s16[57]	15	
VernierLUT_Cnt_T_s16[58]	12	
VernierLUT_Cnt_T_s16[59]	9	
VernierLUT_Cnt_T_s16[69]	6	
VernierLUT_Cnt_T_s16[61]	3	
VernierLUT_Cnt_T_s16[62]	16	
VernierLUT_Cnt_T_s16[63]	13	
VernierLUT_Cnt_T_s16[64]	10	

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Name	Input Value		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	~
tgt_VernierLevelNo_Cnt_T_u08	2	2	~

Test Step 3.4 (Repeat Count = 1)	Innut Value
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	245.2
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
/ernierLUT_Cnt_T_s16[1]	-360
/ernierLUT_Cnt_T_s16[2]	-324
/ernierLUT_Cnt_T_s16[3]	-288
/ernierLUT_Cnt_T_s16[4]	-252
/ernierLUT_Cnt_T_s16[5]	-216
/ernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
/ernierLUT_Cnt_T_s16[8]	-108
/ernierLUT_Cnt_T_s16[9]	-72
/ernierLUT_Cnt_T_s16[10]	-36
/ernierLUT_Cnt_T_s16[11]	0
/ernierLUT_Cnt_T_s16[12]	36
/ernierLUT_Cnt_T_s16[13]	72
/ernierLUT_Cnt_T_s16[14]	108
/ernierLUT_Cnt_T_s16[15]	144
/ernierLUT_Cnt_T_s16[16]	180
/ernierLUT_Cnt_T_s16[17]	216
/ernierLUT_Cnt_T_s16[18]	252
/ernierLUT_Cnt_T_s16[19]	288
/ernierLUT_Cnt_T_s16[20]	324
/ernierLUT_Cnt_T_s16[21]	360
/ernierLUT_Cnt_T_s16[22]	9
/ernierLUT_Cnt_T_s16[23]	0
/ernierLUT_Cnt_T_s16[24]	1
/ernierLUT_Cnt_T_s16[25]	2
/ernierLUT_Cnt_T_s16[26]	3
/ernierLUT_Cnt_T_s16[27]	4
/ernierLUT_Cnt_T_s16[28]	5
/ernierLUT_Cnt_T_s16[29]	6
/ernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2

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Name	Input Value		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	7	7	-
tgt_SpurRevPtr_Cnt_T_u08	7	7	•
tgt_VernierLevelNo_Cnt_T_u08	15	15	-

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DiagnosticThreshold

Project	DigColPs
Module	DigColPs
Test Object	DiagnosticThreshold

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
indule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro "Redundant_Format_1_m" to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac_""

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



### Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 19.00 Cycles Longest Execution Path TS1.2 8.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path => if (FaultPresent\_Cnt\_T\_lgc == TRUE)=>True if (DiagFailed\_m(\*AccumulatorPtr\_Cnt\_T\_u16, DiagSettings\_Cnt\_T\_str) == TRUE)=>True" TS1.2 "Shortest Execution Path => if (FaultPresent\_Cnt\_T\_lgc == TRUE)=>False"

Test Step 1.1 (Repeat Count = 1)			V
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	~

Test Step 1.2 (Repeat Count = 1)			✓.
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	<b>✓</b>



### **Test Case 2: Boundary Test**

### Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

7.00 Cycles
19.00 Cycles
8.00 Cycles
8.00 Cycles
20.00 Cycles
19.00 Cycles
19.00 Cycles
19.00 Cycles
20.00 Cycles
20.00 Cycles
8.00 Cycles
8.00 Cycles
19.00 Cycles
8.00 Cycles
19.00 Cycles
19.00 Cycles
19.00 Cycles
20.00 Cycles
19.00 Cycles
19.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5

TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11 TS2.12 TS2.13 TS2.14

TS2.15 TS2.16

### Description

### VECTOR DESCRIPTION:

TS2.1 All Min

TS2.2 All Max
TS2.3 FaultPresent\_Cnt\_T\_lgc=Min
TS2.4 FaultPresent\_Cnt\_T\_lgc=Max
TS2.5 FacumulatorPtr\_Cnt\_T\_u16=Max
TS2.5 AccumulatorPtr\_Cnt\_T\_u16=Max
TS2.7 AccumulatorPtr\_Cnt\_T\_u16=Pos
TS2.8 DiagSettings\_Cnt\_T\_str.Threshold=Min
TS2.9 DiagSettings\_Cnt\_T\_str.Threshold=Max
TS2.10 DiagSettings\_Cnt\_T\_str.Threshold=Pos
TS2.11 DiagSettings\_Cnt\_T\_str.Pstep=Min
TS2.12 DiagSettings\_Cnt\_T\_str.Pstep=Max
TS2.13 DiagSettings\_Cnt\_T\_str.Pstep=Pos
TS2.14 DiagSettings\_Cnt\_T\_str.Nstep=Min
TS2.15 DiagSettings\_Cnt\_T\_str.Nstep=Max
TS2.16 DiagSettings\_Cnt\_T\_str.Nstep=Pos

T (0) 04/D (0)			
Test Step 2.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	0		
DiagSettings_Cnt_T_str.PStep	0		
DiagSettings_Cnt_T_str.NStep	0		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tot AccumulatorPtr Cnt T u16	0	0	•

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt AccumulatorPtr Cnt T u16	65535	65535	✓

Name	Input Value		
AccumulatorPtr Cnt T u16	tgt AccumulatorPtr Cnt T u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Resul
DiagnosticThreshold()	0	0	•
tgt AccumulatorPtr Cnt T u16	0	0	



Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	200		
DiagSettings_Cnt_T_str.PStep	82		
DiagSettings_Cnt_T_str.NStep	1020		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	24		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt AccumulatorPtr Cnt T u16	106	106	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	300		
DiagSettings_Cnt_T_str.PStep	116		
DiagSettings_Cnt_T_str.NStep	1188		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	~

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	400		
DiagSettings_Cnt_T_str.PStep	150		
DiagSettings_Cnt_T_str.NStep	1356		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	400	400	~

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	500		
DiagSettings_Cnt_T_str.PStep	184		
DiagSettings_Cnt_T_str.NStep	1524		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	54		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tat AccumulatorPtr Cnt T u16	0	0	<b>✓</b>

Test Step 2.8 (Repeat Count = 1)			~
Name	Input Value	Input Value	
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16	i	
DiagSettings_Cnt_T_str.Threshold	0		
DiagSettings_Cnt_T_str.PStep	218		
DiagSettings_Cnt_T_str.NStep	1692		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	95		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	<b>✓</b>
tgt_AccumulatorPtr_Cnt_T_u16	0	0	<b>✓</b>



DiagnosticThreshold

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	252		
DiagSettings_Cnt_T_str.NStep	1860		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	136		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	<b>✓</b>

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1023		
DiagSettings_Cnt_T_str.PStep	286		
DiagSettings_Cnt_T_str.NStep	2028		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	177		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	463	463	<b>✓</b>

Test Step 2.11 (Repeat Count = 1)			· ·
Name	Input Value	Input Value	
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1		
DiagSettings_Cnt_T_str.PStep	0		
DiagSettings_Cnt_T_str.NStep	2196		
FaultPresent_Cnt_T_lgc	0	0	
tgt_AccumulatorPtr_Cnt_T_u16	218		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	•
tgt_AccumulatorPtr_Cnt_T_u16	0	0	•

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	488		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	2364		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	259		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	•
tgt_AccumulatorPtr_Cnt_T_u16	488	488	•

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	928		
DiagSettings_Cnt_T_str.PStep	1045		
DiagSettings_Cnt_T_str.NStep	2532		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	300		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	~





Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1368		
DiagSettings_Cnt_T_str.PStep	645		
DiagSettings_Cnt_T_str.NStep	0		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	341		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	986	986	~

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1808		
DiagSettings_Cnt_T_str.PStep	152		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	382		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	•

Test Step 2.16 (Repeat Count = 1)		✓	
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	2248		
DiagSettings_Cnt_T_str.PStep	8524		
DiagSettings_Cnt_T_str.NStep	2046		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	423		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	2248	2248	✓

### Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 19.00 Cycles TS3.2 8.00 Cycles TS3.3 20.00 Cycles TS3.4 9.00 Cycles

Description

VECTOR DESCRIPTION:

TS3.1 "if (FaultPresent\_Cnt\_T\_lgc == TRUE)=>True if (DiagFailed\_m(\*AccumulatorPtr\_Cnt\_T\_u16, DiagSettings\_Cnt\_T\_str) == TRUE)=>True" TS3.2 if (FaultPresent\_Cnt\_T\_lgc == TRUE)=>False TS3.3 "if (FaultPresent\_Cnt\_T\_lgc == TRUE)=>True if (DiagFailed\_m(\*AccumulatorPtr\_Cnt\_T\_u16, DiagSettings\_Cnt\_T\_str) == TRUE)=>False" TS3.4 ((\*AccumulatorPtr\_Cnt\_T\_u16)>( DiagSettings\_Cnt\_T\_str.NStep))=>TRUE

Test Step 3.1 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	•
tgt AccumulatorPtr Cnt T u16	65535	65535	<b>✓</b>



DiagnosticThreshold

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	~

Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	200		
DiagSettings_Cnt_T_str.PStep	82		
DiagSettings_Cnt_T_str.NStep	1020		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	24		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	<b>✓</b>
tgt_AccumulatorPtr_Cnt_T_u16	106	106	✓

Test Step 3.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1500		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	648	648	~

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DigColPs\_Init1

Project	DigColPs
Module	DigColPs
Test Object	DigColPs_Init1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>✓</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes		
Name	Value	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 1098.00 Cycles Shortest Execution Path TS1.2 821.00 Cycles Longest Execution Path

#### Description

VECTOR DESCRIPTION:

TS1.1 "Shortest Execution Path => (Redundant\_Format\_1\_m("(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32) != Rte\_Pim\_DigColPsEOL()->R\_ColTrim\_Cnt\_u32)=FALSE

 $(Redundant\_Format\_1\_m(*(uint32*)\&Rte\_Pim\_DigColPsEOL().>ColTrim\_Deg\_f32) != Rte\_Pim\_DigColPsEOL().>R\_ColTrim\_Cnt\_u32) = FALSE\_Pim\_DigColPsEOL().$ 

|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32) != Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)=FALSE ||((uint16)Redundant\_Format\_1\_m(Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16) != Rte\_Pim\_DigColPsEOL()->R\_TrimCom\_Cnt\_u16)=TRUE "

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	2		
k_SpurAngSenseLPFFc_Hz_f32	9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	124		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2500		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0248195529	0.024819543 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>

Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value Expe	ected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.0124877691 0.012	487743 ± 0.00048828125	•

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Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180	180 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951	3167485951	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200	200 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231	3166175231	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100	100	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435	65435	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•



#### **Test Case 2: Boundary Test**

#### Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

#### CPU Cycles:

TS2.1 1068.00 Cycles 1070.00 Cycles 1071.00 Cycles 1071.00 Cycles 824.00 Cycles 1072.00 Cycles TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 1072.00 Cycles 1072.00 Cycles 825.00 Cycles 1071.00 Cycles 1073.00 Cycles 1074.00 Cycles 1072.00 Cycles TS2.7 TS2.9 TS2.10 TS2.11 TS2.12 825.00 Cycles 1072.00 Cycles 1070.00 Cycles 1071.00 Cycles 824.00 Cycles TS2.13 TS2.14 TS2.14 TS2.15 TS2.16 TS2.17 TS2.18 TS2.19 TS2.20 TS2.21 1072.00 Cycles 1072.00 Cycles 1072.00 Cycles 825.00 Cycles 1072.00 Cycles 1072.00 Cycles 824.00 Cycles 1071.00 Cycles 824.00 Cycles TS2.22 TS2.23 TS2.24 TS2.25 TS2.26 824.00 Cycles 1071.00 Cycles 1071.00 Cycles 824.00 Cycles 1071.00 Cycles 1073.00 Cycles 1073.00 Cycles 1073.00 Cycles TS2.27 TS2.28 TS2.29 TS2.30 TS2.31 TS2.32 826.00 Cvcles TS2.33 TS2.34 TS2.35 TS2.36 1073.00 Cycles 826.00 Cycles 1073.00 Cycles

#### Description

### VECTOR DESCRIPTION:

TS2 1All Min TS2.2All Max

TS2.3k\_ColAngSenseLPFFc\_Hz\_f32=Min TS2.4k\_ColAngSenseLPFFc\_Hz\_f32=Max TS2.5k\_ColAngSenseLPFFc\_Hz\_f32=pos

TS2.5k\_ColAngSenseLPFFc\_Hz\_f32=pos
TS2.6DigColPs\_ColAngleLPFKSV\_Cnt\_M\_str.K=Min
TS2.7DigColPs\_ColAngleLPFKSV\_Cnt\_M\_str.K=Max
TS2.8DigColPs\_ColAngleLPFKSV\_Cnt\_M\_str.K=Pos
TS2.9k\_SpurAngSenseLPFFc\_Hz\_f32=Min
TS2.10k\_SpurAngSenseLPFFc\_Hz\_f32=Max
TS2.11k\_SpurAngSenseLPFFc\_Hz\_f32=Pos
TS2.12DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.K=Min
TS2.13DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.K=Max
TS2.14DigColPs\_SpurAngleLPFKSV\_Cnt\_M\_str.K=Pos
TS2.15Rte\_Pim\_DigColPsEOL.COlTrim\_Deg\_f32=Min
TS2.16Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Min

TS2.16Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Max TS2.17Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Pos TS2.18Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Neg

TS2.19Rte\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32=Zero TS2.20Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32=Min

TS2.20Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_132=Max TS2.22Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_132=Pos

TS2.23Rte Pim\_DigColPsEOL.SpurTrim\_Deg\_f32=Neg TS2.24Rte\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32=Zero TS2.25Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=Min

TS2.26Rte Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=Max TS2.27Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16=Pos TS2.28Rte\_Pim\_DigColPsEOL.R\_ColTrim\_Cnt\_u32=Min

TS2.29Rte Pim\_DigColPsEOL.R\_ColTrim\_Cnt\_u32=Max TS2.30Rte\_Pim\_DigColPsEOL.R\_ColTrim\_Cnt\_u32=Pos TS2.31Rte\_Pim\_DigColPsEOL.R\_SpurTrim\_Cnt\_u32=Min

TS2.32Rte Pim\_DigColPsEOL.R\_SpurTrim\_Cnt\_u32=Min TS2.33Rte Pim\_DigColPsEOL.R\_SpurTrim\_Cnt\_u32=Nos TS2.33Rte Pim\_DigColPsEOL.R\_TrimCom\_Cnt\_u16=Min TS2.35Rte Pim\_DigColPsEOL.R\_TrimCom\_Cnt\_u16=Max TS2.36Rte\_Pim\_DigColPsEOL.R\_TrimCom\_Cnt\_u16=Pos

Test Step 2.1 (Repeat Count = 1)		✓
Name	Input Value	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
k_ColAngSenseLPFFc_Hz_f32	1	
k_SpurAngSenseLPFFc_Hz_f32	1	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	0	

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Name	Input Value	Input Value		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~	
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~	
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>	
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>~</b>	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•	
tat Dim DiaColDeEOL D. TrimCom Cnt u16	61047	61047	-	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	•

Took Stop 2.2 (Bonock Count = 4)			
Test Step 2.2 (Repeat Count = 1)			·
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	65535		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	•
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	-
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	

Test Step 2.3 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.012	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
k_ColAngSenseLPFFc_Hz_f32	1	
k_SpurAngSenseLPFFc_Hz_f32	8.5	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	124	
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525	

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Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.101307333	0.101307321 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>

Test Sten 2.4 (Beneat Count = 4)			
Test Step 2.4 (Repeat Count = 1)	Immut Walica		·
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.015		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	16		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	5		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3210739711		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	36.2		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3186570034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	224		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65311		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.182138205	0.18213822 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	5	5 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3210739711	3210739711	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	36.2000008	36.2 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3186570034	3186570034	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	224	224	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65311	65311	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	-



Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.018		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	30.5		
k_SpurAngSenseLPFFc_Hz_f32	24		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.9		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	324		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.318374097	0.318374099 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.260360897	0.260360885 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.021		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	6		
k_SpurAngSenseLPFFc_Hz_f32	32.9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	13		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	424		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0726258755	0.07262589 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.338624597	0.338624547 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>~</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.7 (Repeat Count = 1)			v)
Name	Input Value		_
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	1		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.024		
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs		
	12		
k_ColAngSenseLPFFc_Hz_f32			
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	17.4		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	108		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	524		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.139977276	0.139977259 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.027		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	18.4		
k_SpurAngSenseLPFFc_Hz_f32	48		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	21		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3193438207		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.1		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3170625125		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.206436098	0.206436105 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.452934027	0.45293398 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	21	21 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3193438207	3193438207	<b>~</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.100006	132.1 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3170625125	3170625125	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	624	624	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64911	64911	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~





Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.01		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.03		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	24		
k_SpurAngSenseLPFFc_Hz_f32	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	25		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	5995		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	156		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	9104		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	724		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.260360897	0.260360885 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.033		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	30		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	29.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6994		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	824		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.314077795	0.314077834 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>~</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



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Test Step 2.11 (Repeat Count = 1)			~
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.03		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.036		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	36		
k_SpurAngSenseLPFFc_Hz_f32	30		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	33		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7993		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	204.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	11320		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	924		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1325		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.363893569	0.363893542 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.314077795	0.314077834 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.04		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	42.9		
k_SpurAngSenseLPFFc_Hz_f32	5		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	37		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	228		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1024		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.416725516	0.416725463 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.05		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	48		
k_SpurAngSenseLPFFc_Hz_f32	10.5		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	41.1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3185285529		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	252		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3162767359		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1124		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64411		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.452934027	0.45293398 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.123612463	0.123612462 ± 0.00048828125	<b>~</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>~</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	41.0999985	41.1 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3185285529	3185285529	<b>~</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	252	252 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3162767359	3162767359	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1124	1124	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64411	64411	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.06		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	54		
k_SpurAngSenseLPFFc_Hz_f32	15		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	45		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	10990		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	276		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	14644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1224		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.492665172	0.492665137 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.171795845	0.171795819 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



T+ 04 0.45 (D+ 0+ -4)			
Test Step 2.15 (Repeat Count = 1)			~
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.07		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.014		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	20.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	11989		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	300.5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	15752		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1324		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.230012119	0.230012123 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	-
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1.1		
k_SpurAngSenseLPFFc_Hz_f32	25		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	12988		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16860		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1424		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0137279034	0.013727909 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•





Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180.25		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.07		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.014		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	20.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-74.29		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	11989		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	300.5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	15752		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1324		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.230012119	0.230012123 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.19 (Repeat Count = 1)			J.
Name	Input Value		Ť
	0.08		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1.1		
k_SpurAngSenseLPFFc_Hz_f32	25		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	12988		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16860		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1424		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0137279034	0.013727909 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Took Ston 2 20 (Bonnet Count = 4)			-0
Test Step 2.20 (Repeat Count = 1)			
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.044		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	5		
k_SpurAngSenseLPFFc_Hz_f32	35.3		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14986		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	19076		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>~</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.358273387	0.358273374 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>~</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt Pim DigColPsEOL.R TrimCom Cnt u16	61047	61047	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~





Test Step 2.21 (Repeat Count = 1)			4
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.11		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.054		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	7.7		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	44		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	15985		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	20184		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1724		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0922271013	0.092227111 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.22 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.064		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	9		
k_SpurAngSenseLPFFc_Hz_f32	45		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	64.6		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	16984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180.25		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	21292		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1824		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.431916416	0.431916394 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.23 (Repeat Count = 1)			J.
	Innut Value		·
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.044		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	5		
k_SpurAngSenseLPFFc_Hz_f32	35.3		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14986		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-74.29		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	19076		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.358273387	0.358273374 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.24 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.11		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.054		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	7.7		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	44		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	15985		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	20184		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1724		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0922271013	0.092227111 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•



Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.13		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.074		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	11		
k_SpurAngSenseLPFFc_Hz_f32	50.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3176660991		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3210739711		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.129101694	0.129101705 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.471848249	0.471848248 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84	84 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3176660991	3176660991	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	5	5 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3210739711	3210739711	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	0	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.26 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.14		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.084		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	13		
k_SpurAngSenseLPFFc_Hz_f32	55		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	104		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	18982		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	15.1		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	23508		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	65535		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.15071702	0.150717003 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.499000609	0.499000604 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.27 (Repeat Count = 1)			s)
Name	Input Value		Ť
	0.15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.094		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1 1 1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	15.1		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	124		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171418111		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	25		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3191341055		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	63291		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.172835946	0.172835917 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	124	124 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171418111	3171418111	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	25	25 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3191341055	3191341055	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244	2244	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	63291	63291	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.28 (Repeat Count = 1)			•
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.16		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.104		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	17		
k_SpurAngSenseLPFFc_Hz_f32	2.2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	144.4		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	35		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	25724		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.19235146	0.192351468 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0272673368	0.027267362 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	-
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	-
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Toot Ston 2 20 (Bonnet Count = 4)			
Test Step 2.29 (Repeat Count = 1)			<u> </u>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.17		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.114		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	19		
k_SpurAngSenseLPFFc_Hz_f32	4		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	45.8		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3184053452		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	500		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65035		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.21239692	0.212396936 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0490230918	0.049023077 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	45.7999992	45.8 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3184053452	3184053452	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	500	500	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65035	65035	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.30 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.18		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.124		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	21		
k_SpurAngSenseLPFFc_Hz_f32	6		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	184		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2451658		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	27940		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	550		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.231944919	0.231944884 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0726258755	0.07262589 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>~</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	



Test Step 2.31 (Repeat Count = 1)			J.
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.19		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.134		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	23.3		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	204		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	65		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	600		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.253826022	0.25382598 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.32 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.144		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	25		
k_SpurAngSenseLPFFc_Hz_f32	10		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	224		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3164602367		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	650		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64885		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.118088603	0.118088622 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	224	224 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3164602367	3164602367	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	650	650	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64885	64885	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~





Test Step 2.33 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.21		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.154		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	27		
k_SpurAngSenseLPFFc_Hz_f32	12.4		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	244.7		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	85		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2145623		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	700		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	3125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.287725627	0.28772557 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.144289374	0.144289358 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.34 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.22		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.164		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	29.8		
k_SpurAngSenseLPFFc_Hz_f32	14		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	264		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	95		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	750		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312351763	0.312351755 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	<b>✓</b>
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.161322653	0.161322631 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~





Test Step 2.35 (Repeat Count = 1)			4
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Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.23		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.174		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	31		
k_SpurAngSenseLPFFc_Hz_f32	16		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	284		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3161587711		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3173908479		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.322643459	0.322643454 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.182138205	0.18213822 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	284	284 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3161587711	3161587711	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105	105 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3173908479	3173908479	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	0	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.184		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	33		
k_SpurAngSenseLPFFc_Hz_f32	18		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	304		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	9444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	115		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	25846		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	850		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	254		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.339455187	0.339455134 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.202437222	0.202437176 ± 0.00048828125	<b>✓</b>
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•



### **Test Case 3: Path Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

821.00 Cycles 823.00 Cycles 1084.00 Cycles 1071.00 Cycles TS3.1 TS3.2 TS3.3 TS3.4

Description VECTOR DESCRIPTION:

TS3.1 "((Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()-

TS3.1 "((Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_ColTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16)!= Rte\_Pim\_DigColPsEOL()->R\_TrimCom\_Cnt\_u16))F" TS3.2 "((Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16)!= Rte\_Pim\_DigColPsEOL()->R\_TrimCom\_Cnt\_u16))F" TS3.3 "((Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16)!= Rte\_Pim\_DigColPsEOL()->R\_TrimCom\_Cnt\_u16))F" TS3.4 "((Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->ColTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_SpurTrim\_Cnt\_u32)|| (Redundant\_Format\_1\_m(\*(uint32\*)&Rte\_Pim\_DigColPsEOL()->SpurTrim\_Deg\_f32)!= Rte\_Pim\_DigColPsEOL()->R\_TrimCom\_Cnt\_u16)|

((uint16)Redundant\_Format\_1\_m(Rte\_Pim\_DigColPsEOL()->TrimComp\_Cnt\_u16) |= Rte\_Pim\_DigColPsEOL()->R\_TrimCom\_Cnt\_u16) |=

Test Step 3.1 (Repeat Count = 1)			
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	-
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	-
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	-
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	-
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180	180 ± 0.00048828125	<b>-</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951	3167485951	-
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200	200 ± 0.00048828125	-
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231	3166175231	-
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100	100	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435	65435	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>



Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	2		
k_SpurAngSenseLPFFc_Hz_f32	9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0248195529	0.024819543 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220	220 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511	3164864511	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	3		
k_SpurAngSenseLPFFc_Hz_f32	10		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	120		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171942399		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	150		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4265000		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	550		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	655		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0369973183	0.036997347 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.118088603	0.118088622 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	<b>✓</b>
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•

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Test Step 3.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	4		
k_SpurAngSenseLPFFc_Hz_f32	11		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	260		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6548212		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	340		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16598742		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1321		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	43625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0490230918	0.049023077 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.129101694	0.129101705 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>

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ConstrainOneRev

Project	DigColPs
Module	DigColPs
Test Object	ConstrainOneRev

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>✓</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes		
Name	Value	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>	



### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 41.00 Cycles Longest Execution Path TS1.2 9.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path =>

(Input\_Deg\_T\_f32 < D\_ZERO\_ULS\_F32)=>TRUE"
TS1.2 "Shortest Execution Path =>

(Input\_Deg\_T\_f32 > D\_ONEREV\_DEGREESPREV\_F32)=>FALSE (Input\_Deg\_T\_f32 < D\_ZERO\_ULS\_F32)=>FALSE"

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Input_Deg_T_f32	-1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Input_Deg_T_f32	0		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	~

## Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 41.00 Cycles TS2.2 42.00 Cycles TS2.3 9.00 Cycles TS2.4 16.00 Cycles TS2.5 21.00 Cycles

Description VECTOR DESCRIPTION:

TS2.1 Input\_Deg\_T\_f32=Min TS2.2 Input\_Deg\_T\_f32=Max TS2.3 Input\_Deg\_T\_f32=Zero TS2.4 Input\_Deg\_T\_f32=Pos TS2.5 Input\_Deg\_T\_f32=Neg

Test Step 2.1 (Repeat Count = 1)			✓.
Name	Input Value		
Input_Deg_T_f32	-1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
Input_Deg_T_f32	1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	360	360 ± 0.00048828125	~

Test Step 2.3 (Repeat Count = 1)			V
Name	Input Value		
Input_Deg_T_f32	0		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	~





Test Step 2.4 (Repeat Count = 1) ✓			~
Name	Input Value		
Input_Deg_T_f32	800.5		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	80.5	80.5 ± 0.00048828125	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
Input_Deg_T_f32	-750.2		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	329.799988	329.8 ± 0.00048828125	~

### Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 3.00 Cycles TS3.2 11.00 Cycles

Description

VECTOR DESCRIPTION:

 $\label{eq:total_$ 

Test Step 3.1 (Repeat Count = 1)		✓	
Name	Input Value		
Input_Deg_T_f32	500		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	140	140 ± 0.00048828125	~

Test Step 3.2 (Repeat Count = 1)			
Name	Input Value		
Input_Deg_T_f32	-500		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	220	220 ± 0.00048828125	~

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DigColPs\_SCom\_NxtClrTrim

Project DigColPs
Module DigColPs

Test Object DigColPs\_SCom\_NxtClrTrim

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigCoIPs\utp\contract -I\$(PROJECTROOT)\DigCoIPs\utp\contractS_DigCoIPs -I\$(PROJECTROOT)\DigCoIPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path\)include

lame	Text
Name Nodule 'DigColPs'	Text

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	

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DigColPs\_SCom\_NxtClrTrim



Attributes		
Name	Value	
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>	
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>	



# DigColPs\_SCom\_NxtClrTrim

## Test Case 1: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 18.00 Cycles

Description Vector Description:

TS1.1 Clear All The Trim Variables

Test Step 1.1 (Repeat Count = 1)			
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~

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 Project
 DigColPs

 Module
 DigColPs

 Test Object
 DigColPs\_SCom\_CustSetTrim

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs\utp\contr

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested.  Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code.  Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.  Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>



DigColPs\_SCom\_CustSetTrim

### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 1027.00 Cycles TS1.2 1499.00 Cycles

#### Description Vector Description:

TS1.1 "Shortest Execution Path =>
((I2CColSensorFault\_Cnt\_T\_lgc = False) And (I2CSpurSensorFault\_Cnt\_T\_lgc = False)\_
And (DigColPs\_I2CHwCustData\_Uls\_M\_u16 <> D\_I2CHWCUSTDATAUNKWN\_CNT\_U16)\_
And (DigColPs\_TrimCompStatic\_Cnt\_M\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)\_
And (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16))=>FALSE;
(Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16))=> TRUE"
TS1.2 "Longest ExecutionPath =>

TS1.2 "Longest ExecutionPath =>

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	•
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360	360 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	~

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Test Step 1.2 (Repeat Count = 1)	Innert Males		
Name	Input Value		
DigColPsInt_GetCustData()	1		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	44		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12		
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	•
DigColPs_SCom_CustSetTrim()	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	· · · · · ·
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	•
tgt Pim DigColPsEOL.R TrimCom Cnt u16	65534	65534	•



### **Test Case 2: Boundary Test**

### Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

### CPU Cycles:

TS2.1 1356.00 Cycles
TS2.2 1026.00 Cycles
TS2.3 1028.00 Cycles
TS2.4 1027.00 Cycles
TS2.5 1033.00 Cycles
TS2.6 1037.00 Cycles
TS2.7 1048.00 Cycles
TS2.8 1027.00 Cycles
TS2.9 1356.00 Cycles
TS2.10 1027.00 Cycles
TS2.11 1356.00 Cycles
TS2.12 1027.00 Cycles
TS2.13 1492.00 Cycles
TS2.14 1027.00 Cycles
TS2.15 1432.00 Cycles
TS2.15 1627.00 Cycles
TS2.16 1027.00 Cycles
TS2.17 1356.00 Cycles
TS2.18 1027.00 Cycles
TS2.19 1431.00 Cycles
TS2.19 1431.00 Cycles
TS2.19 1207.00 Cycles
TS2.19 1481.00 Cycles
TS2.19 1481.00 Cycles
TS2.20 1027.00 Cycles
TS2.21 1356.00 Cycles
TS2.21 1356.00 Cycles
TS2.21 1356.00 Cycles

### Description

### Vector Description:

TS2.1All Min

TS2.2All Max
TS2.3DigColPs | I2CHwSpurAngle | Deg | M | f32=Min
TS2.4DigColPs | I2CHwSpurAngle | Deg | M | f32=Min
TS2.4DigColPs | I2CHwSpurAngle | Deg | M | f32=Max
TS2.5DigColPs | I2CHwSpurAngle | Deg | M | f32=Pos
TS2.6DigColPs | I2CHwColAngle | Deg | M | f32=Pos
TS2.6DigColPs | I2CHwColAngle | Deg | M | f32=Pos
TS2.9DigColPs | I2CSpurSensorFault | Cnt | M | Igc=Min
TS2.10DigColPs | I2CSpurSensorFault | Cnt | M | Igc=Min
TS2.10DigColPs | I2CColSensorFault | Cnt | M | Igc=Min
TS2.11DigColPs | I2CColSensorFault | Cnt | M | Igc=Min
TS2.12DigColPs | I2CColSensorFault | Cnt | M | Igc=Min
TS2.12DigColPs | I2CColSensorFault | Cnt | M | Igc=Min
TS2.12DigColPs | I2CHwColAngleForTrim | Deg | M | f32=Min
TS2.14DigColPs | I2CHwColAngleForTrim | Deg | M | f32=Max
TS2.15DigColPs | I2CHwColAngleForTrim | Deg | M | f32=Pos
TS2.16DigColPs | TrimCompStatic | Cnt | M | u16=Max
TS2.18DigColPs | TrimCompStatic | Cnt | M | u16=Pos
TS2.19DigColPsInt | GetCustData=Min
TS2.20DigColPsInt | GetCustData=Max
TS2.21DigColPsInt | GetCustData=Pos

Test Step 2.1 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	<b>✓</b>



Name	Input Value		
DigColPsInt GetCustData()	511		
DigColPs I2CColSensorFault Cnt M Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	•
DigColPs_SCom_CustSetTrim()	34	34	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360	360 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	•

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	455		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	254.2		
DigColPs_I2CHwColAngle_Deg_M_f32	50		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	965		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1	1 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1	1	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4	4 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456	2456	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142	2142	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525	525	~

Test Step 2.4 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	245
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	11.2
DigColPs_I2CHwColAngle_Deg_M_f32	1.2

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Name	Input Value		
DigColPs I2CHwSpurAngle Deg M f32	360		
DigColPs I2CHwTrimTransCnts Uls M u08	1		
DigColPs I2CSpurSensorFault Cnt M Igc	1		
DigColPs TrimCompStatic Cnt M u16	785		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12.3		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2144		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_SCom_CustSetTrim()	34	34	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12.3000002	12.3 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000	1000	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8	8 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564	3564	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2144	2144	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625	625	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	455		
DigColPs_I2CHwColAngle_Deg_M_f32	44		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.5		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	142		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24	24 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999	1999	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.3999996	12.4 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672	4672	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146	2146	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725	725	

Test Step 2.6 (Repeat Count = 1)		~
Name	Input Value	
DigColPsInt_GetCustData()	211	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	965.2	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwSpurAngle_Deg_M_f32	1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_TrimCompStatic_Cnt_M_u16	1	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
tgt Pim DigColPsEOL.ColTrim Deg f32	36.6	

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Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	16		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2148		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	36.5999985	36.6 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998	2998	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	16	16 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780	5780	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2148	2148	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	825	825	~

Test Step 2.7 (Repeat Count = 1)	1 (3)		
Name	Input Value		
DigColPsInt_GetCustData()	356		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	751.2		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.8		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	34	34	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48	48 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997	3997	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20	20 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888	6888	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925	925	<b>✓</b>



Name	Input Value		
DigColPsInt GetCustData()	478		
DigColPs I2CColSensorFault Cnt M Igc	1		
DigColPs I2CHwColAngleForTrim Deg M f32	25.3		
DigColPs_I2CHwColAngle_Deg_M_f32	2.5		
DigColPs_I2CHwSpurAngle_Deg_M_f32	44		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	854		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4996		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	7996		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2152		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_SCom_CustSetTrim()	34	34	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	60	60 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4996	4996	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	24	24 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	7996	7996	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2152	2152	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1025	1025	<b>✓</b>

Test Step 2.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
	· ·		
DigColPsInt_GetCustData()	356		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	751.2		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.8		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	34	34	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48	48 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997	3997	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20	20 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888	6888	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925	925	~

Test Step 2.10 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	14
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	9.58
DigColPs_I2CHwColAngle_Deg_M_f32	196.2

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Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	84		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4000		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6994		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2156		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84	84 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6994	6994	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32	32 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212	10212	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2156	2156	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1225	1225	<b>✓</b>

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	2		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5		
DigColPs_I2CHwColAngle_Deg_M_f32	14.23		
DigColPs_I2CHwSpurAngle_Deg_M_f32	255		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2000		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.52		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3021458		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.54		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10254		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3200		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	344		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	<b>✓</b>
DigColPs_SCom_CustSetTrim()	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	10	10 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3202351103	3202351103	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigCoIPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055	~
tgt_Pim_DigCoIPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	

Test Step 2.12 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
DigColPsInt_GetCustData()	147	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	87.25	
DigColPs_I2CHwColAngle_Deg_M_f32	36	
DigColPs_I2CHwSpurAngle_Deg_M_f32	124.7	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_TrimCompStatic_Cnt_M_u16	2548	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	108	

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 ${\it DigColPs\_SCom\_CustSetTrim}$ 

Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	40		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2160		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	108	108 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992	8992	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	40	40 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428	12428	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2160	2160	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425	1425	<b>✓</b>

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	365		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	65.25		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	1258		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	32.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	98574		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	64.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	98574		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	965		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	9857		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180	180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3167485951	3167485951	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	~



Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	147		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800		
DigColPs_I2CHwColAngle_Deg_M_f32	24.8		
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.23		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	78.54		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	154785		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	352414		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	785		
tgt_Pim_DigCoIPsEOL.R_TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	<b>✓</b>
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	78.5400009	78.54 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	154785	154785	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84	84 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	352414	352414	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	785	785	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425	1425	~

Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	985.5		
DigColPs_I2CHwColAngle_Deg_M_f32	98.25		
DigColPs_I2CHwSpurAngle_Deg_M_f32	325.1		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	3658		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.254		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_SCom_CustSetTrim()	34	34	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.25400019	6.254 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541	7859541	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547	789547	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548	2548	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	911	911	~

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	325
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	95
DigColPs_I2CHwColAngle_Deg_M_f32	124.7

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Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	62.4		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14258		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	108		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	951247		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4417		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	25		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>~</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	190	190 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3166830591	3166830591	<b>~</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	29	29 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3189243903	3189243903	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	<b>✓</b>

Test Step 2.17 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetCustData()	145		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	7.254		
DigColPs_I2CHwColAngle_Deg_M_f32	65.25		
DigColPs_I2CHwSpurAngle_Deg_M_f32	15.25		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4488		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1.036		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	35789		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32.2		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	35789		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4000		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	8		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1.03600001	1.036 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	35789	35789	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	32.2000008	32.2 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	35789	35789	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4000	4000	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	8	8	

Test Step 2.18 (Repeat Count = 1)		<b>→</b>
Name	Input Value	
DigColPsInt_GetCustData()	2	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5	
DigColPs_I2CHwColAngle_Deg_M_f32	14.23	
DigColPs_I2CHwSpurAngle_Deg_M_f32	255	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_TrimCompStatic_Cnt_M_u16	2000	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.52	

DigColPs\_SCom\_CustSetTrim

 $tgt\_Pim\_DigColPsEOL.R\_TrimCom\_Cnt\_u16$ 

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Input Value tgt\_Pim\_DigColPsEOL.R\_ColTrim\_Cnt\_u32 3021458 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 78.54 tgt\_Pim\_DigColPsEOL.R\_SpurTrim\_Cnt\_u32 10254 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 3200 tgt\_Pim\_DigColPsEOL.R\_TrimCom\_Cnt\_u16 344 tgt\_Pim\_DigColPsEOL tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL **Actual Value Expected Value** Result Name DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 6 6 DigColPs\_SCom\_CustSetTrim() 0 0 Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) 504 504  $Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)$ 0 0 Rte\_Call\_Sa\_DigColPs\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) 0 0 10 ± 0.00048828125 tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 10 tgt\_Pim\_DigColPsEOL.R\_ColTrim\_Cnt\_u32 3202351103 3202351103 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 191 ± 0.00048828125 191 tgt\_Pim\_DigColPsEOL.R\_SpurTrim\_Cnt\_u32 3166765055 3166765055 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 1

65534

65534

Test Step 2.19 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	985.5		
DigColPs_I2CHwColAngle_Deg_M_f32	98.25		
DigColPs_I2CHwSpurAngle_Deg_M_f32	325.1		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	3658		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.254		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	6.25400019	6.254 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7859541	7859541	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360	360 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	789547	789547	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2548	2548	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	911	911	~

DigColPs\_SCom\_CustSetTrim



Test Step 2.20 (Repeat Count = 1) Name Input Value DigColPsInt\_GetCustData() 511 DigColPs\_I2CColSensorFault\_Cnt\_M\_Igc 0 DigColPs\_I2CHwColAngleForTrim\_Deg\_M\_f32 745 DigColPs\_I2CHwColAngle\_Deg\_M\_f32 115.3 DigColPs\_I2CHwSpurAngle\_Deg\_M\_f32 147.8 DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 2 DigColPs\_I2CSpurSensorFault\_Cnt\_M\_lgc 0 DigColPs\_TrimCompStatic\_Cnt\_M\_u16 4025 Rte\_Inst\_Sa\_DigColPs tgt\_Rte\_Inst\_Sa\_DigColPs tgt\_Pim\_DigColPsEOL.ColTrim\_Deg\_f32 0.9857 tgt\_Pim\_DigColPsEOL.R\_ColTrim\_Cnt\_u32 152 tgt\_Pim\_DigColPsEOL.SpurTrim\_Deg\_f32 332.5 tgt Pim DigColPsEOL.R SpurTrim Cnt u32 2145 tgt\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 1258 tgt\_Pim\_DigColPsEOL.R\_TrimCom\_Cnt\_u16 tgt\_Pim\_DigColPsEOL  $tgt\_Rte\_Inst\_Sa\_DigColPs.Pim\_DigColPsEOL$ **Actual Value Expected Value** Result Name DigColPs\_I2CHwTrimTransCnts\_Uls\_M\_u08 DigColPs\_SCom\_CustSetTrim() 34 34

Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0.985700011	0.9857 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	152	152	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	332.5	332.5 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2145	2145	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1258	1258	<b>~</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	789	789	~
Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	255		
DigColPs_I2CColSensorFault_Cnt_M_lgc	1		

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	255		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	357.2		
DigColPs_I2CHwColAngle_Deg_M_f32	58.21		
DigColPs_I2CHwSpurAngle_Deg_M_f32	196.5		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	4152		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	148		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	124.7		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	158		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	154		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	75.0999985	75.1 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	148	148	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	124.699997	124.7 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	158	158	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4	4	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	154	154	



### **Test Case 3: Path Test**

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles

TS3.1 1027.00 Cycles TS3.2 1031.00 Cycles TS3.3 1356.00 Cycles TS3.4 1492.00 Cycles TS3.5 1500.00 Cycles TS3.6 1032.00 Cycles TS3.7 1036.00 Cycles TS3.8 1047.00 Cycles

### Description

### Vector Description:

TS3.1 (|2CColSensorFault\_Cnt\_T\_lgc = False)=>TRUE And (|2CSpurSensorFault\_Cnt\_T\_lgc = False)=>FALSE && (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)==>True
TS3.2 (|2CColSensorFault\_Cnt\_T\_lgc = False)=>FALSE And (|2CSpurSensorFault\_Cnt\_T\_lgc = False)=>FALSE && (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)==>False
TS3.3 "(|12CColSensorFault\_Cnt\_T\_lgc = False) And (|2CSpurSensorFault\_Cnt\_T\_lgc = False)\_
And (DigColPs\_ICHWCustData\_UIS\_M\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)=>False
And (DigColPs\_TrimCompStatic\_Cnt\_M\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)\_
And (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)\_
And (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)\_
And (Rte\_Pim\_DigColPsEOL.TrimComp\_Cnt\_u16 <> D\_TRIMCOMPLETE\_CNT\_U16)\_=>TRUE
TS3.4 "((|2CColSensorFault\_Cnt\_T\_lgc == FALSE) && (|2CSpurSensorFault\_Cnt\_T\_lgc == FALSE)
&& (DigColPs\_12CHWCustData\_UIS\_M\_u16 !D\_12CHWCUSTDATAUNKWN\_CNT\_U16)
&& (Rte\_Pim\_DigColPsEOL()>TrimComp\_Cnt\_u16 !D\_TRIMCOMPLETE\_CNT\_U16) ==>True && (|DigColPs\_12CHWCUSTDATAUNKWN\_CNT\_U16)
&& (Rte\_Pim\_DigColPsEOL()>TrimComp\_Cnt\_u16 !D\_TRIMCOMPLETE\_CNT\_U16) |==>True && (|DigColPs\_12CHWCUSTDATAUNKWN\_CNT\_U16)
&& ((DigColPs\_12CHWCustData\_UIS\_M\_u16 &D\_12CHWCUSTDATAUNKWN\_CNT\_U16) ==>False && (|DigColPs\_12CHWCUSTDATAUNKWN\_CNT\_U16) ==>False ==>False ==>FRUE\_And (|DigColPs\_TimCompStatic\_Cnt\_M\_16 <> D\_TRIMCOMPLETE\_CNT\_U16))"
TS3.7 "((|2CColSensorFault\_Cnt\_T\_lgc = False)==>TRUE\_And (|DigColPs\_12CHWCUSTDATAUNKWN\_CNT\_U16)==>FALSE\_And (|DigColPs\_12CHWCUSTDATAUNKWN\_CNT\_U16)==>FALSE\_And (|DigColPs\_12CHWCUSTDATA

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	96.58		
DigColPs_I2CHwColAngle_Deg_M_f32	50		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	<b>✓</b>
DigColPs_SCom_CustSetTrim()	34	34	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1	1 ± 0.00048828125	<b>~</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1	1	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4	4 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456	2456	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2142	2142	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525	525	



Test Step 3.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	511		
DigColPs_I2CColSensorFault_Cnt_M_Igc	1		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	7.254		
DigColPs_I2CHwColAngle_Deg_M_f32	1		
DigColPs_I2CHwSpurAngle_Deg_M_f32	360		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1		
DigColPs_TrimCompStatic_Cnt_M_u16	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	12	12 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1000	1000	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	8	8 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3564	3564	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	625	625	<b>✓</b>

Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DigColPsInt_GetCustData()	2		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5		
DigColPs_I2CHwColAngle_Deg_M_f32	14.23		
DigColPs_I2CHwSpurAngle_Deg_M_f32	255		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2000		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.52		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3021458		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.54		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10254		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3200		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	344		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	10	10 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3202351103	3202351103	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	•

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	5
DigColPs_I2CHwColAngle_Deg_M_f32	44

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Name	Input Value		
DigColPs_I2CHwSpurAngle_Deg_M_f32	12		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3		
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	0	0	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>~</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	185	185 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167158271	3167158271	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	191	191 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166765055	3166765055	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	~

Test Step 3.5 (Repeat Count = 1)			~	
Name	Input Value			
DigColPsInt_GetCustData()	1			
DigColPs_I2CColSensorFault_Cnt_M_Igc	0			
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1800			
DigColPs_I2CHwColAngle_Deg_M_f32	44			
DigColPs_I2CHwSpurAngle_Deg_M_f32	12			
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4			
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0			
DigColPs_TrimCompStatic_Cnt_M_u16	2			
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	<b>✓</b>	
DigColPs_SCom_CustSetTrim()	0	0	<b>✓</b>	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	<b>✓</b>	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	<b>✓</b>	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	~	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	~	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~	
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534		

Test Step 3.6 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	511	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	455	
DigColPs_I2CHwColAngle_Deg_M_f32	44	
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.5	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_TrimCompStatic_Cnt_M_u16	142	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
tgt Pim DigColPsEOL.ColTrim Deg f32	24	

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Name	Input Value		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24	24 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999	1999	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12.3999996	12.4 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672	4672	-
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2146	2146	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725	725	~

Test Step 3.7 (Repeat Count = 1)			· ·
Name	Input Value		
DigColPsInt_GetCustData()	0		
DigColPs_I2CColSensorFault_Cnt_M_lgc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0		
DigColPs_I2CHwColAngle_Deg_M_f32	0		
DigColPs_I2CHwSpurAngle_Deg_M_f32	0		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	-180 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1020002303	1020002303	<b>✓</b>
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	-180 ± 0.00048828125	<b>✓</b>
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1020002303	1020002303	<b>✓</b>
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	<b>✓</b>
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65534	65534	<b>✓</b>

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Test Step 3.8 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetCustData()	356		
DigColPs_I2CColSensorFault_Cnt_M_Igc	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	751.2		
DigColPs_I2CHwColAngle_Deg_M_f32	360		
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.8		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4		
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0		
DigColPs_TrimCompStatic_Cnt_M_u16	2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	6	~
DigColPs_SCom_CustSetTrim()	34	34	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	48	48 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997	3997	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	20	20 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888	6888	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	1	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925	925	~