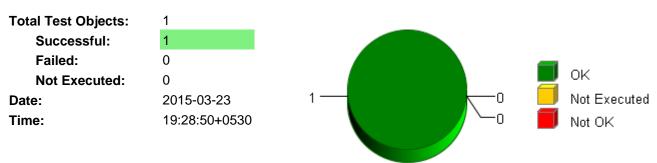


#### Summary

#### **Overall Test Object Results (including Coverage)**



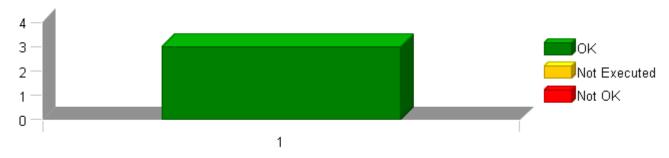
#### **Selected Project Items**

Test Object "CBD\_UnitTest/ReturnFireWall/ReturnFirewall\_Per1"

#### **Used Test Environments**

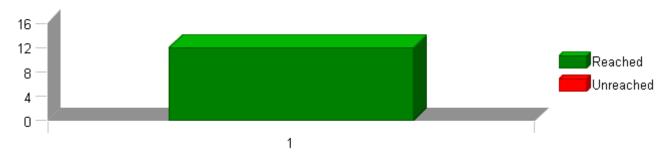
TI TMS 570 PLS UDE (Default)

#### **Test Case Results for Each Test Object (without Coverage)**



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

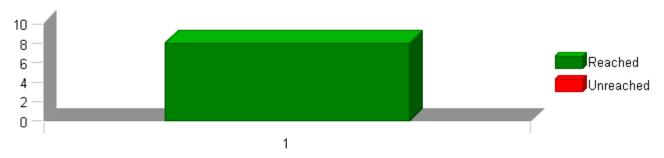
#### Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

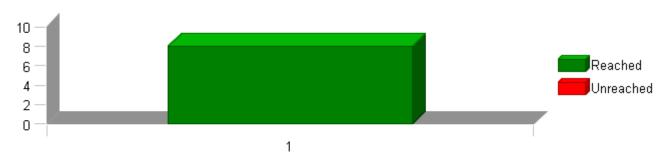


#### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

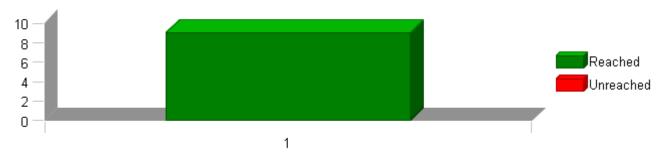
#### **Decision Coverage: Total Decision Outcomes for Each Test Object**



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

#### MC/DC Coverage: Total Condition Combinations for Each Test Object

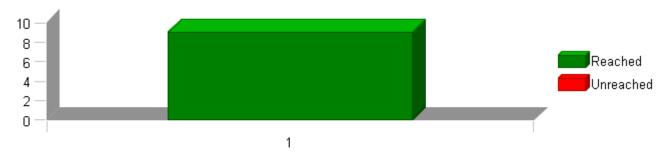


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



#### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

#### **TEST OVERVIEW REPORT**

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#### **Test Object List**

Project Return

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	МСС	Test Cases	Result
	Return	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
	ReturnFireWall	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
1	ReturnFirewall_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~

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ReturnFirewall\_Per1

Project	Return
Module	ReturnFireWall
Test Object	ReturnFirewall_Per1

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\ReturnFirewall_CBD			
Configuration File	D:\Synergy_Work_Area\ReturnFirewall_CBD\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml			
Target Environment	TI TMS 570 PLS UDE (Default)			
Kind of Test	Unit Test			
Linker Options				
Source File(s)				
File	\$(PROJECTROOT)\ReturnFirewall\src\Ap_ReturnFirewall.c			
Compiler Options	-D_DATA_ACCESS= -Dconst= -Dstatic= -I\$(PROJECTROOT)\ReturnFirewall\utp\contract -I\$(PROJECTROOT)\ReturnFirewall\utp\contract \Ap_ReturnFirewall -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include			
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c			
Compiler Options	-D_DATA_ACCESS= -Dconst= -Dstatic= -I\$(PROJECTROOT)\ReturnFirewall\utp\contract -I\$(PROJECTROOT)\ReturnFirewall\utp\contract \Ap ReturnFirewall -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include			

Comments/Description/Specification					
Name	Text				



Name of Tester: Surbhi Goel
Code File(s) Under Test:Ap\_ReturnFirewall.c
Code File(s) Version: 10
Module Design Document:Return\_Firewall\_MDD.docx
Module Design Document Version: 10
Data Dictionary Version: 9
Unit Test Plan Version: 6
Optimization Level:Level 2
Compiler (CodeGen) Version: 11
Total FLASH Used (Bytes): 372
Total RAM Used (Bytes): 372
Total RAM Used (Bytes): 9
Total CALS Used (Bytes): 9
Total CALS Used (Bytes): 216
Special Test Requirements:
Test Date: 03-23-2015
Comments:
Note1: Inline Function defined in globalmacro.h is not unit tested.

Note2: In ""ReturnFirewall\_Per1"\* function, ""Defeat\_Return\_Svc\_Cnt\_Jgc"\* always kept FALSE
to make ""if((DefeatReturnSvc\_Cnt\_T\_Jgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode)) ""
condition FALSE except Boundray Test for variables used in Condition and Path coverage.

Attributes					
Name	Value				
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5				
Float Precision	9				
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj				
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src				
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd				
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl				
Target Install Path	\$(ProgramFiles)\pls\UDE 4.0				
Timer Enabled	false				
Timer Prescale	0				
Timer Resolution	1				
Timer Unit	Cycles				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\ReturnFirewall_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics: (With "None" Instrumentation and "WithPS" Environment)

TS1.1 794.00 Cycles Shortest Path TS1.2 1350.00 Cycles Longest Path

Description

Vector Description:

TS1.1 Shortest Execution Path: ((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode))=True

TS1.2 Longest Execution Path:((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode)) =

False
LimitedReturn\_MtrNm\_T\_f32 = Limit\_m(ReturnCmd\_MtrNm\_T\_f32, LwrBound\_MtrNm\_T\_f32, UprBound\_MtrNm\_T\_f32)=False
(Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32) > FLT\_EPSILON)= False"

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[3][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	18022
t2_RtmFWUprBoundY_MtrNm_s4p11[4][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	18022
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-					
Name	Input Value				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022 18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022 18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022 18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022				
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022				
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[4] t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280 17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280				
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280				
t_RtrnFWVehSpd_Kph_u9p7[0]	65408				
t_RtrnFWVehSpd_Kph_u9p7[1]	65408				
t_RtrnFWVehSpd_Kph_u9p7[2]	65408				
t_RtrnFWVehSpd_Kph_u9p7[3]	65408				
t_RtrnFWVehSpd_Kph_u9p7[4]	65408				
t_RtrnFWVehSpd_Kph_u9p7[5]	65408				
t_RtrnFWVehSpd_Kph_u9p7[6]	65408				
t_RtrnFWVehSpd_Kph_u9p7[7]	65408				
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1				
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	900				
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	8.80000019				
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	255				
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt Igc			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe					
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 LimitedReturn MtrNm f32					
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32			
Name	Actual Value	Expected Value	Result		
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓		
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~		
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~		
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	<b>✓</b>		



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~	

Test Step 1.2 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072 4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2 RtrnFWUprBoundY MtrNm s4p11[1][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2 RtrnFWUprBoundY MtrNm s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048 3072
t2 RtrnFWUprBoundY MtrNm s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048 2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168

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Name	Input Value		
t2 RtrnFWUprBoundY MtrNm s4p11[5][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][2]	-14336		
	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192		
t RtrnFWVehSpd Kph u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_Return\_Svc_Return\_Svc_Return\_S$	l target_ReturnFirewall_Per1_Defeat_Return	rn_Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall.ReturnFirewall\_ReturnFirewall\_ReturnFirewall.ReturnFirewall\_ReturnFirewall\_ReturnFirewall.ReturnFirewall\_ReturnFirewall\_ReturnFirewall\_ReturnFirewall.ReturnFirewall\_ReturnFire$	target_ReturnFirewall_Per1_HandwheelPe	osition_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3	2 target_ReturnFirewall_Per1_LimitedReturn	n_MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum\_Cnt\_enu$	target_ReturnFirewall_Per1_MEC_Counter	er_Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed	_	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cfft_T_endiff)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Farain_Cnt_1_u06)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	_



#### **Test Case 2: Boundary Test**

#### Specification

```
Performance Metrics: (With "None" Instrumentation and "WithPS" Environment)
```

CPU Cycles:

TS2.1 1315.00 Cycles
TS2.2 1334.00 Cycles
TS2.3 1351.00 Cycles
TS2.3 1351.00 Cycles
TS2.4 1403.00 Cycles
TS2.5 1329.00 Cycles
TS2.6 1329.00 Cycles
TS2.6 1329.00 Cycles
TS2.7 1406.00 Cycles
TS2.9 1350.00 Cycles
TS2.9 1350.00 Cycles
TS2.10 1341.00 Cycles
TS2.11 1320.00 Cycles
TS2.11 1320.00 Cycles
TS2.12 1344.00 Cycles
TS2.13 1361.00 Cycles
TS2.14 1286.00 Cycles
TS2.15 1361.00 Cycles
TS2.15 1361.00 Cycles
TS2.16 1462.00 Cycles
TS2.17 1376.00 Cycles
TS2.18 1317.00 Cycles
TS2.19 1347.00 Cycles
TS2.19 1347.00 Cycles
TS2.20 1317.00 Cycles
TS2.21 1337.00 Cycles
TS2.22 1334.00 Cycles
TS2.23 1341.00 Cycles
TS2.24 1352.00 Cycles
TS2.25 1412.00 Cycles
TS2.26 1353.00 Cycles
TS2.27 1321.00 Cycles
TS2.28 1409.00 Cycles
TS2.29 1350.00 Cycles
TS2.29 1350.00 Cycles
TS2.23 1341.00 Cycles
TS2.23 1341.00 Cycles
TS2.23 1351.00 Cycles
TS2.23 1351.00 Cycles
TS2.23 1351.00 Cycles
TS2.23 1351.00 Cycles
TS2.23 1341.00 Cycles
TS2.23 1291.00 Cycles
TS2.33 794.00 Cycles
TS2.33 794.00 Cycles

#### Description

#### Vector Description:

TS2.1HandwheelPosition\_HwDeg\_f32=Min TS2.2HandwheelPosition\_HwDeg\_f32=Max TS2.3HandwheelPosition\_HwDeg\_f32=Zero TS2.4HandwheelPosition\_HwDeg\_f32=Pos TS2.5HandwheelPosition\_HwDeg\_f32=Neg TS2.6ReturnCmd\_MtrNm\_f32=Min TS2.7ReturnCmd\_MtrNm\_f32=Max TS2.8ReturnCmd\_MtrNm\_f32=Zero TS2.9ReturnCmd\_MtrNm\_f32=Pos TS2.10ReturnCmd\_MtrNm\_f3z=Pos TS2.10ReturnCmd\_MtrNm\_f3z=Neg TS2.11VehicleSpeed\_Kph\_f3z=Min TS2.12VehicleSpeed\_Kph\_f3z=Max TS2.13VehicleSpeed\_Kph\_f3z=Pos TS2.14t\_RtrnFWVehSpd\_Kph\_u9p7[8]=Min TS2.15t\_RtrnFWVehSpd\_Kph\_u9p7[8]=Max TS2.15I\_RtrnFWVehSpd\_Kph\_u9p7[8]=Max
TS2.16I\_RtrnFWVehSpd\_Kph\_u9p7[8]=Pos
TS2.17t\_RtrnFWUprBoundX\_HwDeg\_s11p4[11]=Min
TS2.18t\_RtrnFWUprBoundX\_HwDeg\_s11p4[11]=Max
TS2.19t\_RtrnFWUprBoundX\_HwDeg\_s11p4[11]=Mid
TS2.20t\_RtrnFWUprBoundX\_HwDeg\_s11p4[11]=Pos
TS2.21t\_RtrnFWUprBoundY\_HwDeg\_s11p4[11]=Nid
TS2.22t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Min
TS2.23t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Mid
TS2.25t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Mid
TS2.25t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Mid
TS2.25t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Pos TS2.24t2\_ktrnFWUprBoundYU\_mtrNm\_s4p11[11]=Mid
TS2.25t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Pos
TS2.26t2\_RtrnFWUprBoundY0\_MtrNm\_s4p11[11]=Neg
TS2.27Defeat\_Return\_Svc\_Cnt\_lgc=Min
TS2.28Defeat\_Return\_Svc\_Cnt\_lgc=Max
TS2.29MEC\_Counter\_Cnt\_enum=Min
TS2.30MEC\_Counter\_Cnt\_enum=Max TS2.31MEC\_Counter\_Cnt\_enum=Pos TS2.32All min

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

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TS2.33All max

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
2 RtrnFWUprBoundY MtrNm s4p11[2][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
:2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
2 RtrnFWUprBoundY MtrNm s4p11[5][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
2 RtrnFWUprBoundY MtrNm s4p11[5][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
2 RtrnFWUprBoundY MtrNm s4p11[5][7]	-7168
	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
11 Opi Dodina i _ina (4) ii _o+p i ([/][o]	-4096
2 RtrnFWI InrRoundY MtrNm s4p11[7][0]	
:2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] :2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-1		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosi	ion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	/trNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.39990234	-4.39990234 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Sten 2.2 (Beneat Count = 4)	
Test Step 2.2 (Repeat Count = 1) Name	Innut Value
	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408 -17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360 14236
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336 -13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtinFWOpiBoundY_MtiNin_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtmFWUprBoundY_MtrNm_s4p11[3][10]	-2048
	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	6144 7168
tz_RtrnFWUprBoundY_MtrNm_s4p11[4][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtmFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtinFWOpiBoundY_MtiNin_s4p11[5][2] t2_RtinFWUprBoundY_MtiNin_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtinFWOpiBoundY_MtiNin_s4p11[6][8]	14336
t2_RtinFWOpiBoundY_MtiNin_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
	528
	V-1
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704 880
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3] t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3] t_RtrnFWUprBoundX_HwDeg_s11p4[4] t_RtrnFWUprBoundX_HwDeg_s11p4[5]	880 1056
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3] t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880

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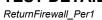


Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1760		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	3520		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	25472		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	32640		
t_RtrnFWVehSpd_Kph_u9p7[7]	38272		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.200000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		<del>-</del>	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	7.64990234	7.64990234 ± 0.00048828125	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2 RtrnFWUprBoundY MtrNm s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2 RtrnFWUprBoundY MtrNm s4p11[0][3]	-12800
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2 RtrnFWUprBoundY MtrNm s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t RtrnFWUprBoundX HwDeg s11p4[1]	-1968
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
	14208
t RtrnFWVehSnd Knh u9n7f41	
t_RtrnFWVehSpd_Kph_u9p7[4]	
t_RtmFWVehSpd_Kph_u9p7[4] t_RtmFWVehSpd_Kph_u9p7[5] t_RtmFWVehSpd_Kph_u9p7[6]	19328 27008

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Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosit	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_N	MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.4 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
12_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
2 RtrnFWUprBoundY MtrNm s4p11[1][7]	12288
2 RtrnFWUprBoundY MtrNm s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
2 RtrnFWUprBoundY MtrNm s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
2 RtrnFWUprBoundY MtrNm s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
2 RtrnFWUprBoundY MtrNm s4p11[2][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
2 RtrnFWUprBoundY MtrNm s4p11[2][8]	-6144
:2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408 -16384
P_RtrnFWUprBoundY_MtrNm_s4p11[4][1] P_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
_RtrnFWUprBoundX_HwDeg_s11p4[8] RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744 -3744
RtrnFWUprBoundX_HwDeg_s11p4[9]	-3/44
_RtrnFWVehSpd_Kph_u9p7[0]	256
_RtrnFWVehSpd_Kph_u9p7[1]	2816
RtrnFWVehSpd Kph u9p7[2]	6656
_RtrnFWVehSpd_Kph_u9p7[3]	9600
_RtrnFWVehSpd_Kph_u9p7[4]	28416
_RtrnFWVehSpd_Kph_u9p7[5]	32256
_RtrnFWVehSpd_Kph_u9p7[6]	28416
RtrnFWVehSpd_Kph_u9p7[7]	32640
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	400.5
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
arget_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.69999988
arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	45.6500015
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_	Cnt_l target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_l	HwDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
	m f32 target ReturnFirewall Per1 LimitedReturn MtrNm f32

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.44189453	3.44189453 ± 0.00048828125	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Nama	Input Value
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
2 RtrnFWUprBoundY MtrNm s4p11[4][6]	8192

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192		
t2 RtrnFWUprBoundY MtrNm s4p11[5][7]	-7168		
t2 RtrnFWUprBoundY MtrNm s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2 RtrnFWUprBoundY MtrNm s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15384 -15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-400.25		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.10000002		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	66.4499969		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_Inst\_Ap\_ReturnFirewall.$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosit	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_N	MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.92041016	-4.92041016 ± 0.00048828125	<b>~</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

T 424 224 D 42	
Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144 7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2 RtrnFWUprBoundY MtrNm s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2 RtrnFWUprBoundY MtrNm s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2 RtrnFWUprBoundY MtrNm s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2 RtrnFWUprBoundY MtrNm s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2 RtrnFWUprBoundY MtrNm s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312

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		•	
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2 RtrnFWUprBoundY MtrNm s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2 RtrnFWUprBoundY MtrNm s4p11[7][2]	-15360		
	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t RtrnFWVehSpd Kph u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtmFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target ReturnFirewall Per1 HandwheelPosition HwDeg f32.value	50		
target ReturnFirewall Per1 MEC Counter Cnt enum.value	2		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	-8.80000019		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	7		
target_ReturnFirewall_Fer1_VerlicleSpeed_Rpi1_isz.value  target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	· .	Svc Cnt lac	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_M	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_		
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	•
The country by telemination and the country of the			

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>





Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192 -7168
t2_RtinFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2 RtrnFWUprBoundY MtrNm s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtmFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2 RtrnFWUprBoundY MtrNm s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	6144 -4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	6144 -4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtmFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	16384 -16384
t2_RtmFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
tz Kulli Wopiboulidi Wullilii 54pi ijojizi	

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	7808		
t_RtrnFWVehSpd_Kph_u9p7[3]	9728		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	20608		
t_RtrnFWVehSpd_Kph_u9p7[6]	20736		
t_RtrnFWVehSpd_Kph_u9p7[7]	21248		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_l	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_I	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048 0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtmFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2 RtrnFWUprBoundY MtrNm s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096 5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtmFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288 14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtmFWUprBoundY_MtrNm_s4p11[5][9] t2_RtmFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtmFW0prBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	10368		
t_RtrnFWVehSpd_Kph_u9p7[3]	9984		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	23168		
t_RtrnFWVehSpd_Kph_u9p7[6]	24448		
t_RtrnFWVehSpd_Kph_u9p7[7]	25344		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	100.800003		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	0		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.2999992		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt	target ReturnFirewall Per1 Defeat Return	Svc Cnt Igc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosit	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	-
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	-
	0	0 ± 0.00048828125	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtinFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360 -14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240 8102
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192 -7400
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	120.349998		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12.6999998		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Retur$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	Cph_f32	
Name	Actual Value	Expected Value	Result
$Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.73730469	2.73730469 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

T+ 0+ 0.40 (D+ 0+	
Test Step 2.10 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Communication   Communicatio		
	Name	Input Value
Part	t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2. Ren'Purple Geory's (Ambra, spin 1989)   1989   1920	t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2 Part   Martin   Set   Tuping		
2. SameWuydeacout, Mahm., sep 10/10         1288           2. FrameWuydeacout, Mahm., sep 10/10         1558           2. FrameWuydeacout, Mahm., sep 11/10         1748           2. FrameWuydeacout, Mahm., sep 11/10         1608           2. FrameWuydeacout, Mahm., sep 11/10         1608           2. FrameWuydeacout, Mahm., sep 11/10         1608           2. FrameWuydeacout, Mahm., sep 11/10         1600           2. FrameWuydeacout, Mahm., sep 11/10         1700           2. FrameWuydeacout, Mahm., sep 11/10         1700           2. FrameWuydeacout, Mahm., sep 11/10         1700           2. FrameWuydeacout, Mahm., se		
2. Print Publishers   Minner   541108		
2   RINTAYURGBOANY   MARTH   SEPT   1720   17468   1		
2. SIRPH AND PROCESS   March age 11   1910   1748   1834   1834   1834   1834   1834   1834   1835   1836		
2_Phin-Pulpiscand   Mindre   1913   1304   13084   1		
2_RRIPA/URBORN/L Mehrs_upin1302  1530   15		
2. Pank Purky Security   Mehry   1941   1930   15300   2. Pank Purky Security   Mehry   1941   13112   1328   13		
2, Pinn Privip Security   Minhing   1941   1931   1228   1208		
P. SIRNEPWUSBOARDY, Minth appl 13001   12288		
2. Bank Puly Board   Mehr   1941   1991   1922		
12. RIFFEW/UPBOUNDY, Mehm _ 1941 1001   1002   1004   10		
2, Brin/Pu/SpaundY, Mehm, api110319   4068   2, Brin/Pu/SpaundY, Mehm, api110310   4068   2, Brin/Pu/SpaundY, Mehm, api110310   2046   2, Brin/Pu/SpaundY, Mehm, api110310   0   0   2, Brin/Pu/SpaundY, Mehm, api110310   4068		
2. RINE-PU/USBOURY   Minns, sep 11(31)  0   0   1. RINE-PU/USBOURY   Minns, sep 11(41)  0   0   1. RINE-PU/USBOURY   Minns, sep 11(41)  1   2048   1. RINE-PU/USBOURY   Minns, sep 11(41)  1   2048   1. RINE-PU/USBOURY   Minns, sep 11(41)  1   5120   1. RINE-PU/USBOURY   Minns, sep 11(51)  1   5120   1. RINE-PU		
2. Reni*Pulysboury   Memin   sept 14(9)   2. Reni*Pulysboury   Memin		
2. RenF-Wydrobound', Mehm. jesh 114 11    244   2. RenF-Wydrobound', Mehm. jesh 114 12    498   2. RenF-Wydrobound', Mehm. jesh 114 13    5120   2. RenF-Wydrobound', Mehm. jesh 114 13    6144   2. RenF-Wydrobound', Mehm. jesh 114 13    7168   2. RenF-Wydrobound', Mehm. jesh 114 13    7168   3. RenF-Wydrobound', Mehm. jesh 114 13    7168   3. RenF-Wydrobound', Mehm. jesh 114 13    7168   3. RenF-Wydrobound', Mehm. jesh 114 13    718   4. RenF-Wydrobound', Mehm. jesh 114 13    718   4. RenF-Wydrobound', Mehm. jesh 114 13    718   5. RenF-Wydrobound', Mehm. jesh 114 13    718   6. RenF-Wydrobound', Mehm. jesh 114 13    718   7. RenF-Wydrobound', Mehm. jesh 114 13    718   7. RenF-Wydrobound', Mehm. jesh 114 13    718   7. RenF-Wydrobound', Mehm. jesh 115 13    718   7. RenF-Wydrobound', M		
P. SIRMPRUPPEDUNY / Minhm. p40114[9]   7188   122888   12288   122888   12288   122888   122888   122888   122888   122888   122888   122888		
2. Brine*PutpScandY*, Minhtin, sept 14(8)   10240		
2. RhmPWUpDeumY_Mhms_s4p114  6    12288   123888   12388   12388   12388   123888   12388   12388   123888   123888   123888   123888   1238		
Part   Published   Part   Pa		
2. RimPWyDeboundY_Minkm_sep114 B S   16384    2. RimPWyDeboundY_Minkm_sep114 B S   16384    2. RimPWyDeboundY_Minkm_sep114 B S   17488    2. RimPWyDeboundY_Minkm_sep114 B S   17488    2. RimPWyDeboundY_Minkm_sep115 S S   16384    2. RimPWyDeboundY_Minkm_sep115 S   16384    2. RimPW		12288
Part		14336
1.7408   1		16384
1.2 RIMF WURPBOUNDY _MRNm_sept   150 15    2. RIMF WURPBOUNDY _MRNm_sept   150 15    2. RIMF WURPBOUNDY _MRNm_sept   150 15    3. RIMF WURPBOUNDY _MRNm_sept   150 16    4. RIMF WURPBOUNDY _MRNm_sept   150 16    5. RIMF WURPBOUNDY _MRNm_sept   150 16    6. RIMF WURPBOUNDY _MRNm_sept   150 16    6. RIMF WURPBOUNDY _MRNm_sept   150 16    7. RIMF WURPBOUNDY _MRNm_sept   150 16    8. RIMF WURPBOUNDY _MRNm_sept   150 16    8. RIMF WURPBOUNDY _MRNm_sept   150 16    9. RIMF WURPBOUNDY _MRNm_sept   150 16    10. RIMF WURPBOUNDY _MRNm_sept   1		17408
2. RITH PWUPBOUNDY _MINNm_s4p1 15 2    1-5350		-17408
12. RITHFWUpBoundY_MrNm_s4p1 [15] 3	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
2_RmF\PVUprBoundY_Minm_s4p11[5] 4  -13312   12_Rmm*\PVUprBoundY_Minm_s4p11[5] 5  -10240   12_Rmm*\PVUprBoundY_Minm_s4p11[5] 5  -10240   12_Rmm*\PVUprBoundY_Minm_s4p11[5] 5  -8192   12_Rmm*\PVUprBoundY_Minm_s4p11[5] 5  -8192   12_Rmm*\PVUprBoundY_Minm_s4p11[5] 5  -8084   12_Rmm*\PVUprBoundY_Minm_s4p11[6] 5  -8084   12_Rmm*\PVUprBoundY_Minm_s4p11[6] 5  -8084   12_Rmm*\PVUprBoundY_Minm_s4p11[6] 5  -8120   12_Rmm*\PVUprBoundY_Minm_s4p11[6] 5  -8184   12_Rmm*\PVUprBound	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
2_RITEPHUPBoundY_Minkm_s4p11[5]5   -12288   -10240   -1	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
12. RITHFWUprBoundY_Mirkm_s4p115[ 6]       -10240         12. RITHFWUprBoundY_Mirkm_s4p115[ 7]       -8192         12. RITHFWUprBoundY_Mirkm_s4p115[ 8]       -6144         12. RITHFWUprBoundY_Mirkm_s4p116[ 9]       -4066         12. RITHFWUprBoundY_Mirkm_s4p116[ 10]       -2048         12. RITHFWUprBoundY_Mirkm_s4p116[ 1]       2048         12. RITHFWUprBoundY_Mirkm_s4p116[ 1]       4066         12. RITHFWUprBoundY_Mirkm_s4p116[ 3]       5120         12. RITHFWUprBoundY_Mirkm_s4p116[ 3]       5120         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       6144         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       10240         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       10240         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       10240         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       1634         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       1634         12. RITHFWUprBoundY_Mirkm_s4p116[ 6]       17408         12. RITHFWUprBoundY_Mirkm_s4p117[ 6]       17408         12. RITHFWUprBoundY_Mirkm_s4p117[ 6]       17408         12. RITHFWUprBoundY_Mirkm_s4p117[ 6]       16384         12. RITHFWUprBoundY_Mirkm_s4p117[ 6]       1634         12. RITHFWUprBoundY_Mirkm_s4p117[ 6]       1634         12. RITHFWUprBoundY_Mirkm_s4p117[ 6]       1634 <t< td=""><td>t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]</td><td>-13312</td></t<>	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
2_RtmFWUprBoundY_MtrNm_s4p11[5][7]	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
2_RtmFWUprBoundY_MtrNm_s4p11[5] 8	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
12_RtmFWUpfboundY_MirNm_s4p11[5][9]	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
2_RtmFWUpfBoundY_MtrNm_s4p11[6][10]   -2048	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	
2. RtmFWUpfboundY_MtnNm_s4p11[6][0]   2048   2. RtmFWUpfboundY_MtnNm_s4p11[6][1]   2048   2. RtmFWUpfboundY_MtnNm_s4p11[6][2]   4096   2. RtmFWUpfboundY_MtnNm_s4p11[6][3]   5120   2. RtmFWUpfboundY_MtnNm_s4p11[6][4]   6144	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
2_RtmFWUpBoundY_MinNm_s4p11[6][1]   2048   2048   22	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	
2. RtmFWUprBoundY_MtrNm_s4p11[6][2]   4096     2. RtmFWUprBoundY_MtrNm_s4p11[6][3]   5120     2. RtmFWUprBoundY_MtrNm_s4p11[6][4]   6144     2. RtmFWUprBoundY_MtrNm_s4p11[6][5]   7168     2. RtmFWUprBoundY_MtrNm_s4p11[6][6]   10240     2. RtmFWUprBoundY_MtrNm_s4p11[6][7]   12288     2. RtmFWUprBoundY_MtrNm_s4p11[6][9]   16384     2. RtmFWUprBoundY_MtrNm_s4p11[6][9]   16384     2. RtmFWUprBoundY_MtrNm_s4p11[6][9]   17408     2. RtmFWUprBoundY_MtrNm_s4p11[7][0]   17408     2. RtmFWUprBoundY_MtrNm_s4p11[7][0]   17408     2. RtmFWUprBoundY_MtrNm_s4p11[7][1]   16384     2. RtmFWUprBoundY_MtrNm_s4p11[7][2]   15360     2. RtmFWUprBoundY_MtrNm_s4p11[7][3]   14336     2. RtmFWUprBoundY_MtrNm_s4p11[7][3]   14336     2. RtmFWUprBoundY_MtrNm_s4p11[7][5]   12288     2. RtmFWUprBoundY_MtrNm_s4p11[7][6]   10240     2. RtmFWUprBoundY_MtrNm_s4p11[7][6]   10240     2. RtmFWUprBoundY_MtrNm_s4p11[7][6]   4096     2. RtmFWUprBoundY_MtrNm_s4p11[7][6]   6144     3. RtmFWUprBoundY_MtrNm_s4p11[7][6]   6144     3. RtmFWUprBoundY_MtrNm_s4p11[7][6]   6144     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]   6144     6. RtmFWUprBoundY_MtrNm_s4p11[7][6]   6144     7. RtmFWU		
12_RtmFWUprBoundY_MtrNm_s4p11[6][3]   5120     12_RtmFWUprBoundY_MtrNm_s4p11[6][4]   6144     12_RtmFWUprBoundY_MtrNm_s4p11[6][6]   7168     12_RtmFWUprBoundY_MtrNm_s4p11[6][6]   10240     12_RtmFWUprBoundY_MtrNm_s4p11[6][7]   12288     12_RtmFWUprBoundY_MtrNm_s4p11[6][8]   14336     12_RtmFWUprBoundY_MtrNm_s4p11[6][9]   16384     12_RtmFWUprBoundY_MtrNm_s4p11[6][9]   16384     12_RtmFWUprBoundY_MtrNm_s4p11[6][9]   16384     12_RtmFWUprBoundY_MtrNm_s4p11[7][0]   17408     12_RtmFWUprBoundY_MtrNm_s4p11[7][1]   16384     12_RtmFWUprBoundY_MtrNm_s4p11[7][1]   16384     12_RtmFWUprBoundY_MtrNm_s4p11[7][2]   15360     12_RtmFWUprBoundY_MtrNm_s4p11[7][3]   14336     12_RtmFWUprBoundY_MtrNm_s4p11[7][3]   14336     12_RtmFWUprBoundY_MtrNm_s4p11[7][4]   13312     12_RtmFWUprBoundY_MtrNm_s4p11[7][6]   10240     13_RtmFWUprBoundY_MtrNm_s4p11[7][6]   10248     14_RtmFWUprBoundY_MtrNm_s4p11[7][6]   10248     14_RtmFWUprBoundY_MtrNm_s4p11[7		
2. RtmFWUprBoundY_MtrNm_s4p11[6][4]   6144     2. RtmFWUprBoundY_MtrNm_s4p11[6][6]   7168     2. RtmFWUprBoundY_MtrNm_s4p11[6][6]   10240     2. RtmFWUprBoundY_MtrNm_s4p11[6][7]   12288     2. RtmFWUprBoundY_MtrNm_s4p11[6][8]   14336     2. RtmFWUprBoundY_MtrNm_s4p11[6][9]   16384     2. RtmFWUprBoundY_MtrNm_s4p11[6][9]   17408     2. RtmFWUprBoundY_MtrNm_s4p11[7][0]   -17408     2. RtmFWUprBoundY_MtrNm_s4p11[7][0]   -16384     2. RtmFWUprBoundY_MtrNm_s4p11[7][1]   -16384     2. RtmFWUprBoundY_MtrNm_s4p11[7][2]   -15360     2. RtmFWUprBoundY_MtrNm_s4p11[7][3]   -14336     2. RtmFWUprBoundY_MtrNm_s4p11[7][8]   -13312     2. RtmFWUprBoundY_MtrNm_s4p11[7][8]   -12288     2. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     3. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     5. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     6. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     6. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     6. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     7. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240     8. RtmFWUprBoundY_MtrNm_s4p11[7][6]   -10240		
12_RImFWUprBoundY_MtrNm_s4p11[6][5]   7168   10240		
10240     2. RtmFWUprBoundY_MtrNm_s4p11[6][6]     2. RtmFWUprBoundY_MtrNm_s4p11[6][8]     2. RtmFWUprBoundY_MtrNm_s4p11[6][8]     3. RtmFWUprBoundY_MtrNm_s4p11[6][9]     3. RtmFWUprBoundY_MtrNm_s4p11[6][9]     4. RtmFWUprBoundY_MtrNm_s4p11[6][9]     4. RtmFWUprBoundY_MtrNm_s4p11[6][10]     4. RtmFWUprBoundY_MtrNm_s4p11[7][1]     4. RtmFWUprBoundY_MtrNm_s4p11[7][2]     4. RtmFWUprBoundY_MtrNm_s4p11[7][2]     4. RtmFWUprBoundY_MtrNm_s4p11[7][3]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     5. RtmFWUprBoundY_MtrNm_s4p11[7][6]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     5. RtmFWUprBoundY_MtrNm_s4p11[7][6]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     5. RtmFWUprBoundY_MtrNm_s4p11[7][6]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     5. RtmFWUprBoundY_MtrNm_s4p11[7][6]     4. RtmFWUprBoundY_MtrNm_s4p11[7][6]     5. RtmFWUprBoundY_MtrNm_s4p11[7][6]     6. RtmFWUprBoundY_MtrNm_s4p11[7][6]     7. RtmFWUprBoundY_MtrNm_s4p11[7][6]     8. RtmFWUprBoundY_MtrNm_s4p1[7][6]     8. RtmFWUprBoundY_MtrNm_s4p1[7][6]     8. RtmFWUprBoundY_MtrNm_s4p1[7][6]     8. RtmFWUprBoundY_MtrNm_s4p1[7][6]     8. RtmFWUprBoundY_MtrNm_s4p1[7][6]		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][7]     12288       t2_RtmFWUprBoundY_MtrNm_s4p11[6][8]     14336       t2_RtmFWUprBoundY_MtrNm_s4p11[6][10]     17408       t2_RtmFWUprBoundY_MtrNm_s4p11[7][0]     -17408       t2_RtmFWUprBoundY_MtrNm_s4p11[7][1]     -16384       t2_RtmFWUprBoundY_MtrNm_s4p11[7][1]     -16384       t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]     -15360       t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]     -14336       t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]     -13312       t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]     -13312       t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]     -10240       t2_RtmFWUprBoundY_MtrNm_s4p11[7][7]     -8192       t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]     -6144       t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]     -6144       t2_RtmFWUprBoundY_MtrNm_s4p11[7][9]     -4096       t2_RtmFWUprBoundX_HwDeg_s11p4[0]     176       t_RtmFWUprBoundX_HwDeg_s11p4[0]     176       t_RtmFWUprBoundX_HwDeg_s11p4[1]     528       t_RtmFWUprBoundX_HwDeg_s11p4[3]     704       t_RtmFWUprBoundX_HwDeg_s11p4[3]     880       t_RtmFWUprBoundX_HwDeg_s11p4[6]     1056       t_RtmFWUprBoundX_HwDeg_s11p4[6]     1056       t_RtmFWUprBoundX_HwDeg_s11p4[6]     1056       t_RtmFWUprBoundX_HwDeg_s11p4[7]     1408		
14336     12_RtrnFWUprBoundY_MtrNm_s4p11[6][9]   16384     12_RtrnFWUprBoundY_MtrNm_s4p11[6][9]   17408     12_RtrnFWUprBoundY_MtrNm_s4p11[7][0]   -17408     12_RtrnFWUprBoundY_MtrNm_s4p11[7][1]   -16384     12_RtrnFWUprBoundY_MtrNm_s4p11[7][1]   -16384     12_RtrnFWUprBoundY_MtrNm_s4p11[7][2]   -15360     12_RtrnFWUprBoundY_MtrNm_s4p11[7][3]   -14336     12_RtrnFWUprBoundY_MtrNm_s4p11[7][4]   -13312     12_RtrnFWUprBoundY_MtrNm_s4p11[7][6]   -12288     12_RtrnFWUprBoundY_MtrNm_s4p11[7][6]   -10240     12_RtrnFWUprBoundY_MtrNm_s4p11[7][6]   -6144     12_RtrnFWUprBoundY_MtrNm_s4p11[7][8]   -6144     12_RtrnFWUprBoundY_MtrNm_s4p11[7][9]   -4096     12_RtrnFWUprBoundY_MtrNm_s4p11[7][0]   -2048     12_RtrnFWUprBoundY_MtrNm_s4p1[7][0]   -2048     12_RtrnFWUprBoundX_HwDeg_s11p4[0]   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     176   -76   -76   -76     177   -76   -76     177   -76   -76     177   -76   -76     177   -76     177   -76   -76     177   -76   -76     177   -76   -76     177   -76   -76     177   -76   -76     177   -76   -76     177		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][9]     16384       t2_RtmFWUprBoundY_MtrNm_s4p11[6][10]     17408       t2_RtmFWUprBoundY_MtrNm_s4p11[7][0]     -17408       t2_RtmFWUprBoundY_MtrNm_s4p11[7][1]     -16384       t2_RtmFWUprBoundY_MtrNm_s4p11[7][2]     -15360       t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]     -14336       t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]     -13312       t2_RtmFWUprBoundY_MtrNm_s4p11[7][5]     -12288       t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]     -10240       t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]     -6144       t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]     -6144       t2_RtmFWUprBoundY_MtrNm_s4p11[7][9]     -4096       t2_RtmFWUprBoundY_MtrNm_s4p11[7][0]     -2048       t2_RtmFWUprBoundX_HwDeg_s11p4[0]     176       t_RtmFWUprBoundX_HwDeg_s11p4[1]     352       t_RtmFWUprBoundX_HwDeg_s11p4[2]     528       t_RtmFWUprBoundX_HwDeg_s11p4[2]     528       t_RtmFWUprBoundX_HwDeg_s11p4[4]     880       t_RtmFWUprBoundX_HwDeg_s11p4[5]     1056       t_RtmFWUprBoundX_HwDeg_s11p4[6]     1232       t_RtmFWUprBoundX_HwDeg_s11p4[6]     1232       t_RtmFWUprBoundX_HwDeg_s11p4[7]     1408		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][10]       17408         t2_RtmFWUprBoundY_MtrNm_s4p11[7][0]       -17408         t2_RtmFWUprBoundY_MtrNm_s4p11[7][1]       -16384         t2_RtmFWUprBoundY_MtrNm_s4p11[7][2]       -15360         t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]       -14336         t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]       -13312         t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]       -12288         t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]       -10240         t2_RtmFWUprBoundY_MtrNm_s4p11[7][7]       -8192         t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]       -6144         t2_RtmFWUprBoundY_MtrNm_s4p11[7][9]       -4096         t2_RtmFWUprBoundY_MtrNm_s4p11[7][10]       -2048         t_RtmFWUprBoundX_HwDeg_s11p4[0]       176         t_RtmFWUprBoundX_HwDeg_s11p4[1]       352         t_RtmFWUprBoundX_HwDeg_s11p4[2]       528         t_RtmFWUprBoundX_HwDeg_s11p4[3]       704         t_RtmFWUprBoundX_HwDeg_s11p4[4]       880         t_RtmFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtmFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtmFWUprBoundX_HwDeg_s11p4[7]       1408		
t2_RtmFWUprBoundY_MtrNm_s4p11[7][0]       -17408         t2_RtmFWUprBoundY_MtrNm_s4p11[7][1]       -16384         t2_RtmFWUprBoundY_MtrNm_s4p11[7][2]       -15360         t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]       -14336         t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]       -13312         t2_RtmFWUprBoundY_MtrNm_s4p11[7][5]       -12288         t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]       -10240         t2_RtmFWUprBoundY_MtrNm_s4p11[7][7]       -8192         t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]       -6144         t2_RtmFWUprBoundY_MtrNm_s4p11[7][9]       -4096         t2_RtmFWUprBoundY_MtrNm_s4p11[7][10]       -2048         t_RtmFWUprBoundX_HwDeg_s11p4[0]       176         t_RtmFWUprBoundX_HwDeg_s11p4[1]       352         t_RtmFWUprBoundX_HwDeg_s11p4[3]       704         t_RtmFWUprBoundX_HwDeg_s11p4[4]       880         t_RtmFWUprBoundX_HwDeg_s11p4[6]       1056         t_RtmFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtmFWUprBoundX_HwDeg_s11p4[7]       1408		
t2_RtmFWUprBoundY_MtrNm_s4p11[7][1]       -16384         t2_RtmFWUprBoundY_MtrNm_s4p11[7][2]       -15360         t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]       -14336         t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]       -13312         t2_RtmFWUprBoundY_MtrNm_s4p11[7][5]       -12288         t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]       -10240         t2_RtmFWUprBoundY_MtrNm_s4p11[7][7]       -8192         t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]       -6144         t2_RtmFWUprBoundY_MtrNm_s4p11[7][9]       -4096         t2_RtmFWUprBoundY_MtrNm_s4p11[7][0]       -2048         t_RtmFWUprBoundX_HwDeg_s11p4[0]       176         t_RtmFWUprBoundX_HwDeg_s11p4[1]       352         t_RtmFWUprBoundX_HwDeg_s11p4[3]       704         t_RtmFWUprBoundX_HwDeg_s11p4[4]       880         t_RtmFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtmFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtmFWUprBoundX_HwDeg_s11p4[7]       1408		
t2_RtmFWUprBoundY_MtrNm_s4p11[7][2] -15360 t2_RtmFWUprBoundY_MtrNm_s4p11[7][3] -14336 t2_RtmFWUprBoundY_MtrNm_s4p11[7][4] -13312 t2_RtmFWUprBoundY_MtrNm_s4p11[7][6] -10240 t2_RtmFWUprBoundY_MtrNm_s4p11[7][7] -8192 t2_RtmFWUprBoundY_MtrNm_s4p11[7][8] -6144 t2_RtmFWUprBoundY_MtrNm_s4p11[7][9] -4096 t2_RtmFWUprBoundY_MtrNm_s4p11[7][9] -2048 t_RtmFWUprBoundY_MtrNm_s4p11[7][10] -2048 t_RtmFWUprBoundY_MtrNm_s4p11[7][10] -5208 t_RtmFWUprBoundX_HwDeg_s11p4[3] -704 t_RtmFWUprBoundX_HwDeg_s11p4[3] -704 t_RtmFWUprBoundX_HwDeg_s11p4[6] -1056 t_RtmFWUprBoundX_HwDeg_s11p4[6] -1232 t_RtmFWUprBoundX_HwDeg_s11p4[7] -1408		
t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]       -14336         t2_RtmFWUprBoundY_MtrNm_s4p11[7][4]       -13312         t2_RtmFWUprBoundY_MtrNm_s4p11[7][5]       -12288         t2_RtmFWUprBoundY_MtrNm_s4p11[7][6]       -10240         t2_RtmFWUprBoundY_MtrNm_s4p11[7][7]       -8192         t2_RtmFWUprBoundY_MtrNm_s4p11[7][8]       -6144         t2_RtmFWUprBoundY_MtrNm_s4p11[7][9]       -4096         t2_RtmFWUprBoundY_MtrNm_s4p11[7][10]       -2048         t_RtmFWUprBoundX_HwDeg_s11p4[0]       176         t_RtmFWUprBoundX_HwDeg_s11p4[1]       352         t_RtmFWUprBoundX_HwDeg_s11p4[2]       528         t_RtmFWUprBoundX_HwDeg_s11p4[3]       704         t_RtmFWUprBoundX_HwDeg_s11p4[4]       880         t_RtmFWUprBoundX_HwDeg_s11p4[6]       1056         t_RtmFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtmFWUprBoundX_HwDeg_s11p4[7]       1408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4] -13312 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] -6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] -2048 t_RtrnFWUprBoundX_HwDeg_s11p4[0] 176 t_RtrnFWUprBoundX_HwDeg_s11p4[1] 352 t_RtrnFWUprBoundX_HwDeg_s11p4[2] 528 t_RtrnFWUprBoundX_HwDeg_s11p4[3] 704 t_RtrnFWUprBoundX_HwDeg_s11p4[4] 880 t_RtrnFWUprBoundX_HwDeg_s11p4[5] 1056 t_RtrnFWUprBoundX_HwDeg_s11p4[6] 1232 t_RtrnFWUprBoundX_HwDeg_s11p4[7] 1408		
tz_RtrnFWUprBoundY_MtrNm_s4p11[7][5] -12288 tz_RtrnFWUprBoundY_MtrNm_s4p11[7][6] -10240 tz_RtrnFWUprBoundY_MtrNm_s4p11[7][7] -8192 tz_RtrnFWUprBoundY_MtrNm_s4p11[7][8] -6144 tz_RtrnFWUprBoundY_MtrNm_s4p11[7][9] -4096 tz_RtrnFWUprBoundY_MtrNm_s4p11[7][10] -2048 t_RtrnFWUprBoundX_HwDeg_s11p4[0] 176 t_RtrnFWUprBoundX_HwDeg_s11p4[1] 352 t_RtrnFWUprBoundX_HwDeg_s11p4[2] 528 t_RtrnFWUprBoundX_HwDeg_s11p4[3] 704 t_RtrnFWUprBoundX_HwDeg_s11p4[4] 880 t_RtrnFWUprBoundX_HwDeg_s11p4[5] 1056 t_RtrnFWUprBoundX_HwDeg_s11p4[6] 1232 t_RtrnFWUprBoundX_HwDeg_s11p4[7] 1408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] -6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] -2048 t_RtrnFWUprBoundX_HwDeg_s11p4[0] 176 t_RtrnFWUprBoundX_HwDeg_s11p4[1] 352 t_RtrnFWUprBoundX_HwDeg_s11p4[2] 528 t_RtrnFWUprBoundX_HwDeg_s11p4[3] 704 t_RtrnFWUprBoundX_HwDeg_s11p4[4] 880 t_RtrnFWUprBoundX_HwDeg_s11p4[5] 1056 t_RtrnFWUprBoundX_HwDeg_s11p4[6] 1232 t_RtrnFWUprBoundX_HwDeg_s11p4[7] 1408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]       -4096         t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]       -2048         t_RtrnFWUprBoundX_HwDeg_s11p4[0]       176         t_RtrnFWUprBoundX_HwDeg_s11p4[1]       352         t_RtrnFWUprBoundX_HwDeg_s11p4[2]       528         t_RtrnFWUprBoundX_HwDeg_s11p4[3]       704         t_RtrnFWUprBoundX_HwDeg_s11p4[4]       880         t_RtrnFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408		
tz_RtrnFWUprBoundY_MtrNm_s4p11[7][10] -2048 t_RtrnFWUprBoundX_HwDeg_s11p4[0] 176 t_RtrnFWUprBoundX_HwDeg_s11p4[1] 352 t_RtrnFWUprBoundX_HwDeg_s11p4[2] 528 t_RtrnFWUprBoundX_HwDeg_s11p4[3] 704 t_RtrnFWUprBoundX_HwDeg_s11p4[4] 880 t_RtrnFWUprBoundX_HwDeg_s11p4[5] 1056 t_RtrnFWUprBoundX_HwDeg_s11p4[6] 1232 t_RtrnFWUprBoundX_HwDeg_s11p4[7] 1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]       176         t_RtrnFWUprBoundX_HwDeg_s11p4[1]       352         t_RtrnFWUprBoundX_HwDeg_s11p4[2]       528         t_RtrnFWUprBoundX_HwDeg_s11p4[3]       704         t_RtrnFWUprBoundX_HwDeg_s11p4[4]       880         t_RtrnFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]       352         t_RtrnFWUprBoundX_HwDeg_s11p4[2]       528         t_RtrnFWUprBoundX_HwDeg_s11p4[3]       704         t_RtrnFWUprBoundX_HwDeg_s11p4[4]       880         t_RtrnFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408		176
t_RtrnFWUprBoundX_HwDeg_s11p4[3]       704         t_RtrnFWUprBoundX_HwDeg_s11p4[4]       880         t_RtrnFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408		352
t_RtrnFWUprBoundX_HwDeg_s11p4[3]       704         t_RtrnFWUprBoundX_HwDeg_s11p4[4]       880         t_RtrnFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408		528
t_RtrnFWUprBoundX_HwDeg_s11p4[5]       1056         t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408		704
t_RtrnFWUprBoundX_HwDeg_s11p4[6]       1232         t_RtrnFWUprBoundX_HwDeg_s11p4[7]       1408	t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[7] 1408	t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
	t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t RtmFWUnrRoundX HwDeg s11n4/81 1584	t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
	t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584

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	I		
Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	145.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-2.25		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.52490234	5.52490234 ± 0.00048828125	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

lame	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
P_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
P_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
P_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
P_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
P_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
P_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
P_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
P_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048

ReturnFirewall\_Per1

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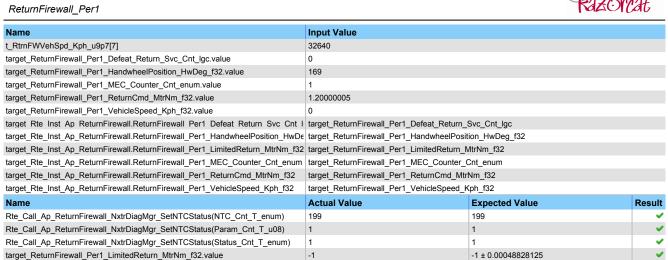




Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384 -16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtmFWUprBoundY_MtrNm_s4p11[4][1]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880 -800
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-600 -672
t_RtrnFWUprBoundX_HwDeg_s11p4[10] t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtmFWVehSpd_Kph_u9p7[0]	2560
	6400
t RimeWvenSnd Knn 119n/121	0.700
t_RtrnFWVehSpd_Kph_u9p7[2] t RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[3]	9600 12800
	9600 12800 19200

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
12_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
12_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	14336

@ Report created by TESSY V3.1.12, report template V2.1

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408 -16384
P_RtrnFWUprBoundY_MtrNm_s4p11[4][1] P_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2 RtrnFWUprBoundY MtrNm s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
_RtrnFWUprBoundX_HwDeg_s11p4[8] RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744 -3744
_ktrnFwUprBoundX_HwDeg_s11p4[9] _ktrnFWUprBoundX_HwDeg_s11p4[10]	-3/44 -3408
_RtrnFWVehSpd_Kph_u9p7[0]	128
_RtrnFWVehSpd_Kph_u9p7[1]	1408
RtrnFWVehSpd Kph u9p7[2]	6528
_RtrnFWVehSpd_Kph_u9p7[3]	9600
_RtrnFWVehSpd_Kph_u9p7[4]	14208
_RtrnFWVehSpd_Kph_u9p7[5]	19328
_RtrnFWVehSpd_Kph_u9p7[6]	14208
_RtrnFWVehSpd_Kph_u9p7[7]	19840
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	193
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
arget_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.29999995
arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_	_Cnt_I target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_	HwD€ target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
	m f32 target ReturnFirewall Per1 LimitedReturn MtrNm f32

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	1.29999995	1.29999995 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.13 (Repeat Count = 1)	Immust Malus
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
I2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
12_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
12_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
12_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
12_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
12_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384		
	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192		
	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t RtrnFWVehSpd Kph u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	505.549988		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.54999995		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	55.5		
$target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_Inst_Return\_Svc\_Cnt\_Inst_Ret$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.54999995	3.54999995 ± 0.00048828125	•





Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.14 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288	
2 RtrnFWUprBoundY MtrNm s4p11[4][8]	14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312	

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2 RtrnFWUprBoundY MtrNm s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360		
t2_RtmFWOprBoundY_MtrNm_s4p11[7][2] t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312 -12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	0		
t_RtrnFWVehSpd_Kph_u9p7[2]	0		
t_RtrnFWVehSpd_Kph_u9p7[3]	0		
t_RtrnFWVehSpd_Kph_u9p7[4]	0		
t_RtrnFWVehSpd_Kph_u9p7[5]	0		
t_RtrnFWVehSpd_Kph_u9p7[6]	0		
t_RtrnFWVehSpd_Kph_u9p7[7]	0		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	241.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	1.70000005		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	0		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt lac	
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 HandwheelPosition HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	,	_ ·-	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		_	
target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr		
		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	1	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	1.70000005	1.70000005 ± 0.00048828125	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>~</b>



Post   Part	Test Step 2.15 (Repeat Count = 1)	· ·
Rig. Hard, P. Celestri-Frewall		Input Value
2. Part Purplement   Mannay and 19   19   19   19   19   19   19   19		
2. Remin-Vigle-Bound, Minhm, sel-11 (1923)   -12000     2. Remin-Vigle-Bound, Minhm, sel-11 (1924)   -11 (1924)   -11 (1924)     2. Remin-Vigle-Bound, Minhm, sel-11 (1924)   -11 (1924)   -11 (1924)   -12 (192		
2. Ramin'spideousey   Mehm   sel 1003	t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
	t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
2. Perr Puri Bound's (Minhor, set 10)     4922     2. Perr Puri Bound's (Minhor, set 10)     4922     2. Perr Puri Bound's (Minhor, set 10)     4968     2. Perr Puri Bound's (Minhor, set 11)     4968     2. P		
2.   Janne Wildy Beaumy   Menhal   1947   10   10		
2_   Part   March		
22 Pers   March   1998   199		
2. RIMPHUPSONOMY_MINN_perior 1910 90   2048   2. RIMPHUPSONOMY_MINN_perior 1910 90   2048   2. RIMPHUPSONOMY_MINN_perior 1910 91   2048   2. RIMPHUPSONOMY_MINN_perior 1910 91   2048   2. RIMPHUPSONOMY_MINN_perior 1910 91   2049   2. RIMPHUPSONOMY_MIN		· · ·
2. RITHENDURGERINAN, MINNING, 1991 (19)10   2048   2. RITHENDURGERINAN, MINNING, 1991 (19)10   2072   2. RITHENDURGERINAN, MINNING, 1991 (19)11   3072   2. RITHENDURGERINAN, MINNING, 1991 (19)11   4066   2. RITHENDURGERINAN, MINNING, 1991 (19)11   6114   2. RITHENDURGERINAN, MINNING, 1991 (19)11   6114   2. RITHENDURGERINAN, MINNING, 1991 (19)11   6114   2. RITHENDURGERINAN, MINNING, 1991 (19)11   6122   6. RITHENDURGERINAN, MINNING, 1991 (19)11   6123   6. RITHENDURGERINAN, MINNING, 1991 (19)11   6123   6. RITHENDURGERINAN, MINNING, 1991 (19)11   6123   6. RITHENDURGERINAN, MINNING, 1991 (12)1   6. RITHENDURGERINAN, MINNING, 1991 (12)1   6. RITHENDURGERINAN, MINNING, 1991 (12)2   6. RITHENDURGERINAN, MINNING, 1991 (12)3   6. RITHENDURGERINAN, MINNING, 1991 (13)3   6. RITHENDURGERINAN, MINNING, 1991 (13)3		
2. Rinn'Ruys Bound's, Mehm as \$11 1010   3072   3		
22 PimPrVUpSoundY_Mehm_abs11[10]		2048
2. RIFFAPUR BOUNDY, MANN— 451 11[10]	t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
2. RimFWUpSourdy Mintm. sp111[19]   7168	t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
2. Parti-Vujurbaury   Minits   sept 1(1)  5   1   1   1   1   1   1   1   1   1	t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
Refire/Pub/Boundy   Minkm   sep11[19]   10240   12288   1228		
12   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1228     22   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1228     23   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1238     24   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1238     25   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1538     25   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1538     26   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1538     27   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1538     28   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1228     29   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1228     20   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1228     20   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1228     21   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1228     22   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1208     23   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1208     24   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1208     25   Partin Pulsarbourdy   Minhtm. seph 11 (1)   11 (1)   1208     26   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     27   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     28   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     29   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     20   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     20   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     21   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     22   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     23   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     24   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     25   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     26   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     27   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     28   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     29   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208     20   Partin Pulsarbourdy   Minhtm. seph 11 (1)   1208		
12. RINFPWUPBOUNTY MINNIN _94011[1]8]   14336   1433		
2. Phin=Pulp Bound', Min'm, apt   11  0     2. Phin=Pulp Bound', Min'm, apt   12  0    3. Phin=Pulp Bound', Min'm, apt   12  0    3. Phin=Pulp Bound', Min'm, apt   12  0    4. Phin=Pulp Bound', Min'm, apt   12  0    5. Phin=Pulp Bound', Min'm, apt   12  0    6. Phin=Pulp Bound', Min'm, apt   12  0    7. Phin=Pulp Bound', Min'm, apt   12  0    7. Phin=Pulp Bound', Min'm, apt   12  0    8. Phin=Pulp Bound', Min'm, apt   12  0    9. Phin=Pulp Bound', Min'm, apt   13  0    9. Phin=Pulp Bound', M		
12. Rither Wuy Bound Y, Minhm. 9491   1   1   1   1   1   1   1   1   1		
12, Patter-Pulp Cardonn'   Minhin   1961   1   1970   19		
Partic Mulpr Bound		
2, RimFWUpBoundY, Minkm, sept 11/2  2    2, RimFWUpBoundY, Minkm, sept 11/2  4    1, 2, RimFWUpBoundY, Minkm, sept 11/2  7    1, 4, 188   1, 2, RimFWUpBoundY, Minkm, sept 11/2  7    1, 4, 188   1, 2, RimFWUpBoundY, Minkm, sept 11/2  7    2, RimFWUpBoundY, Minkm, sept 11/3  7    2, RimFWUpBoundY, Minkm, sept 11/4		
22 RIMF WUprBoundY_MinYm_s4p11[2][6]   -10240	t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
2_RIMFPWJprBoundY_Mirkm_sep112[6]   -10240   -	t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
2. RIMF WUpfBoundY _Mitnm_s4p112[IP]	t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
2. RIMFWUpRboundY _MixIm_ selpt   12  17    -7168		-10240
12. RIMFWUpBoundY_Mknm_s4p11[2] 8]		
2. RithFWUprBoundY_Mirklm_s4p11(2) 01   2048    2. RithFWUprBoundY_Mirklm_s4p11(3) 01   2048    2. RithFWUprBoundY_Mirklm_s4p11(3) 01   3072    2. RithFWUprBoundY_Mirklm_s4p11(4) 01   3072    2. RithFWUprBoundY_Mirklm_s		
12 RITEPWUPBoundY_Mirkm_s4p11(3)(1) 2048 12 RITEPWUPBoundY_Mirkm_s4p11(3)(1) 2048 12 RITEPWUPBoundY_Mirkm_s4p11(3)(1) 3072 12 RITEPWUPBoundY_Mirkm_s4p11(3)(2) 4096 12 RITEPWUPBoundY_Mirkm_s4p11(3)(3) 5120 12 RITEPWUPBoundY_Mirkm_s4p11(3)(4) 6144 12 RITEPWUPBoundY_Mirkm_s4p11(3)(5) 7168 12 RITEPWUPBoundY_Mirkm_s4p11(3)(6) 8192 12 RITEPWUPBoundY_Mirkm_s4p11(3)(6) 12288 12 RITEPWUPBoundY_Mirkm_s4p11(3)(6) 13384 12 RITEPWUPBoundY_Mirkm_s4p11(3)(6) 13384 12 RITEPWUPBoundY_Mirkm_s4p11(4)(1) 15380 12 RITEPWUPBoundY_Mirkm_s4p11(4)(1) 15380 12 RITEPWUPBoundY_Mirkm_s4p11(4)(1) 15380 12 RITEPWUPBoundY_Mirkm_s4p11(4)(1) 15380 12 RITEPWUPBoundY_Mirkm_s4p11(4)(6) 15384 12 RITEPWUPBoundY_Mirkm_s4p11(6)(6) 15484		
2_RtmFWUprBoundY_MtrNm_s4p11(3) 0  2048   12_RtmFwUprBoundY_MtrNm_s4p11(3) 1  3072   12_RtmFwUprBoundY_MtrNm_s4p11(3) 2  4096   12_RtmFwUprBoundY_MtrNm_s4p11(3) 3  5120   12_RtmFwUprBoundY_MtrNm_s4p11(3) 3  6144   12_RtmFwUprBoundY_MtrNm_s4p11(3) 6  8192   12_RtmFwUprBoundY_MtrNm_s4p11(3) 6  8192   12_RtmFwUprBoundY_MtrNm_s4p11(3) 6  8192   12_RtmFwUprBoundY_MtrNm_s4p11(3) 6  12288   12_RtmFwUprBoundY_MtrNm_s4p11(3) 8  12288   12_RtmFwUprBoundY_MtrNm_s4p11(3) 8  12288   12_RtmFwUprBoundY_MtrNm_s4p11(3) 9  14336   12_RtmFwUprBoundY_MtrNm_s4p11(4) 0  -16384   12_RtmFwUprBoundY_MtrNm_s4p11(4) 0  -17686   12_RtmFwUprBoundY_MtrNm_s4p11(4) 0  -17686   12_RtmFwUprBoundY_MtrNm_s4p11(4) 0  -17686   12_RtmFwUprBoundY_MtrNm_s4p11(4) 0  -17686   12_RtmFwUprBoundY_MtrNm_s4p11(4) 0  -2048   12_RtmFwUprBoundY_MtrNm_s4p11(5) 0  -2048   12_RtmFwUprBoundY_MtrNm_s4p11(5) 0  -2048   12_RtmFwUprBoundY_MtrNm_s4p11(5) 0  -2048   12_RtmFwUprBound		
12_RtmFWUpfBoundY_MtrNm_s4p11[3][1]   3072   4096   12_RtmFWUpfBoundY_MtrNm_s4p11[3][2]   4096   12_RtmFWUpfBoundY_MtrNm_s4p11[3][3]   5120   12_RtmFWUpfBoundY_MtrNm_s4p11[3][4]   6144   12_RtmFWUpfBoundY_MtrNm_s4p11[3][5]   7168   12_RtmFWUpfBoundY_MtrNm_s4p11[3][6]   8192   12_RtmFWUpfBoundY_MtrNm_s4p11[3][7]   10240   12_RtmFWUpfBoundY_MtrNm_s4p11[3][7]   10240   12_RtmFWUpfBoundY_MtrNm_s4p11[3][8]   12288   12_RtmFWUpfBoundY_MtrNm_s4p11[3][9]   14336   12_RtmFWUpfBoundY_MtrNm_s4p11[3][9]   14336   12_RtmFWUpfBoundY_MtrNm_s4p11[3][9]   14336   12_RtmFWUpfBoundY_MtrNm_s4p11[4][9]   16384   12_RtmFWUpfBoundY_MtrNm_s4p11[4][9]   14336		
2_RtmFWUprBoundY_MtrNm_s4p11[3][2]   4096		
12. RTIFFWUpfBoundY_MitNm_s4p113  3		
2_RtmFWUpfBoundY_MtnNm_s4p11[3][5]   7168   8192   2_RtmFWUpfBoundY_MtnNm_s4p11[3][6]   8192   12288		5120
2. RtmFWUprBoundY_MtrNm_s4p11[3] 6    8192   12. RtmFWUprBoundY_MtrNm_s4p11[3] 7    10240   12. RtmFWUprBoundY_MtrNm_s4p11[3] 8    12288   12. RtmFWUprBoundY_MtrNm_s4p11[3] 9    14336   12. RtmFWUprBoundY_MtrNm_s4p11[3] 10    16384   12. RtmFWUprBoundY_MtrNm_s4p11[4] 0    16384   12. RtmFWUprBoundY_MtrNm_s4p11[4] 11    15360   12. RtmFWUprBoundY_MtrNm_s4p11[4] 2    14336   12. RtmFWUprBoundY_MtrNm_s4p11[4] 3    12288   12. RtmFWUprBoundY_MtrNm_s4p11[4] 3    12288   12. RtmFWUprBoundY_MtrNm_s4p11[4] 3    12288   12. RtmFWUprBoundY_MtrNm_s4p11[4] 6    10240   12. RtmFWUprBoundY_MtrNm_s4p11[4] 6    8192   12. RtmFWUprBoundY_MtrNm_s4p11[4] 6    8192   12. RtmFWUprBoundY_MtrNm_s4p11[4] 9    4096   12. RtmFWUprBoundY_MtrNm_s4p11[4] 9    4096   12. RtmFWUprBoundY_MtrNm_s4p11[4] 10    2048   12. RtmFWUprBoundY_MtrNm_s4p11[6] 0    2048   12. Rtm	t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
12. RtmFWUprBoundY_MtrNm_s4p11[3][7]     10240       12. RtmFWUprBoundY_MtrNm_s4p11[3][8]     12288       12. RtmFWUprBoundY_MtrNm_s4p11[3][9]     14336       12. RtmFWUprBoundY_MtrNm_s4p11[4][0]     16384       12. RtmFWUprBoundY_MtrNm_s4p11[4][1]     -16384       12. RtmFWUprBoundY_MtrNm_s4p11[4][1]     -15360       12. RtmFWUprBoundY_MtrNm_s4p11[4][2]     -14336       12. RtmFWUprBoundY_MtrNm_s4p11[4][3]     -12800       12. RtmFWUprBoundY_MtrNm_s4p11[4][4]     -12288       12. RtmFWUprBoundY_MtrNm_s4p11[4][6]     -8192       12. RtmFWUprBoundY_MtrNm_s4p11[4][6]     -8192       12. RtmFWUprBoundY_MtrNm_s4p11[4][7]     -7168       12. RtmFWUprBoundY_MtrNm_s4p11[4][9]     -8192       12. RtmFWUprBoundY_MtrNm_s4p11[4][9]     -806       12. RtmFWUprBoundY_MtrNm_s4p11[4][9]     -2048       12. RtmFWUprBoundY_MtrNm_s4p11[6][0]     -2048       12. RtmFWUprBoundY_MtrNm_s4p11[5][0]     2048       12. RtmFWUprBoundY_MtrNm_s4p11[5][0]     -2048       12. RtmFWUprBoundY_MtrNm_s4p11[5][3]     -5120       12. RtmFWUprBoundY_MtrNm_s4p11[5][3]     -6144       12. RtmFWUprBoundY_MtrNm_s4p11[5][6]     -6144       12. RtmFWUprBoundY_MtrNm_s4p11[5][6]     -6144       12. RtmFWUprBoundY_MtrNm_s4p11[5][6]     -6144       12. RtmFWUprBoundY_MtrNm_s4p11[5][6]     -6144       12. RtmFWU	t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
12   RtmFWUprBoundY_MtrNm_s4p11[3] 8    14336   1433		
1.2. RtmFWUprBoundY_MtrNm_s4p11[3][9]   14336   16384   1638		
12_RtmFWUprBoundY_MtrNm_s4p11[3][10]   16384     12_RtmFWUprBoundY_MtrNm_s4p11[4][0]   -16384     12_RtmFWUprBoundY_MtrNm_s4p11[4][1]   -15300     12_RtmFWUprBoundY_MtrNm_s4p11[4][3]   -12800     12_RtmFWUprBoundY_MtrNm_s4p11[4][3]   -12800     12_RtmFWUprBoundY_MtrNm_s4p11[4][4]   -12288     12_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -10240     12_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -8192     12_RtmFWUprBoundY_MtrNm_s4p11[4][7]   -7168     12_RtmFWUprBoundY_MtrNm_s4p11[4][8]   6144     12_RtmFWUprBoundY_MtrNm_s4p11[4][9]   -2048     12_RtmFWUprBoundY_MtrNm_s4p11[4][9]   -2048     12_RtmFWUprBoundY_MtrNm_s4p11[5][0]   2048     12_RtmFWUprBoundY_MtrNm_s4p11[5][1]   3072     12_RtmFWUprBoundY_MtrNm_s4p11[5][1]   3072     12_RtmFWUprBoundY_MtrNm_s4p11[5][2]   4096     12_RtmFWUprBoundY_MtrNm_s4p11[5][2]   4096     12_RtmFWUprBoundY_MtrNm_s4p11[5][3]   5120     12_RtmFWUprBoundY_MtrNm_s4p11[5][3]   5120     12_RtmFWUprBoundY_MtrNm_s4p11[5][6]   6144     14_Rt		
2_RtmFWUprBoundY_MtrNm_s4p11[4][0]   -16384     2_RtmFWUprBoundY_MtrNm_s4p11[4][1]   -15360     2_RtmFWUprBoundY_MtrNm_s4p11[4][2]   -14336     2_RtmFWUprBoundY_MtrNm_s4p11[4][3]   -12288     2_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -10240     2_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -8192     2_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -8192     2_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -4096     2_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -4096     2_RtmFWUprBoundY_MtrNm_s4p11[4][6]   -2048     2_RtmFWUprBoundY_MtrNm_s4p11[5][0]   -2048     2_RtmFWUprBoundY_MtrNm_s4p11[5][0]   -2048     2_RtmFWUprBoundY_MtrNm_s4p11[5][1]   -2048     2_RtmFWUprBoundY_MtrNm_s4p11[5][1]   -2048     2_RtmFWUprBoundY_MtrNm_s4p11[5][1]   -3072     2_RtmFWUprBoundY_MtrNm_s4p11[5][2]   -4096     2_RtmFWUprBoundY_MtrNm_s4p11[5][3]   -5120     2_RtmFWUprBoundY_MtrNm_s4p11[5][6]   -7168     2_RtmFWUprBoundY_M		
12_RtmFWUprBoundY_MtrNm_s4p11[4][1]       -15360         12_RtmFWUprBoundY_MtrNm_s4p11[4][2]       -14336         12_RtmFWUprBoundY_MtrNm_s4p11[4][3]       -12800         12_RtmFWUprBoundY_MtrNm_s4p11[4][4]       -12288         12_RtmFWUprBoundY_MtrNm_s4p11[4][5]       -10240         12_RtmFWUprBoundY_MtrNm_s4p11[4][6]       -8192         12_RtmFWUprBoundY_MtrNm_s4p11[4][7]       -7168         12_RtmFWUprBoundY_MtrNm_s4p11[4][8]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       7168         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       10240         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       1238         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       14336         12_RtmFWUprBoundY_Mt		
12_RtmFWUprBoundY_MtrNm_s4p11[4][2]       -14336         12_RtmFWUprBoundY_MtrNm_s4p11[4][3]       -12800         12_RtmFWUprBoundY_MtrNm_s4p11[4][4]       -12288         12_RtmFWUprBoundY_MtrNm_s4p11[4][6]       -10240         12_RtmFWUprBoundY_MtrNm_s4p11[4][6]       -8192         12_RtmFWUprBoundY_MtrNm_s4p11[4][7]       -7168         12_RtmFWUprBoundY_MtrNm_s4p11[4][8]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtmFWUprBoundY_MtrNm_s4p1[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtmFWUprBoundY_MtrNm_s4p11[5][8]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[6][6]       14336         12_RtmFWUprBoundY_Mt		
12_RtrnFWUprBoundY_MtrNm_s4p11[4] 3    -12800     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 4    -12288     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 5    -10240     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 5    -8192     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 6    -8192     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 8    6144     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 9    -4096     12_RtrnFWUprBoundY_MtrNm_s4p11[4] 10    -2048     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 0    2048     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 1   3072     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 2   4096     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 3    5120     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 3    5120     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 4   6144     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 6   8192     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 6   8192     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 7   10240     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 8   12288     12_RtrnFWUprBoundY_MtrNm_s4p11[5] 9   14336     13_RtrnFWUprBoundY_MtrNm_s4p11[5] 9   14336     13_RtrnFWUprBoundY_Mtr		
12_RtmFWUprBoundY_MtrNm_s4p11[4][4]       -12288         12_RtmFWUprBoundY_MtrNm_s4p11[4][5]       -10240         12_RtmFWUprBoundY_MtrNm_s4p11[4][6]       -8192         12_RtmFWUprBoundY_MtrNm_s4p11[4][7]       -7168         12_RtmFWUprBoundY_MtrNm_s4p11[4][8]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       10240         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       12288         12_RtmFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtmFWUprBoundY_MtrNm_s4p11[6][0]       16384		
12_RtmFWUprBoundY_MtrNm_s4p11[4][6]       -8192         12_RtmFWUprBoundY_MtrNm_s4p11[4][7]       -7168         12_RtmFWUprBoundY_MtrNm_s4p11[4][8]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtmFWUprBoundY_MtrNm_s4p11[4][10]       -2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       7168         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       10240         12_RtmFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtmFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[6][0]       16384         12_RtmFWUprBoundY_MtrNm_s4p11[6][0]       16384		-12288
12_RtrnFWUprBoundY_MtrNm_s4p11[4][7]       -7168         12_RtrnFWUprBoundY_MtrNm_s4p11[4][8]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtrnFWUprBoundY_MtrNm_s4p11[4][10]       -2048         12_RtrnFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtrnFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtrnFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtrnFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       14336         12_RtrnFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384	t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
12_RtmFWUprBoundY_MtrNm_s4p11[4][8]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtmFWUprBoundY_MtrNm_s4p11[4][10]       -2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtmFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtmFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtmFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[4][9]       -4096         12_RtrnFWUprBoundY_MtrNm_s4p11[4][10]       -2048         12_RtrnFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtrnFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtrnFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtrnFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtrnFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
12_RtmFWUprBoundY_MtrNm_s4p11[4][10]       -2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtmFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtmFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtmFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       2048         12_RtmFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtmFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtmFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtmFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtmFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtmFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtmFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtmFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtmFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtmFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtmFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[5][1]       3072         12_RtrnFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtrnFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtrnFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[5][2]       4096         12_RtrnFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtrnFWUprBoundY_MtrNm_s4p11[5][0]       16384         12_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[5][3]       5120         12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         12_RtrnFWUprBoundY_MtrNm_s4p11[5][5]       7168         12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         12_RtrnFWUprBoundY_MtrNm_s4p11[5][7]       10240         12_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtrnFWUprBoundY_MtrNm_s4p11[5][10]       16384         12_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]       6144         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]       7168         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]       10240         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]       16384         t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]       7168         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]       8192         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]       10240         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]       16384         t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]     10240       t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]     12288       t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]     14336       t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]     16384       t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]     -16384		7168
12_RtrnFWUprBoundY_MtrNm_s4p11[5][8]       12288         12_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         12_RtrnFWUprBoundY_MtrNm_s4p11[5][10]       16384         12_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]       14336         t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]       16384         t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]       -16384	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
ız_кингwuprbounuт_мітиm_s4p1т[o][1]  -15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] -14336		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][3] -12800		

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- Trotain novali_1 or r		•	
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2 RtrnFWUprBoundY MtrNm s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2 RtrnFWUprBoundY MtrNm s4p11[7][1]	3072		
t2 RtrnFWUprBoundY MtrNm s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2 RtrnFWUprBoundY MtrNm s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2 RtrnFWUprBoundY MtrNm s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
t RtrnFWUprBoundX HwDeg s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t RtrnFWUprBoundX HwDeg s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t RtrnFWUprBoundX HwDeg s11p4[9]	-3680		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtrnFWVehSpd_Kph_u9p7[0]	65408		
t_RtrnFWVehSpd_Kph_u9p7[1]	65408		
t_RtrnFWVehSpd_Kph_u9p7[2]	65408		
t RtrnFWVehSpd Kph u9p7[3]	65408		
t_RtrnFWVehSpd_Kph_u9p7[4]	65408		
t_RtmFWVehSpd_Kph_u9p7[5]	65408		
t_RtrnFWVehSpd_Kph_u9p7[6]	65408		
t_RtrnFWVehSpd_Kph_u9p7[7]	65408		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	265		
target ReturnFirewall Per1 MEC Counter Cnt enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.85000002		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	67		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I		Sup Cat Inc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall Per1_MEC_Counter_Cnt_enum		_	
target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target ReturnFirewall_Per1_MEC_Counter_ target ReturnFirewall Per1 ReturnCmd Mt		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	<del>-</del>	
		The second secon	Daguit
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.16 (Repeat Count = 1)	<b>√</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048 0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtmFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2 RtrnFWUprBoundY MtrNm s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096 5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtmFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288 14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtmFWUprBoundY_MtrNm_s4p11[5][9] t2_RtmFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtmFW0prBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

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2, RimPWUprBoundY_MthMm_s4p11[7][4]				
2, RmiPWUpfboundY, Minkm_s4p117[13]	Name	Input Value		
2, RimPWUprBoundY_MthMm_s4p11[7][4]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
2, RimFWUprBoundY_Minkm_sep11[7][6]   10240	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
12 RIMFWUprBoundY_Mixhm_s4p11[7][8]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240		
RimFWUpRboundY_MixIm_s4p11[7][9]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288		
2   RtmFWUprBoundY_MtrNm_s4p11[7][10]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336		
RimFWUprBoundX_HwDeg_s11p4[1]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384		
RimFWUprBoundX_HwDeg_s11p4[1]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408		
RimFWUprBoundX_HWDeg_s11p4[2]	t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
RimFWUprBoundX_HWDeg_s11p4[3]	t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
RitnFWUprBoundX_HwDeg_s11p4[4]	t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072		
RimFWUprBoundX_HwDeg_s11p4[5]	t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
E, RrinFWupfBoundX, HwDeg_s11p4[6]       -5696         L, RrinFWUprBoundX, HwDeg_s11p4[8]       -5520         L, RrinFWUprBoundX, HwDeg_s11p4[8]       -3744         L, RrinFWUprBoundX, HwDeg_s11p4[9]       -3660         L, RrinFWUprBoundX, HwDeg_s11p4[10]       -3408         L, RrinFWuprBoundX, Mpu_9p7[1]       2560         L, RrinFWehSpd, Kph_u9p7[2]       6400         L, RrinFWwehSpd, Kph_u9p7[3]       9600         L, RrinFWwehSpd, Kph_u9p7[6]       12800         L, RrinFWwehSpd, Kph_u9p7[7]       12800         L, RrinFWwehSpd, Kph_u9p7[7]       32640         Larget, ReturnFirewall_Per1_Defeat, Return_Svc_Cnt_lgc, value       0         Larget, ReturnFirewall_Per1_HandwheelPosition_HwDeg_132 value       299,450012         Larget, ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value       0         Larget, ReturnFirewall_Per1_HerumGd, MtrNm_132 value       2.01999998         Larget, ReturnFirewall_Per1_VehicleSpeed_Kph_132.value       78.1999969         Larget, ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDeg_132       1282         Larget, ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDeg_132       1282         Larget, ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum       1282         Larget, ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum       1282         Larget	t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
RtmFWUprBoundX_HwDeg_s11p4[7]   .5520       RtmFWUprBoundX_HwDeg_s11p4[8]       RtmFWUprBoundX_HwDeg_s11p4[19]       RtmFWUprBoundX_HwDeg_s11p4[10]       RtmFWUprBoundX_HwDeg_s11p4[10]       RtmFWUehSpd_Kph_u9p7[0]       RtmFWehSpd_Kph_u9p7[1]       RtmFWWehSpd_Kph_u9p7[2]       RtmFWWehSpd_Kph_u9p7[3]       RtmFWWehSpd_Kph_u9p7[3]       RtmFWWehSpd_Kph_u9p7[6]       RtmFWWehSpd_Kph_u9p7[6]       RtmFWWehSpd_Kph_u9p7[7]       RtmFWehSpd_Kph_u9p7[7]       RtmFWehSpd_Kph_u9p7[7]       RtmFWehSpd_Kpd_u9p7[7]       RtmFWehSpd_Kpd_u9p7[8]       RtmFWehSpd_Kpd_u9p7[8]       RtmFWehSpd_Kpd_u9p7[7]       RtmFWehSpd_Kpd_u9p7[8]       RtmFWehSpd	t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
L RimFWUprBoundX_HwDeg_st1p4[8]       -3744         L RimFWUprBoundX_HwDeg_st1p4[9]       -3680         L RimFWUprBoundX_HwDeg_st1p4[10]       -3408         L RimFWWehSpd_Kph_u9p7[0]       0         L RimFWWehSpd_Kph_u9p7[1]       2560         L RimFWWehSpd_Kph_u9p7[3]       9800         L RimFWWehSpd_Kph_u9p7[3]       12800         L RimFWWehSpd_Kph_u9p7[6]       19200         L RimFWWehSpd_Kph_u9p7[6]       25600         L RimFWehSpd_Kph_u9p7[6]       25600         L RimFWehSpd_Kph_u9p7[7]       32640         Larget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value       20         Larget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value       0         Larget_ReturnFirewall_Per1_HandWheelPosition_HwDeg_dS2.value       20.1999998         Larget_ReturnFirewall_Per1_VehicleSpeed_Kph_132.value       20.1999998         Larget_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_Mirkm_f32       21 arget_ReturnFirewall_Per1_LimitedReturn_Mirkm_f32       21 arget_ReturnFirewall_Mirkm_f32         Larget_Ret_Isst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum       1 arget_ReturnFirewall_Mirkm_f32 </td <td>t_RtrnFWUprBoundX_HwDeg_s11p4[6]</td> <td>-5696</td> <td></td> <td></td>	t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
RimFWUprBoundX_HwDeg_s11p4[9]   -3680   -3408   -340	t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
RtmFWUpfBoundX_HwDeg_s11p4[10]   3-3408	t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t_RtmFWVehSpd_Kph_u9p7[0] 0 t_RtmFWVehSpd_Kph_u9p7[1] 2560 t_RtmFWVehSpd_Kph_u9p7[2] 6400 t_RtmFWVehSpd_Kph_u9p7[3] 9600 t_RtmFWVehSpd_Kph_u9p7[4] 12800 t_RtmFWVehSpd_Kph_u9p7[6] 12800 t_RtmFWVehSpd_Kph_u9p7[6] 12800 t_RtmFWVehSpd_Kph_u9p7[6] 12600 t_RtmFWVehSpd_Kph_u9p7[6] 12600 t_RtmFWVehSpd_Kph_u9p7[6] 12600 t_RtmFWVehSpd_Kph_u9p7[6] 12600 t_RtmFWVehSpd_Kph_u9p7[7] 32640 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_Berd_Counter_Cnt_enum.value 100 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 100 target_ReturnFirewall_Per1_VehicleSpeed_Kph_132.value 126099998 target_ReturnFirewall_Per1_VehicleSpeed_Kph_132.value 126099998 target_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Namo Actual Value Expected Value Result Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680		
t_RtmFWVehSpd_Kph_u9p7[1]	t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtmFWVehSpd_Kph_u9p7[2]	t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtmFWVehSpd_Kph_u9p7[3] 9600  t_RtmFWVehSpd_Kph_u9p7[4] 12800  t_RtmFWVehSpd_Kph_u9p7[5] 19200  t_RtmFWVehSpd_Kph_u9p7[6] 25600  t_RtmFWVehSpd_Kph_u9p7[7] 32640  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0  target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value 289.450012  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 20.1999998  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 20.1999998  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 20.1999998  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_NEC_Counter_Cnt_enum 4 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_NEC_Counter_Cnt_enum 4 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_NeticleSpeed_Kph_f32  Name  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  0 0 0	t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtmFWWehSpd_Kph_u9p7[4] 12800  t_RtmFWWehSpd_Kph_u9p7[5] 25600  t_RtmFWWehSpd_Kph_u9p7[6] 25600  t_RtmFWWehSpd_Kph_u9p7[7] 32640  target_RetumFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0  target_RetumFirewall_Per1_MEC_Counter_Cnt_enum.value 1289.450012  target_RetumFirewall_Per1_MEC_Counter_Cnt_enum.value 20199998  target_RetumFirewall_Per1_VehicleSpeed_Kph_f32.value 20199998  target_RetumFirewall_Per1_VehicleSpeed_Kph_f32.value 201999989  target_RetumFirewall_Per1_VehicleSpeed_Kph_f32.value 201999989  target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDe_target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDe_target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 1288  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 1288  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum 12888  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum 12888  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_NeticleSpeed_Kph_f32 12888  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 128888  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1288888  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1288888888888888888888888888888888888	t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtmFWWehSpd_Kph_u9p7[5] 19200  t_RtmFWWehSpd_Kph_u9p7[6] 25600  t_RtmFWWehSpd_Kph_u9p7[7] 32640  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 289.450012  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 20199998  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 201999998  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 201999998  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 201999998  target_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDetatarget_Ret_inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDetatarget_Ret_inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnComd_MtrNm_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnComd_MtrNm_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  target_Ret_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCSta	t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtmFWVehSpd_Kph_u9p7[6]	t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t RtmFWVehSpd_Kph_u9p7[7] 32640  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 289.450012  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 200.  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 200.  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 200.  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 200.  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 200.  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 200.  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 200.  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_Svc_Cnt_lgc 200.  target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.  target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.  target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.  target_Ret_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum 200.  target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.  Name 200.  Actual Value 200.  Expected Value 200.  Result	t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Stat	t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum)  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rec_Call_Ap_Retu	t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_Ret lnst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_Rete_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Netc_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Actual Value  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Rte_Call_Ap_Re	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_Ret Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Rte_Call	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	289.450012		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target Rte Inst Ap ReturnFirewall Per1 Defeat Return Svc Cnt   target_Rte_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDe   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32   target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32   target_Rte_Inst_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)   199   1	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.01999998		
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  0  target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_ReturnFirewall_Per1_Mec_C	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	78.1999969		
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_nt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_nt_T_enum)  0  0	$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
NameActual ValueExpected ValueResultRte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)199199✓Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)11✓Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)00✓	target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 199 199 199  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_I	Cph_f32	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 1 1	Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 0 0	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	<b>✓</b>
12.1. 12.1. 13.32.1. 13.1.	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 2.01999998 ± 0.00048828125 ✓	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.01999998	2.01999998 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.17 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtinFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360 -14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240 8102
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192 -7400
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	

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Totalii ii ovali <u>-</u> i ori			0
Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	313		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.19000006		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	87		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Retur$	target_ReturnFirewall_Per1_HandwheelPosit	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_N	MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

T4 04 0.40 (D4 04 - 4)	.a
Test Step 2.18 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Input Value t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][2] 4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][3] 5120 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[2][4] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][5] 7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][6] 10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][7] 12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][8] 14336  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][9]$ 16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][10] 17408 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][0] -17408 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][1] -16384  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][2]$ -15360 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][3] -14336 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[3][4] -13312 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][5] -12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][6] -10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][7] -8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][8] -6144  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][9]$ -4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][10] -2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][0] 0 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][1] 2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][2] 4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][3] 5120 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][4] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][5] 7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][6] 10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][7] 12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][8] 14336  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][9]$ 16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][10] 17408 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][0] -17408 t2 RtrnFWUprBoundY MtrNm s4p11[5][1] -16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][2] -15360 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][3] -14336 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[5][4] -13312 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][5] -12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][6] -10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][7] -8192 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[5][8] -6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][9] -4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][10] -2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][0] 0 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][1] 2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][2] 4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][3] 5120 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][4] t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][5] 7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][6] 10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][7] 12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][8] 14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][9] 16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][10] 17408 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][0] -17408 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][1] -16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][2] -15360 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][3] -14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][4] -13312 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][5] -12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][6] -10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][7] -8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][8] -6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][9] -4096 -2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][10]  $t\_RtrnFWUprBoundX\_HwDeg\_s11p4[0]$ 17280 17280 t RtrnFWUprBoundX HwDea s11p4[1] t\_RtrnFWUprBoundX\_HwDeg\_s11p4[2] 17280 t RtrnFWUprBoundX HwDeg s11p4[3] 17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[4] 17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[5] 17280 17280  $t\_RtrnFWUprBoundX\_HwDeg\_s11p4[6]$ t\_RtrnFWUprBoundX\_HwDeg\_s11p4[7] 17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[8] 17280

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	17792		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	30592		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	337		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.3599999		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I target_ReturnFirewall_Per1_Defeat_Return_t		Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_Handw$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	/trNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-8.5	-8.5 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336 -12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240 -8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	0
t_RtrnFWUprBoundX_HwDog_s11p4[9]	0 0
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	
t_RtrnFWVehSpd_Kph_u9p7[0] t_RtrnFWVehSpd_Kph_u9p7[1]	128 2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528 9600
t_RtrnFWVehSpd_Kph_u9p7[2] t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[2]	

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Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	361.399994		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.52999997		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54.75		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_N	/ltrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.20 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
2 RtrnFWUprBoundY MtrNm s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2 RtrnFWUprBoundY MtrNm s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
2 RtrnFWUprBoundY MtrNm s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
2 RtrnFWUprBoundY MtrNm s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
12_RtinFWOpiBoundY_MtrNm_s4p11[2][7] 12_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
:2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
:2_RtmFWUprBoundY_MtrNm_s4p11[2][10]	-2048
2_RtmFWUprBoundY_MtrNm_s4p11[2][10] 2_RtmFWUprBoundY_MtrNm_s4p11[3][0]	0
	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408 -16384
P_RtrnFWUprBoundY_MtrNm_s4p11[4][1] P_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360 -15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
_RtrnFWUprBoundX_HwDeg_s11p4[0]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[1]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[2]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[3]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[4]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[5]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[6]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[7]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[8]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[9]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[10]	8000 256
_RtrnFWVehSpd_Kph_u9p7[0] _RtrnFWVehSpd_Kph_u9p7[1]	2816
RtrnFWVehSpd Kph u9p7[2]	6656
_RtrnFWVehSpd_Kph_u9p7[3]	9600
_RtrnFWVehSpd_Kph_u9p7[4]	28416
_RtrnFWVehSpd_Kph_u9p7[5]	32000
_RtrnFWVehSpd_Kph_u9p7[6]	32256
_RtrnFWVehSpd_Kph_u9p7[7]	32640
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	385.519989
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
arget ReturnFirewall Per1 ReturnCmd MtrNm f32.value	2.72000003
arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.8499985
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_u	
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_t	

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	m target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0	0 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.21 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
I2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336		
t2_RtmFWUprBoundY_MtrNm_s4p11[5][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-8000		
t_RtmFWVehSpd_Kph_u9p7[0]	0		
t_RtmFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	409.230011		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.86999989		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12.5		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I	target_ReturnFirewall_Per1_Defeat_Return	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 MEC Counter Cnt enum	target_ReturnFirewall_Per1_MEC_Counter_0	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	<del>-</del>	
			Desuit
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.86999989	2.86999989 ± 0.00048828125	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall t2 RtrnFWUprBoundY MtrNm s4p11[0][0]	target_Rte_Inst_Ap_ReturnFirewall -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312

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ReturnFirewall_Per1		(10)	201Cab
Name	Input Value		
t2 RtrnFWUprBoundY MtrNm s4p11[5][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600 14208		
t_RtrnFWVehSpd_Kph_u9p7[4] t RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVenSpd_Kph_u9p7[6]	19328		
t_RtrnFwVenSpd_Kph_u9p7[7]	19840		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_beleat_Return_svc_cnt_igc.value target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	433		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	3.03999996		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt lac	
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 HandwheelPosition HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	_	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	r coult
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.762695313	-0.762695313 ± 0.00048828125	_
3			

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	•





Test Step 2.23 (Repeat Count = 1)	la de la companya de
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096 -2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtmFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2 RtrnFWUprBoundY MtrNm s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800

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2. RimPrWUpSoundY, Mehm, asks 1186 4   1288   128			• "	
2. RemPVU/pRoundY, Mehm, ask p110 61   1,0940	Name	Input Value		
2. RBHPKUJPSDum/N. Mehm   sep110015   10240	t2 RtrnFWUprBoundY MtrNm s4p11[6][4]	1		
2. RBMPW/UpSoundY, Mehtm. sel-110[9]   5102   12		-10240		
2. RBINFWUyBoundY_MMPm_sep116[8]		-8192		
12, RhirPWUpBoundY_MMPin_sep116[9]   4096		-7168		
12. RIFIFWUPSOWNY MNN's 491   17   17   17   18   18   18   18   1	t2 RtrnFWUprBoundY MtrNm s4p11[6][8]	6144		
12, RinnFWUpRoundY, Minns, sq.9115  10    2048	t2 RtrnFWUprBoundY MtrNm s4p11[6][9]	-4096		
2. RinFPWUpRoundY_MNNm_s4p11[7] 0  2948		-2048		
12, RITH/PW/JDROundY_MNNins_49117[12]   4096   12, RITH/PW/JDROundY_MNNins_49117[13]   5120   12, RITH/PW/JDROundY_MNNins_49117[13]   5120   12, RITH/PW/JDROundY_MNNins_49117[13]   5120   12, RITH/PW/JDROundY_MNNins_49117[13]   10240   12, RITH/PW/JDROundY_MNNins_49117[13]   12288   12, RITH/PW/JDROundY_MNnins_49117[13]   12288   12, RITH/PW/JDROundY_MNnins_49117[13]   13384   12, RITH/PW/JDROundY_MNnins_49117[13]   13384   12, RITH/PW/JDROundY_MNnins_49117[13]   13384   12, RITH/PW/JDROundY_MNnins_49117[13]   13384   12, RITH/PW/JDROundY_MNnins_49117[13]   1328   12, RITH/PW/JDROundY_MNnins_49117[13]   1328   12, RITH/PW/JDROundY_MNins_49117[13]   1328   12, RITH/PW/JDROundY_MNins_49117[13]   1328   12, RITH/PW/JDROundY_MNins_49117[13]   1322   12, RITH/PW/JDROundY_MNins_49117[13]   1408   12, RITH/PW/MINS_400_MNins_49117[13]   1408   12, RITH/PW/MINS_400_MNins_400_MNins_400_MNins_400_MNins_400_MNins_400_MNins_400_MNins_400_M		3072		
2. RimPWUpfboundY_Mnhm_sdp11[7] 6]   6144   2. RimPWUpfboundY_Mnhm_sdp11[7] 5]   7168   2. RimPWUpfboundY_Mnhm_sdp11[7] 7]   10240   2. RimPWUpfboundY_Mnhm_sdp11[7] 7]   10240   2. RimPWUpfboundY_Mnhm_sdp11[7] 9]   1228   2. RimPWUpfboundY_Mnhm_sdp11[7] 9]   14336   2. RimPWUpfboundX_HnDeg_s11[7] 9]   14336   2. RimPWUpfboundX_HnDeg_s11[7] 9   14438   2.	t2 RtrnFWUprBoundY MtrNm s4p11[7][2]	4096		
2. RimPWUpfBoundY_MnNm_sep11[7] 6]   0144   2. RimPWUpfBoundY_MnNm_sep11[7] 5]   7168   2. RimPWUpfBoundY_MnNm_sep11[7] 7]   10240   2. RimPWUpfBoundY_MnNm_sep11[7] 7]   10240   2. RimPWUpfBoundY_MnNm_sep11[7] 9]   1228   3. RimPWUpfBoundY_MnNm_sep11[7] 9]   14336   3. RimPWUpfBoundY_MnNm_sep11[7] 9]   1634   3. RimPWUpfBoundY_MnNm_sep11[7] 9]   1634   3. RimPWUpfBoundY_MnNm_sep11[7] 9]   1634   3. RimPWUpfBoundY_MnNm_sep11[7] 9]   1634   3. RimPWUpfBoundY_MnNm_sep11[7] 9   1636   3. RimPWUpfBoundY_MnNm_sep11[7] 9   1636   3. RimPWUpfBoundY_MnNm_sep11[7] 9   1636   3. RimPWUpfBoundY_MnNm_sep11[7] 9   1408   3. RimPWUpfBoundY_MnNm_sep11[7] 9   1636	t2 RtrnFWUprBoundY MtrNm s4p11[7][3]	5120		
2. RimPWUpBoundY_Minkm_sep11[7][6]   8192				
2. RimFWUprBoundY_Mrh/m_s4p117[19]   10240				
12 Rtm:PWUpBoundY_MtrNm_s4p11[7] 8]				
12 RimFWUprBoundY_MirNm_s4p11[7] 9				
RimFWUpBoundY_MrNm_s4p11[7]  0]				
RimFWUprBoundX_HwDeg_s11p4[1]   352   528				
RimFWUprBoundX_HwDeg_s11p4[2]   528   52				
RtmFWUprBoundX_HwDeg_s11p4[2]   528				
RitmFWUprBoundX_HwDeg_s11p4[3]				
RitnFWUprBoundX, HwDeg_s11p4[4]   880     LRItnFWUprBoundX, HwDeg_s11p4[5]   1056     LRItnFWUprBoundX, HwDeg_s11p4[6]   1232     LRItnFWUprBoundX, HwDeg_s11p4[7]   1408     LRItnFWUprBoundX, HwDeg_s11p4[8]   1584     LRItnFWUprBoundX, HwDeg_s11p4[9]   1616     LRItnFWUprBoundX, HwDeg_s11p4[9]   1616     LRItnFWUprBoundX, HwDeg_s11p4[10]   1952     LRItnFWUprBoundX, HwDeg_s11p4[10]   1952     LRItnFWUprBoundX, HwDeg_s11p4[10]   1952     LRItnFWUprBoundX, HwDeg_s11p4[10]   1408     LRItnFWWehSpd_Kph_u9p7[1]   1408     LRItnFWWehSpd_Kph_u9p7[3]   1408     LRItnFWWehSpd_Kph_u9p7[3]   14208     LRItnFWWehSpd_Kph_u9p7[3]   14208     LRItnFWWehSpd_Kph_u9p7[6]   20736     LRItnFWWehSpd_Kph_u9p7[6]   20736     LRItnFWWehSpd_Kph_u9p7[6]   21248     LRItnFWWehSpd_Kph_u9p7[6]   21248     LRItnFWWehSpd_Kph_u9p7[6]   20736     LR				
RitmFWUprBoundX_HwDeg_s11p4[6]   1232   1				
RitnFWUprBoundX_HwDeg_s11p4[6]   1232     RitnFWUprBoundX_HwDeg_s11p4[7]   1408   1584     RitnFWUprBoundX_HwDeg_s11p4[9]   1616     RitnFWUprBoundX_HwDeg_s11p4[9]   1616     RitnFWUprBoundX_HwDeg_s11p4[10]   1952     RitnFWUprBoundX_HwDeg_s11p4[7]   1408     RitnFWUprBoundX_HwDeg_s12p4[7]   1408     RitnFWUprBoundX_HwD				
RitmFWUprBoundX_HwDeg_s11p4[7]				
L RtmFWUprBoundX_HwDeg_s11p4[8] 1584  L RtmFWUprBoundX_HwDeg_s11p4[9] 1616  L RtmFWUprBoundX_HwDeg_s11p4[10] 1952  L RtmFWVehSpd_Kph_u9p7[0] 128  L RtmFWVehSpd_Kph_u9p7[1] 1408  L RtmFWVehSpd_Kph_u9p7[2] 7808  L RtmFWVehSpd_Kph_u9p7[3] 9728  L RtmFWVehSpd_Kph_u9p7[3] 14208  L RtmFWVehSpd_Kph_u9p7[6] 20608  L RtmFWVehSpd_Kph_u9p7[6] 20736  L RtmFWVehSpd_Kph_u9p7[7] 21248  L RtmFWehSpd_Kph_u9p7[7] 21248  L RtmFWehSpd_Kph_u9p7[8] 21248  L				
RimFWUprBoundX_HwDeg_s11p4[9]   1616   1952   128				
RitnFWUprBoundX_HwDeg_s11p4[10]   1952   1874   197[0]   128   1874   197[0]   128   1874   197[0]   128   1874	, =			
L_RtmFWVehSpd_Kph_u9p7[0]				
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RtmFWVehSpd_Kph_u9p7[2]				
t_RtmFWVehSpd_Kph_u9p7[3] 9728  t_RtmFWVehSpd_Kph_u9p7[4] 14208  t_RtmFWVehSpd_Kph_u9p7[5] 20608  t_RtmFWVehSpd_Kph_u9p7[6] 20736  t_RtmFWVehSpd_Kph_u9p7[7] 21248  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 457  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 2  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32_value 32.1000004  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32_value 32.1000004  target_ReturnFirewall_Per1_ReturnFirewall_Per1_Defeat_ReturnSvc_Cnt_lgc 32.1000004  target_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturnSvc_Cnt_lgc 43.1000004  target_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCond_MtvDe_f32_value 43.1000004  target_ReturnFirewall_Per1_LimitedReturn_MtvDe_f32_value 43.1000004  target_				
t_RtmFWVehSpd_Kph_u9p7[4] 14208  t_RtmFWVehSpd_Kph_u9p7[5] 20608  t_RtmFWVehSpd_Kph_u9p7[6] 20736  t_RtmFWVehSpd_Kph_u9p7[7] 21248  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 2  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 32  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 32  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 32  target_Ret_Inst_Ap_ReturnFirewall_Per1_Defeat_Return_MtrNm_f32 4  target_Rte_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 5  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 4  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum 4  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 4  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 5  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 6  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 6  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 6  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 6  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 6  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Nefc_Counter_Cnt_enum 6  target_Rte_Inst_Ap_ReturnFirewall_Per1				
t_RtmFWVehSpd_Kph_u9p7[5] 20608  t_RtmFWVehSpd_Kph_u9p7[6] 20736  t_RtmFWVehSpd_Kph_u9p7[7] 21248  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 457  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 2  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 3.21000004  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 23  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDet target_Rte_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDet target_Rte_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpee				
t_RtmFWVehSpd_Kph_u9p7[6] 20736  t_RtmFWVehSpd_Kph_u9p7[7] 21248  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 457  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum_value 2  target_ReturnFirewall_Per1_VehicleSpeed_Kph_g32.value 457  target_ReturnFirewall_Per1_VehicleSpeed_Kph_g32.value 2  target_ReturnFirewall_Per1_VehicleSpeed_Kph_g32.value 2  target_ReturnFirewall_Per1_VehicleSpeed_Kph_g32.value 2  target_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc  target_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDetarget_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDetarget_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 2  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum 2  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_g32 2  Name 2  Actual Value Expected Value Result  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tun08) 1 1 99 199				
t RtmFWVehSpd_Kph_u9p7[7] 21248  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 457  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 457  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 3.21000004  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 3.21000004  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 457  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 457  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 457  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 457  target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc 457  target_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc 457  target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 457  target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 457  target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 457  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 457  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 457  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 457  Name				
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value  target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value  2 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  23 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeg target_Rte_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status				
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value 457  target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 2  target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 3.21000004  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 23  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeg target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_enum)  Rec_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rec_Call_Ap_R				
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 2 target_ReturnFirewall_Per1_ReturnCmd_ltrNm_f32.value 23 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 23 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Netc_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Actual Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Stat				
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDet target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_S				
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value  23  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDetatarget_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32  target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re				
target ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt   target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc   target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe   target_ReturnFirewall_Per1_HandwheelPosition_HwDe   target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32   target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum   target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32   target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Net_Counter_Cnt_enum   target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32   target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32   target_ReturnFirewall_Per1_NeturnFirewall_Net				
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Re_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		-		
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name    Actual Value   Expected Value   Result				
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32    Name				
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **ReturnFirewall_Per1_VehicleSpeed_Kph_f32  **Ret			_	
target_Rte_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32  Name  Actual Value  Expected Value  Result  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  0  0				
Name     Actual Value     Expected Value     Result       Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)     199     199     ✓       Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)     1     1     ✓       Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)     0     0     ✓			_	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 199 199 199  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			<u> </u>	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 1 1	Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 0 0	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 3.21000004 3.21000004 ± 0.00048828125 ✓	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.21000004	3.21000004 ± 0.00048828125	<b>~</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.24 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	0

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048 4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144 7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384 17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-17406
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

ReturnFirewall Per1

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Input Value t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][2] 4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][3] 5120 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[7][4] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][5] 7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][6] 10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][7] 12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][8] 14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][9] 16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][10] 17408 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[0] -8000 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[1] -6400 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[2] -5680 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[3] -4000 -2000 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[4] t\_RtrnFWUprBoundX\_HwDeg\_s11p4[5] -1584 -1200 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[6] t\_RtrnFWUprBoundX\_HwDeg\_s11p4[7] -960 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[8] -880 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[9] -800 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[10] -672 t\_RtrnFWVehSpd\_Kph\_u9p7[0] 128 t\_RtrnFWVehSpd\_Kph\_u9p7[1] 1408 t\_RtrnFWVehSpd\_Kph\_u9p7[2] 10368 t\_RtrnFWVehSpd\_Kph\_u9p7[3] 9984 t\_RtrnFWVehSpd\_Kph\_u9p7[4] 14208 t\_RtrnFWVehSpd\_Kph\_u9p7[5] 23168 t\_RtrnFWVehSpd\_Kph\_u9p7[6] 23424 t\_RtrnFWVehSpd\_Kph\_u9p7[7] 24064 target ReturnFirewall Per1 Defeat Return Svc Cnt lgc.value 0  $target\_Return Firewall\_Per1\_Handwheel Position\_HwDeg\_f32.value$ 481.75 target ReturnFirewall Per1 MEC Counter Cnt enum.value target\_ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32.value 3.38000011 target ReturnFirewall Per1 VehicleSpeed Kph f32.value 44.5 target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt | target\_ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_lgc target\_Rte\_inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDe target\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32 \\ target\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32 \\ tar$ target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32 target\_ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32  $target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_VehicleSpeed\_Kph\_f32$ target\_ReturnFirewall\_Per1\_VehicleSpeed\_Kph\_f32 **Actual Value Expected Value** Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) 199 199 Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	<b>✓</b>

3.92919922

3.92919922 ± 0.00048828125

Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

target\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32.value

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ReturnFirewall Per1 Input Value t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][4] -12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][5] -10240 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[1][6] -8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][7] -7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][8] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][9] -4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][10] -2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][0] 2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][1] 3072 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][2] 4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][3] 5120 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][4] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][5] 7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][6] 8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][7] 10240 12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][8] t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][9] 14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][10] 16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][0] -16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][1] -15360 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][2] -14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][3] -12800 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][4] -12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][5] -10240 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[3][6] -8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][7] -7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][8] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][9] -4096 t2 RtrnFWUprBoundY MtrNm s4p11[3][10] -2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][0] 2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][1] 3072 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][2] 4096 t2 RtrnFWUprBoundY MtrNm s4p11[4][3] 5120 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][4] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][5] 7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][6] 8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][7] 10240  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][8]$ 12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][9] 14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][10] 16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][0] -16384 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][1] -15360 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][2] -14336 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][3] -12800  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][4]$ -12288 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][5] -10240 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][6] -8192 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][7] -7168 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][8] 6144 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][9] -4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][10] -2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][0] 2048 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][1] 3072 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][2] 4096 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][3] 5120 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][4] 6144

7168

8192

10240

12288

14336 16384

-16384

-15360

-14336

-12800

-12288

-10240

-8192

-7168

6144

-4096

-2048

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][5]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][6]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][7]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][8]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][9]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][10]  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][0]$ 

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][1]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][2]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][3]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][4]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][5]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][6]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][7]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][8]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][9]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][10]

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	505.25		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.54999995		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	55.2000008		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.54999995	3.54999995 ± 0.00048828125	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Stan 2 26 (Banest Count = 4)	
Test Step 2.26 (Repeat Count = 1)	·
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360 -14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408 -17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168 10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144 4006
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096 -2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-2048 160
t_RtrnFWUprBoundX_HwDeg_s11p4[0] t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtmFWUprBoundX_HwDeg_s11p4[1]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440

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target\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32.value

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2.51074219 ± 0.00048828125

	ı		
Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	529.299988		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.72000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	66.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_Hand$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
$target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

2.51074219

Test Step 2.27 (Repeat Count = 1)	🗸
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
12_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
.2_RtmFWUprBoundY_MtrNm_s4p11[4][9] :2_RtmFWUprBoundY_MtrNm_s4p11[4][10]	17408
	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360 14236
12_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
12_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
2 RtrnFWUprBoundY MtrNm s4p11[7][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
2_RtmFWUprBoundY_MtrNm_s4p11[7][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288 -10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240 9102
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192 -6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
:_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
:_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
:_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952
	128
:_RtrnFWVehSpd_Kph_u9p7[0]	
t_RtrnFWVehSpd_Kph_u9p7[0] t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[1]	6528
t_RtrnFWVehSpd_Kph_u9p7[1] t_RtrnFWVehSpd_Kph_u9p7[2]	
t_RtrnFWVehSpd_Kph_u9p7[1] t_RtrnFWVehSpd_Kph_u9p7[2] t_RtrnFWVehSpd_Kph_u9p7[3]	6528 9600
t_RtrnFWVehSpd_Kph_u9p7[1] t_RtrnFWVehSpd_Kph_u9p7[2]	6528

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Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	19840		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	50		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	7		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HwDerturnFirewall\_Pert1\_HandwheelPosition\_HandwheelPositio$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_N	/ltrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.10009766	5.10009766 ± 0.00048828125	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>~</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2 RtrnFWUprBoundY MtrNm s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2 RtrnFWUprBoundY MtrNm s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2 RtrnFWUprBoundY MtrNm s4p11[1][10]	16384
t2 RtrnFWUprBoundY MtrNm s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2 RtrnFWUprBoundY MtrNm s4p11[2][2]	-14336
t2 RtrnFWUprBoundY MtrNm s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2 RtrnFWUprBoundY MtrNm s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2 RtrnFWUprBoundY MtrNm s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2 RtrnFWUprBoundY MtrNm s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2 RtrnFWUprBoundY MtrNm s4p11[3][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	12288
	,

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2 RtrnFWUprBoundY MtrNm s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	
_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
:_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
:_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
_RtrnFWVehSpd_Kph_u9p7[0]	128
RtrnFWVehSpd_Kph_u9p7[1]	1408
	7808
RtrnFWVehSpd_Kph_u9p7[3]	9728
RtrnFWVehSpd_Kph_u9p7[4]	14208
_RtrnFWVehSpd_Kph_u9p7[5]	20608
_RtrnFWVehSpd_Kph_u9p7[6]	20736
_RtrnFWVehSpd_Kph_u9p7[7]	21248
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
arget_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54
	I target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Deteat_Return_Svc_Cnt_ arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwD	

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	Im_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384 17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288		
t2 RtrnFWUprBoundY MtrNm s4p11[6][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408 128		
t_RtrnFWVehSpd_Kph_u9p7[0] t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[i] t_RtrnFWVehSpd_Kph_u9p7[2]	10368		
t_RtrnFWVehSpd_Kph_u9p7[3]	9984		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t RtrnFWVehSpd Kph u9p7[5]	23168		
t_RtrnFWVehSpd_Kph_u9p7[6]	23424		
t_RtrnFWVehSpd_Kph_u9p7[7]	24064		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	100		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	0		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_	target_ReturnFirewall_Per1_Defeat_Return_	_Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwD	target_ReturnFirewall_Per1_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enur	target_ReturnFirewall_Per1_MEC_Counter_	_Cnt_enum	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32$	target_ReturnFirewall_Per1_ReturnCmd_Mt	rNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	Kph_f32	
		Expected Value	Resu
Name	Actual Value		Itesu
Name  Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	Resu
		199	Resu
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199		i i i i i i i i i i i i i i i i i i i



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.30 (Repeat Count = 1)		•
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target Rte Inst Ap ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360 -14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192	
t2 RtrnFWUprBoundY MtrNm s4p11[3][7]	-7168	
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120	
t2 RtrnFWUprBoundY MtrNm s4p11[4][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168	
t2 RtrnFWUprBoundY MtrNm s4p11[4][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800	

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120 1280		
t_RtrnFWUprBoundX_HwDog_s11p4[7]	1440		
t_RtrnFWUprBoundX_HwDog_s11p4[8]	1600		
t_RtmFWUprBoundX_HwDeg_s11p4[9] t_RtmFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t RtrnFWVehSpd Kph u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target ReturnFirewall Per1 HandwheelPosition HwDeg f32.value	120		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	3		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt Igc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mt		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_l	_	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	. tooul
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	
	I and the second	t and the second	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~





Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384 17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384 -15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtmFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2 RtrnFWUprBoundY MtrNm s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtmFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2 RtrnFWUprBoundY MtrNm s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtmFWUprBoundY_MtrNm_s4p11[5][1]	-17406
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtmFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtmFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtmFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtmFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtmFWUprBoundY_MtrNm_s4p11[6][3]	5120
re_izeru aaobinoonio i _withani _94b i i[o][o]	0120

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Retumpliewali_peri			12CTUOID
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	145.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-2.25		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.25		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HwDerturnFirewall\_Per1\_HandwheelPosition\_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum\_ReturnFirewall\_Retu$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_I	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.59570313	5.59570313 ± 0.00048828125	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	•
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.32 (Repeat Count = 1)	<b>√</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-18022

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-18022 -18022
t2_RtmFWUprBoundY_MtrNm_s4p11[0][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-18022 -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-18022 -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-18022 -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-18022 -18022
t2_RtmFWUprBoundY_MtrNm_s4p11[5][3]	-18022
t2_RtmFWUprBoundY_MtrNm_s4p11[5][4]	-18022
t2_RtmFWUprBoundY_MtrNm_s4p11[5][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-18022

ReturnFirewall Per1

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Input Value t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][2] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][3] -18022 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[7][4] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][5] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][6] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][7] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][8] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][9] -18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][10] -18022 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[0] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[1] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[2] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[3] -17280 -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[4] t\_RtrnFWUprBoundX\_HwDeg\_s11p4[5] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[6] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[7] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[8] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[9] -17280 t\_RtrnFWUprBoundX\_HwDeg\_s11p4[10] -17280 t\_RtrnFWVehSpd\_Kph\_u9p7[0] 0 t\_RtrnFWVehSpd\_Kph\_u9p7[1] 0 t\_RtrnFWVehSpd\_Kph\_u9p7[2] n t\_RtrnFWVehSpd\_Kph\_u9p7[3] 0 t\_RtrnFWVehSpd\_Kph\_u9p7[4] 0 t\_RtrnFWVehSpd\_Kph\_u9p7[5] 0 t\_RtrnFWVehSpd\_Kph\_u9p7[6] 0 t\_RtrnFWVehSpd\_Kph\_u9p7[7] 0 target ReturnFirewall Per1 Defeat Return Svc Cnt lgc.value 0  $target\_Return Firewall\_Per1\_Handwheel Position\_HwDeg\_f32.value$ -900 target ReturnFirewall Per1 MEC Counter Cnt enum.value 0 target\_ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32.value -8.80000019 target ReturnFirewall Per1 VehicleSpeed Kph f32.value target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt | target\_ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_lgc target\_Rte\_inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDe target\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_ReturnFirewall\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32 \\ target\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32 \\ tar$ target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32  $target\_ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32$  $target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_VehicleSpeed\_Kph\_f32$ target\_ReturnFirewall\_Per1\_VehicleSpeed\_Kph\_f32 **Actual Value Expected Value** Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) 199 199 Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) Rte\_Call\_Ap\_ReturnFirewall\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

8.79980469

8.79980469 ± 0.00048828125

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022

target\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32.value

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ReturnFirewall Per1 Input Value t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][4] 18022 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][5] t2 RtrnFWUprBoundY\_MtrNm\_s4p11[1][6] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][7] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][8] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][9] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[1][10] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][0] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][1] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][2] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][3] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][4] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][5] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][6] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][7] 18022 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][8] t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][9] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[2][10] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][0] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][1] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][2] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][3] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][4] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][5] 18022 t2 RtrnFWUprBoundY\_MtrNm\_s4p11[3][6] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][7] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][8] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[3][9] 18022 t2 RtrnFWUprBoundY MtrNm s4p11[3][10] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][0] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][1] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][2] 18022 t2 RtrnFWUprBoundY MtrNm s4p11[4][3] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][4] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][5] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][6] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][7] 18022  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][8]$ 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][9] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[4][10] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][0] 18022 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][1] t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][2] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][3] 18022  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][4]$ 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][5] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][6] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][7] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][8] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][9] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[5][10] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][0] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][1] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][2] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][3] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][4] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][5] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][6] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][7] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][8] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][9] 18022 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[6][10]  $t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][0]$ 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][1] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][2] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][3] 18022 t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][4] 18022

18022

18022

18022

18022

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18022

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][5]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][6]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][7]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][8]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][9]

t2\_RtrnFWUprBoundY\_MtrNm\_s4p11[7][10]



ReturnFirewall\_Per1

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	65408		
t_RtrnFWVehSpd_Kph_u9p7[1]	65408		
t_RtrnFWVehSpd_Kph_u9p7[2]	65408		
t_RtrnFWVehSpd_Kph_u9p7[3]	65408		
t_RtrnFWVehSpd_Kph_u9p7[4]	65408		
t_RtrnFWVehSpd_Kph_u9p7[5]	65408		
t_RtrnFWVehSpd_Kph_u9p7[6]	65408		
t_RtrnFWVehSpd_Kph_u9p7[7]	65408		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Retur$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	<b>~</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>✓</b>

#### **Test Case 3: Path Test**

Specification

Performance Metrics: (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS3.1 1315.00 Cycles TS3.2 1334.00 Cycles TS3.3 1351.00 Cycles TS3.4 1409.00 Cycles TS3.5 794.00 Cycles

Description

Vector Description:

 $TS3.1"((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) \&\& (MECCounter\_Cnt\_T\_enum != ProductionMode)) = False \\ LimitedReturn\_MtrNm\_T\_f32 = Limit\_m(ReturnCmd\_MtrNm\_T\_f32, LwrBound\_MtrNm\_T\_f32, UprBound\_MtrNm\_T\_f32) = True \\ (Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32) > FLT\_EPSILON) = True \\ (Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32 - LimitedRe$ 

TS3.2"((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode)) = False LimitedReturn\_MtrNm\_T\_f32 = Limit\_m(ReturnCmd\_MtrNm\_T\_f32, LwrBound\_MtrNm\_T\_f32, UprBound\_MtrNm\_T\_f32)=False (Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32) > FLT\_EPSILON)= True"
TS3.3"((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode)) = False LimitedReturn\_MtrNm\_T\_f32 = Limit\_m(ReturnCmd\_MtrNm\_T\_f32, LwrBound\_MtrNm\_T\_f32, UprBound\_MtrNm\_T\_f32)=False (Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32) > FLT\_EPSILON)= False"
TS3.4"((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode)) = False LimitedReturn\_MtrNm\_T\_f32 = Limit\_m(ReturnCmd\_MtrNm\_T\_f32, LwrBound\_MtrNm\_T\_f32, UprBound\_MtrNm\_T\_f32)=False (Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32, LyrBound\_MtrNm\_T\_f32, LyrBound\_MtrNm\_T\_f32)=False (Abs\_f32\_m(ReturnCmd\_MtrNm\_T\_f32 - LimitedReturn\_MtrNm\_T\_f32) > FLT\_EPSILON)= True"
TS3.5((DefeatReturnSvc\_Cnt\_T\_lgc != D\_FALSE\_CNT\_LGC) && (MECCounter\_Cnt\_T\_enum != ProductionMode))=True

Test Step 3.1 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12200 -10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-7168 6144
t2_RtmFWOprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2 RtrnFWUprBoundY MtrNm s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048 3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	3072 4096
tz_ktrn=vvoprBoundY_MtrNm_s4p11[6][2] t2_Rtrn=WUprBoundY_MtrNm_s4p11[6][3]	5120
tz_RtrnFwUprBoundY_MtrNm_s4p11[6][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtmFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtmFW0prBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
~	1.02.0

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2 RtrnFWUprBoundY MtrNm s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2 RtrnFWUprBoundY MtrNm s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2 RtrnFWUprBoundY MtrNm s4p11[7][6]	-8192		
t2 RtrnFWUprBoundY MtrNm s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t RtrnFWUprBoundX HwDeg s11p4[0]	160		
t RtrnFWUprBoundX HwDeg s11p4[1]	320		
t RtrnFWUprBoundX HwDeg s11p4[2]	480		
t RtrnFWUprBoundX HwDeg s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t RtrnFWUprBoundX HwDeg s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t RtrnFWUprBoundX HwDeg s11p4[7]	1280		
t RtrnFWUprBoundX HwDeg s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t RtrnFWUprBoundX HwDeg s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t RtrnFWVehSpd Kph u9p7[1]	2560		
t RtrnFWVehSpd Kph u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t RtrnFWVehSpd Kph u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t RtrnFWVehSpd Kph u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target ReturnFirewall Per1 Defeat Return Svc Cnt Igc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	-1		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I		Svc Cnt lac	
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 HandwheelPosition HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 MEC Counter Cnt enum		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mt	· <del>-</del>	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	_	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	- Nosult
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	-
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.39990234	-4.39990234 ± 0.00048828125	
target_ineturn rewall_reri_Limiteureturn_ivitrivin_i32.value	-4.00000204	-4.J333UZ34 I U.UUU400Z01Z3	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>~</b>

Test Step 3.2 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1760		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	3520		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	25472		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	32640		
t_RtrnFWVehSpd_Kph_u9p7[7]	38272		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.200000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_Inst_Return\_Svc\_Cnt\_Inst\_Ret$	_l target_ReturnFirewall_Per1_Defeat_Return	_Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwEarter and the property of the pro$	target_ReturnFirewall_Per1_HandwheelPos	sition_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3000000000000000000000000000000000000$	target_ReturnFirewall_Per1_LimitedReturn_	_MtrNmf32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enurrenter (Counter\_Cnt\_enurrenter) (Counter\_Cnt\_$	target_ReturnFirewall_Per1_MEC_Counter_	_Cnt_enum	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_ReturnCmd\_MtrNm\_f32$	target_ReturnFirewall_Per1_ReturnCmd_M	trNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	Kph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	7.64990234	7.64990234 ± 0.00048828125	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	<b>✓</b>
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>~</b>

Test Step 3.3 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240

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Name	Input Value	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288	
t2_rxtm vvoprBoundY_mitrvm_s4p11[2][5]	-10240	
	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]		
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096	
12_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048	
12_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288	
12_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144	
	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	8192	
12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336	
:2_RtmFWUprBoundY_MtrNm_s4p11[7][9]	16384	
	-8000	
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-1968	
t_RtrnFWUprBoundX_HwDeg_s11p4[1]		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600	
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424	

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ReturnFirewall\_Per1

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_I$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_ReturnFirewall\_Per1\_HandwheelPosition\_HwDeltarget\_ReturnFirewall\_Retur$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32$	target_ReturnFirewall_Per1_LimitedReturn_N	/trNm_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	<b>~</b>

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	3072 4096
t2_RtinFWOprBoundY_MtrNm_s4p11[7][2] t2_RtinFWOprBoundY_MtrNm_s4p11[7][3]	5120
t2 RtrnFWUprBoundY MtrNm s4p11[7][4]	6144
t2 RtrnFWUprBoundY MtrNm s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
t PtrnEMM/obCod Kab u0p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[0] t_RtrnFWVehSpd_Kph_u9p7[1]	1408

 $target\_ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f32.value$ 

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0.259765625 ± 0.00048828125

ReturnFirewall\_Per1

Name	Input Value		
t RtrnFWVehSpd Kph u9p7[2]	7808		
t RtrnFWVehSpd Kph u9p7[3]	9728		
t RtrnFWVehSpd Kph u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	20608		
t_RtrnFWVehSpd_Kph_u9p7[6]	20736		
t_RtrnFWVehSpd_Kph_u9p7[7]	21248		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_LimitedReturn\_MtrNm\_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_N	/ltrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	<b>V</b>

0.259765625

Input Value
target_Rte_Inst_Ap_ReturnFirewall
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ReturnFirewall\_Per1

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[3][5]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[3][7]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
12 RtrnFWUprBoundY MtrNm s4p11[4][5]	18022
	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	
12_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022 18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022
I2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022
I2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022
I2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022
12 RtrnFWUprBoundY MtrNm s4p11[6][10]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[7][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022
2 RtrnFWUprBoundY MtrNm s4p11[7][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022
_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
r_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
:_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280
:_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280
:_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280
_RtrnFWVehSpd_Kph_u9p7[0]	65408
_RtrnFWVehSpd_Kph_u9p7[1]	65408
_RtrnFWVehSpd_Kph_u9p7[2]	65408
:_RtrnFWVehSpd_Kph_u9p7[3]	65408
 _RtrnFWVehSpd_Kph_u9p7[4]	65408
E_RtrnFWVehSpd_Kph_u9p7[5]	65408
t_RtrnFWVehSpd_Kph_u9p7[6]	65408
t_RtrnFWVehSpd_Kph_u9p7[7]	65408
	1
arget ReturnFirewall Per1 Defeat Return Svc Cnt loc value	
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900

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Name	Input Value		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_Defeat\_Return\_Svc\_Cnt\_I$	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
$target\_Rte\_Inst\_Ap\_ReturnFirewall.ReturnFirewall\_Per1\_MEC\_Counter\_Cnt\_enum$	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~