**Integration Manual**

**For**

**Return Firewall**

**VERSION: 1.0**

**DATE: 15-JAN-2015**

**Prepared By:**

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**Revision History**

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 15-Jan-15 | SB |

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# Abbrevations And Acronyms

|  |  |
| --- | --- |
| **Abbreviation** | **Description** |
| DFD | Design functional diagram |
| MDD | Module design Document |
|  | <ADD more to the table if applicable> |
|  |  |
|  |  |

# References

This section lists the title & version of all the documents that are referred for development of this document

|  |  |  |
| --- | --- | --- |
| **Sr. No.** | **Title** | **Version** |
| 1 | SF36 Return Firewall | 005 |

# Dependencies

## SWCs

|  |  |
| --- | --- |
| **Module** | **Required Feature** |
| **None** | <Addition of global data, function>\*. |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be referred. Developer should track the references.

## Global Functions(Non RTE) to be provided to Integration Project

None

# Configuration REQUIREMeNTS

## Build Time Config

|  |  |  |
| --- | --- | --- |
| **Modules** | **Notes** |  |
| **None** |  |  |

## Configuration Files to be provided by Integration Project

Ap\_ReturnFirewall\_Cfg.h for checkpoint enable

## Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Notes** | **SWC** |
| **ReturnFirewallGeneral**/**ReturnFirewallCPEnable** | To enable checkpoints | ReturnFirewall |

## DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| **ISR Name** | **VIM #** | **Priority Dependency** | **Notes** |
| **None** |  |  |  |

## Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| **Constant** | **Notes** | **SWC** |
| **None** |  |  |

# Integration DATAFLOW REQUIREMENTS

## Required Global Data Inputs

HandwheelPosition\_HwDeg\_f32

ReturnCmd\_MtrNm\_f32

VehicleSpeed\_Kph\_f32

Defeat\_Return\_Svc\_Cnt\_lgc

MEC\_Counter\_Cnt\_enum

## Required Global Data Outputs

LimitedReturn\_MtrNm\_f32

## Specific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| **Init** | **Scheduling Requirements** | **Trigger** |
| **None** |  |  |

|  |  |  |
| --- | --- | --- |
| **Runnable** | **Scheduling Requirements** | **Trigger** |
| **ReturnFirewall\_Per1** | triggered on TimingEvent  Disabled in WARMINIT and OFF | 2ms |

**.**

# Memory Map REQUIREMENTS

## Mapping

|  |  |  |
| --- | --- | --- |
| **Memory Section** | **Contents** | **Notes** |
| RETURNFIREWALL\_START\_SEC\_VAR\_CLEARED\_32 |  |  |
| RETURNFIREWALL\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| **Feature** | **RAM** | **ROM** |
| **None** |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| **Block Name** |
| **None** |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| **Block Name** |
| **None** |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Appendix

*<This section is for appendix>*