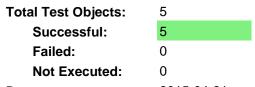
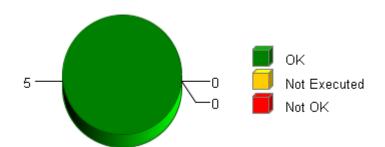


#### **Summary**

## **Overall Test Object Results (including Coverage)**



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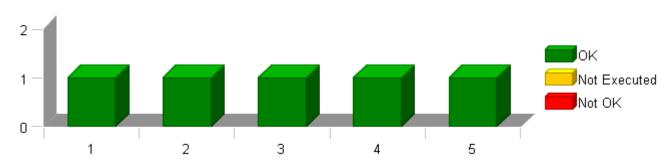
#### **Selected Project Items**

Test Collection "CBD\_UnitTest"

#### **Used Test Environments**

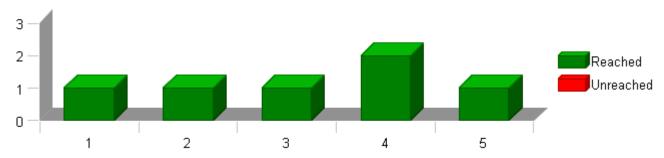
TI TMS 570 PLS UDE (Default)

### **Test Case Results for Each Test Object (without Coverage)**



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

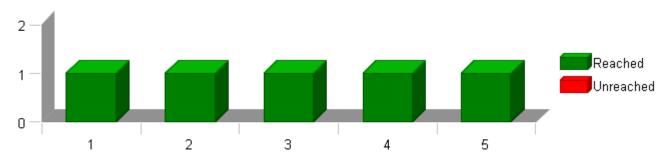
## Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.



# Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

## **TEST OVERVIEW REPORT**

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## **Test Object List**

Project Demlf

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	Test Cases Resu	ult
	Demlf	100 %	100 %	5 of 5 passed	~
	CBD_UnitTest	100 %	100 %	5 of 5 passed	•
	Demlf	100 %	100 %	5 of 5 passed	•
1	Demlf_DemShutdown	100 %	100 %	1 of 1 passed	•
2	DemIf DummyFunction	100 %	100 %	1 of 1 passed	•
3	Demlf RestartDem	100 %	100 %	1 of 1 passed	~
4	Demlf SetEventStatus	100 %	100 %	1 of 1 passed	•
5	Demlf SetOperationCycleState	100 %	100 %	1 of 1 passed	~

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DemIf\_RestartDem

 Project
 Demlf

 Module
 Demlf

 Test Object
 Demlf\_RestartDem

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Project Root Directory D:\Synergy_Work_Area\PSA_DemIf	
Configuration File D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml	
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File \$(PROJECTROOT)\Demlf\src\Ap_Demlf.c	
Compiler Options	-I\$(PROJECTROOT)\Demlflutp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4_\ccsv4\tools \compiler\tms470\include

Name	Text
Module 'Demif'	Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_Demlf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: NAEter EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:""CBD_Sandbox_dbg.map"" Map file is embedded for reference."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	

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DemIf\_RestartDem

## Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 229 Cycles

Description Vector Description:

TS1.1 Only Call trace is checked

### Test Step 1.1 (Repeat Count = 1)

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Dem_Init	1	Dem_Init	1	~

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DemIf\_DummyFunction

Project Demlf
Module Demlf

Test Object Demlf\_DummyFunction

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xn	
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\Demlf\src\Ap_Demlf.c
Compiler Options	-I\$(PROJECTROOT)\Demlflutp\contract -I\$(PROJECTROOT)\NxtrLib\\include -I\$(PROJECTROOT)\StdDef\\include -ID:\ccsv4_\ccsv4\\tools \compiler\tms470\\\include

lame	Text
Jodule 'Demif'	Name of Tester: Madhupama Duarah Code File(s) Under Test: Ap_Demlf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:""CBD_Sandbox_dbg.map"" Map file is embedded for reference."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	

DemIf\_DummyFunction

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## Test Case 1: Boundary Test

**Description** Vector Description:

No inpur output variables present

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Demlf\_DemShutdown

Project Demlf
Module Demlf

Test Object Demlf\_DemShutdown

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf				
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml				
Target Environment	TI TMS 570 PLS UDE (Default)				
Kind of Test	Unit Test				
Linker Options					
Source File(s)					
File	\$(PROJECTROOT)\Demlf\src\Ap_Demlf.c				
Compiler Options	-I\$(PROJECTROOT)\Demlflutp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4_\ccsv4\tools \compiler\tms470\include				

Name	Text
Module 'Demif'	Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_Demlf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:""CBD_Sandbox_dbg.map"" Map file is embedded for reference."

Attributes				
Name	Value			
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl			
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2			
Time Unit	Cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			

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Demlf\_DemShutdown

## Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 226 Cycles

Description Vector Description:

TS1.1 Only Call trace is checked

### Test Step 1.1 (Repeat Count = 1)

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Dem_Shutdown	1	Dem_Shutdown	1	~

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DemIf\_SetEventStatus

Project Demlf
Module Demlf

Test Object DemIf\_SetEventStatus

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf				
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml				
Target Environment	TI TMS 570 PLS UDE (Default)				
Kind of Test	Unit Test				
Linker Options					
Source File(s)					
File	\$(PROJECTROOT)\Demlf\src\Ap_Demlf.c				
Compiler Options	-I\$(PROJECTROOT)\Demlf\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4_\ccsv4\tools \compiler\tms470\include				

Name	Text
Module 'Demif'	Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_Demlf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:""CBD_Sandbox_dbg.map"" Map file is embedded for reference."

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl			
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2			
Time Unit	Cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			

DemIf\_SetEventStatus()



# Test Case 1: Boundary Test Specification Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 240 Cycles TS1.2 240 Cycles TS1.3 240 Cycles TS1.4 240 Cycles TS1.5 240 Cycles TS1.5 240 Cycles TS1.5 240 Cycles TS1.7 240 Cycles TS1.7 240 Cycles TS1.8 240 Cycles TS1.9 240 Cycles TS1.1 240 Cycles

TS1.1 Eventld min
TS1.2 Eventld max
TS1.3 Eventld pos
TS1.4 EventStatus min
TS1.5 EventStatus max
TS1.6 EventStatus pos
TS1.7 Dem\_SetEventStatus=min
TS1.8 Dem\_SetEventStatus=mid
TS1.10 All min
TS1.11 All max

 Test Step 1.1 (Repeat Count = 1)

 Name
 Input Value

 Dem\_SetEventStatus()
 0

 EventId
 0

 EventStatus
 45

 Name
 Actual Value
 Expected Value
 Result

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~

0

0

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
Dem_SetEventStatus()	13		
EventId	255		
EventStatus	67		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	13	13	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Dem SetEventStatus	1	Dem SetEventStatus	1	

Test Step 1.3 (Repeat Count = 1)			
Name	Input Value		
Dem_SetEventStatus()	127		
EventId	90		
EventStatus	78		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	127	127	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~





Test Step 1.4 (Repeat Count = 1)			
Name	Input Value		
Dem_SetEventStatus()	45		
EventId	34		
EventStatus	0		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	45	45	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~

Test Step 1.5 (Repeat Count = 1)			
Name	Input Value		
Dem_SetEventStatus()	116		
EventId	56		
EventStatus	255		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	116	116	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~	

Test Step 1.6 (Repeat Count = 1)			
Name	Input Value		
Dem_SetEventStatus()	31		
EventId	67		
EventStatus	65		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	31	31	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~	

Test Step 1.7 (Repeat Count = 1)			
Name	Input Value		
Dem_SetEventStatus()	0		
EventId	12		
EventStatus	21		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~	

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
Dem_SetEventStatus()	255		
EventId	45		
EventStatus	31		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	255	255	~

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DemIf\_SetEventStatus

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
Dem_SetEventStatus()	113		
EventId	84		
EventStatus	56		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	113	113	~

Test Step Call Trace		<b>✓</b>		
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
Dem_SetEventStatus()	0		
EventId	0		
EventStatus	0		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	0	0	~

Test Step Call Trace		V		
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~

Test Step 1.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Dem_SetEventStatus()	255		
EventId	255		
EventStatus	255		
Name	Actual Value	Expected Value	Result
Demlf_SetEventStatus()	255	255	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	~

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Demlf\_SetOperationCycleState

Project Demlf Module Demlf

Test Object Demlf\_SetOperationCycleState

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\Demif\src\Ap_Demif.c
Compiler Options	-I\$(PROJECTROOT)\Demlf\utp\contract -I\$(PROJECTROOT)\NxtrLib\\include -I\$(PROJECTROOT)\StdDef\\include -ID:\ccsv4_\ccsv4\\tools \compiler\tms470\\include

Name	Text
Module 'Demif'	Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_Demlf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:""CBD_Sandbox_dbg.map"" Map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



# DemIf\_SetOperationCycleState

#### Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 239 Cycles TS1.2 239Cycles TS1.3 239 Cycles TS1.4 239 Cycles TS1.5 239 Cycles TS1.6 239 Cycles TS1.7 239 Cycles TS1.8 239 Cycles

Description Vector Description:

TS1.1 NxtrOperationCycleId min TS1.2 NxtrOperationCycleId max TS1.3 NxtrOperationCycleId pos TS1.4 NxtrCycleState min TS1.5 NxtrCycleState max TS1.6 NxtrCycleState pos TS1.7 All min TS1.8 All max

Test Step 1.1 (Repeat Count = 1)		
Name	Input Value	
NxtrCycleState	45	
NxtrOperationCycleId	0	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	~

Test Step 1.2 (Repeat Count = 1)		✓
Name	Input Value	
NxtrCycleState	67	
NxtrOperationCycleId	255	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem SetOperationCycleState	1	Dem SetOperationCycleState	1	_

Test Step 1.3 (Repeat Count = 1)			
Name	Input Value		
NxtrCycleState	78		
NxtrOperationCycleId	90		

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	~

Test Step 1.4 (Repeat Count = 1)		
Name	Input Value	
NxtrCycleState	0	
NxtrOperationCycleId	34	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Dem SetOperationCycleState	1	Dem SetOperationCycleState	1	

Test Step 1.5 (Repeat Count = 1)	✓
Name	Input Value
NxtrCycleState	255

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Demlf\_SetOperationCycleState

Name	Input Value
NxtrOperationCycleId	56

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	~

Test Step 1.6 (Repeat Count = 1)		
Name	Input Value	
NxtrCycleState	65	
NxtrOperationCycleId	67	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	~

Test Step 1.7 (Repeat Count = 1)		✓
Name	Input Value	
NxtrCycleState	0	
NxtrOperationCycleId	0	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Dem SetOperationCycleState	1	Dem SetOperationCycleState	1	_

Test Step 1.8 (Repeat Count = 1)		
Name	Input Value	
NxtrCycleState	255	
NxtrOperationCvcleId	255	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	•