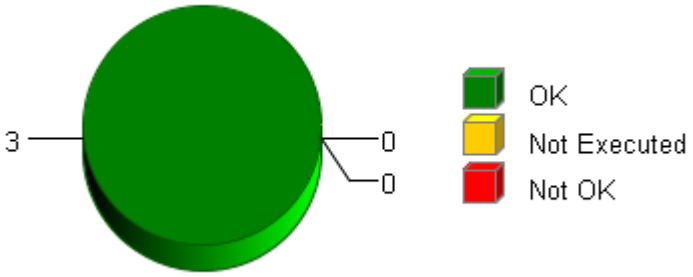
Summary

Total Test Objects:	3
Successful:	3
Failed:	0
Not Executed:	0
Date:	2016-02-25
Time:	13:10:44+0530

Overall Test Object Results (including Coverage)



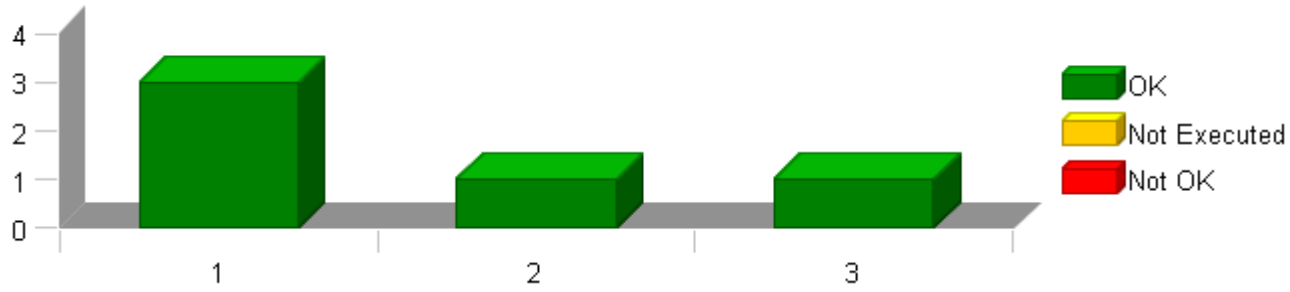
Selected Project Items

Test Object "CBD_UnitTest/Ap_ePWM2/ePWM2_Per1"
Test Object "CBD_UnitTest/Ap_ePWM2/ePWM2_Trns1"
Test Object "CBD_UnitTest/Ap_ePWM2/ePWM2_Trns2"

Used Test Environments

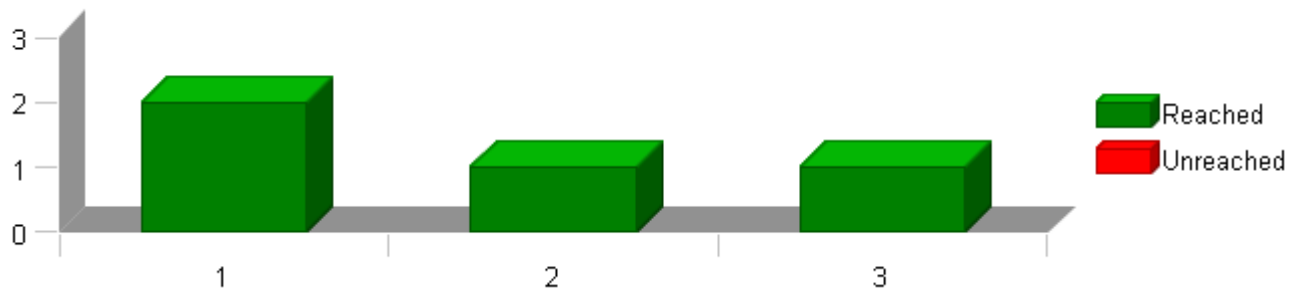
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



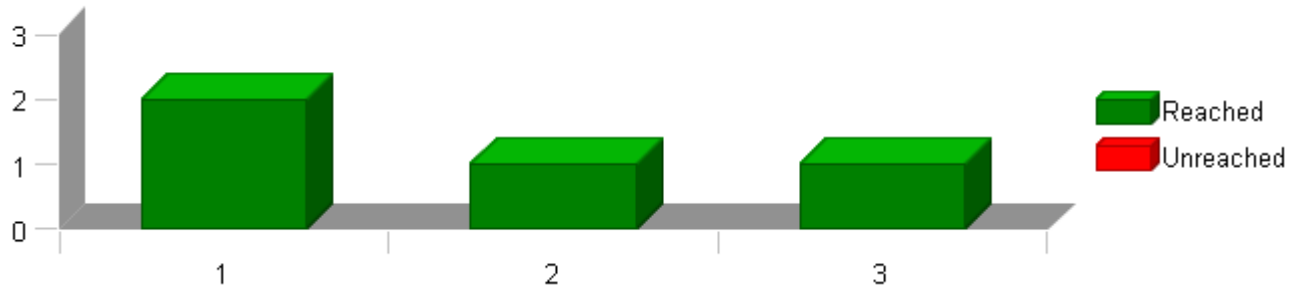
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



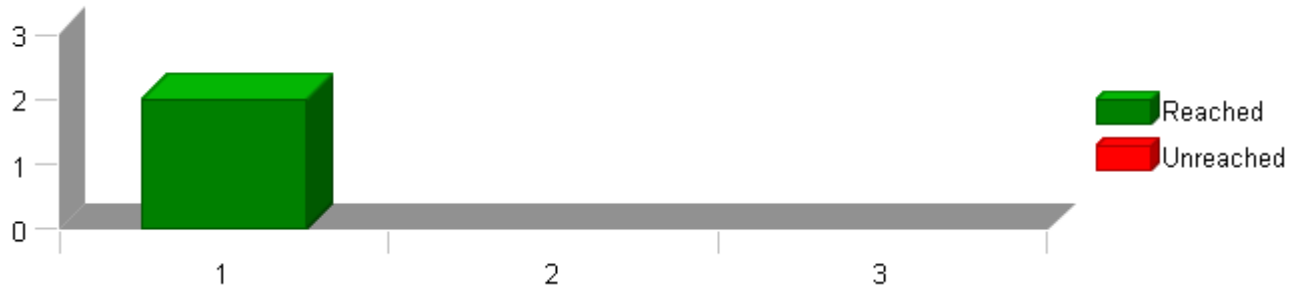
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

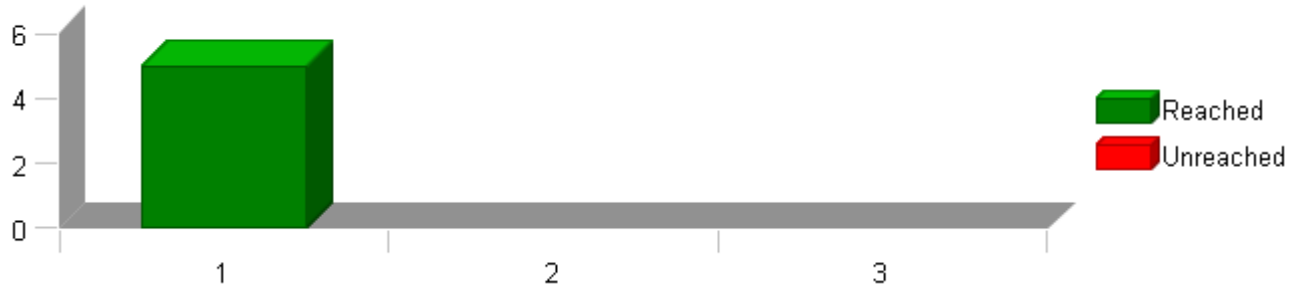
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

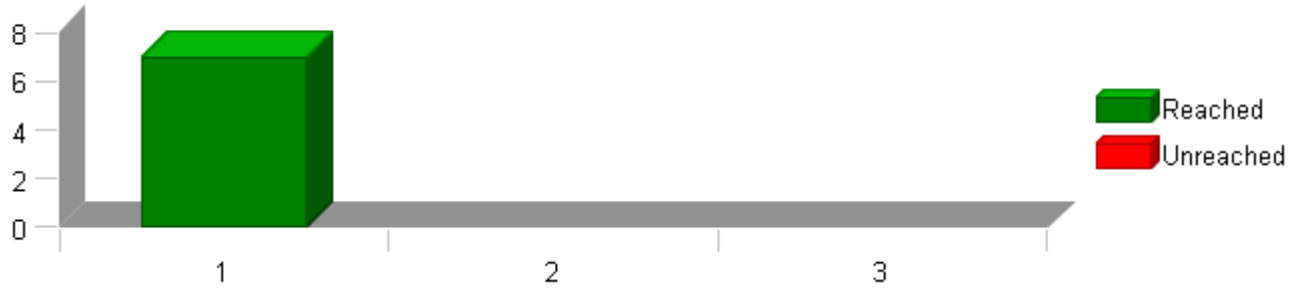
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	Ap_ePWM	100 %	100 %	100 %	100 %	100 %	5 of 5 passed	✔
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	5 of 5 passed	✔
	Ap_ePWM2	100 %	100 %	100 %	100 %	100 %	5 of 5 passed	✔
1	ePWM2_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
2	ePWM2_Trns1	100 %	100 %	-	-	-	1 of 1 passed	✔
3	ePWM2_Trns2	100 %	100 %	-	-	-	1 of 1 passed	✔

TEST DETAILS REPORT

ePWM2_Per1

2016-02-25, 13:09:12+0530

Project	Ap_ePWM
Module	Ap_ePWM2
Test Object	ePWM2_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ePWM_FIASA_326_327
Configuration File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(SOURCEROOT)\ePWM\src\Ap_ePWM2.c
Compiler Options	-D_DATA_ACCESS= -D_STATIC= -D__inline= -Dconst= -I$(SOURCEROOT)\ePWM\utp\contract\Ap_ePWM2 -I$(SOURCEROOT)\ePWM\utp\contract -I$(SOURCEROOT)\ePWM\include -I$(SOURCEROOT)\NxtrLib\include -I$(SOURCEROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'Ap_ePWM2'	**********Unit Test Information********** Name of Tester:Chandrakanth Sheegi Code File(s) Under Test:Ap_ePWM2.c Code File(s) Version:EA3#5 Module Design Document:ePWM_2_MDD.docx Module Design Document Version:EA3#4 Data Dictionary Version:6 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):204 Total RAM Used (Bytes):0 Total CALS Used (Bytes):6 Special Test Requirements:NA Test Date:2/25/2016 Comments:""NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false

Attributes

Name	Value
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

ePWM2_Per1

2016-02-25, 13:09:12+0530

Test Case 1: Metrics test

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)


TS1.1 9.00 Cycles
TS1.2 43.00 Cycles
```

Description

Vector Description:

TS1.1"Shortest Execution Path==>
(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
  || ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"
TS1.2"Longest Execution Path==>
(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
  || ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔

Name	Actual Value	Expected Value	Result
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Case 2: Boundary test ✔

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)

TS2.1 9.00 Cycles
TS2.2 36.00 Cycles
TS2.3 9.00 Cycles
TS2.4 9.00 Cycles
TS2.5 9.00 Cycles
TS2.6 36.00 Cycles
TS2.7 9.00 Cycles
TS2.8 36.00 Cycles
TS2.9 9.00 Cycles
TS2.10 9.00 Cycles
```

Description

Vector Description:

TS2.1All Min
TS2.2All Max
TS2.3DiagStsCtrldDisRmpPres_Cnt_lgc = Min
TS2.4DiagStsCtrldDisRmpPres_Cnt_lgc = Max
TS2.5DiagStsNonRecRmpToZeroFltPres_Cnt_lgc = Min
TS2.6DiagStsNonRecRmpToZeroFltPres_Cnt_lgc = Max
TS2.7RampDwnStatusComplete_Cnt_lgc = Min
TS2.8RampDwnStatusComplete_Cnt_lgc = Max
TS2.9CtrldDmpStsCmp_Cnt_lgc = Min
TS2.10CtrldDmpStsCmp_Cnt_lgc = Max

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.2 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp

Name	Input Value
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp

Name	Input Value
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.6 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc

Name	Input Value
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.7 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc

Name	Input Value
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.9 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.10 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5

Name	Input Value
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Case 3: Path test ✔

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)

TS3.1 9.00 Cycles
TS3.2 9.00 Cycles
TS3.3 11.00 Cycles
TS3.4 36.00 Cycles
TS3.5 43.00 Cycles
TS3.6 10.00 Cycles
TS3.7 43.00 Cycles
```

Description

Vector Description:

TS3.1"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=True"
TS3.2"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"
TS3.3"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"
TS3.4"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=True
"

TS3.5"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"
TS3.6"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"
TS3.7"(((RampDwnStatusComplete_Cnt_T_lgc == TRUE) && (DiagStsNonRecRmpToZeroFltPres_Cnt_T_lgc == TRUE))
|| ((CtrldDmpStsCmp_Cnt_T_lgc == TRUE) && (DiagStsCtrldDisRmpPres_Cnt_T_lgc == TRUE)))=False"

Test Step 3.1 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔

Name	Actual Value	Expected Value	Result
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.2 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.3 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.4 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.5 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	1
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.6 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	1
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.7 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ePWM2	target_Rte_Inst_Ap_ePWM2
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc	target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc	target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cn	target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc
target_Rte_Inst_Ap_ePWM2.ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc	target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc
target_ePWM1_temp.DBCTL	11
target_ePWM1_temp.AQCSFRC	5
target_ePWM2_Per1_CtrldDmpStsCmp_Cnt_lgc.value	1
target_ePWM2_Per1_DiagStsCtrldDisRmpPres_Cnt_lgc.value	0
target_ePWM2_Per1_DiagStsNonRecRmpToZeroFltPres_Cnt_lgc.value	0
target_ePWM2_Per1_RampDwnStatusComplete_Cnt_lgc.value	0
target_ePWM2_temp.DBCTL	11
target_ePWM2_temp.AQCSFRC	5
target_ePWM3_temp.DBCTL	11
target_ePWM3_temp.AQCSFRC	5

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

TEST DETAILS REPORT

ePWM2_Trns2

2016-02-25, 13:10:08+0530

Project	Ap_ePWM
Module	Ap_ePWM2
Test Object	ePWM2_Trns2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ePWM_FIASA_326_327
Configuration File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(SOURCEROOT)\ePWM\src\Ap_ePWM2.c
Compiler Options	-D_DATA_ACCESS= -D_STATIC= -D__inline= -Dconst= -I$(SOURCEROOT)\ePWM\utp\contract\Ap_ePWM2 -I$(SOURCEROOT)\ePWM\utp\contract -I$(SOURCEROOT)\ePWM\include -I$(SOURCEROOT)\NxtrLib\include -I$(SOURCEROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'Ap_ePWM2'	**********Unit Test Information********** Name of Tester:Chandrakanth Sheegi Code File(s) Under Test:Ap_ePWM2.c Code File(s) Version:EA3#5 Module Design Document:ePWM_2_MDD.docx Module Design Document Version:EA3#4 Data Dictionary Version:6 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):204 Total RAM Used (Bytes):0 Total CALS Used (Bytes):6 Special Test Requirements:NA Test Date:2/25/2016 Comments:"NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Attributes

Name	Value
Workspace File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Check for output ✔

Specification `Performance metrics(With "None" Instrumentation and "WithPS" environment)`

`TS1.1 45.00 Cycles`

Description Vector Description:

TS1.1Check for Call Trace

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

TEST DETAILS REPORT

ePWM2_Trns1

2016-02-25, 13:09:45+0530

Project	Ap_ePWM
Module	Ap_ePWM2
Test Object	ePWM2_Trns1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ePWM_FIASA_326_327
Configuration File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(SOURCEROOT)\ePWM\src\Ap_ePWM2.c
Compiler Options	-D_DATA_ACCESS= -D_STATIC= -D__inline= -Dconst= -I$(SOURCEROOT)\ePWM\utp\contract\Ap_ePWM2 -I$(SOURCEROOT)\ePWM\utp\contract -I$(SOURCEROOT)\ePWM\include -I$(SOURCEROOT)\NxtrLib\include -I$(SOURCEROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'Ap_ePWM2'	**********Unit Test Information********** Name of Tester:Chandrakanth Sheegi Code File(s) Under Test:Ap_ePWM2.c Code File(s) Version:EA3#5 Module Design Document:ePWM_2_MDD.docx Module Design Document Version:EA3#4 Data Dictionary Version:6 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):204 Total RAM Used (Bytes):0 Total CALS Used (Bytes):6 Special Test Requirements:NA Test Date:2/25/2016 Comments:"NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Attributes

Name	Value
Workspace File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Check for output ✔

Specification `Performance metrics(With "None" Instrumentation and "WithPS" environment)`

`TS1.1 45.00 Cycles`

Description Vector Description:

TS1.1Check for Call Trace

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	11	11	✔
target_ePWM1_temp.AQCSFRC	0	0	✔
target_ePWM2_temp.DBCTL	11	11	✔
target_ePWM2_temp.AQCSFRC	0	0	✔
target_ePWM3_temp.DBCTL	11	11	✔
target_ePWM3_temp.AQCSFRC	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔