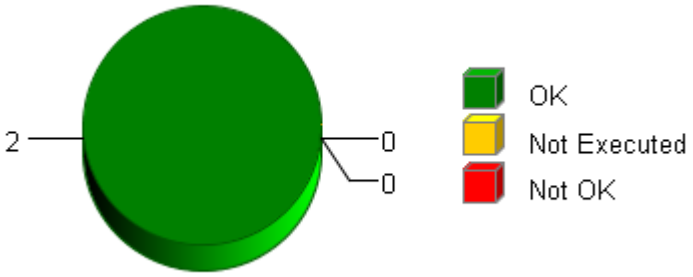
Summary

Total Test Objects:	2
Successful:	2
Failed:	0
Not Executed:	0
Date:	2016-02-25
Time:	13:05:50+0530

Overall Test Object Results (including Coverage)



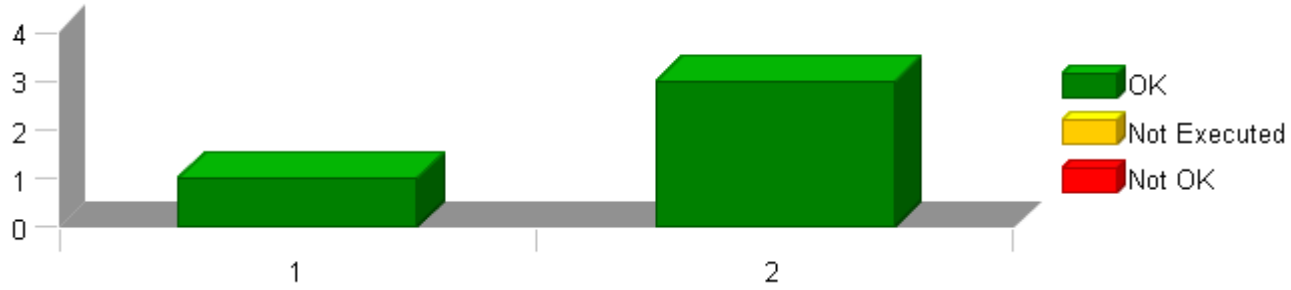
Selected Project Items

Module "CBD_UnitTest/ePWM"

Used Test Environments

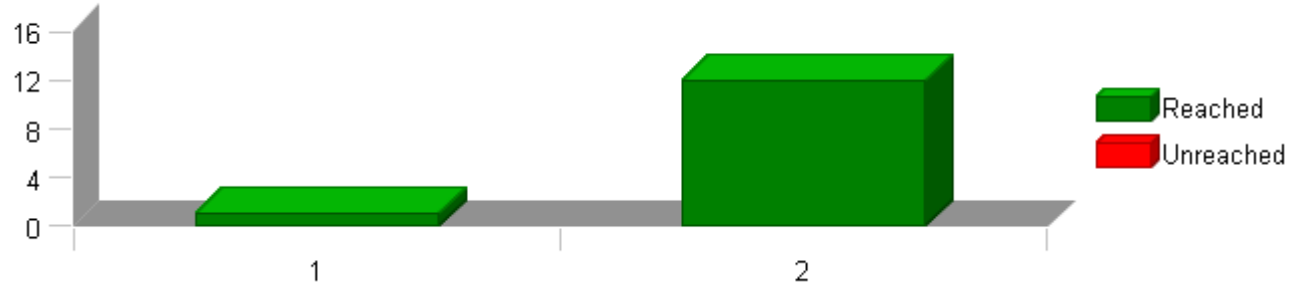
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



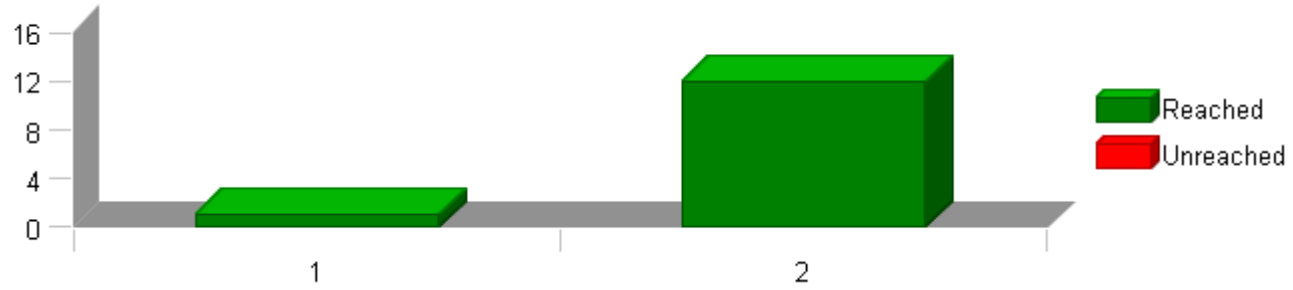
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



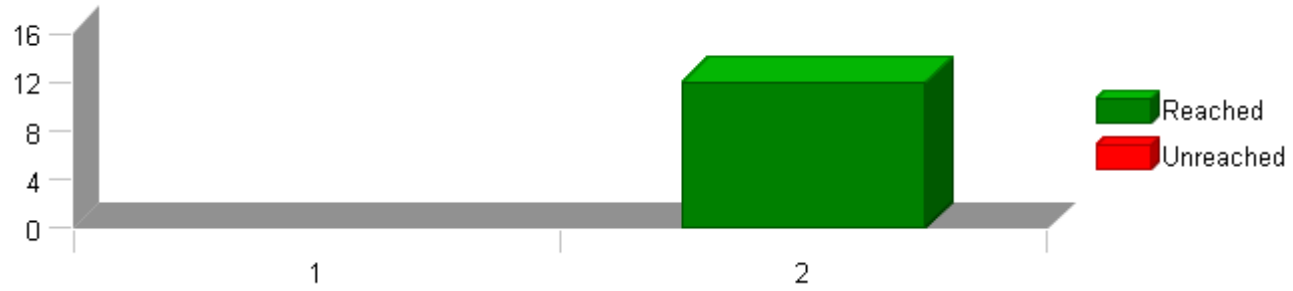
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

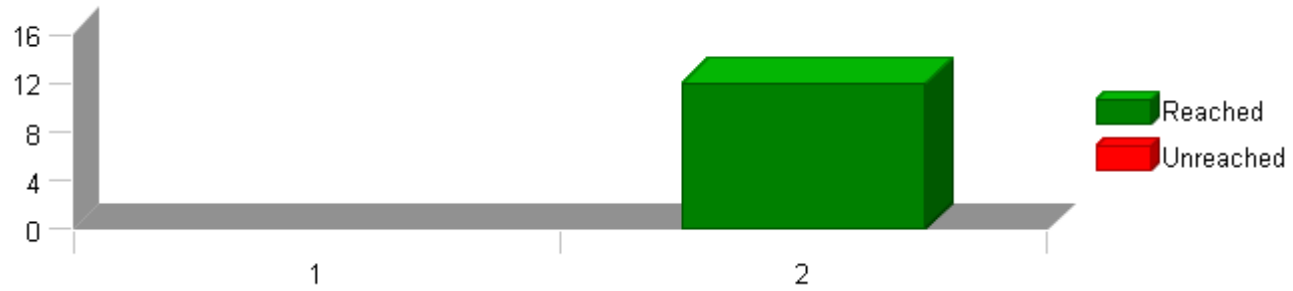
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

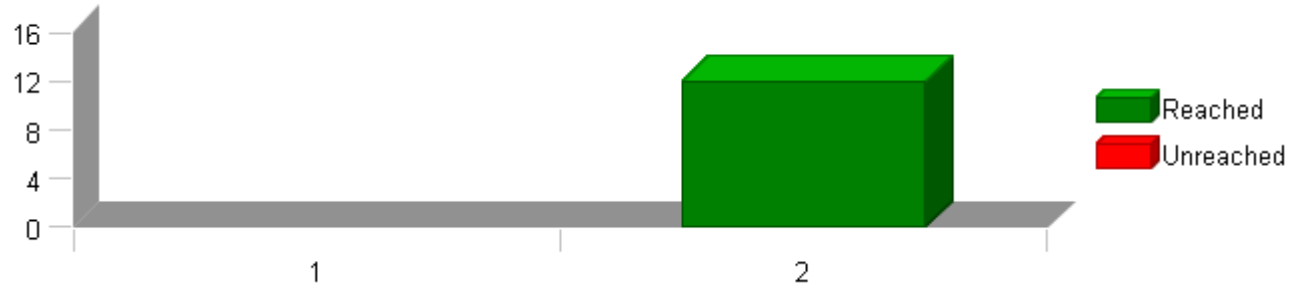
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	Ap_ePWM	100 %	100 %	100 %	100 %	100 %	4 of 4 passed	✔
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	4 of 4 passed	✔
	ePWM	100 %	100 %	100 %	100 %	100 %	4 of 4 passed	✔
1	ePWM_Init1	100 %	100 %	-	-	-	1 of 1 passed	✔
2	ePWM_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔

TEST DETAILS REPORT

ePWM_Init1

2016-02-25, 13:04:09+0530

Project	Ap_ePWM
Module	ePWM
Test Object	ePWM_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ePWM_FIASA_326_327
Configuration File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(SOURCEROOT)\ePWM\src\ePWM.c
Compiler Options	-D_DATA_ACCESS= -D_STATIC= -D__inline= -Dconst= -I$(SOURCEROOT)\ePWM\utp\contract\ePWM -I$(SOURCEROOT)\ePWM\utp\contract -I$(SOURCEROOT)\ePWM\include -I$(SOURCEROOT)\NxtrLib\include -I$(SOURCEROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'ePWM'	**********Unit Test Information********** Name of Tester:Chandrakanth Sheegi Code File(s) Under Test:ePWM.c Code File(s) Version:EA3#6 Module Design Document:ePWM_1_MDD.docx Module Design Document Version:EA3#6 Data Dictionary Version:6 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):944 Total RAM Used (Bytes):0 Total CALS Used (Bytes):6 Special Test Requirements:NA Test Date:2/25/2016 Comments:""NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Attributes

Name	Value
Workspace File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary test

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)

TS 1.1 309.00 Cycles
TS 1.2 309.00 Cycles
TS 1.3 309.00 Cycles
TS 1.4 309.00 Cycles
TS 1.5 309.00 Cycles
TS 1.6 309.00 Cycles
TS 1.7 309.00 Cycles
TS 1.8 309.00 Cycles
```

Description

Vector Description:

TS1.1All min
TS1.2All max
TS1.3k_PwmDeadBand_Cnt_u16==>min
TS1.4k_PwmDeadBand_Cnt_u16==>max
TS1.5k_PwmDeadBand_Cnt_u16==>Pos/Default
TS1.6k_PwmRelay_Cnt_u16==>min
TS1.7k_PwmRelay_Cnt_u16==>max
TS1.8k_PwmRelay_Cnt_u16==>Pos/default

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	0
k_PwmRelay_Cnt_u16	0
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	0	0	✔
target_ePWM1_temp.DBRED	0	0	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	0	0	✔
target_ePWM2_temp.DBRED	0	0	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	0	0	✔
target_ePWM3_temp.DBRED	0	0	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
			✔

Name	Actual Value	Expected Value	Result
target_ePWM3_temp.ETSEL	0	0	
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	0	0	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	1024
k_PwmRelay_Cnt_u16	65535
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	1024	1024	✔
target_ePWM1_temp.DBRED	1024	1024	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	1024	1024	✔
target_ePWM2_temp.DBRED	1024	1024	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔

Name	Actual Value	Expected Value	Result
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	1024	1024	✔
target_ePWM3_temp.DBRED	1024	1024	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	65535	65535	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.3 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	0
k_PwmRelay_Cnt_u16	1025
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	0	0	✔
target_ePWM1_temp.DBRED	0	0	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔

Name	Actual Value	Expected Value	Result
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	0	0	✔
target_ePWM2_temp.DBRED	0	0	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	0	0	✔
target_ePWM3_temp.DBRED	0	0	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	1025	1025	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.4 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	1024
k_PwmRelay_Cnt_u16	625
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔

TEST DETAILS REPORT

ePWM_Init1

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Name	Actual Value	Expected Value	Result
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	1024	1024	✔
target_ePWM1_temp.DBRED	1024	1024	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	1024	1024	✔
target_ePWM2_temp.DBRED	1024	1024	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	1024	1024	✔
target_ePWM3_temp.DBRED	1024	1024	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	625	625	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.5 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	15
k_PwmRelay_Cnt_u16	3214

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Name	Input Value
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	15	15	✔
target_ePWM1_temp.DBRED	15	15	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	15	15	✔
target_ePWM2_temp.DBRED	15	15	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	15	15	✔
target_ePWM3_temp.DBRED	15	15	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	3214	3214	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.6 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	120
k_PwmRelay_Cnt_u16	0
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	120	120	✔
target_ePWM1_temp.DBRED	120	120	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	120	120	✔
target_ePWM2_temp.DBRED	120	120	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	120	120	✔
target_ePWM3_temp.DBRED	120	120	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	0	0	✔
target_ePWM7_temp.AQCTLB	33	33	✔

Name	Actual Value	Expected Value	Result
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.7 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	66
k_PwmRelay_Cnt_u16	65535
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	66	66	✔
target_ePWM1_temp.DBRED	66	66	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	66	66	✔
target_ePWM2_temp.DBRED	66	66	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔
target_ePWM3_temp.DBFED	66	66	✔
target_ePWM3_temp.DBRED	66	66	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔

Name	Actual Value	Expected Value	Result
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	65535	65535	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.8 (Repeat Count = 1) ✔

Name	Input Value
ePWM1_temp	target_ePWM1_temp
ePWM2_temp	target_ePWM2_temp
ePWM3_temp	target_ePWM3_temp
ePWM4_temp	target_ePWM4_temp
ePWM7_temp	target_ePWM7_temp
k_PwmDeadBand_Cnt_u16	485
k_PwmRelay_Cnt_u16	2500
target_ePWM1_temp.DBCTL	11
target_ePWM2_temp.DBCTL	11
target_ePWM3_temp.DBCTL	11

Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✔
target_ePWM1_temp.TBPHS	0	0	✔
target_ePWM1_temp.TBPRD	65535	65535	✔
target_ePWM1_temp.CMPCTL	0	0	✔
target_ePWM1_temp.CMPA	2499	2499	✔
target_ePWM1_temp.AQCTLA	289	289	✔
target_ePWM1_temp.CMPB	2499	2499	✔
target_ePWM1_temp.DBCTL	8	8	✔
target_ePWM1_temp.AQCSFRC	5	5	✔
target_ePWM1_temp.DBFED	485	485	✔
target_ePWM1_temp.DBRED	485	485	✔
target_ePWM1_temp.TZCTL	4095	4095	✔
target_ePWM1_temp.ETSEL	0	0	✔
target_ePWM1_temp.PCCTL	0	0	✔
target_ePWM2_temp.TBCTL	8196	8196	✔
target_ePWM2_temp.TBPHS	0	0	✔
target_ePWM2_temp.TBPRD	65535	65535	✔
target_ePWM2_temp.CMPCTL	0	0	✔
target_ePWM2_temp.CMPA	2499	2499	✔
target_ePWM2_temp.AQCTLA	288	288	✔
target_ePWM2_temp.CMPB	2499	2499	✔
target_ePWM2_temp.DBCTL	8	8	✔
target_ePWM2_temp.AQCSFRC	5	5	✔
target_ePWM2_temp.DBFED	485	485	✔
target_ePWM2_temp.DBRED	485	485	✔
target_ePWM2_temp.TZCTL	4095	4095	✔
target_ePWM2_temp.ETSEL	0	0	✔
target_ePWM2_temp.PCCTL	0	0	✔
target_ePWM3_temp.TBCTL	8196	8196	✔
target_ePWM3_temp.TBPHS	0	0	✔
target_ePWM3_temp.TBPRD	65535	65535	✔
target_ePWM3_temp.CMPCTL	0	0	✔
target_ePWM3_temp.CMPA	2499	2499	✔
target_ePWM3_temp.AQCTLA	288	288	✔
target_ePWM3_temp.CMPB	2499	2499	✔
target_ePWM3_temp.DBCTL	8	8	✔
target_ePWM3_temp.AQCSFRC	5	5	✔

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Name	Actual Value	Expected Value	Result
target_ePWM3_temp.DBFED	485	485	✔
target_ePWM3_temp.DBRED	485	485	✔
target_ePWM3_temp.TZCTL	4095	4095	✔
target_ePWM3_temp.ETSEL	0	0	✔
target_ePWM3_temp.PCCTL	0	0	✔
target_ePWM4_temp.TBCTL	4	4	✔
target_ePWM4_temp.TBPHS	0	0	✔
target_ePWM4_temp.TBPRD	65535	65535	✔
target_ePWM4_temp.CMPCTL	0	0	✔
target_ePWM4_temp.CMPA	2499	2499	✔
target_ePWM4_temp.CMPB	65535	65535	✔
target_ePWM4_temp.DBCTL	0	0	✔
target_ePWM4_temp.TZCTL	4095	4095	✔
target_ePWM4_temp.ETSEL	60416	60416	✔
target_ePWM4_temp.ETPS	4352	4352	✔
target_ePWM4_temp.PCCTL	0	0	✔
target_ePWM7_temp.TBCTL	4	4	✔
target_ePWM7_temp.TBPHS	0	0	✔
target_ePWM7_temp.TBPRD	65535	65535	✔
target_ePWM7_temp.CMPCTL	0	0	✔
target_ePWM7_temp.CMPA	2500	2500	✔
target_ePWM7_temp.AQCTLB	33	33	✔
target_ePWM7_temp.DBCTL	0	0	✔
target_ePWM7_temp.TZCTL	4095	4095	✔
target_ePWM7_temp.ETSEL	0	0	✔
target_ePWM7_temp.PCCTL	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

TEST DETAILS REPORT

ePWM_Per1

2016-02-25, 13:05:16+0530

Project	Ap_ePWM
Module	ePWM
Test Object	ePWM_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ePWM_FIASA_326_327
Configuration File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(SOURCEROOT)\ePWM\src\ePWM.c
Compiler Options	-D_DATA_ACCESS= -D_STATIC= -D__inline= -Dconst= -I$(SOURCEROOT)\ePWM\utp\contract\ePWM -I$(SOURCEROOT)\ePWM\utp\contract -I$(SOURCEROOT)\ePWM\include -I$(SOURCEROOT)\NxtrLib\include -I$(SOURCEROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'ePWM'	**********Unit Test Information********** Name of Tester:Chandrakanth Sheegi Code File(s) Under Test:ePWM.c Code File(s) Version:EA3#6 Module Design Document:ePWM_1_MDD.docx Module Design Document Version:EA3#6 Data Dictionary Version:6 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):944 Total RAM Used (Bytes):0 Total CALS Used (Bytes):6 Special Test Requirements:NA Test Date:2/25/2016 Comments:""NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false

Attributes

Name	Value
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\ePWM_FIASA_326_327\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics test

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)

TS1.1 179.00 Cycles
TS1.2 188.00 Cycles
```

Description

Vector Description:

TS1.1"Shortest Execution Path==>
(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)=False,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)=False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)=True,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)=False,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)=False,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)=False"
TS1.2"Longest Execution Path==>(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>True,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False
"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	4852
DummyVarDCA	5125
DummyVarDCB	1951
DummyVarDCC	4832
DummyVarPeriodIn	5879
k_ADCTrig1Offset_Cnt_s16	129

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5126	5126	✔
DummyVar2A	1429	1429	✔
DummyVar2B	3380	3380	✔
DummyVar3A	1	1	✔
DummyVar3B	4833	4833	✔
DummyVar4A	2275	2275	✔
DummyVar4BIn	4852	4852	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	1226
DummyVarDCA	452
DummyVarDCB	6000
DummyVarDCC	1550
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	607

Name	Actual Value	Expected Value	Result
DummyVar1A	2239	2239	✔
DummyVar1B	2691	2691	✔
DummyVar2A	1	1	✔
DummyVar2B	5999	5999	✔
DummyVar3A	1690	1690	✔
DummyVar3B	3240	3240	✔
DummyVar4A	1858	1858	✔
DummyVar4BIn	1226	1226	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Case 2: Boundary test

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)

TS2.1 188.00 Cycles
TS2.2 179.00 Cycles
TS2.3 185.00 Cycles
TS2.4 185.00 Cycles
TS2.5 182.00 Cycles
TS2.6 182.00 Cycles
TS2.7 182.00 Cycles
TS2.8 182.00 Cycles
TS2.9 188.00 Cycles
TS2.10 185.00 Cycles
TS2.11 188.00 Cycles
TS2.12 185.00 Cycles
TS2.13 182.00 Cycles
TS2.14 188.00 Cycles
TS2.15 188.00 Cycles
TS2.16 188.00 Cycles
TS2.17 188.00 Cycles
TS2.18 188.00 Cycles
TS2.19 188.00 Cycles
TS2.20 182.00 Cycles
```

Description

Vector Description:

TS2.1All min
TS2.2All max
TS2.3PWMPeriod_u16==>Min
TS2.4PWMPeriod_u16==>Max
TS2.5PWMPeriod_u16==>Pos
TS2.6DCPhsAComp_u16==>Min
TS2.7DCPhsAComp_u16==>Max
TS2.8DCPhsAComp_u16==>Pos
TS2.9DCPhsBComp_u16==>Min
TS2.10DCPhsBComp_u16==>Max
TS2.11DCPhsBComp_u16==>Pos
TS2.12DCPhsCComp_u16==>Min
TS2.13DCPhsCComp_u16==>Max
TS2.14DCPhsCComp_u16==>Pos
TS2.15ePWM4CMPB_Cnt_u16==>Min
TS2.16ePWM4CMPB_Cnt_u16==>Max
TS2.17ePWM4CMPB_Cnt_u16==>Pos
TS2.18k_ADCTrig1Offset_Cnt_s16==>Min
TS2.19k_ADCTrig1Offset_Cnt_s16==>Max
TS2.20k_ADCTrig1Offset_Cnt_s16==>Pos/Default

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	0
DummyVarDCA	0
DummyVarDCB	0
DummyVarDCC	0
DummyVarPeriodIn	2000
k_ADCTrig1Offset_Cnt_s16	0

Name	Actual Value	Expected Value	Result
DummyVar1A	465	465	✔
DummyVar1B	465	465	✔
DummyVar2A	465	465	✔
DummyVar2B	465	465	✔
DummyVar3A	465	465	✔
DummyVar3B	465	465	✔
DummyVar4A	465	465	✔
DummyVar4BIn	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.2 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	6000
DummyVarDCA	6000
DummyVarDCB	6000
DummyVarDCC	6000
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	1000

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5999	5999	✔
DummyVar2A	1	1	✔

Name	Actual Value	Expected Value	Result
DummyVar2B	5999	5999	✔
DummyVar3A	1	1	✔
DummyVar3B	5999	5999	✔
DummyVar4A	1465	1465	✔
DummyVar4BIn	6000	6000	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	1252
DummyVarDCA	252
DummyVarDCB	1325
DummyVarDCC	125
DummyVarPeriodIn	2000
k_ADCTrig1Offset_Cnt_s16	125

Name	Actual Value	Expected Value	Result
DummyVar1A	339	339	✔
DummyVar1B	591	591	✔
DummyVar2A	1	1	✔
DummyVar2B	1326	1326	✔
DummyVar3A	402	402	✔
DummyVar3B	527	527	✔
DummyVar4A	340	340	✔
DummyVar4BIn	1252	1252	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	4313
DummyVarDCA	1637
DummyVarDCB	301
DummyVarDCC	5599
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	982

Name	Actual Value	Expected Value	Result
DummyVar1A	1646	1646	✔
DummyVar1B	3283	3283	✔
DummyVar2A	2314	2314	✔
DummyVar2B	2615	2615	✔
DummyVar3A	1	1	✔
DummyVar3B	5600	5600	✔
DummyVar4A	1483	1483	✔
DummyVar4BIn	4313	4313	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.5 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	5376
DummyVarDCA	5236
DummyVarDCB	3108
DummyVarDCC	5956
DummyVarPeriodIn	5957
k_ADCTrig1Offset_Cnt_s16	312

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5237	5237	✔
DummyVar2A	889	889	✔
DummyVar2B	3997	3997	✔
DummyVar3A	1	1	✔
DummyVar3B	5956	5956	✔
DummyVar4A	2131	2131	✔
DummyVar4BIn	5376	5376	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.6 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	1335
DummyVarDCA	0
DummyVarDCB	4542
DummyVarDCC	5038
DummyVarPeriodIn	5420
k_ADCTrig1Offset_Cnt_s16	240

Name	Actual Value	Expected Value	Result
DummyVar1A	2175	2175	✔
DummyVar1B	2175	2175	✔
DummyVar2A	1	1	✔
DummyVar2B	4543	4543	✔
DummyVar3A	1	1	✔
DummyVar3B	5039	5039	✔
DummyVar4A	1935	1935	✔
DummyVar4BIn	1335	1335	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.7 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	2136
DummyVarDCA	6000
DummyVarDCB	5236
DummyVarDCC	2456
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	879

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5999	5999	✔
DummyVar2A	1	1	✔
DummyVar2B	5237	5237	✔
DummyVar3A	1237	1237	✔
DummyVar3B	3693	3693	✔
DummyVar4A	1586	1586	✔
DummyVar4BIn	2136	2136	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.8 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	4852
DummyVarDCA	5125
DummyVarDCB	1951
DummyVarDCC	4832
DummyVarPeriodIn	5879
k_ADCTrig1Offset_Cnt_s16	129

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5126	5126	✔
DummyVar2A	1429	1429	✔
DummyVar2B	3380	3380	✔
DummyVar3A	1	1	✔
DummyVar3B	4833	4833	✔
DummyVar4A	2275	2275	✔
DummyVar4BIn	4852	4852	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.9 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	2133
DummyVarDCA	1540
DummyVarDCB	0
DummyVarDCC	2520
DummyVarPeriodIn	3665
k_ADCTrig1Offset_Cnt_s16	570

Name	Actual Value	Expected Value	Result
DummyVar1A	527	527	✔
DummyVar1B	2067	2067	✔
DummyVar2A	1297	1297	✔
DummyVar2B	1297	1297	✔
DummyVar3A	37	37	✔
DummyVar3B	2557	2557	✔
DummyVar4A	727	727	✔
DummyVar4BIn	2133	2133	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	1226
DummyVarDCA	452
DummyVarDCB	6000
DummyVarDCC	1550
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	607

Name	Actual Value	Expected Value	Result
DummyVar1A	2239	2239	✔
DummyVar1B	2691	2691	✔
DummyVar2A	1	1	✔
DummyVar2B	5999	5999	✔
DummyVar3A	1690	1690	✔
DummyVar3B	3240	3240	✔
DummyVar4A	1858	1858	✔
DummyVar4BIn	1226	1226	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.11 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	3474
DummyVarDCA	185
DummyVarDCB	3474
DummyVarDCC	1047
DummyVarPeriodIn	5561
k_ADCTrig1Offset_Cnt_s16	610

Name	Actual Value	Expected Value	Result
DummyVar1A	2153	2153	✔
DummyVar1B	2338	2338	✔
DummyVar2A	508	508	✔
DummyVar2B	3982	3982	✔
DummyVar3A	1722	1722	✔
DummyVar3B	2769	2769	✔
DummyVar4A	1635	1635	✔
DummyVar4BIn	3474	3474	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.12 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	3780
DummyVarDCA	1936
DummyVarDCB	5431
DummyVarDCC	0
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	694

Name	Actual Value	Expected Value	Result
DummyVar1A	1497	1497	✔
DummyVar1B	3433	3433	✔
DummyVar2A	1	1	✔
DummyVar2B	5432	5432	✔
DummyVar3A	2465	2465	✔
DummyVar3B	2465	2465	✔
DummyVar4A	1771	1771	✔
DummyVar4BIn	3780	3780	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.13 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	122
DummyVarDCA	126
DummyVarDCB	6000
DummyVarDCC	6000
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	850

Name	Actual Value	Expected Value	Result
DummyVar1A	2402	2402	✔
DummyVar1B	2528	2528	✔
DummyVar2A	1	1	✔
DummyVar2B	5999	5999	✔
DummyVar3A	1	1	✔
DummyVar3B	5999	5999	✔

Name	Actual Value	Expected Value	Result
DummyVar4A	1615	1615	✔
DummyVar4BIn	122	122	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.14 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	365
DummyVarDCA	1783
DummyVarDCB	2500
DummyVarDCC	3160
DummyVarPeriodIn	4612
k_ADCTrig1Offset_Cnt_s16	97

Name	Actual Value	Expected Value	Result
DummyVar1A	879	879	✔
DummyVar1B	2662	2662	✔
DummyVar2A	521	521	✔
DummyVar2B	3021	3021	✔
DummyVar3A	191	191	✔
DummyVar3B	3351	3351	✔
DummyVar4A	1674	1674	✔
DummyVar4BIn	365	365	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.15 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	0
DummyVarDCA	1411
DummyVarDCB	738
DummyVarDCC	4155
DummyVarPeriodIn	5675
k_ADCTrig1Offset_Cnt_s16	394

Name	Actual Value	Expected Value	Result
DummyVar1A	1597	1597	✔
DummyVar1B	3008	3008	✔
DummyVar2A	1933	1933	✔
DummyVar2B	2671	2671	✔
DummyVar3A	225	225	✔
DummyVar3B	4380	4380	✔
DummyVar4A	1908	1908	✔
DummyVar4BIn	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.16 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	6000
DummyVarDCA	4852
DummyVarDCB	4582
DummyVarDCC	1235
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	823

Name	Actual Value	Expected Value	Result
DummyVar1A	39	39	✔
DummyVar1B	4891	4891	✔
DummyVar2A	174	174	✔

Name	Actual Value	Expected Value	Result
DummyVar2B	4756	4756	✔
DummyVar3A	1847	1847	✔
DummyVar3B	3082	3082	✔
DummyVar4A	1642	1642	✔
DummyVar4BIn	6000	6000	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.17 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	804
DummyVarDCA	1527
DummyVarDCB	4784
DummyVarDCC	658
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	268

Name	Actual Value	Expected Value	Result
DummyVar1A	1701	1701	✔
DummyVar1B	3228	3228	✔
DummyVar2A	73	73	✔
DummyVar2B	4857	4857	✔
DummyVar3A	2136	2136	✔
DummyVar3B	2794	2794	✔
DummyVar4A	2197	2197	✔
DummyVar4BIn	804	804	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.18 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	2549
DummyVarDCA	2043
DummyVarDCB	4178
DummyVarDCC	1743
DummyVarPeriodIn	5910
k_ADCTrig1Offset_Cnt_s16	0

Name	Actual Value	Expected Value	Result
DummyVar1A	1398	1398	✔
DummyVar1B	3441	3441	✔
DummyVar2A	331	331	✔
DummyVar2B	4509	4509	✔
DummyVar3A	1548	1548	✔
DummyVar3B	3291	3291	✔
DummyVar4A	2420	2420	✔
DummyVar4BIn	2549	2549	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.19 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	1122
DummyVarDCA	269
DummyVarDCB	2410
DummyVarDCC	2393
DummyVarPeriodIn	5696
k_ADCTrig1Offset_Cnt_s16	1000

Name	Actual Value	Expected Value	Result
DummyVar1A	2178	2178	✔
DummyVar1B	2447	2447	✔
DummyVar2A	1108	1108	✔
DummyVar2B	3518	3518	✔
DummyVar3A	1116	1116	✔
DummyVar3B	3509	3509	✔
DummyVar4A	1313	1313	✔
DummyVar4BIn	1122	1122	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.20 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	2973
DummyVarDCA	2895
DummyVarDCB	97
DummyVarDCC	3354
DummyVarPeriodIn	3765
k_ADCTrig1Offset_Cnt_s16	50

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	2896	2896	✔
DummyVar2A	1299	1299	✔
DummyVar2B	1396	1396	✔
DummyVar3A	1	1	✔
DummyVar3B	3355	3355	✔
DummyVar4A	1297	1297	✔
DummyVar4BIn	2973	2973	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Case 3: Path test

Specification

```
Performance metrics(With "None" Instrumentation and
"WithPS" environment)

TS3.1 188.00 Cycles
TS3.2 185.00 Cycles
TS3.3 185.00 Cycles
TS3.4 182.00 Cycles
TS3.5 185.00 Cycles
TS3.6 182.00 Cycles
```

Description

Vector Description:

TS3.1"(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False"
TS3.2"(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False"
TS3.3"(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False"
TS3.4"(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False"
TS3.5"(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>True,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False
"
TS3.6"(CompAPhaseA_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM1CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseB_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>True,
(ePWM2CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>False,
(CompAPhaseC_Cnt_T_u16 > D_DUTYCYCLESHIFT_CNT_U16)==>False,
(ePWM3CompB_Cnt_T_u16 > AdjPWMPeriod_Cnt_T_u16)==>True"

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	0
DummyVarDCA	0
DummyVarDCB	0
DummyVarDCC	0
DummyVarPeriodIn	3632
k_ADCTrig1Offset_Cnt_s16	0

Name	Actual Value	Expected Value	Result
DummyVar1A	1281	1281	✔
DummyVar1B	1281	1281	✔
DummyVar2A	1281	1281	✔
DummyVar2B	1281	1281	✔
DummyVar3A	1281	1281	✔
DummyVar3B	1281	1281	✔
DummyVar4A	1281	1281	✔
DummyVar4BIn	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.2 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	0
DummyVarDCA	3215
DummyVarDCB	0
DummyVarDCC	0
DummyVarPeriodIn	3632
k_ADCTrig1Offset_Cnt_s16	0

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	3216	3216	✔
DummyVar2A	1281	1281	✔
DummyVar2B	1281	1281	✔
DummyVar3A	1281	1281	✔
DummyVar3B	1281	1281	✔
DummyVar4A	1281	1281	✔
DummyVar4BIn	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.3 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	0
DummyVarDCA	3215
DummyVarDCB	5252
DummyVarDCC	0
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	0

Name	Actual Value	Expected Value	Result
DummyVar1A	857	857	✔
DummyVar1B	4072	4072	✔
DummyVar2A	1	1	✔
DummyVar2B	5253	5253	✔
DummyVar3A	2465	2465	✔
DummyVar3B	2465	2465	✔
DummyVar4A	2465	2465	✔
DummyVar4BIn	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.4 (Repeat Count = 1) ✔

Name	Input Value
DummyVar4BIn	2136
DummyVarDCA	6000
DummyVarDCB	5236
DummyVarDCC	2456
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	879

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5999	5999	✔
DummyVar2A	1	1	✔
DummyVar2B	5237	5237	✔
DummyVar3A	1237	1237	✔
DummyVar3B	3693	3693	✔
DummyVar4A	1586	1586	✔
DummyVar4BIn	2136	2136	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.5 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	1226
DummyVarDCA	452
DummyVarDCB	6000
DummyVarDCC	1550
DummyVarPeriodIn	6000
k_ADCTrig1Offset_Cnt_s16	607

Name	Actual Value	Expected Value	Result
DummyVar1A	2239	2239	✔
DummyVar1B	2691	2691	✔
DummyVar2A	1	1	✔
DummyVar2B	5999	5999	✔
DummyVar3A	1690	1690	✔
DummyVar3B	3240	3240	✔
DummyVar4A	1858	1858	✔
DummyVar4BIn	1226	1226	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.6 (Repeat Count = 1)

Name	Input Value
DummyVar4BIn	5376
DummyVarDCA	5236
DummyVarDCB	3108
DummyVarDCC	5956
DummyVarPeriodIn	5957
k_ADCTrig1Offset_Cnt_s16	312

Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✔
DummyVar1B	5237	5237	✔
DummyVar2A	889	889	✔
DummyVar2B	3997	3997	✔
DummyVar3A	1	1	✔
DummyVar3B	5956	5956	✔
DummyVar4A	2131	2131	✔
DummyVar4BIn	5376	5376	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔