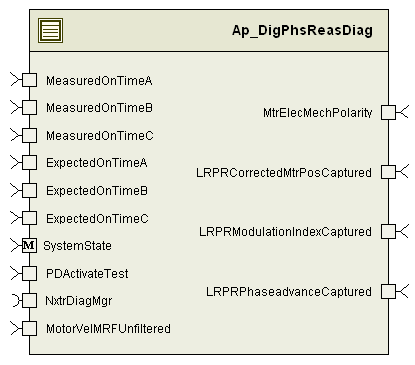
# Module --

# High-Level Description

This module compares the commanded duty cycle to each phase with the feedback from the NHET module. The values are compared, compensated with a previously defined fixed value, filtered, and compared against a valid threshold.

# Figures

## Component Diagram



## Diagram – Function Data Sharing

No Shared Data.

# Variable Data Dictionary

For details on module input / output variable, refer to the Data Dictionary for the application. Input / output variable names are listed here for reference.

|  |  |  |
| --- | --- | --- |
| Module Inputs | Module Outputs | |
| ExpectedOnTimeA\_Cnt\_u32 | |  |
| ExpectedOnTimeB\_Cnt\_u32 | |  |
| ExpectedOnTimeC\_Cnt\_u32 | |  |
| MeasuredOnTimeA\_Cnt\_u32 | |  |
| MeasuredOnTimeB\_Cnt\_u32 | |  |
| MeasuredOnTimeC\_Cnt\_u32 | |  |
| LRPRCorrectedMtrPosCaptured\_rev\_u0p16 | |  |
| LRPRPhaseadvanceCaptured\_Cnt\_s16 | |  |
| LRPRModulationIndexCaptured\_Uls\_f32 | |  |
| MotorVelMRFUnfiltered\_MtrRadpS\_f32 | |  |
| ElecMechPolarity\_Cnt\_s08 | |  |
| PDActivateTest\_Cnt\_lgc | |  |

## Module Internal Variables

This section identifies the name, range and resolutions for module specific data created by this module. If there are no range restrictions on the variable, the term “FULL” is placed into the table for legal range.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Variable Name | Resolution | Legal Range  (min) | Legal Range  (max) | Software Segment |
| SVDiag\_FilterSV\_Cnt\_M\_s18p13[3] | 2-13 | -210000 | 210000 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_32 |
| SVDiag\_PrevLRPRHighSector\_Cnt\_M\_lgc | N/A | FALSE | TRUE | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |
| SVDiag\_LRPRHighSector\_Cnt\_M\_lgc | N/A | FALSE | TRUE | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |
| SVDiag\_PrevLRPRLowSector\_Cnt\_M\_lgc | N/A | FALSE | TRUE | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |
| SVDiag\_LRPRLowSector\_Cnt\_M\_lgc | N/A | FALSE | TRUE | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |
| SVDiag\_LRPRAdjModldAComp\_Cnt\_M\_f32 | Single precision float | 0 | 1 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_FLOAT32 |
| SVDiag\_PrevLRPRAdjModldComp\_Cnt\_M\_f32 | Single precision float | 0 | 1 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_32 |
| SVDiag\_PrevLRPRPhsAdvComp\_Cnt\_M\_u16 | 1 | 0 | 6144 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_16 |
| SVDiag\_LRPRPhsAdvComp\_Cnt\_M\_u16 | 1 | 0 | 6144 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_16 |
| SVDiag\_PhaseOffset\_Rev\_M\_u0p16 | 2-16 | 0 | 1 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_16 |
| SVDiag\_LowPhReasErrorAcc\_Cnt\_M\_u16 | 1 | 0 | 1000 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_16 |
| SVDiag\_HighResPhsReasDisable\_M\_u8 | 1 | 0 | 100 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_8 |
| SVDiag\_LowResPhsReasDisable\_M\_u8 | 1 | 0 | 100 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_8 |
| SVDiag\_MaxNrCommOffVltg\_Cnt\_M\_f32 | Single precision float | 0 | 86400 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_FLOAT32 |
| SVDiag\_LRPRHighLimit\_Cnt\_D\_f32[3] | Single precision float | 0 | 160000 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_FLOAT32 |
| SVDiag\_LRPRLowLimit\_Cnt\_D\_f32[3] | Single precision float | 0 | 160000 | DIGPHSREASDIAG\_START\_SEC\_VAR\_CLEARED\_FLOAT32 |

### User defined typedef definition/declaration

This section documents any user types uniquely used for the module.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Typedef Name | Element Name | User Defined Type | Legal Range  (min) | Legal Range  (max) |
|  |  |  |  |  |

# Constant Data Dictionary

## Calibration Constants

This section lists the calibrations used by the module. For details on calibration constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| k\_PhsReasErrorTerm\_Cnt\_s16 |
| k\_ErrorFiltKn\_Cnt\_u16 |
| k\_ErrorThresh\_Cnt\_u32 |
| k\_PhsReasEnableThresh\_Cnt\_u32 |
| k\_LRPRMtrVelDiagEnable\_MtrRadpS\_T\_f32 |
| k\_LowResPhsReas\_Cnt\_str |
| k\_LowResPhsReasMinTol\_Uls\_f32 |
| k\_LowResPhsReasMaxTol\_Uls\_f32 |
| t\_CommOffsetTblY\_Cnt\_u16 |
| k\_LRPRCommOffsetMargin\_Uls\_f32 |

The time constant for the filter is analyzed in the following filter analysis workbook:



## Program(fixed) Constants

### Embedded Constants

All embedded constants whose values are provided in Eng units will be evaluated to the equivalent counts by using the FPM\_InitFixedPoint\_m() macro within the #define statement.

#### Local

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| D\_ERRORLIMIT\_CNT\_S32 | 8 | Counts | 262136 |
| D\_PHASEA\_CNT\_U16 | 1 | Counts | 0 |
| D\_PHASEB\_CNT\_U16 | 1 | Counts | 1 |
| D\_PHASEC\_CNT\_U16 | 1 | Counts | 2 |
| D\_RESETDIGDIAGACTIVE\_CNT\_U08 | 1 | Counts | 0x00U |
| D\_LRPHSREASLOOPRATE\_CNT\_F32 | 1 | Counts | 0.002F |
| D\_ERRORLIMIT\_CNT\_S32 | 1 | Counts | 262136L/\*2^18-8==262136\*/ |
| D\_PHASEA\_CNT\_U16 | 1 | Counts | 0U |
| D\_PHASEB\_CNT\_U16 | 1 | Counts | 1U |
| D\_PHASEC\_CNT\_U16 | 1 | Counts | 2U |
| D\_NHETFREQUENCY\_HZ\_F32 | Single precision float | Counts | 80000000.0F |
| D\_NHETCTSPERLRPRLOOP\_CNT\_F32 | Single precision float | Counts | (D\_NHETFREQUENCY\_HZ\_F32\*D\_LRPHSREASLOOPRATE\_CNT\_F32) |
| D\_MAXPWMFREQ\_HZ\_U32 | Single precision float | Counts | 18000 |
| D\_MAXNRCOMMOFFVLTG\_CNT\_F32 | Single precision float | Counts | (D\_LRPHSREASLOOPRATE\_CNT\_F32\*D\_MAXPWMFREQ\_CNT\_F32\*t\_CommOffsetTblY\_Cnt\_u16[0]) |
| D\_PHSADVCNTSPERREV\_CNT\_U16 | 1 | Counts | 6144U |
| D\_PHSADVCNTS180DEG\_CNT\_U16 | 1 | Counts | 3072U/\*(D\_PHSADVCNTSPERREV\_CNT\_S16/2U)\*/ |
| D\_PHSPOSADVCNTS90DEG\_CNT\_U16 | 1 | Counts | 1536U/\*(D\_PHSADVCNTSPERREV\_CNT\_S16/4U)\*/ |
| D\_PHSNEGADVCNTS90DEG\_CNT\_S16 | 1 | Counts | -1536/\*(-D\_PHSADVCNTSPERREV\_CNT\_S16/4U)\*/ |
| D\_0DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.0,u0p16\_T)) |
| D\_30DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.0833333333,u0p16\_T)) |
| D\_60DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.1666666666,u0p16\_T)) |
| D\_240DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.6666666666,u0p16\_T)) |
| D\_120DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.3333333333,u0p16\_T)) |
| D\_180DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.5,u0p16\_T)) |
| D\_PHASEAOFFSETNRM\_REV\_U0P16 | 2-16 | rev | D\_0DEG\_REV\_U0P16 |
| D\_PHASEBOFFSETNRM\_REV\_U0P16 | 2-16 | rev | ((uint16)(D\_PHASEAOFFSETNRM\_REV\_U0P16-D\_120DEG\_REV\_U0P16)) |
| D\_PHASECOFFSETNRM\_REV\_U0P16 | 2-16 | rev | (D\_PHASEAOFFSETNRM\_REV\_U0P16+D\_120DEG\_REV\_U0P16) |
| D\_PHASEAOFFSETINV\_REV\_U0P16 | 2-16 | rev | D\_60DEG\_REV\_U0P16 |
| D\_PHASEBOFFSETINV\_REV\_U0P16 | 2-16 | rev | (D\_PHASEAOFFSETINV\_REV\_U0P16+D\_120DEG\_REV\_U0P16) |
| D\_PHASECOFFSETINV\_REV\_U0P16 | 2-16 | rev | ((uint16)(D\_PHASEAOFFSETINV\_REV\_U0P16-D\_120DEG\_REV\_U0P16)) |
| D\_REVPCNT\_ULS\_U0P32 | 1 | Uls | 699051UL/\*(FPM\_InitFixedPoint\_m(1/D\_PACNTSPREV\_ULS\_U16P0,u0p32\_T))\*/ |
| D\_PACNTSPREV\_ULS\_U16P0 | 1 | Uls | 6144U |
| D\_SCALER16\_CNT\_U16 | 1 | cnt | 16U |
| D\_35DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.0972222222,u0p16\_T)) |
| D\_205DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.5972222222,u0p16\_T)) |
| D\_245DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.6805555555,u0p16\_T)) |
| D\_355DEG\_REV\_U0P16 | 1 | rev | (FPM\_InitFixedPoint\_m(0.9861111111,u0p16\_T)) |
| D\_POSITIVEONE\_CNT\_S8 | 1 | cnt | 1 |

#### Global

This section lists the global constants used by the module. For details on global constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| <None> |

### Module specific Lookup Tables Constants

(This is for lookup tables (arrays) with fixed values, same name as other tables)

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Value | Software Segment |
| None |  |  |  |

# Functions/Macros used by the Sub-Modules

## Library Functions / Macros

The library and functions / Macros that are called by the various sub modules are identified below,

1. Limit\_m
2. FPM\_Fix\_m
3. LPF\_SvUpdate\_s16InFixKTrunc\_m
4. LPF\_OpUpdate\_s16InFixKTrunc\_m
5. Max\_m
6. Min\_M
7. Abs\_s16\_m
8. Abs\_f16\_m

## Data Hiding Functions

1. <None>

## Global Functions/Macros Defined by this Module

None

## Local Functions/Macros Used by this MDD only

### Local Function #1

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Function Name** | PhaseGroundTabLookupoffset | Type | Min | Max | UTP Tol. |
| **Arguments Passed** | MtrElecMechPol\_Cnt\_s8 | Sint8 | -1 | 1 |  |
|  |  |  |  |  |  |
| **Return Value** | None |  |  |  |  |

#### Description

### Generates the phase offset Local Function #2

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Function Name** | Read\_CountToRev | Type | Min | Max | UTP Tol. |
| **Arguments Passed** | Var\_Cnt\_T\_u16 | Unit16 | 0 | 6144 |  |
|  |  |  |  |  |  |
| **Return Value** | CountToRev\_Rev\_T\_u16 | Unit16 | 0 | 1 |  |

#### Description

Converts Var\_Cnt\_T\_u16 to Unit16 with range of 0 to 1 and precision of 2-16. It coverts Electrical degree(Count) to Rev

# Software Module Implementation

## Runtime Environment (RTE) Initial Values

This section lists the initial values of data written by this module but controlled by the RTE. After RTE initialization, the data in this table will contain these values.

|  |  |
| --- | --- |
| Data | Value |
| Rte\_InitValue\_ExpectedOnTimeA\_Cnt\_u32 | 0 |
| Rte\_InitValue\_ExpectedOnTimeB\_Cnt\_u32 | 0 |
| Rte\_InitValue\_ExpectedOnTimeC\_Cnt\_u32 | 0 |
| Rte\_InitValue\_MeasuredOnTimeA\_Cnt\_u32 | 0 |
| Rte\_InitValue\_MeasuredOnTimeB\_Cnt\_u32 | 0 |
| Rte\_InitValue\_MeasuredOnTimeC\_Cnt\_u32 | 0 |
| Rte\_InitValue\_LRPRCorrectedMtrPosCaptured\_Rev\_f32 |  |
| Rte\_InitValue\_LRPRModulationIndexCaptured\_Uls\_f32 | 0 |
| Rte\_InitValue\_LRPRPhaseadvanceCaptured\_Cnt\_s16 | 0 |
| Rte\_InitValue\_MotorVelMRFUnfiltered\_MtrRadpS\_f32 |  |
| Rte\_InitValue\_MtrElecMechPolarity\_Cnt\_s08 | 0 |
| Rte\_ InitValue\_ PDActivateTest\_Cnt\_lgc | 0 |

## Initialization Functions

### DigPhsReasDiag\_Init

## Periodic Functions

### Per: DigPhsReasDiag\_ Per1

#### Design Rationale

The ParamBits\_Cnt\_T\_u08 variable used in this function, which is passed when calling NxtrDiagMgr\_SetNTCStatus, is set according to the following table (derived from the DTC Outline Structure document):

|  |  |
| --- | --- |
| Bit 0 | Phase A: measured less than expected |
| Bit 1 | Phase A: measured greater than expected |
| Bit 2 | Phase B: measured less than expected |
| Bit 3 | Phase B: measured greater than expected |
| Bit 4 | Phase C: measured less than expected |
| Bit 5 | Phase C: measured greater than expected |
| Bit6 | Systematic diagnostics LRPR Active |

#### Program Flow Start

Rte\_Call\_DigPhsReasDiag\_Per1\_CP0\_CheckpointReached()

#### Store Module Inputs to Local copies

MeasuredOnTime\_Cnt\_T\_u32[D\_PHASEA\_CNT\_U16] = Rte\_IRead\_DigPhsReasDiag\_Per1\_MeasuredOnTimeA\_Cnt\_u32();

MeasuredOnTime\_Cnt\_T\_u32[D\_PHASEB\_CNT\_U16] = Rte\_Iread\_DigPhsReasDiag\_Per1\_MeasuredOnTimeB\_Cnt\_u32();

MeasuredOnTime\_Cnt\_T\_u32[D\_PHASEC\_CNT\_U16] = Rte\_Iread\_DigPhsReasDiag\_Per1\_MeasuredOnTimeC\_Cnt\_u32();

ExpectedOnTime\_Cnt\_T\_u32[D\_PHASEA\_CNT\_U16] = Rte\_Iread\_DigPhsReasDiag\_Per1\_ExpectedOnTimeA\_Cnt\_u32();

ExpectedOnTime\_Cnt\_T\_u32[D\_PHASEB\_CNT\_U16] = Rte\_Iread\_DigPhsReasDiag\_Per1\_ExpectedOnTimeB\_Cnt\_u32();

ExpectedOnTime\_Cnt\_T\_u32[D\_PHASEC\_CNT\_U16] = Rte\_Iread\_DigPhsReasDiag\_Per1\_ExpectedOnTimeC\_Cnt\_u32();

LRPRMtrPosCaptured\_Rev\_T\_f32=Rte\_IRead\_DigPhsReasDiag\_Per1\_LRPRCorrectedMtrPosCaptured\_Rev\_f32();

LRPRPhaseadvanceCaptured\_Cnt\_T\_s16 =Rte\_IRead\_DigPhsReasDiag\_Per1\_LRPRPhaseadvanceCaptured\_Cnt\_s16();

LRPRModIdx\_Uls\_T\_f32 = Rte\_IRead\_DigPhsReasDiag\_Per1\_LRPRModulationIndexCaptured\_Uls\_f32();

MotorVelMRFUnfilt\_MtrRadpS\_T\_f32 = Rte\_IRead\_DigPhsReasDiag\_Per1\_MotorVelMRFUnfiltered\_MtrRadpS\_f32();

MtrElecMechPolarity\_Cnt\_T\_s08=Rte\_IRead\_DigPhsReasDiag\_Per1\_ElecMechPolarity\_Cnt\_s08();

PDActivateTest\_Cnt\_T\_lgc = Rte\_IRead\_DigPhsReasDiag\_Per1\_PDActivateTest\_Cnt\_lgc();

#### Subfunction Execution







#### Store Local copy of outputs into Module Outputs

None

#### Program Flow End

Rte\_Call\_DigPhsReasDiag\_Per1\_CP1\_CheckpointReached()

### Per: DigPhsReasDiag\_Trans1

#### Design Rationale

Reset the HighRes Phase Reasonableness and LowRes Phase Reasonableness delay counter to 0 upon entering in Operate mode

#### Program Flow Start



## Fault Recovery Functions

None

## Shutdown Functions

None

## Interrupt Functions

None

## Serial Communication Functions

None

# Execution Requirements

## Execution Sequence of the Module

## Execution Rates for sub-modules called by the Scheduler

This table serves as reference for the Scheduler design

|  |  |  |
| --- | --- | --- |
| Function Name | Calling Frequency | System State(s) in which the function is called |
| DigPhsReasDiag\_Per1 | 2 ms | Operate |
| DigPhsReasDiag\_Trans1 | Upon Transition | Upon entering in Operate |

## Execution Requirements for Serial Communication Functions

|  |  |
| --- | --- |
| Function Name | Sub-Module called by (Serial Comm Function Name) |
| <None> |  |

# Memory Map Definition Requirements

## Sub Modules (Functions)

This table identifies the software segments for functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
|  |  |

## Local Functions

This table identifies the software segments for local functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
|  |  |

# Known Issues / Limitations With Design

1. INLINE functions defined in globalmacro.h are not unit tested

# Revision Control Log

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Item #** | **Rev #** | **Change Description** | **Date** | **Author Initials** |
| 1 | 1.0 | Initial Version | 25-Jan-12 | OT |
| 2 | 2.0 | Changed Input Names to Match FDD | 27-Jan-12 | OT |
| 3 | 3.0 | Updated Operation states to show only runs in Operate | 27Jan12 | SMW |
| 4 | 4.0 | Updated to latest FDD changes (rev 002-G), consolidated common processing | 31-Jan-12 | OT |
| 5 | 5 | Added checkpoints and memmap software segment is updated for static variables | 26-Sep-12 | Selva |
| 6 | 6 | Updated for Changes for LRPR | 26 Oct 12 | Selva |
| 7 | 7.0 | Updated to FDD V007 | 15 Mar 13 | SP |
| 8 | 8 | Anamoly 4968,4953 changes | 7-May-13 | Selva |
| 9 | 9 | Anamoly 5000 changes | 13-May-13 | Selva |
| 10 | 10 | Fixing Ranges and Data type for unit test results | 16-May-13 | Selva |
| 11 | 11 | Added Cal “k\_LRPRCommOffsetMargin\_Uls\_f32” for FDD v4 | 26-Jun-13 | Selva |
| 12 | 12 | Corrected anomaly 5174 | 30-Jul-13 | VT |
| 13 | 13 | Update range per unit test results | 12-Aug-13 | VT |
| 14 | 14 | Update module and display variables with SVDiag | 3-Oct-13 | VT |