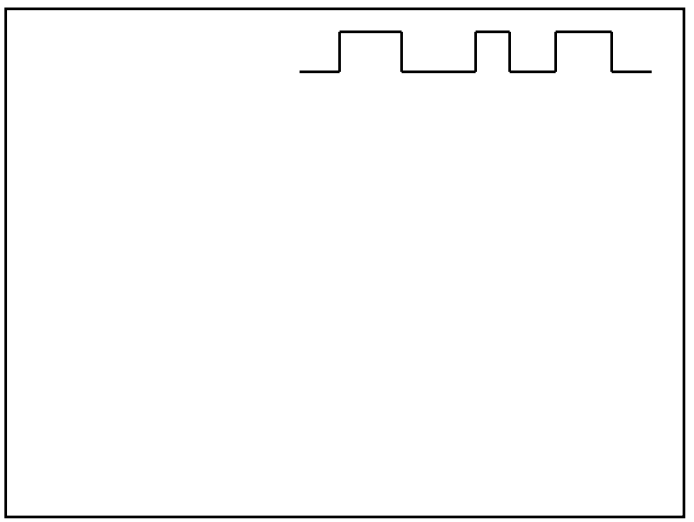

Design Entity - Gate Level Example

Half-Adder

Input signals: x, y

Described

in – input signal
out – output signal
inout – bidirectional signal

Architecture