after Keyword

Signal propagation of the XOR and AND gates must be taken into account.

Both gates require 5 ns propagation delay

signal assignment statements define this through the **after** Keyword.

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The Full-Adder Model

The full-adder simulates the signal transitions at gate-level

Propagation delay

Constant can be used to declare a constant of a particular type.

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A concurrent signal assignment statement (CSA)

In a more general form:

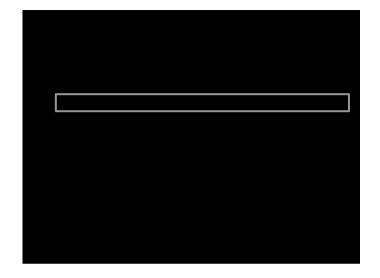
signal <= value expression after time expression;

In the example,

The Time-Value p6.5Trepresents the future value of the signal.

Also called transaction.

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Signals and Time

