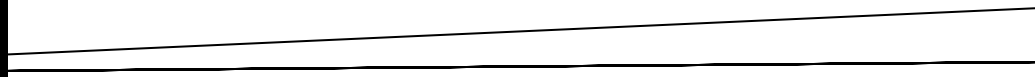


CPU  
MEM  
IO

OS

## CPU Operation (three)

11/11/11



### Address Bus

CPU puts address on the bus, all other other devices read of that bus

### Data Busses

Data: Instructions and Operand information.

One bus takes data from CPU to MEM and I O.

Other bus brings data from I O and MEM to CPU.

Problem - many MEM and I O putting data onto one bus - conflict?

System

