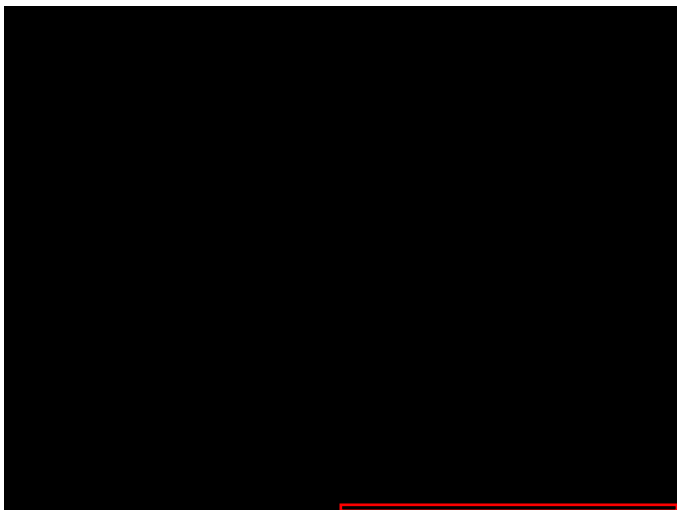


Register Transfer via MUX Diagram



Address register information is transmitted over an address bus to memory

