

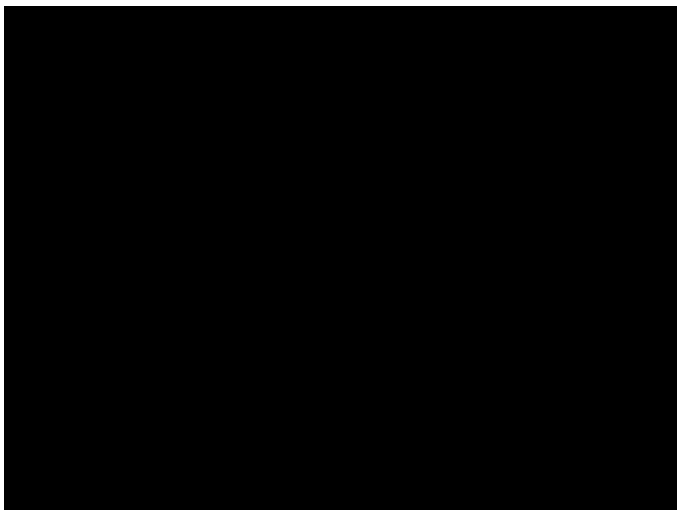
$A1'A0$  to  $B2^1, B2^0$

VMA to  $C2^1$

with  $x = 1, 2$  ( $AC1A^1$  and  $AC1A^2$ )

$C2^{10x}$  from decoder to  $C2^{0x}$  on  $B2221$

$B\backslash W$  from CPU to  $B\backslash W$  on  $B2221$





2BA4

VPA and VMA Generation (1)