

after Keyword

Signal propagation of the XOR and AND gates must be taken into account.

Both gates require 5 ns propagation delay

signal assignment statements define this through the **after** Keyword.

The Full-Adder Model

The full-adder simulates the signal transitions at gate-level

Propagation delay

Constant can be used to declare a constant of a particular type.

A concurrent signal assignment statement (CSA)

In a more general form:

```
signal <= value expression after time expression;
```

In the example,

The Time-value p6.5T represents the future value of the signal.

Also called transaction.



Signals and Time



