Memory M Address

The following address sources are used lo fetch:

PC - Bus A -

This register should be selected through an additional bit control signals:

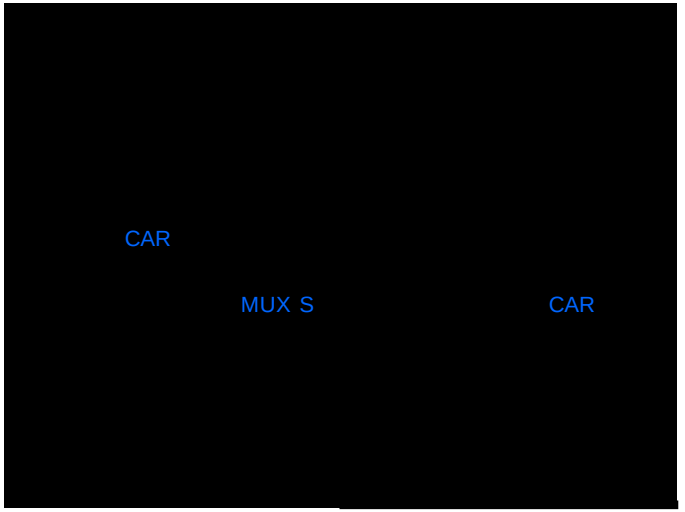
TD, TA, TB

These control signal are to the left of:

SA, SB, DR

TD||DR

PC ← PC + se AD



MUX S determines whether the CAR is:

- Incremented
- Loaded

Sequencer Control Fields