

Logic circuit design derived from Minimal 1.5 project by Carston Herting (Slu4).
<http://minimal-cpu-system.boards.net>
 Some modifications has been made.
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TITLE: Bravo V1.0-M1-Program Counter

REV: 1.0



Company:

Sheet: 1/3

Date: 2021-11-22

Drawn By: Kaveh Majidi (CONFIG IOI)



TITLE: Bravo V1.0-M1-Clock

REV: 1.0

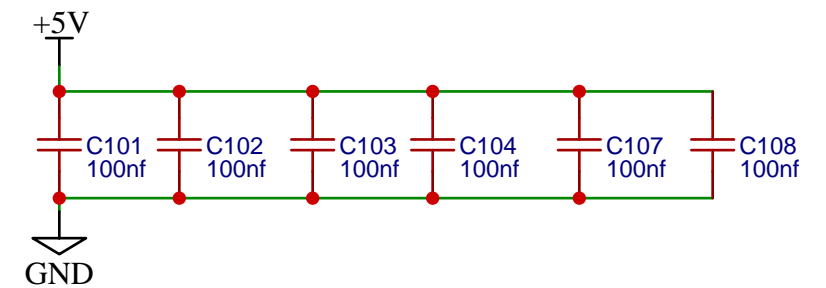
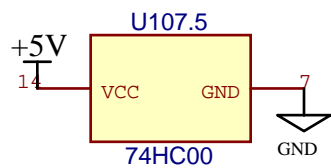
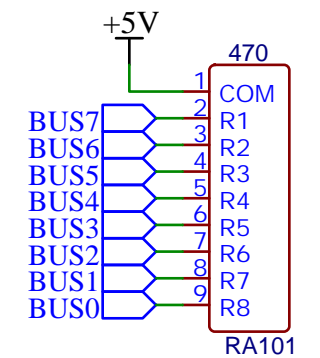
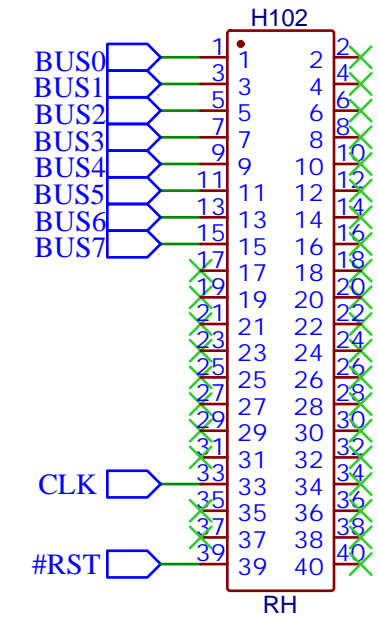
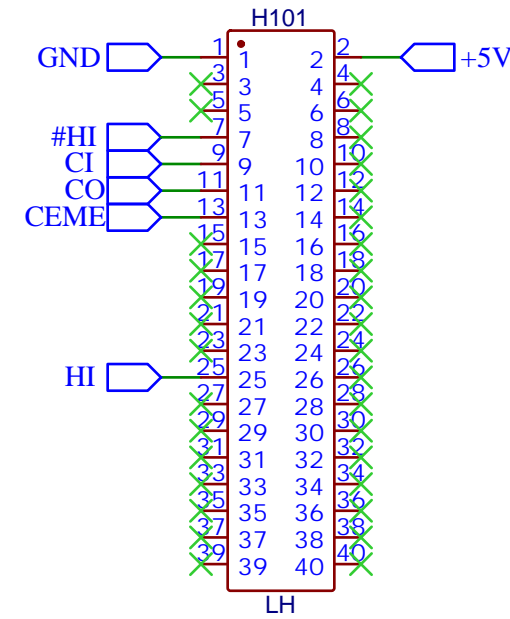
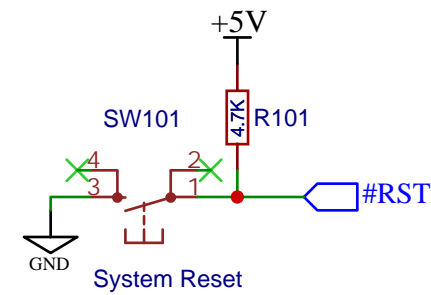


Company:

Sheet: 2/3

Date: 2021-11-23

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TITLE:Bravo V1.0-M1-Common

REV: 1.0



Company:

Sheet: 3/3

Date: 2021-11-23

Drawn By: Kaveh Majidi (CONFIG IOI)