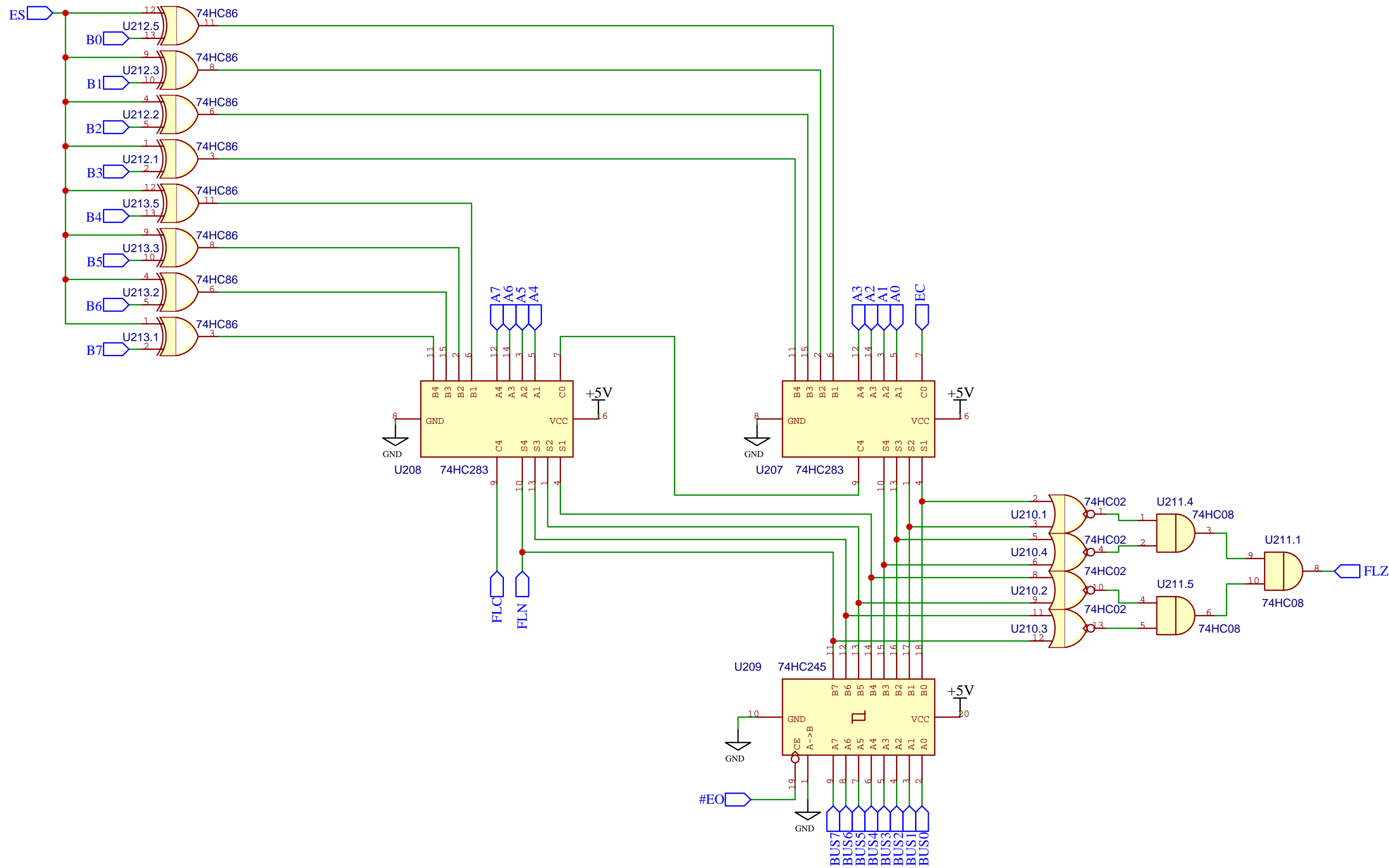




Date: 2021-11-21 Drawn By: Kaveh Majidi (CONFIG IOI)



Logic circuit design derived from Minimal 1.5 project by Carston Herting (Slu4).  
<http://minimal-cpu-system.boards.net>  
 Some modifications has been made.  
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TITLE: Bravo V1.0-M2-ALU

REV: 1.0

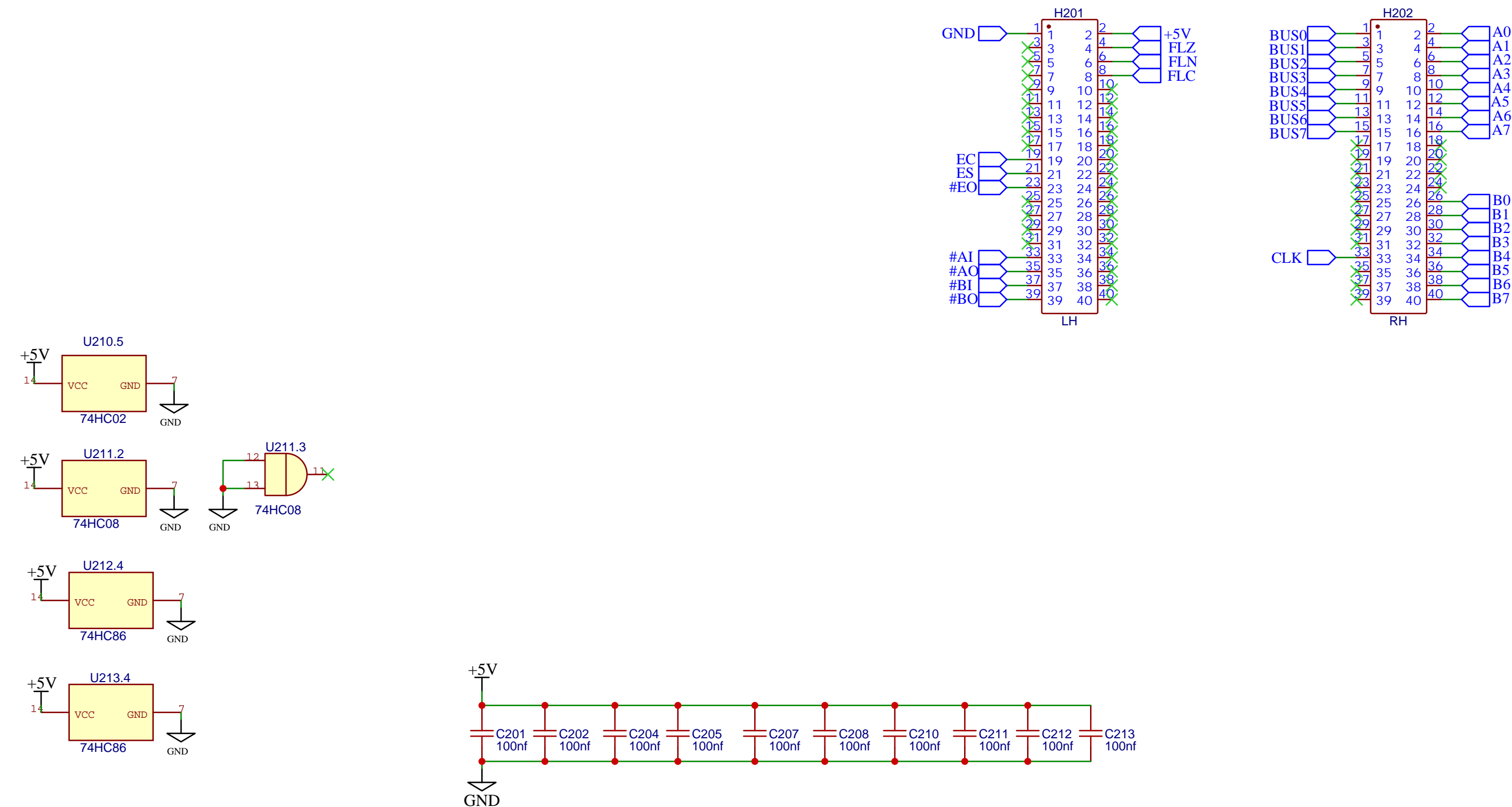


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TITLE: Bravo V1.0-M2-Common

REV: 1.0



Company:

Sheet: 4/4

Date: 2021-11-21

Drawn By: Kaveh Majidi (CONFIG IOI)