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Eco VENTVENT

v1.1

- No Antenna Diversity
- Internal Motor Current Sensing
(No external sensing resistors)

Revision Notes:

v1.0 - Initial Release	Sep. 2014.
v1.1 - Replaced Y2 with 16 MHz (STM32L051 max is 24 MHz) Replaced C37 and C39 18pF with 30pF (performance improvement) Used 220pF for C38 (based on Ref Design) Adjusted R23 and R25 values for 5V output Pull up R26 and R33 to VIN instead to 1V9 (improved current consumption) Replaced C42 100nF with 10nF (STM recommendation) Renamed Port names in Main Controller sheet Removed choke CM1 (not neccessary) Replaced R39 22R with 200R (optimal light, improved current consumption) Renamed Radio GPIO names to match SPIRIT1 pin names Added ESD protection for SWD port lines (D2, R26, R33) Added ESD protection for spare USART lines (D3, D4) Replaced Y1 with 7pF load capacitance option (STM recommendation) Removed U.FL antenna option (not neccessary) Added one more antenna for antenna diversity w/ RF switch (in v2.0) Added RF Shield Replaced Y3 with smaller package to fit under RF shield Replaced R22 100R with 0R (improved DC/DC performance) Replaced 1x10 connector with two 1x4 and one 1x2 (low profiles) Removed SWD test points TP2, TP3, TP4, TP5 (not neccessary) Replaced U7 with Winbond W25Q80BWSNIG (MX25U8035MI is EOL) Replaced U3 with Trinamic TMC5130A Added micro switches interface options Removed battery ADC circuit, Q2, R13, R45, R46 (not neccessary) Added switch for SPI Flash power (improved current consumption) Replaced R25 168 KOhm with 0 Ohm, DNP R23 (5V output settings) Added 3 more fiducial points for bottom side components Removed CRS/RTS from UART interface (not neccessary) V_MOT_FPWM used for Level Shifter power switch (Q2 and R9) Added negation to V_MOT_EN net name	Jan. 2015.

Mounting Holes




Fiducial Points

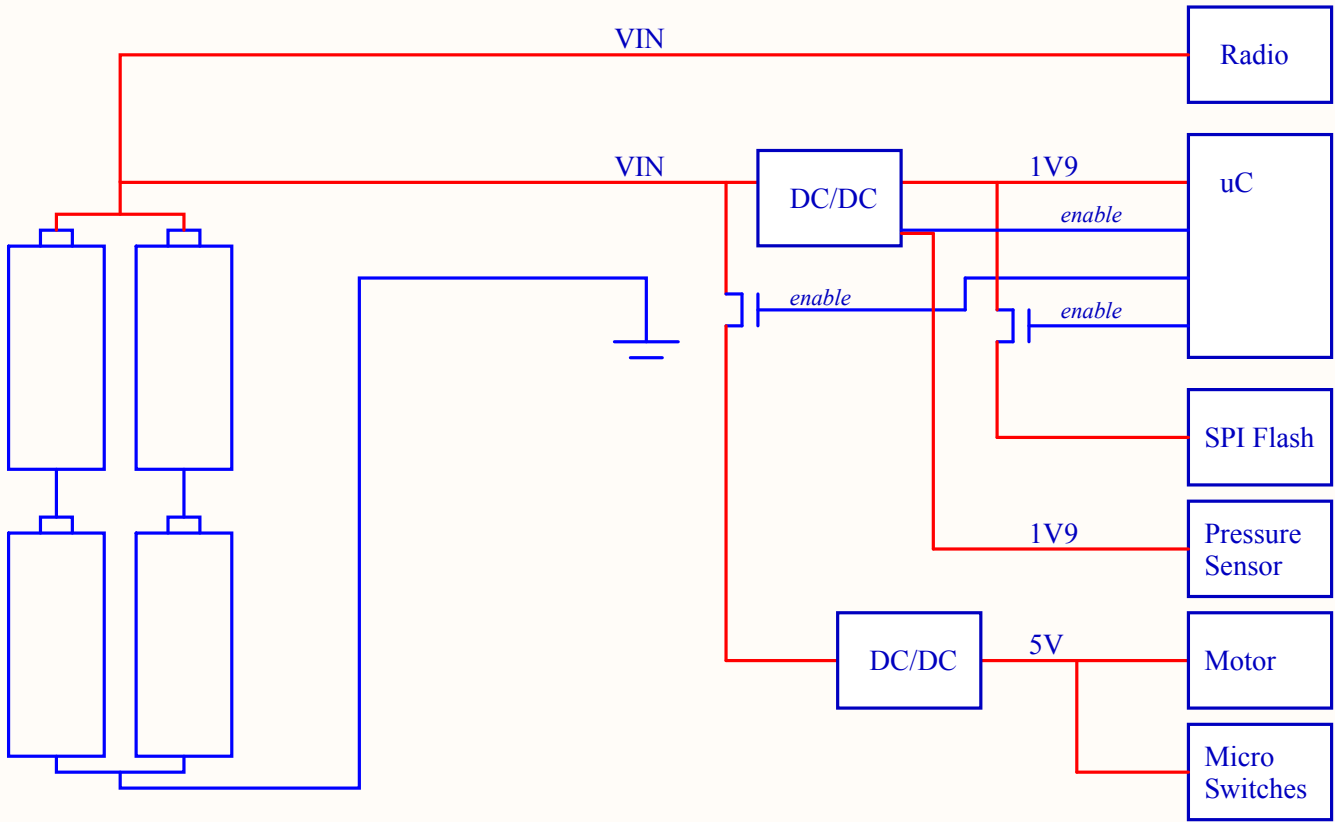
Top




Bottom



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Size: A	Doc. No: *	Rev: 1.1		
Date: 1/15/2015	Time: 2:12:08 PM	Sheet 1 of 8		
Drawn By: Ivica Matovic		Checked By: *	Approved By: *	
File: eVV_Cover Page.SchDoc				



Title: <i>eVV_Power Supply Block Diagram</i>			Bitgear Stevana Markovica 8 11080 Belgrade Serbia www.bitgear.rs	
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File: eVV_Power Supply Block Diagram.SchDoc				

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U_eVV_Cover Page
eVV_Cover Page.SchDoc

U_eVV_Power Supply Block Diagram
eVV_Power Supply Block Diagram.SchDoc

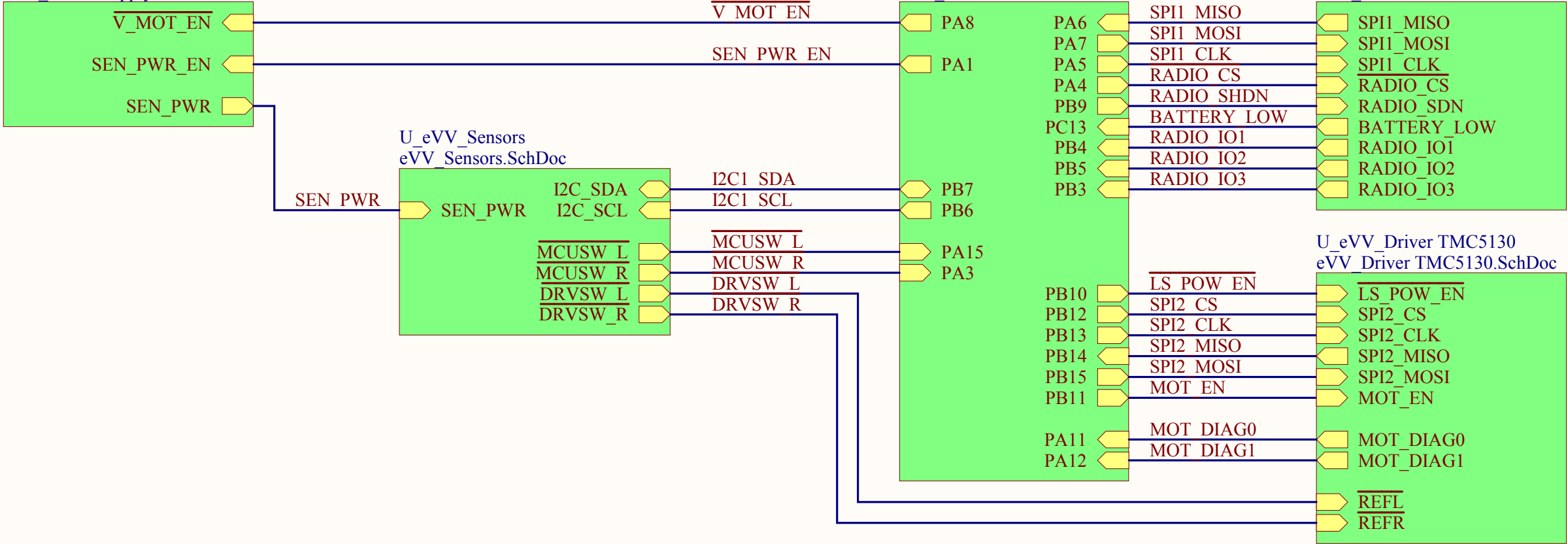
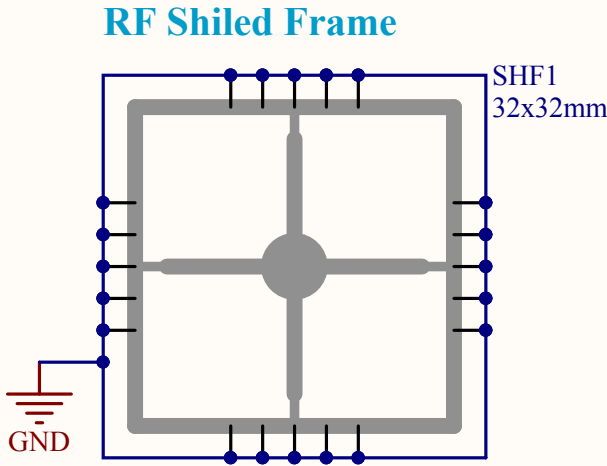
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eVV_Power Supply.SchDoc

U_eVV_Main Controller
eVV_Main Controller.SchDoc

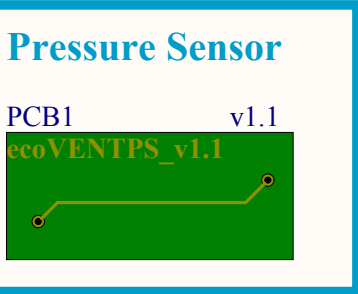
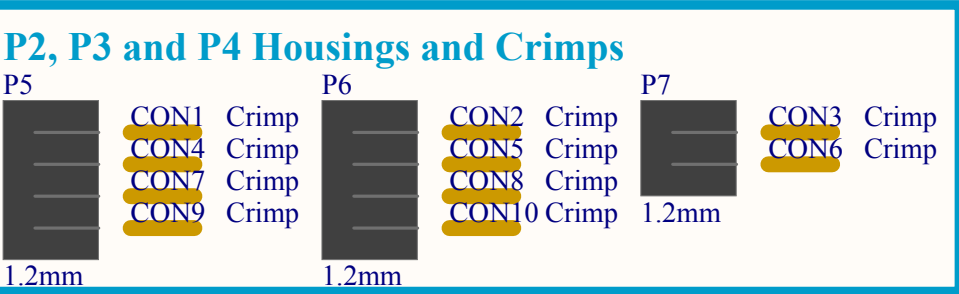
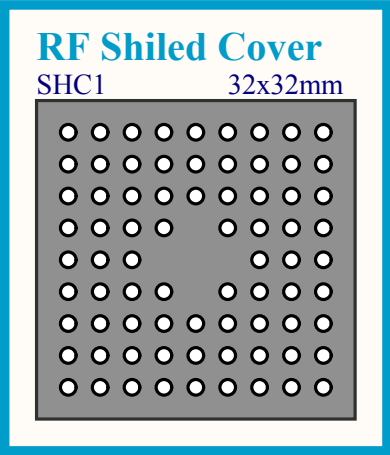
U_eVV_Radio
eVV_Radio.SchDoc


U_eVV_Sensors
eVV_Sensors.SchDoc

U_eVV_Driver TMC5130
eVV_Driver TMC5130.SchDoc

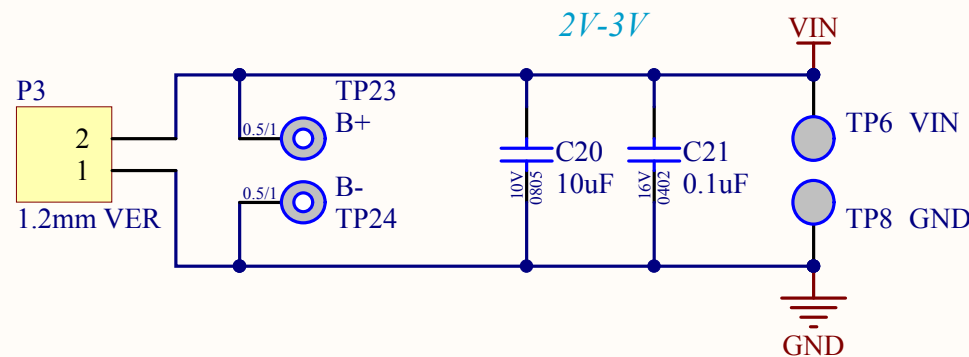


Off-board Parts

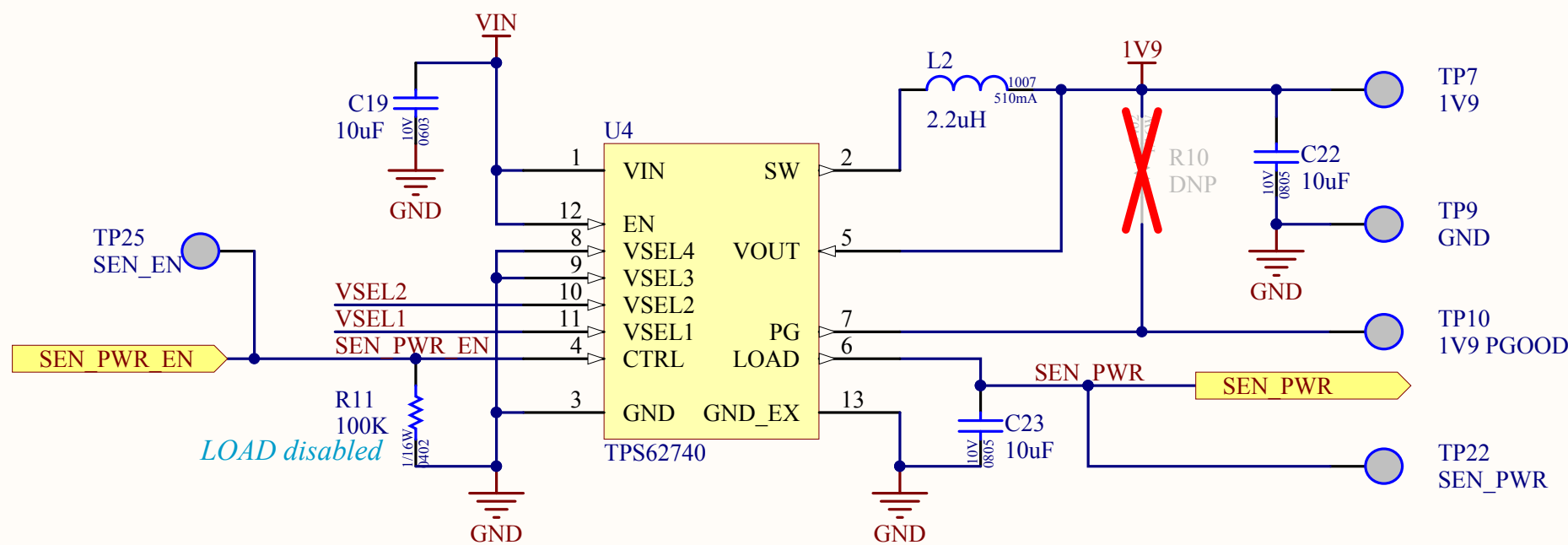


Title: <i>eVV_TopLevel</i>			Bitgear Stevana Markovica 8 11080 Belgrade Serbia <i>www.bitgear.rs</i>	
Size: A	Doc. No: *	Rev: 1.1		
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Drawn By: Ivica Matovic		Checked By: *	Approved By: *	
File: eVV_TopLevel TMC.SchDoc				

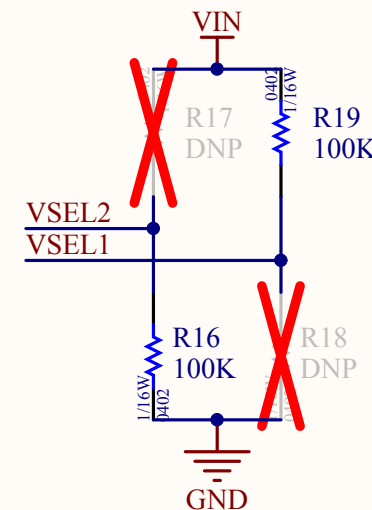
Input Power Stage



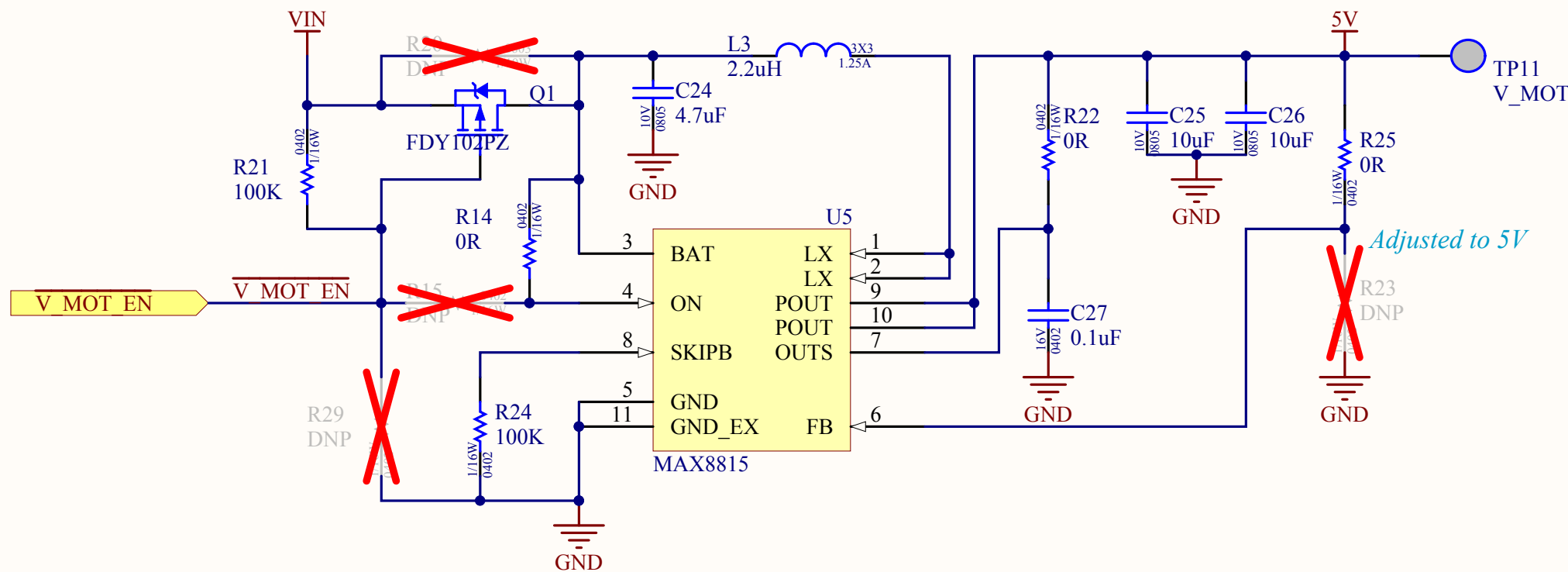
+1.9V Voltage Supply for uC



Voltage Output Selection (Default 1.9V)



+5V Voltage Supply for Motor Circuit



Default Settings:

Motor Switcher is OFF (V_MOT_EN to High-Z)
DC/DC is in Normal Mode(SKIPB=Low)

Title: **eVV_Power Supply Block Diagram**

Size: **A**

Doc. No: *

Rev: **1.1**

Date: **1/15/2015**

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Sheet **4** of **8**

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File: **eVV_Power Supply.SchDoc**

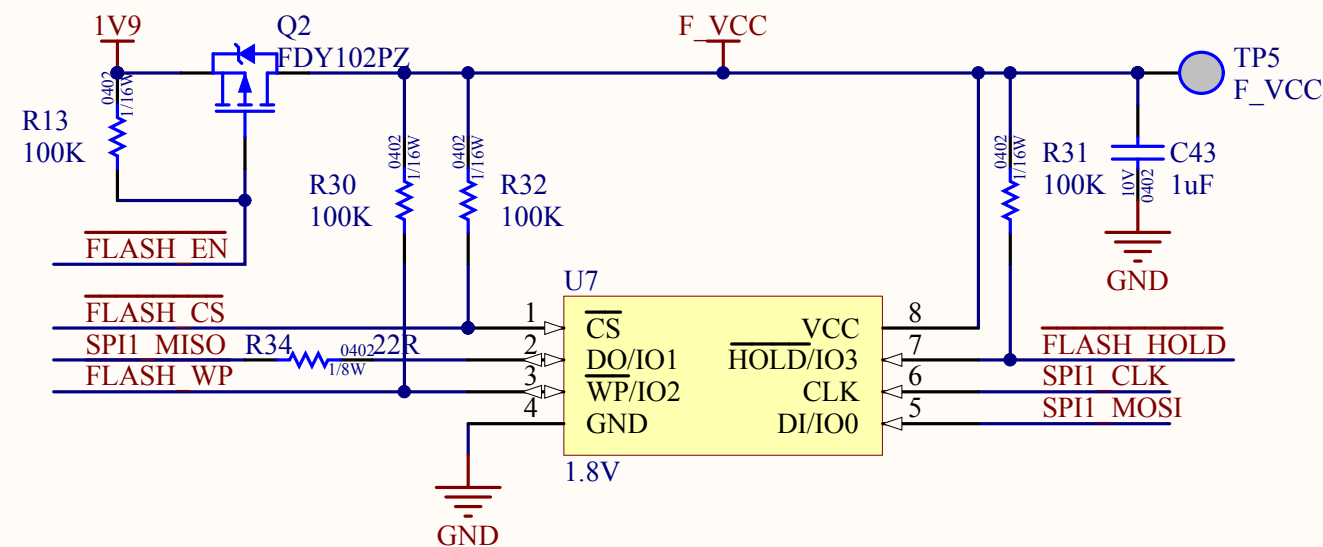
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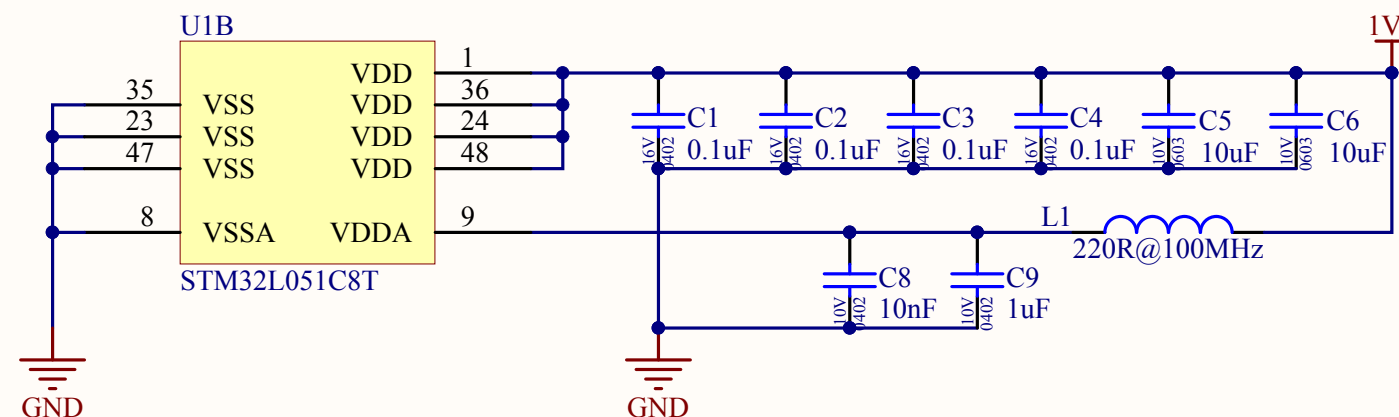
A

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SPI Flash



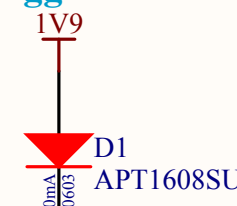
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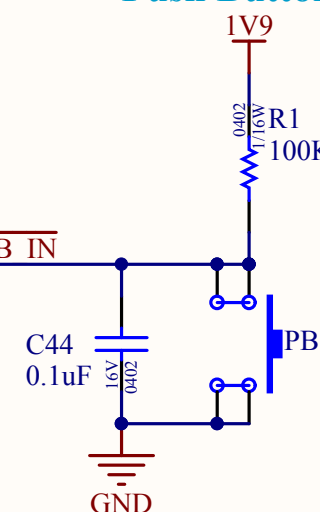
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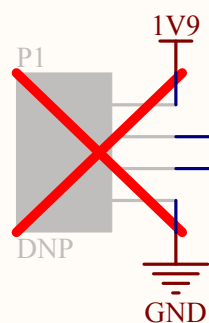
Debugg LEDs



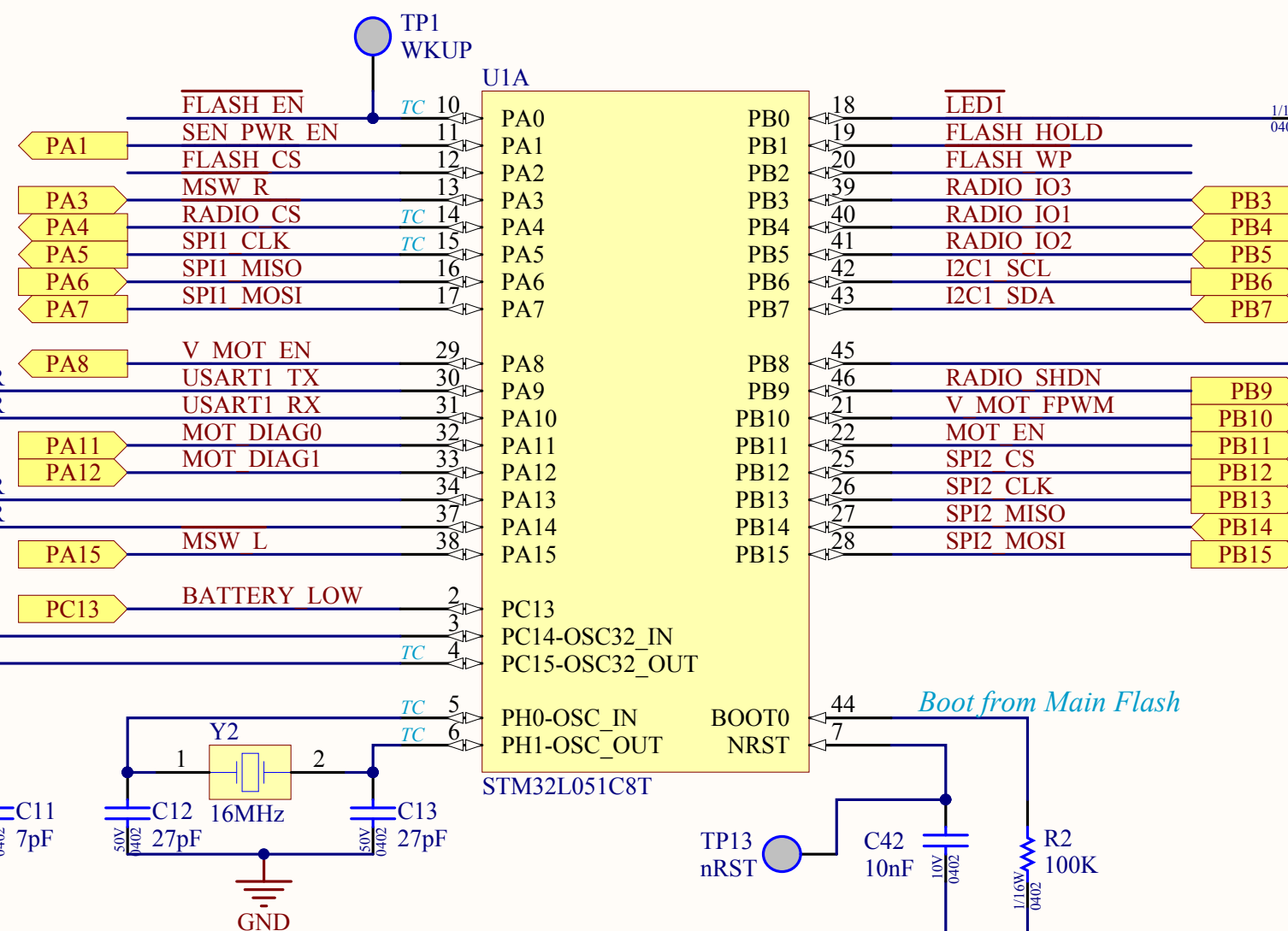
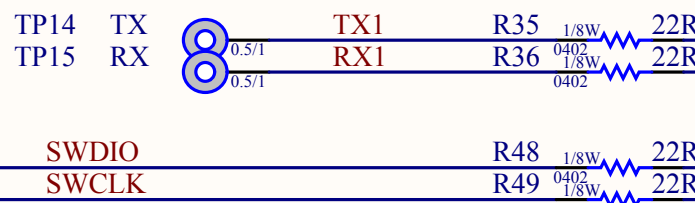
Push Button



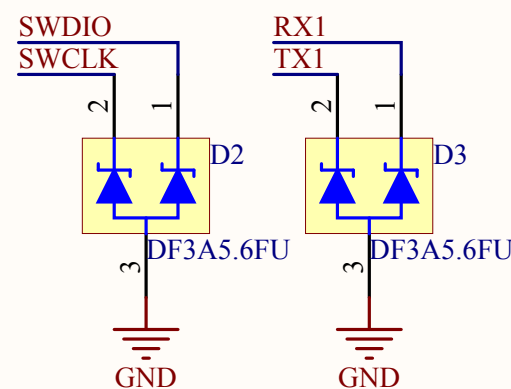
Programming Interface



UART Test Points



ESD Protection



Firmware Note:
Define PB11 as push pull output

Title: **eVV_Main Controller**Size: **A**

Doc. No: *

Rev: **1.1**Date: **1/15/2015** Time: **2:12:09 PM**Sheet **5** of **8**Drawn By: **Ivica Matovic**

Checked By: *

Approved By: *

File: **eVV_Main Controller.SchDoc**

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Motor Driver Power

Layout Note:
C16 Place near VSA pin

Peak motor current is set to
360mA
(vsense=1)

Layout Note:
Place C18 close to VCC pin.
Place C47 close to VCC_IO pin.

Level Shifter Power Switch

Controller Interface


MOTOR

Firmware Notes:
diag0_int_pushpull = 1 for DIAG0 push pull
diag1_poscomp_pushpull = 1 for DIAG1 push pull

pol_stop_l = 1 for REFL active low
pol_stop_r = 1 for REFR active low

internal_Rsense = 0 for Normal operation
I_scale_analog = 0 for normal operation (internal ref voltage)

Configuration:
SPI_MODE = 1 - SPI/UART available
SD_MODE = 0 - Internal ramp generator
SWSEL = 0 - SPI is used, DIAGx available

Title: eVV_Driver TMC5130			Bitgear Stevana Markovica 8 11080 Belgrade Serbia www.bitgear.rs		
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Drawn By: Ivica Matovic		Checked By: *	Approved By: *		
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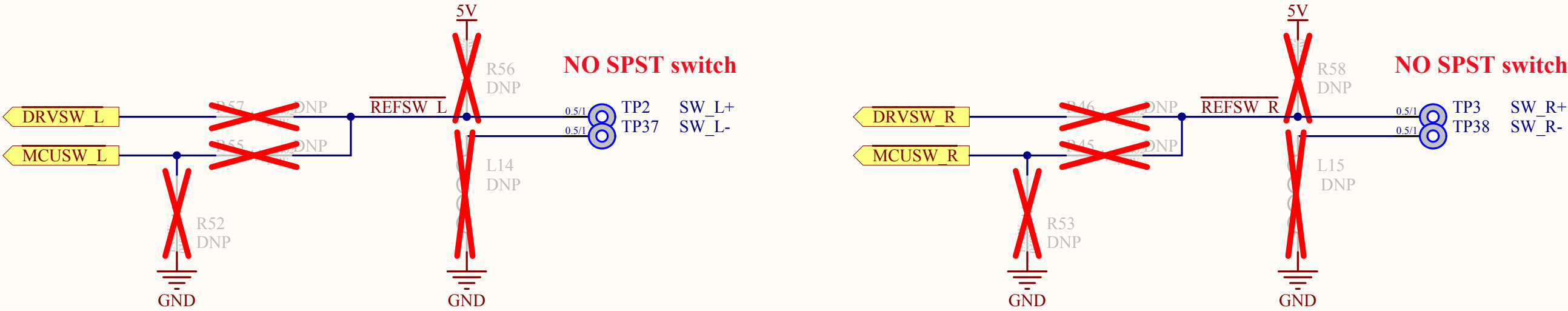
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Micro Switches Interface

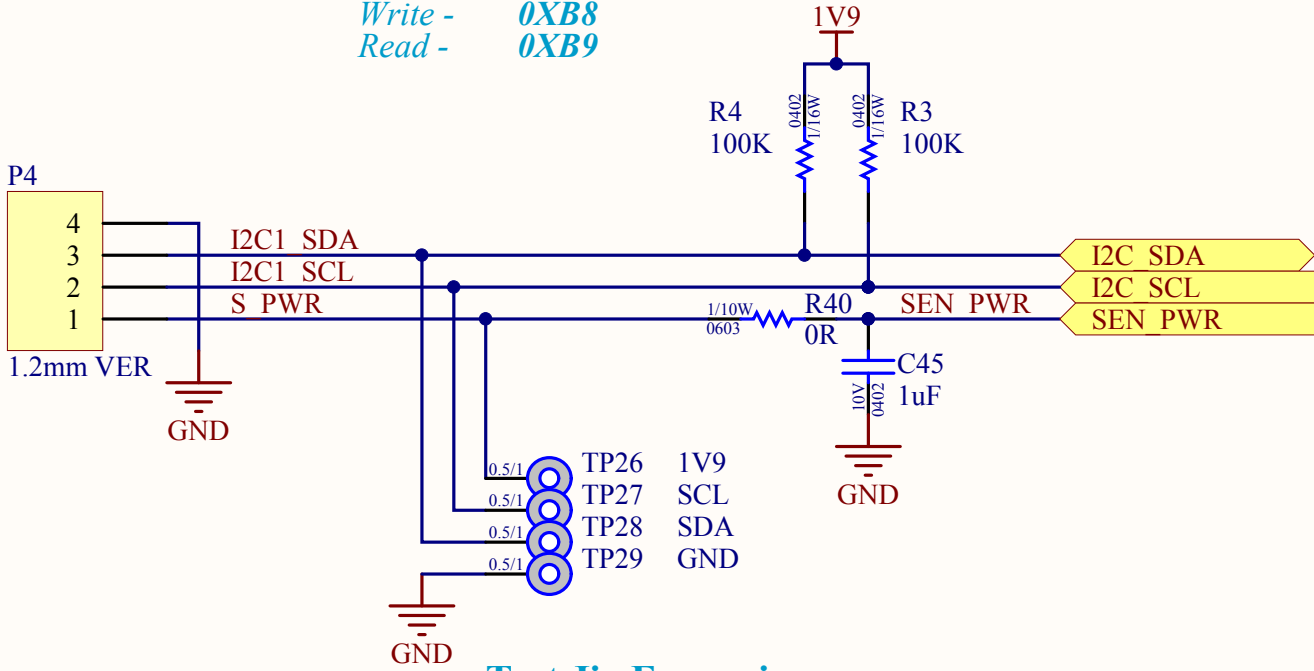
Connected to Motor Driver
Connected to Controller




I2C Pressure Sensor

STM LPS25H:

Write - 0XB8
Read - 0XB9



Test Jig Expansion

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Drawn By: Ivica Matovic		Checked By: *		
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