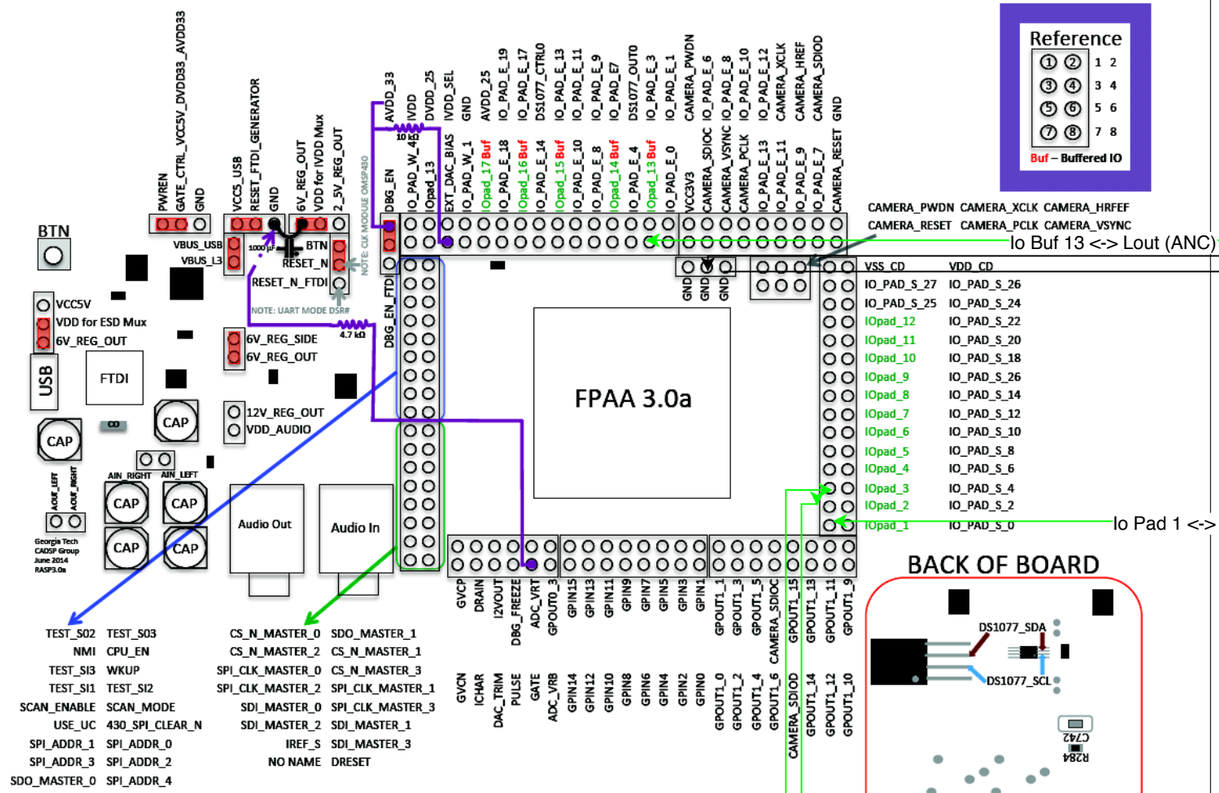
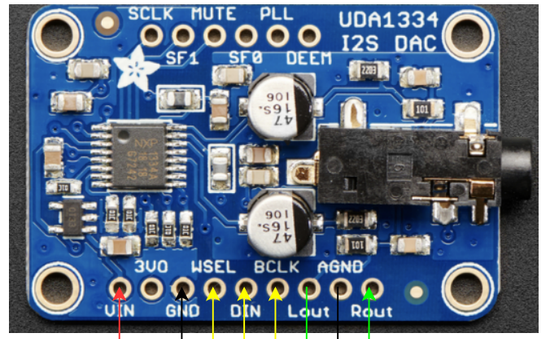
RASP 3.0A FPAA Board



I2S DAC



Left Speaker

Right Speaker

Io Buf 13 <-> Lout (ANC)

Gnd

3.3 V

GPIO 26 <-> BCLK/BCK

GPIO14 <-> WSEL/LRCK

GPIO25 <-> DIN/DATA

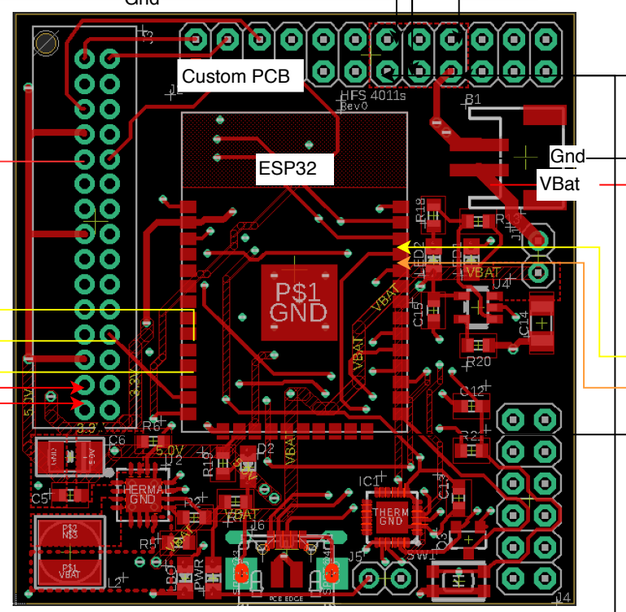
Right Speaker (No ANC)

Gnd

Gnd

Agnd

LiPo Battery



Gnd

VBat

Io Pad 1 <-> Lout

Io Pad 2 <-> AUD

Io Pad 3 <-> AUD

3.3 V

Gnd

Outside Headphone Mic

Gnd

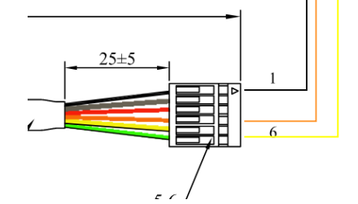
Inside Headphone Mic

GPIO 1 (TX) <-> RXD

GPIO 3 (RX) <-> TXD

Gnd

FTDI Cable



PIN WIRING

P1		P2
1	BLACK	1(Ground)
2	BROWN	2(CTS)
3	RED	3(5V)
4	ORANGE	4(TXD 3V level)
5	YELLOW	5(RXD 3V level)
6	GREEN	6(RTS)

FPAA ANC Algorithm