Claire Seidel

Dedicated university student with experience in circuit building, programming, and project management looking to enhance technical skills and knowledge. Possesses an aptitude for leadership, proficient time-management skills, and a fervent desire to learn.

Contact

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Experience

Hardware

Designed and constructed various amplifiers and buffers utilizing both Bipolar-Junction Transistor and MOSFETs

Systematically tested and adjusted stages of circuits with the use of an Analog Discover 2 to improve overall performance

Analyzed resistive, inductive, and capacitive circuits to better understand functionality

Software

Extensive use of Verilog HDL to design structures of circuits for specific application

Created functioning programs in both C++ and Python

Applicable Coursework
Electronics
Electrical Circuit Theory
Signals and Systems
Digital System Design

Skills

Circuit Design · Multisim · SPICE Simulations · C++ · Python · Verilog HDL · Microsoft Office

Problem Solving · Communication · Quick Learning · Creative · Detail Oriented · Motivated · Solution Driven

Education

GPA 3.869/4.00

Pursuing B.S. in Electrical Engineering, Minor in Mathematics Texas A&M University College Station, TX August 2019 - May 2023

High School Diploma - Valedictorian Smithville High School Smithville, TX August 2015 - May 2019

Extracurricular Activities

Institute of Electrical and Electronics Engineers September 2020 – Current

Catholic Student Association September 2020 – Current

FIRST Robotics Program – President August 2016 – May 2020 Smithville High School