

Connor Goldberg

connor@connorgoldberg.com

(845) 283-1954

204 Hardenburgh Road
Pine Bush, NY 12566

98 Crittenden Way, Apt. 5
Rochester, NY 14623

OBJECTIVE

Obtain a co-op for the Summer of 2014 to apply and reinforce the information I have learned, as well as learn more about Electrical Engineering.

EDUCATION

ROCHESTER INSTITUTE OF TECHNOLOGY, Rochester, NY

Degree: Bachelor and Master of Science in Electrical Engineering, expected May 2017

GPA: 3.78/4.00

Relevant Courses:

Differential Equations

Multivariable Calculus

Intro to Computer Science (Python)

Physics I, II

Intro to Digital Systems with Lab

Circuits I with Lab

Digital Systems II with Lab

Circuits II*

Semiconductor Device Physics*

Advanced Programming for Engineers (C++)

* Expected completion May 2014.

SKILLS

Verilog, VHDL, FPGAs, Altera Quartus, ModelSim, Oscilloscopes, C++ Programming, Python Programming, Mac OSX, Windows, Linux, Microsoft Office, iWork, Photoshop, American Sign Language, Spanish

PROJECTS/LABS

Digital Systems II Lab

Both VHDL and Verilog HDL languages were used to design and construct a RISC (Reduced Instruction Set Computer) CPU, including the Data Path and the Control Unit. The CPU was constructed using a combination of behavioral and structural design methodology.

Circuits Lab

Analog DC circuits were designed and analyzed using Cadence Capture CIS, then constructed on prototyping boards to analyze with function generators, multimeters, and oscilloscopes.

Assembler

An assembler for a custom architecture designed in Digital Systems II was made using C++. *

*Expected completion May 2014.

ACTIVITIES

RIT Racquetball: Club/Team Member: 2012 – present

Vice President: 2014 – present

RIT Rubik's Cube Club: Co-founder

Vice President 2013 – present

RIT Varsity Swimming Team: 2012 – 2013

JOB EXPERIENCE

Teaching Assistant: 2014 – present

Instructing a lab section for Digital Systems II; this includes teaching the lab in addition to grading and providing office hours for help. The lab consists of using VHDL and Verilog with an FPGA to make small projects that eventually build up to a fully functional CPU.

Assistant DJ: 2012 – present

Assist in setting up and running an entire sound system for music, as well as running the karaoke system for various types of occasions such as weddings and parties.

VOLUNTEERING

New Prospect Church Food Pantry 2008 – present

Relay for Life

INTERESTS

Geocaching, Kickstarter, Swimming, Racquetball, Hockey, Programming