

Website

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Connor J. Smith

BASc. Candidate, Computer Engineering

University of Toronto, Expected May 2017

Software Engineering Co-Op

Top Hat, Inc.

tophat.com

May 2015 – Present

Director of Mentorship

General First Year Engineering

March 2014 – March 2015

Real Time Assembly Interpreter

University of Toronto

March 2015

Stock Analysis Algorithm

UTEK Engineering Competition

January 2015

BASc., Computer Engineering

University of Toronto

cGPA: 3.89/4.0

Expected May 2017

Experience

Overhauled the textbook content platform used by nearly two million students and professors worldwide.

Surpassed the duties of a typical intern by being a leading voice in the planning and execution process to help make critical engineering decisions.

Operated an organization of over 50 upper year mentors designed to help integrate 200 first year engineering students into university life.

Challenges included coordinating and training all mentors, communicating with external groups and managing a budget used to execute various team-building events throughout the year.

Projects

Engineered a working assembly language interpreter using Verilog HDL and the NIOS II assembly language on the Altera DE2 FPGA platform.

Modified processor hardware source code to implement custom instructions to improve the speed and maintainability of the project.

Designed and implemented an algorithm within a team of 4 engineers to optimize profits as a part of an open-ended competition.

Algorithm used a combination of linear algebra, calculus and heuristic optimizations to deliver an efficient and accurate solution.

Education

Pursuing a BASc. with a focus on embedded systems and networks.

Awarded for continually demonstrating both outstanding academic performance and community involvement.

Ranked within the top 10 students in my year.

Skills & Technologies

Python – Javascript – HTML5/CSS3 – C/C++
Verilog – Assembly Language – Django – Quartus II
Git – NI Multisim – Linux/bash – Vim – OS X
MATLAB – Oscilloscopes