

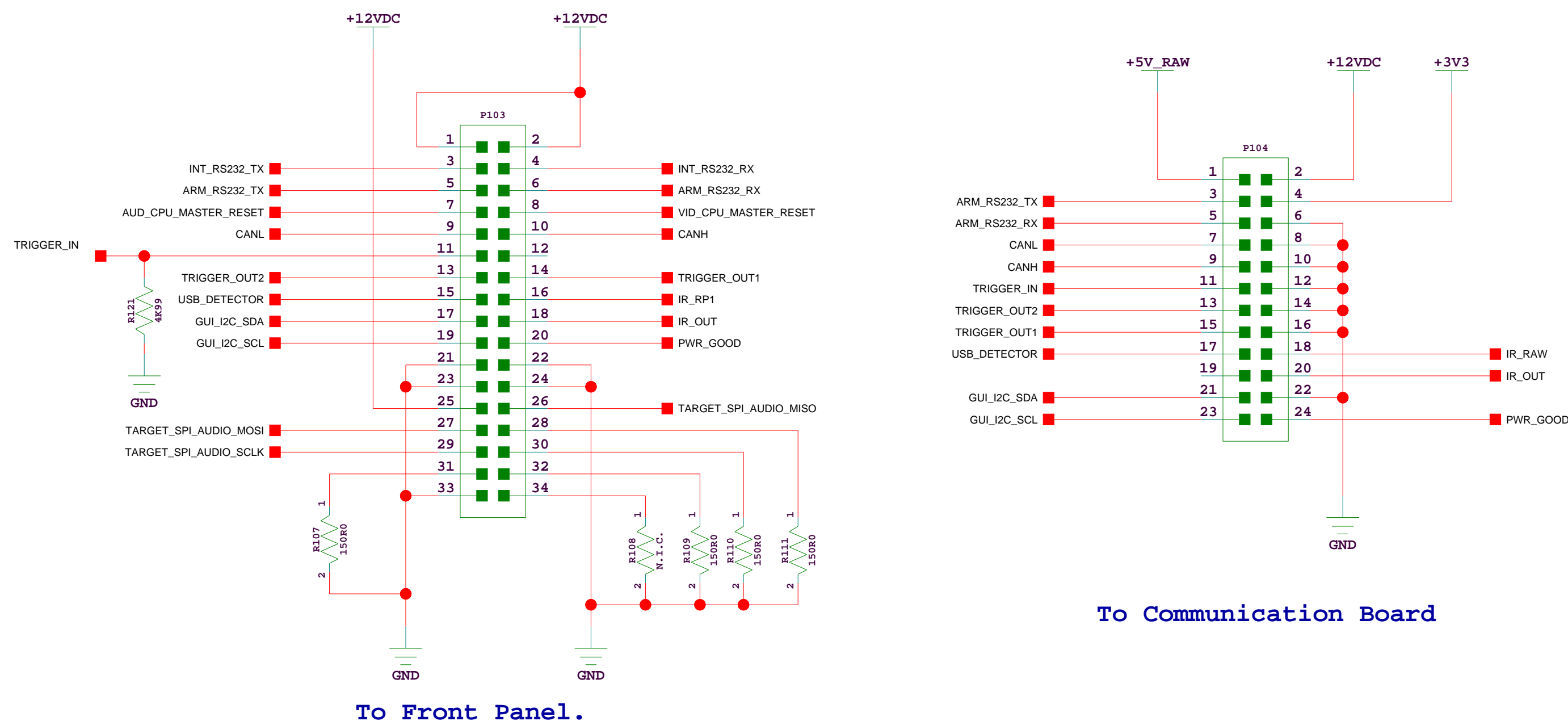
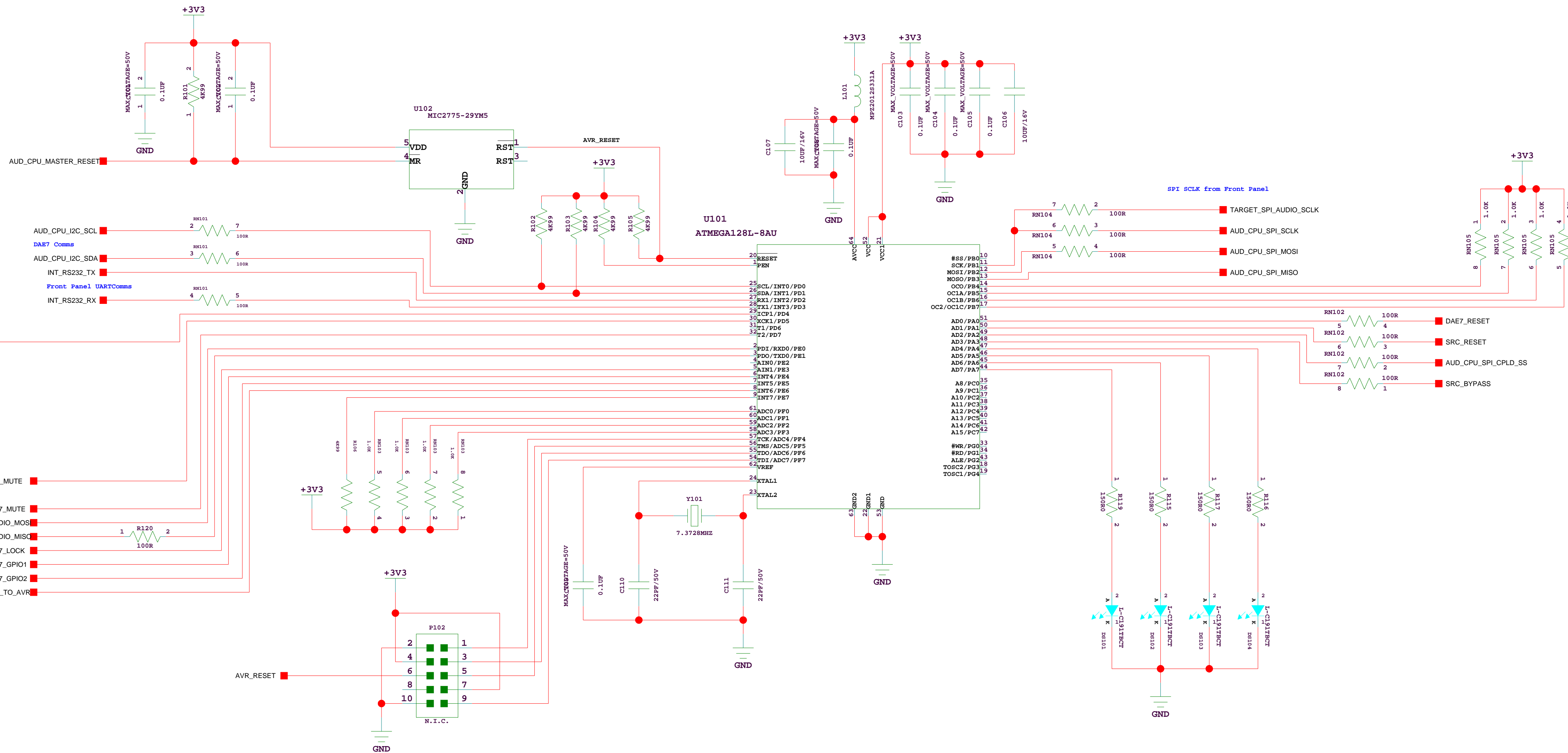
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1. +12VDC supply to front panel
2. +12VDC supply to front panel
3. TX from ARM to AVR for Communication
4. RX to ARM from AVR for Communication
5. ARM RS232 TX to RS232 and USB connectors.
6. ARM RS232 RX to RS232 and USB connectors.
7. Reset for AVR from ARM (ESA RST1)
8. USB_DETECTOR (Was ESA RST2)
9. CANL
10. CANH
11. Trigger_IN(notused)
12. NC
13. Trigger_OUT2
14. Trigger_OUT1
15. IR_RP2
16. IR_RP1
17. GUI_I2C_SDA (to Ac control board)
18. IR_OUT
19. GUI_I2C_SCL (to Ac control board)
20. PWRGGOOD
21. GND
22. GND
23. GND
24. GND
25. +12VDC supply to front panel
26. TARGET_SPI_AUDIO_MISO
27. TARGET_SPI_AUDIO_MOSI
28. TARGET_SPI_AUDIO_SS
29. TARGET_SPI_AUDIO_SCLK
30. NC
31. NC
32. NC
33. GND
34. NC



To Communication Board

To Front Panel.

TITLE

R6703-04-02S

SIZE

DWG NO

D

REV

A

DRAWN BY: R.K.

SCALE

SHEET 1 of 17

11-12-2012 15:50

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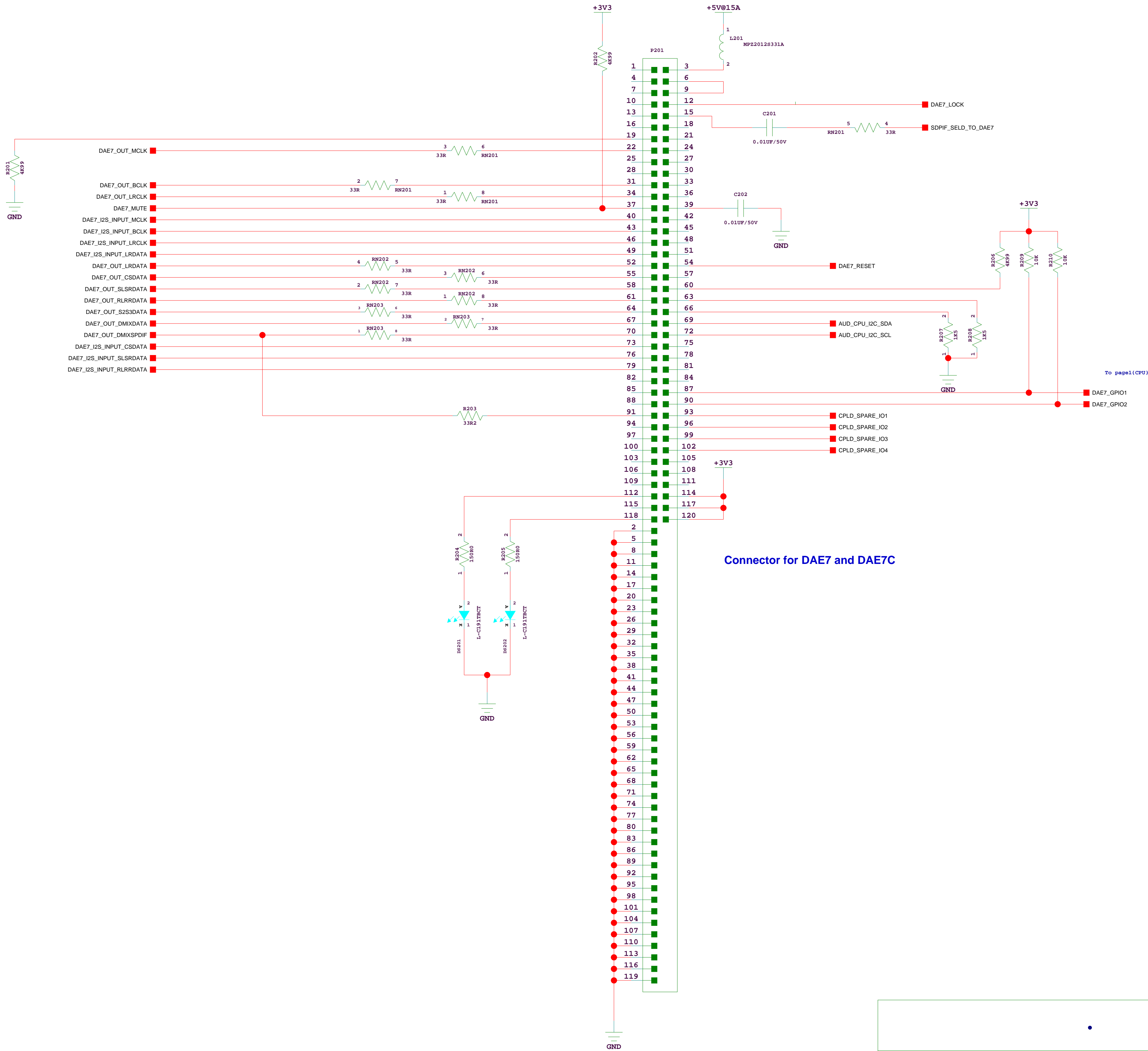
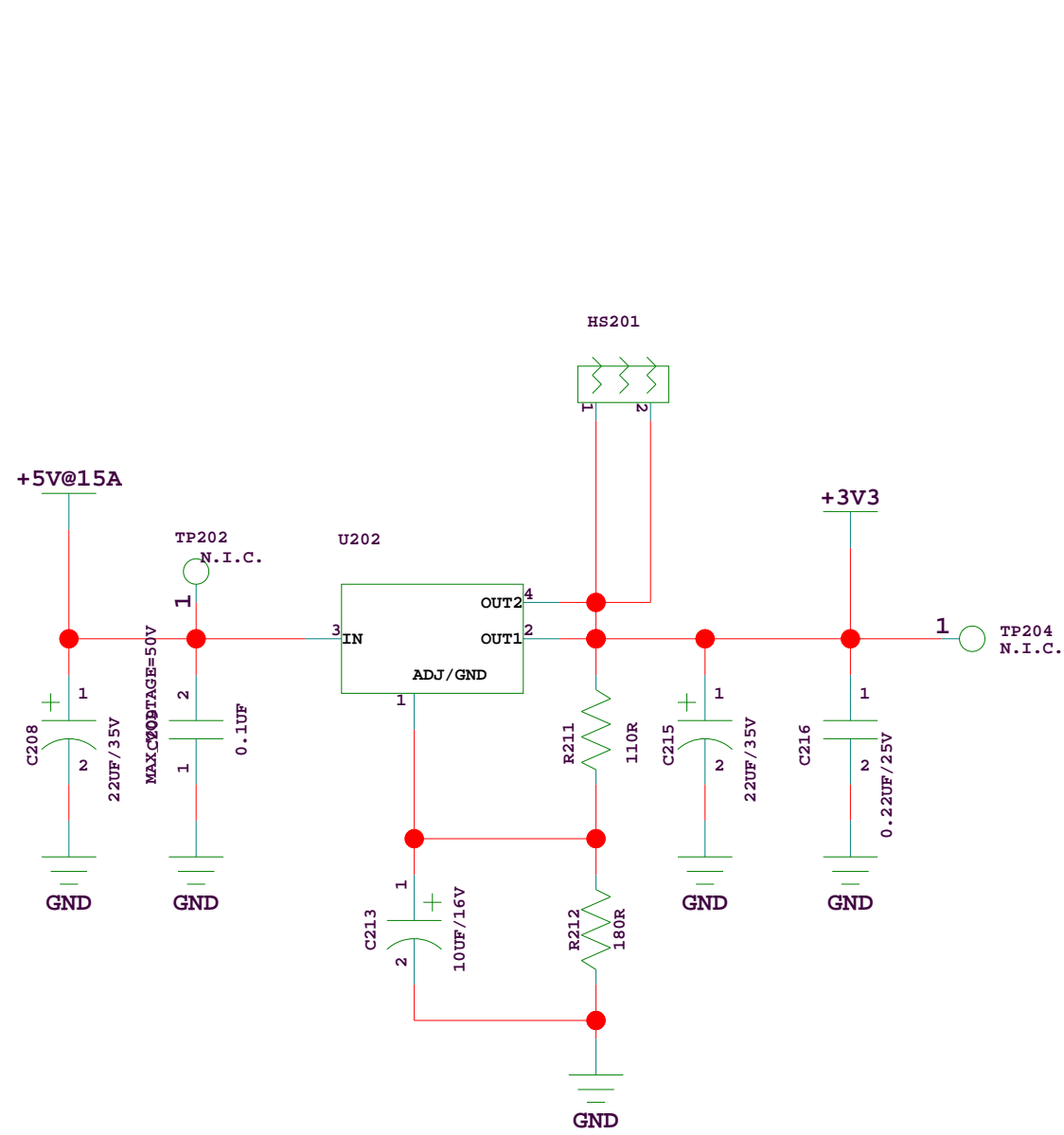
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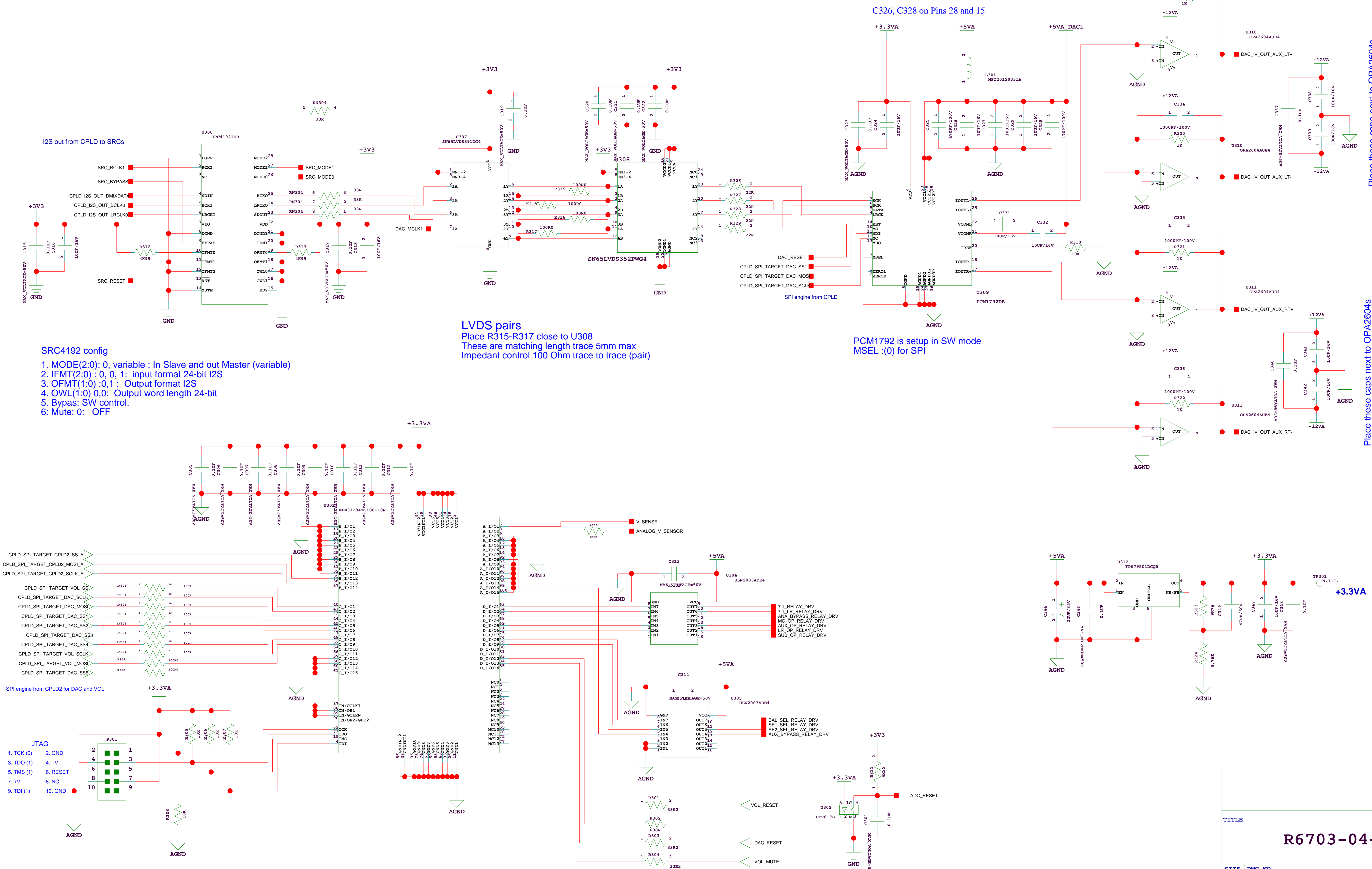
NOTES:
RN201 - 1 element unused
RN202 - 1 element unused

DAE7 PIN ASSIGNMENT

1.NC	2.GNDD	2.5VA
4.NC	5.GNDD	6.3.3VA
7.NC	8.GNDD	9.3.3VA_spdif
10.NC	11.GNDD	12.LOCK
13.CLKOUT	14.GNDD	15.SPDI F0_RXP0
16.KEY1	17.GNDD	18.SPDI F0_RXP1
19.AHCLK1>0	20.GNDD	21.SPDI F0_RXP2
22.MCLK_T1/R1	23.GNDD	24.SPDI F0_RXP3
25.SCLK_R1	26.GNDD	27.SPDI F0_RXP4
28.FCLK_R1	29.GNDD	30.SPDI F0_RXP5
31.SCLK_T1	32.GNDD	33.SPDI F0_RXP6
34.FCLK_T1	35.GNDD	36.SPDI F0_RXP7
37.MUTE	38.GNDD	39.SPDI F0_RXN
40.ALTO_MCLK	41.GNDD	42.SPDI F_TX0
43.ALTO_SCLK	44.GNDD	45.1.2VD
46.ALTO_FCLK	47.GNDD	48.1.2VD
49.ALTO_DATA	50.GNDD	51.1.2VD
52.AXR[0][1]AXR1[14]	53.GNDD	54.HOST_RST
55.AXR[0]2]AXR1[13]	56.GNDD	57.SELF_RST
58.AXR[0]3]AXR1[12]	59.GNDD	60.PWR_RST
61.AXR[0]4]AXR1[11]	62.GNDD	63.I2C_ADDR0
64.AXR[0]5]AXR1[10]	65.GNDD	66.I2C_ADDR1
67.AXR[0]6]AXR1[9]	68.GNDD	69.I2C1_SDA
70.AXR[0]7]AXR1[8]	71.GNDD	72.I2C1_SCL
73.AXR[0]8]AXR1[7]	74.GNDD	75.FSX1
76.AXR[0]9]AXR1[6]	77.GNDD	78.I2C0_SDA
79.AXR[0]10]AXR1[5]	80.GNDD	81.I2C0_SCL
82.AXR[0]11]AXR1[4]	83.GNDD	84.KEY
85.AXR[0]12]AXR1[3]	86.GNDD	87.I2C_ADDR0/GPO[1] NIC line
88.AXR[0]13]AXR1[2]	89.GNDD	90.I2C_ADDR1/GPO[2] NIC Line
91.AXR[0]14]AXR1[1]	92.GNDD	93.DAE_SCK_GPO[9]
94.AXR[0]15]AXR1[0]	95.GNDD	96.DAE_MOSI_GPO[10]
97.AHCLKX0	98.GNDD	99.DAE_MISO_GPO[11]
100.ACLKX0	101.GNDD	102.DAE_SS0_GPO[12]
103.AFSX0	104.GNDD	105.DAE_SS1_GPO[13]
106.AMUTEIN0	107.GNDD	108.DAE_SS2_GPO[14]
109.AMUTE0	110.GNDD	111.DAE_SS3_GPO[15]
112.AHCLK0	113.GNDD	114.3.3VD
115.ACLK0	116.GNDD	117.3.3VD
118.AFSR0	119.GNDD	120.3.3VD


TITLE			
R6703-04-02S			
SIZE D	DWG NO		REV A
SCALE	SHEET 2 of 17		11-12-2012 15:50

DRAWN BY: R.K.



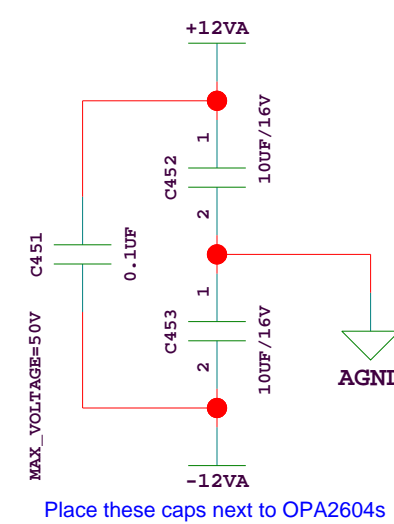
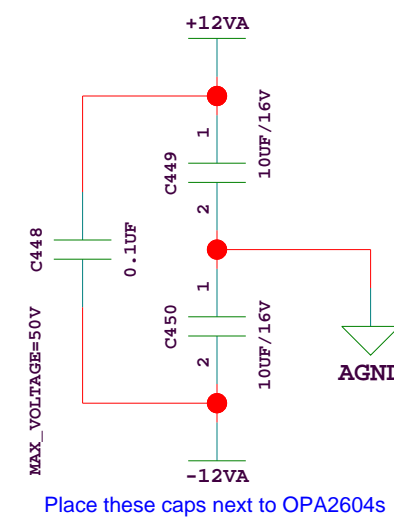
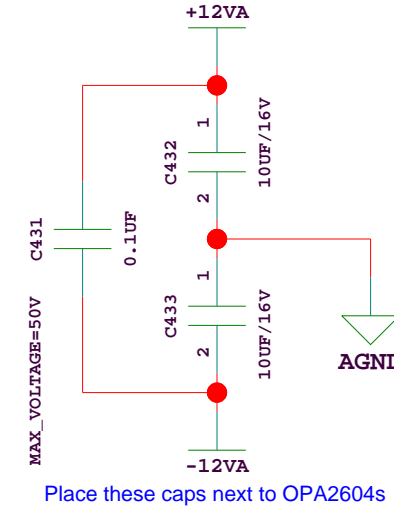
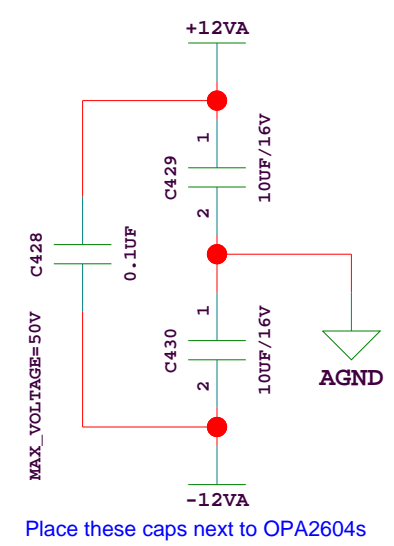
Place these caps next to OPA2604s

Place these caps next to OPA2604s

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<div style="text-align: center;"> <p>TITLE</p> <p>R6703-04-02S</p> </div>		
<div style="text-align: center;"> <p>SIZE</p> <p>D</p> </div>	<div style="text-align: center;"> <p>DWG NO</p> </div>	<div style="text-align: center;"> <p>REV</p> <p>A</p> </div>
<div style="text-align: center;"> <p>SCALE</p> <p>SHEET 3 of 17</p> </div>		<div style="text-align: center;"> <p>11-12-2012 15:50</p> </div>

Connected to pin23
Connected to pin57

1. SRC4184 Hardware MODE
 1. MODE(2:0) : 0, variable : In Slave and out Master (variable)
 2. IFMT(2:0) : 0, 0, 1 : input format 24-bit I2S
 3. OFMT(1:0) : 0, 1 : Output format I2S
 4. OWL(1:0) 0, 0 : Output word length 24-bit
 5. LGRPX(1:0) : 0, 0 : 64 samples delay
 6. DDN : 1 : Direct Downsampler Enable
 7. DEMP(1:0) : 0, 0 : Disable
 8. Bypass : 0 : SW control mode
 9. MUTE : 0 : OFF
2. DO NOT CONNECT Pins 27 and 28



TITLE		
R6703-04-02S		
SIZE	DWG NO	REV
D		A
SCALE	SHEET 4 of 17	11-12-2012.15:50

DRAWN BY: R.K.

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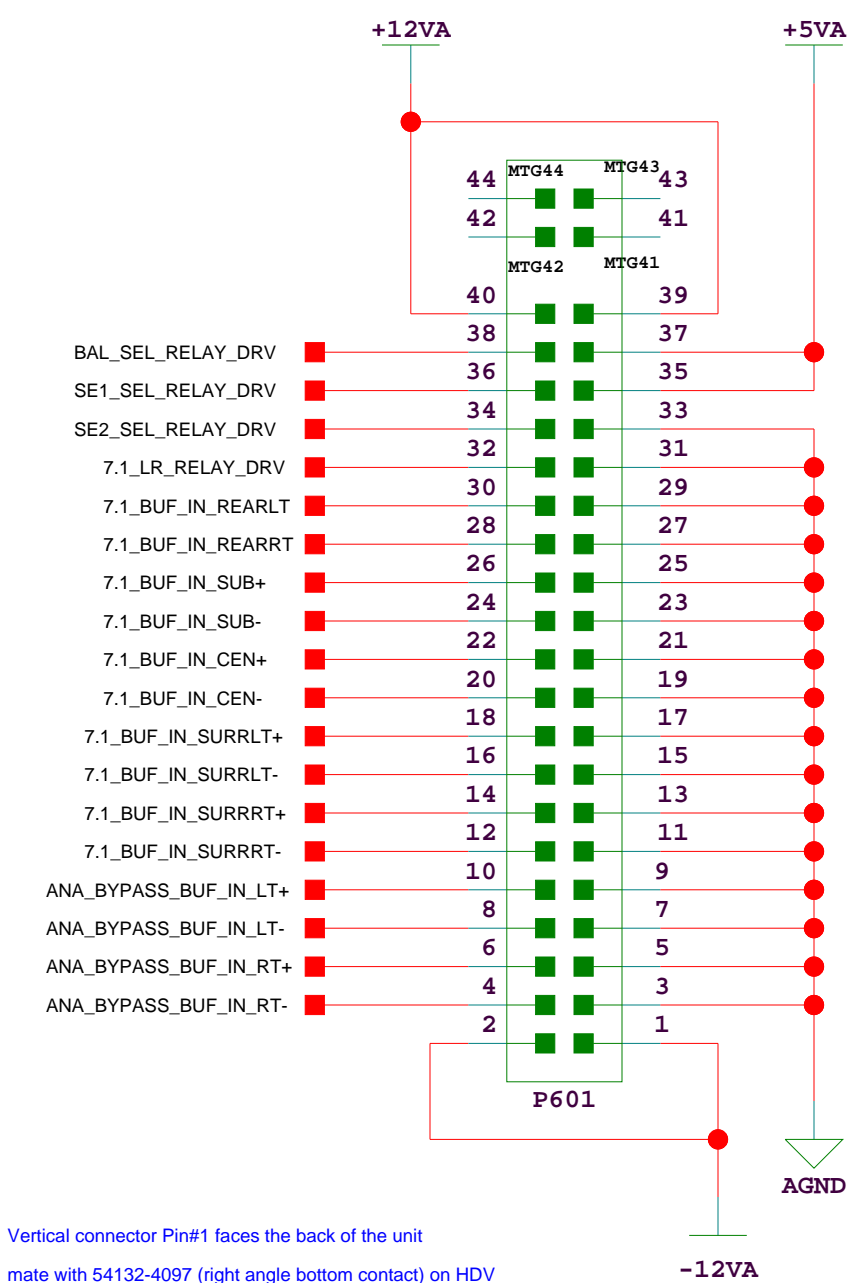
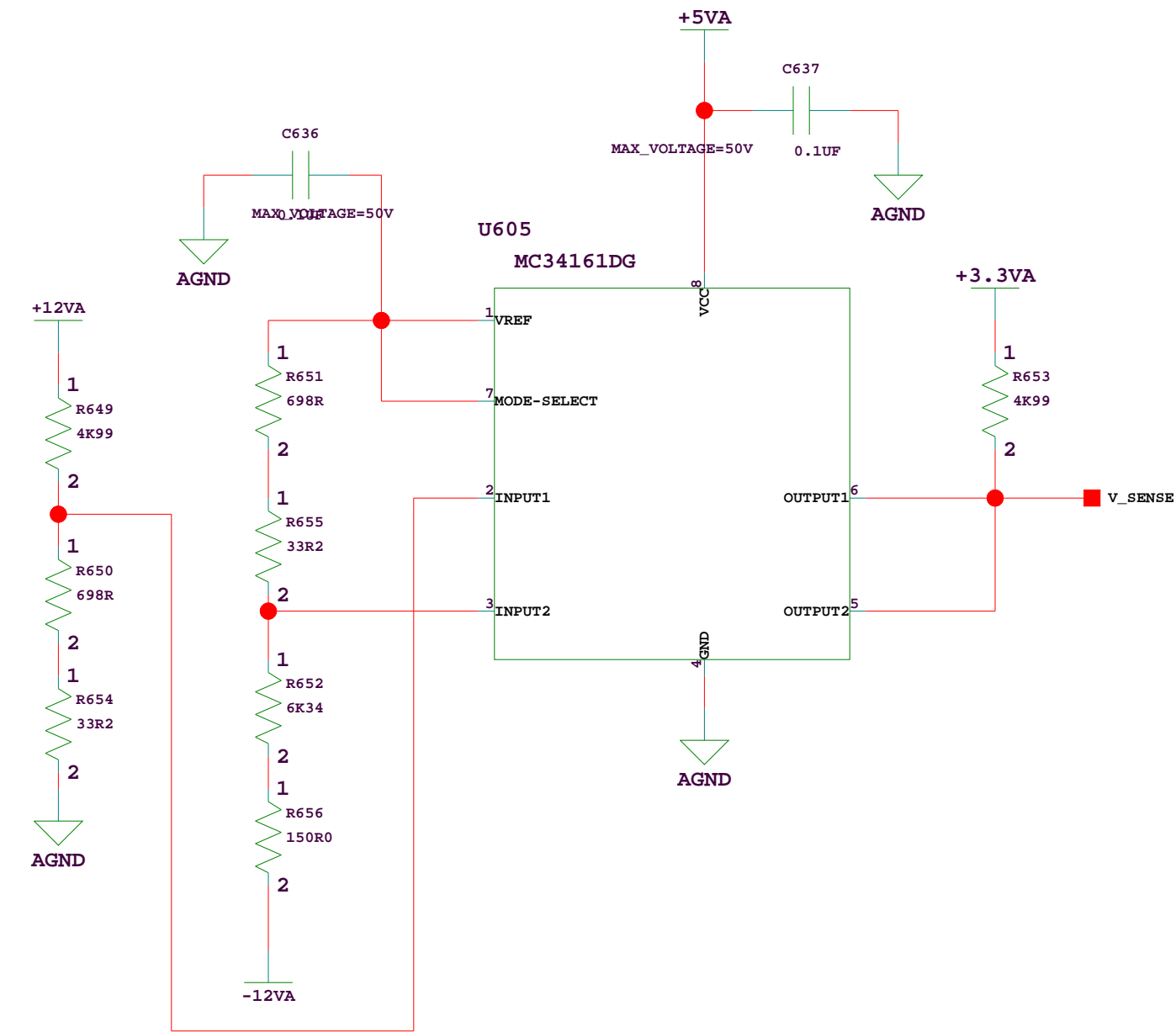
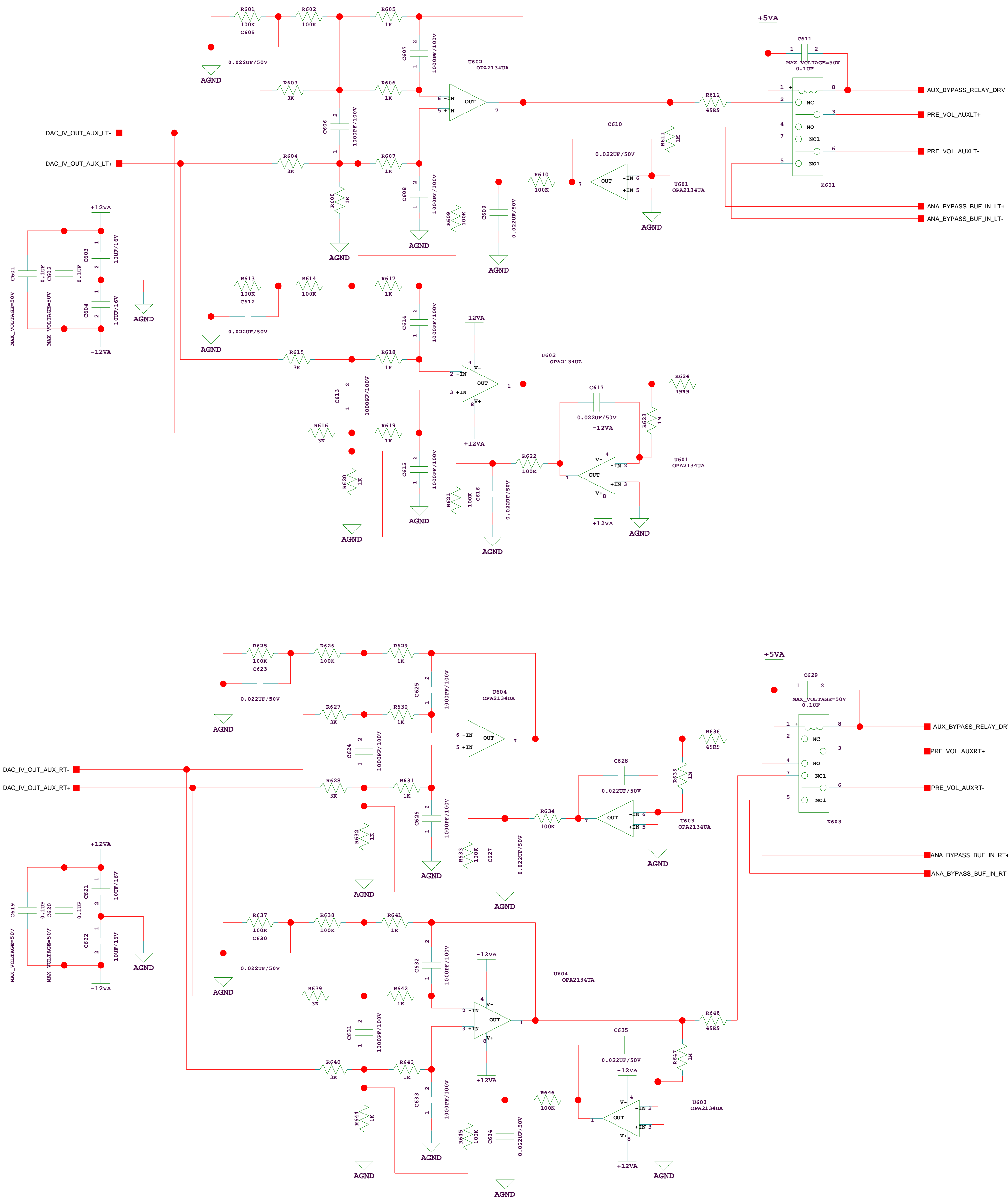
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TITLE

R6703-04-02S

SIZE

DWG NO

D

REV

A

DRAWN BY: R.K.

SCALE

SHEET 6 of 17

11-12-2012.15:50



11-12-2012_15:50

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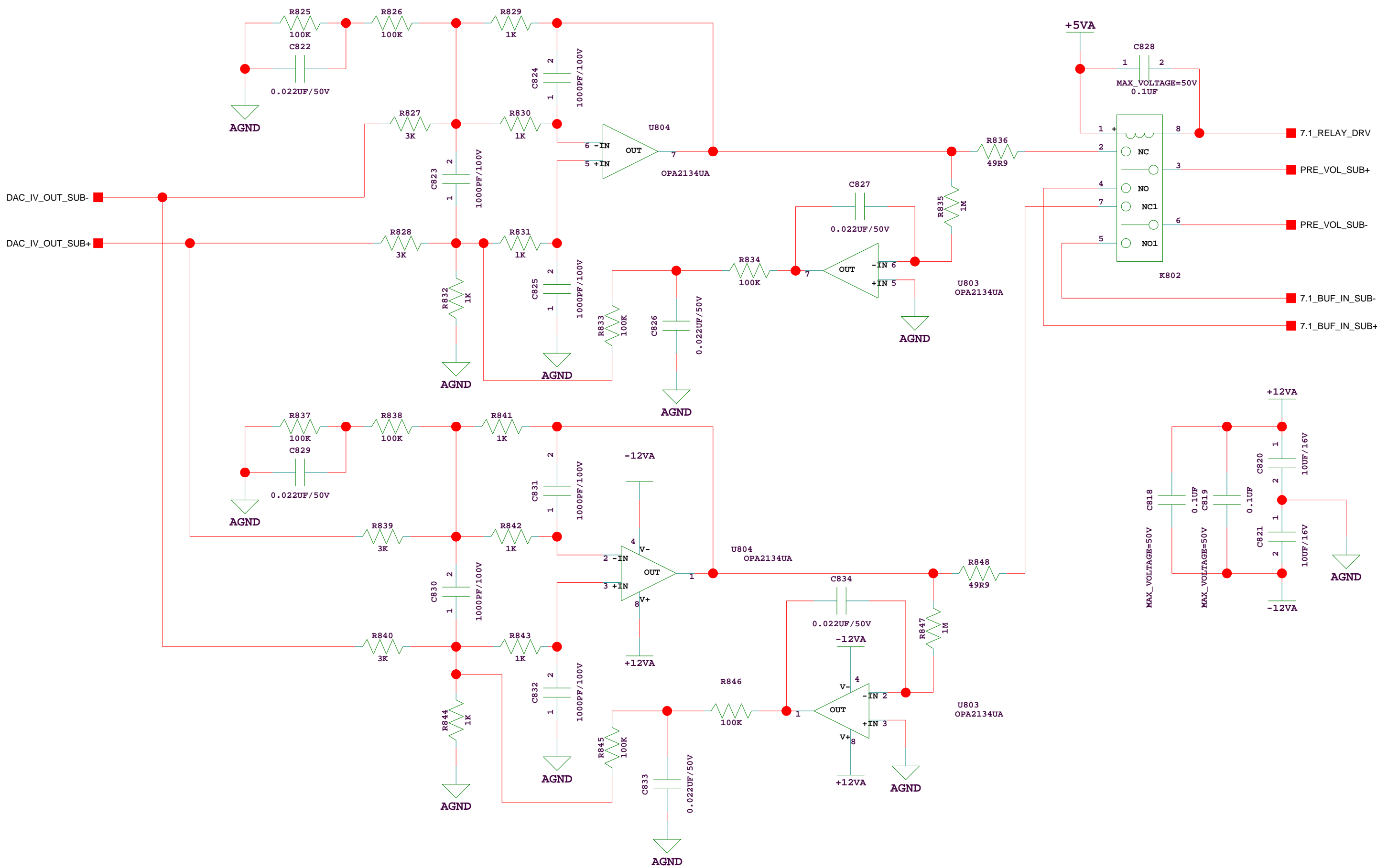
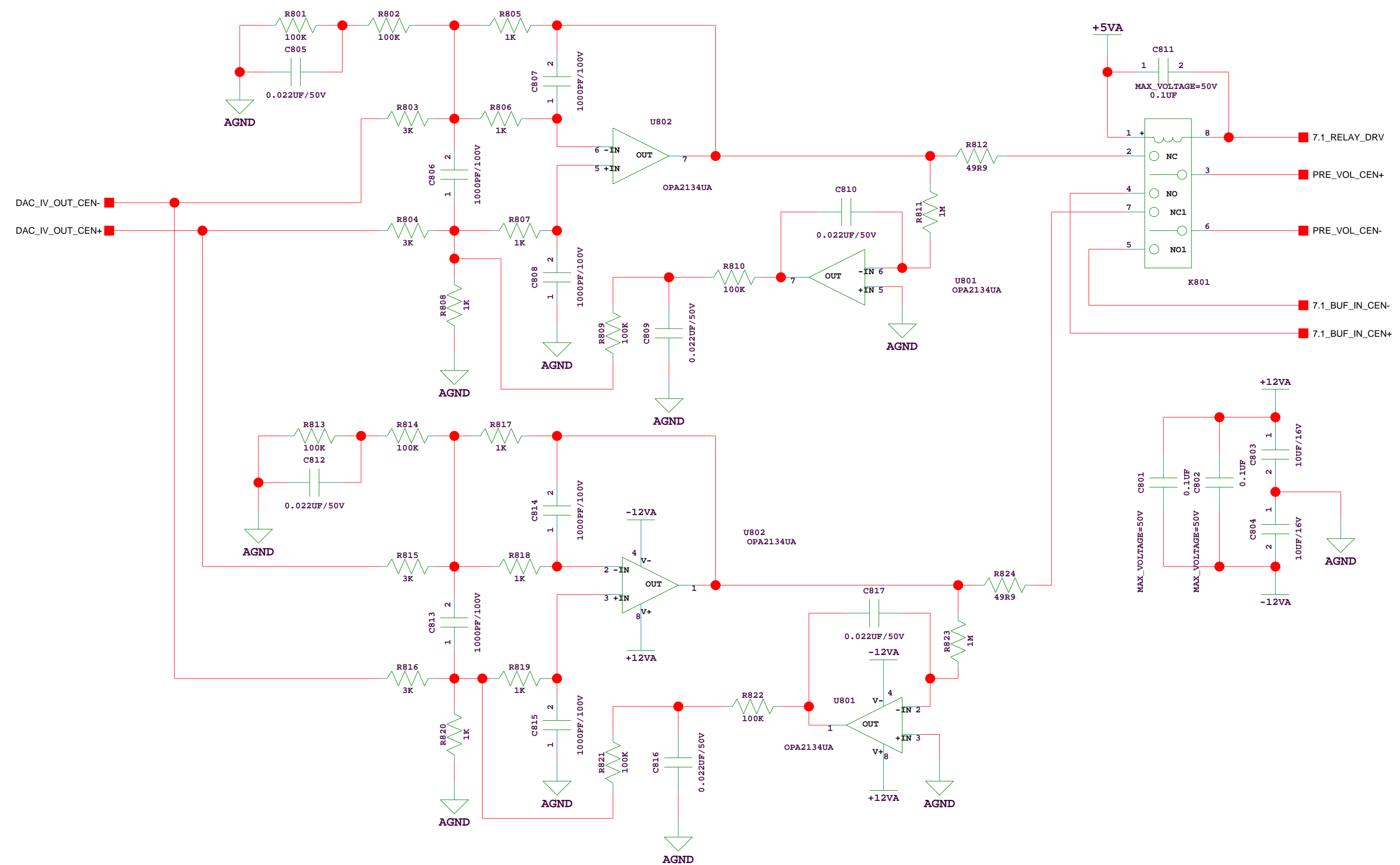
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TITLE			
R6703-04-02S			
SIZE	DWG NO	REV	
D		A	
DRAWN BY: R.K.		SHEET 8 of 17	11-12-2012.15:50

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SPI engine from CPLD

To MAIN channel outputs

TITLE

R6703-04-02S

SIZE

D

DWG NO

REV

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DRAWN BY: R.K.

SCALE

SHEET 9 of 17

11-12-2012.15:50

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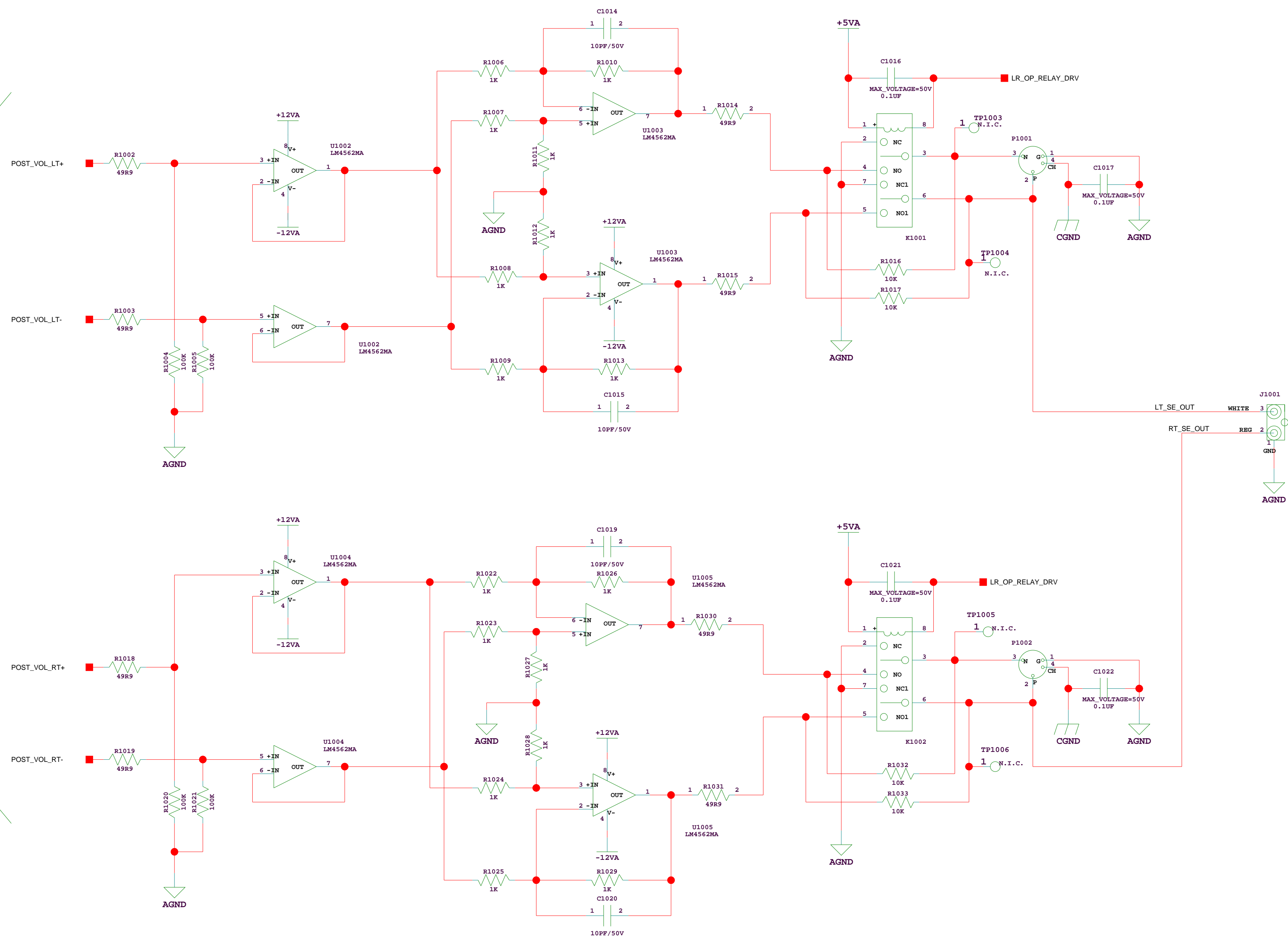
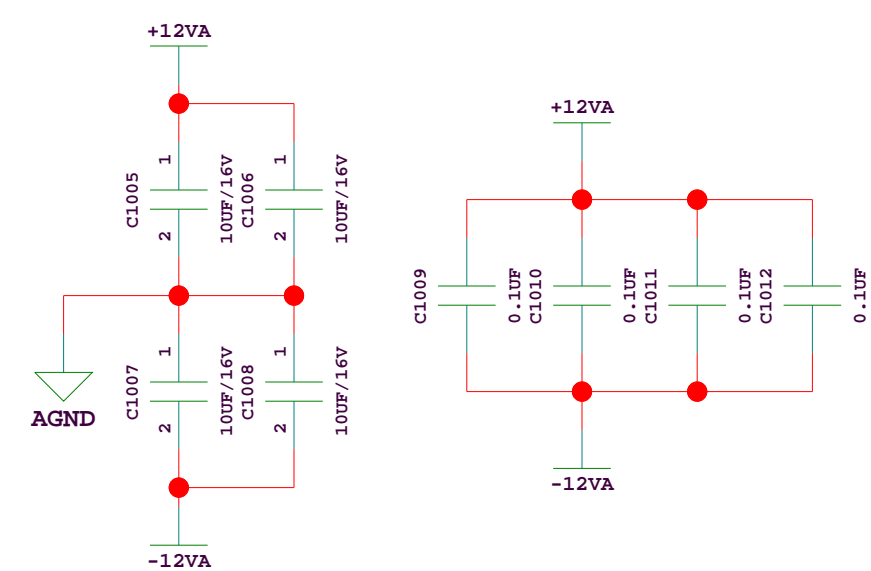
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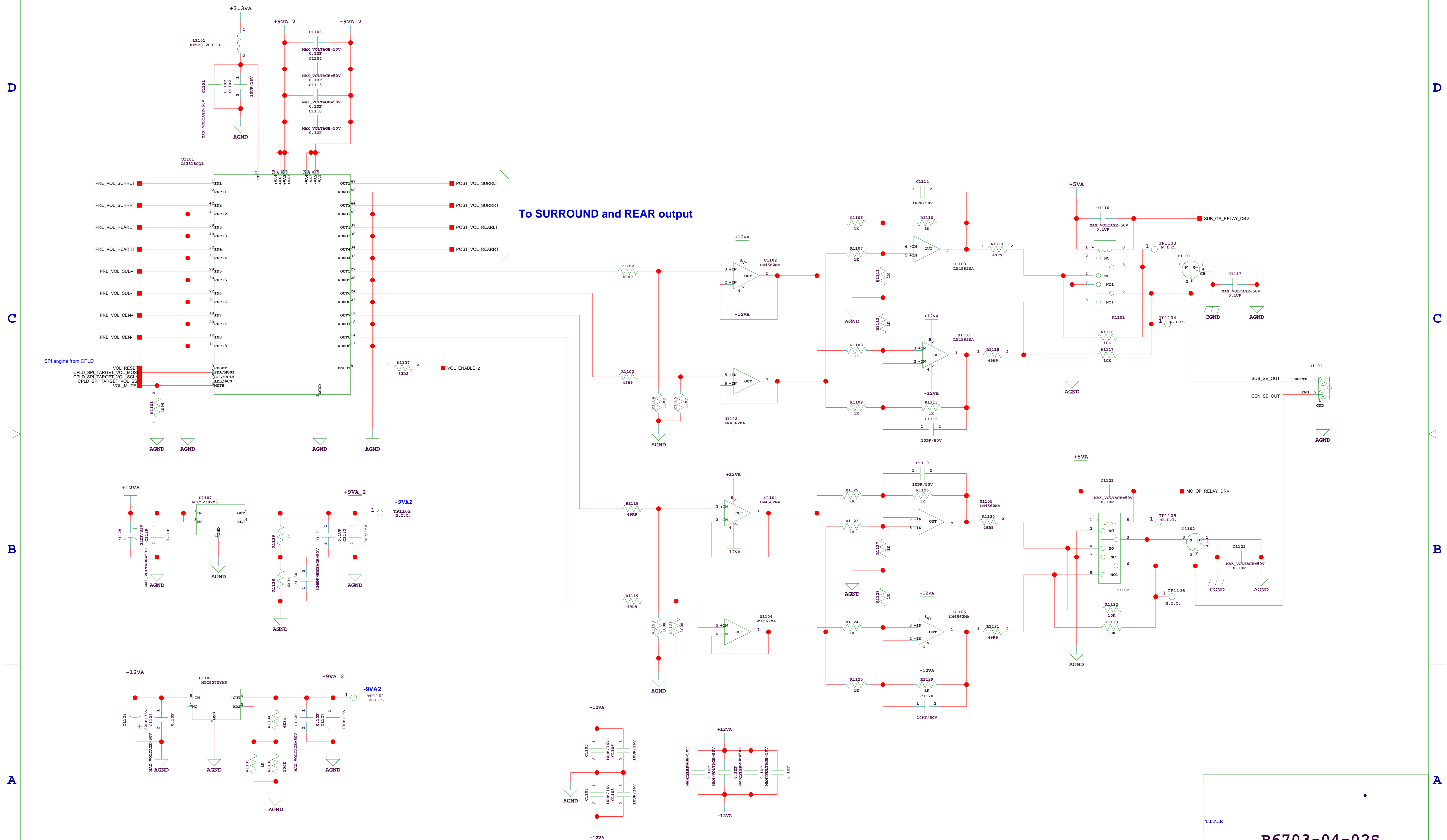
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MAIN LT and RT channels from vol control Page 9



TITLE		
R6703-04-02S		
SIZE	DWG NO	REV
D		A
DRAWN BY: R.K.		11-12-2012 16:00
SCALE	SHEET 10 of 17	



TITLE		
R6703-04-02S		
SIZE D	DWG NO	REV A
SCALE	SHEET 11 of 17	11-12-2012 15:50

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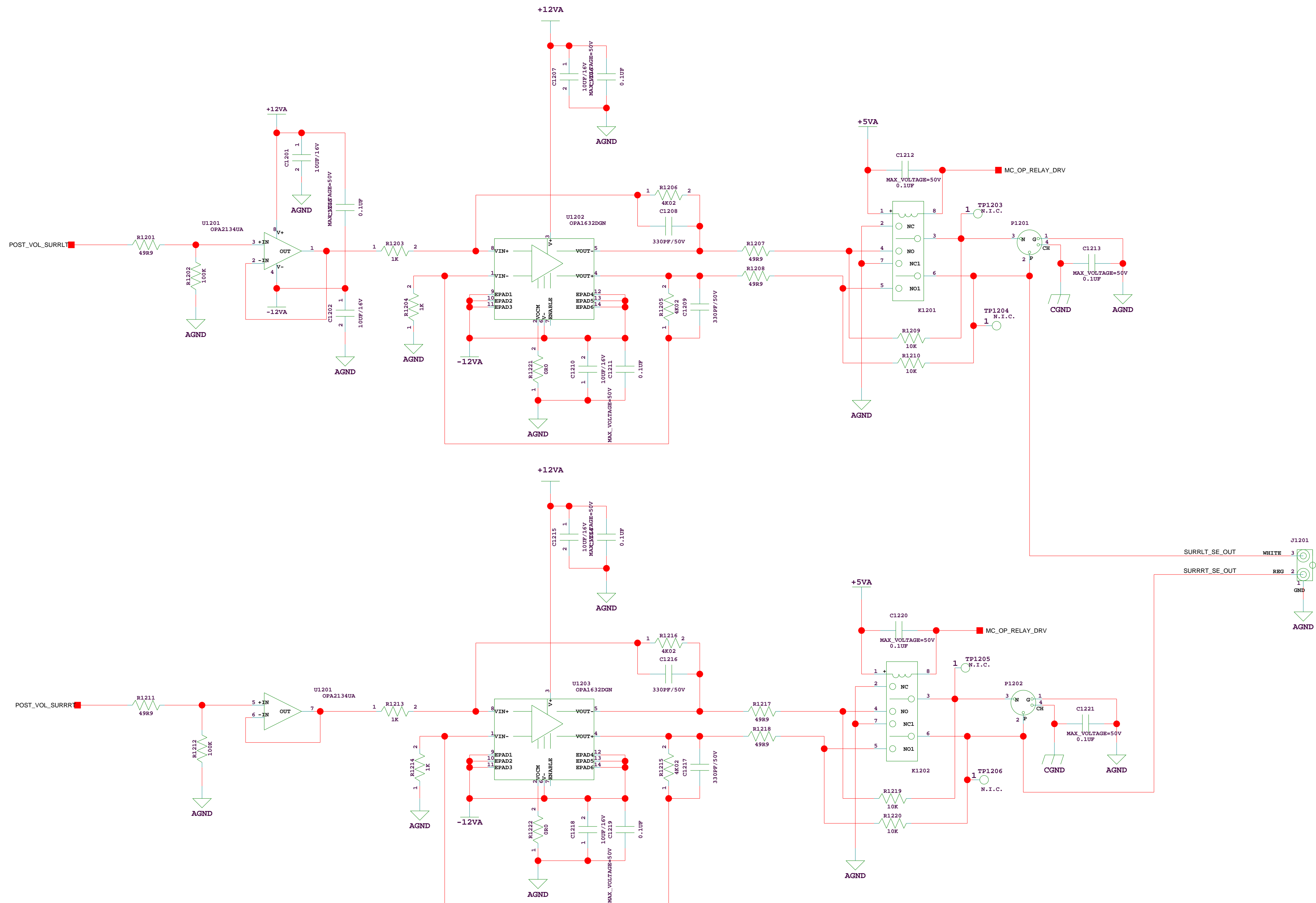
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From Vol chip2



TITLE		
R6703-04-02S		
SIZE	DWG NO	REV
D		A
DRAWN BY: R.K.		11-12-2012 15:50
SCALE	SHEET 12 of 17	

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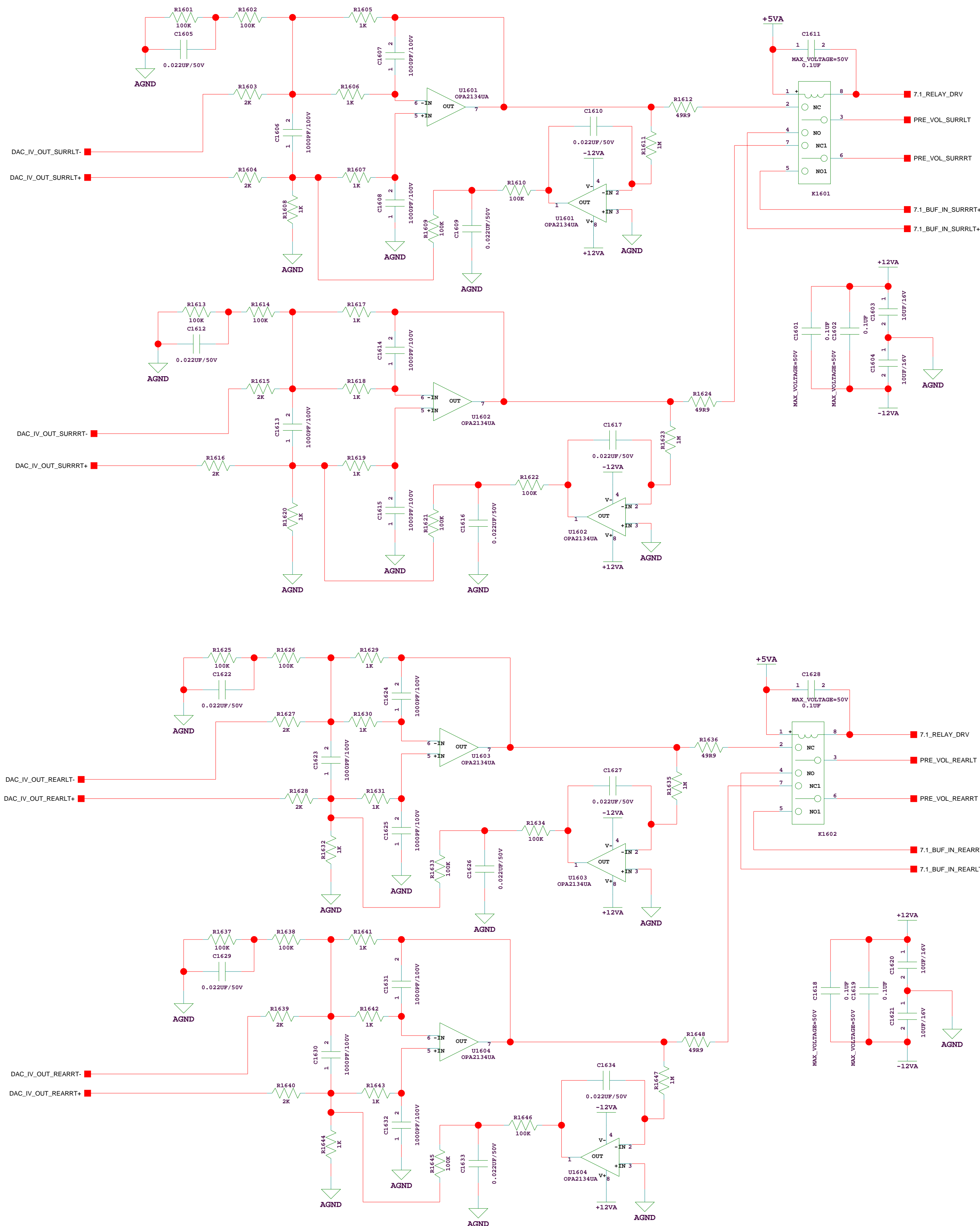
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TITLE		
R6703-04-02S		
SIZE	DWG NO	REV
D		A
DRAWN BY: R.K.		SCALE
SHEET 16 of 17		11-12-2012.15:50

ECN HISTORY:

ECN351 - 2007/12/18: Rev. Document number to R6703-02-01S
Change all instances of G201-1367-XR00 (1000pF 100V 1%) to G201-1461-XR00 (1000pF 100V 2.5%).
Added missing Silpads (G307-1078-XR00) for BOM

ECN359 - 2008/01/11: Rev. Document number to R6703-02-02S
Change U102 to G208-1407-XR00 (BOM Error).
Change R517 - R520 and R522 - R525 to G211-1764-XR00.
Change R1203, R1204, R1213, R1214, R1303, R1304, R1313, R1314 to G211-1728-XR00.
Change R1205, R1206, R1215, R1216, R1305, R1306, R1315, R1316 to G211-1926-XR00.

ECN368 - 2008/02/01: Rev. Document number to R6703-02-03S
Change R108 to N.I.C.
Change R517 - R520 and R522 - R525 to G211-1944-XR00.
Change R319, R320, R321, R322, R417, R418, R419, R420, R422, R423, R424, R425 to G211-1837-XR00.
Change R603, R604, R615, R616, R627, R628, R639, R640 to G211-1736-XR00.
Change R703, R704, R715, R716, R727, R728, R739, R740 to G211-1736-XR00.
Change R803, R804, R815, R816, R827, R828, R839, R840 to G211-1736-XR00.
Change U902, U903, U904, U905, U1002, U1003, U1004, U1005, U1102, U1103, U1104, U1105 to 51150978-00.

ECN370 - 2008/02/05: Rev. Document number to R6703-03-00S
Board revision for mechanical issues.
Added R1501 (NIC) as alternate for L1503, L1504 if required.

ECN396 - 2008/04/29: Rev. Document number to R6703-03-01S
Revised for EMI issues.

ECN428 - 2008/08/27: Rev. Document number to R6703-03-02S

ECN432 - 2008/10/06:	Change C529-C532 and C542-C545 to G201-1461-XR00
ECN329 - 2008/12/02:	Change P102, P1505, TP202, TP204, TP301, TP901-906, TP1003-1006, TP1101-1106, TP1203-1206, TP1303-1306, TP1502, TP1505-1512, TP1514 to N.I.C.
	Change D1505 - D1512 to p/n 51096528-00.

ECN457 - 2009/01/14: Rev. Document number to R6703-03-03S
Change C1413, C1414, C1415, C1436 to N.I.C.


ECN512 - 2009/06/19: Rev. Document number to R6703-04-00S
Board revision for EMI and manufacturing issues. See ECN for details.

ECN655 - 2012/07/05: Rev. Document number to R6703-04-01S
Replace C1561 G201-1015-XR00 with 51622067-00.

1. AVR
2. DAE-7 and PS
3. ANALOG CPLD, AUX LT/RT SRC and DAC
4. CEN, SUB, MAIN LR SRC and DAC
5. SURR, REAR, SRC and DAC
6. AUX LR LPF
7. MAIN LR LPF
8. CEN_SUB LPF
9. AUX, MAIN LR VOL and AUX OUTPUT
10. MAIN LR OUTPUT
11. SURR, REAR, SUB, CEN VOL and CEN, SUB OUTPUT
12. SURR OUTPUT
13. REAR OUTPUT
14. DIGITAL CPLD
15. ANALOG POWER SUPPLY
16. SURR, REAR LPF

PARTS FOR ACCOUNTING & BOM

PCN	31073832-04	8103804	PCN DELTA 89900 NOTHER BOARD 84703804
M01	0307-1078-0000	01-09-2201-A	INTERNAL PAD TO-220 ESI-PAD 1000
M02	0307-1078-0000	01-09-2201-A	INTERNAL PAD TO-220 ESI-PAD 1000
M03	0307-1078-0000	01-09-2201-A	INTERNAL PAD TO-220 ESI-PAD 1000

		
<p>TITLE</p> <p style="text-align: center; font-size: 2em; font-weight: bold;">R6703-04-01S</p>		
<p>SIZE</p> <p>D</p>	<p>DWG NO</p>	<p>REV</p> <p>A</p>
<p>SCALE</p>	<p>SHEET 17 of 17</p>	<p>7-5-2012_11:06</p>