

D

C

B

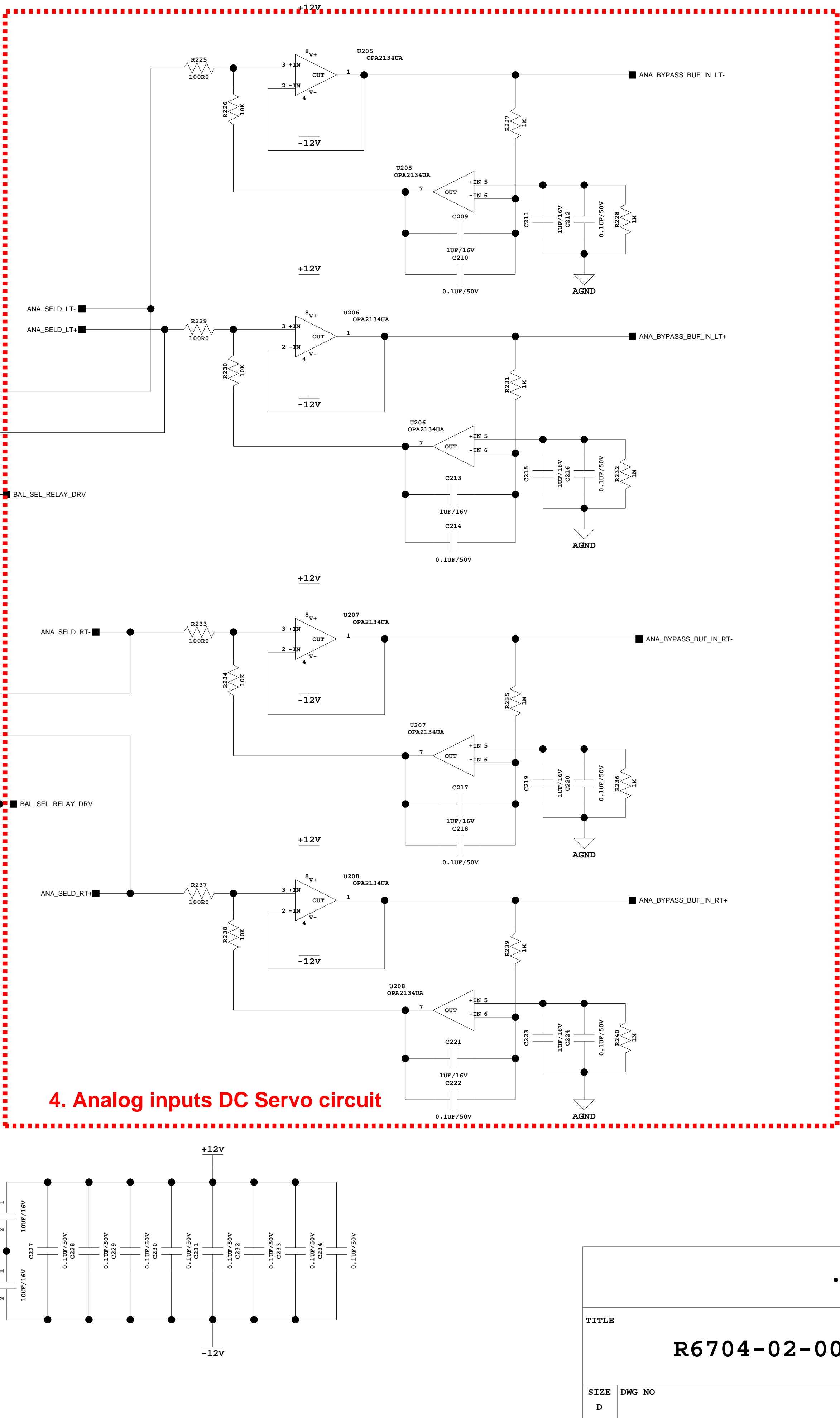
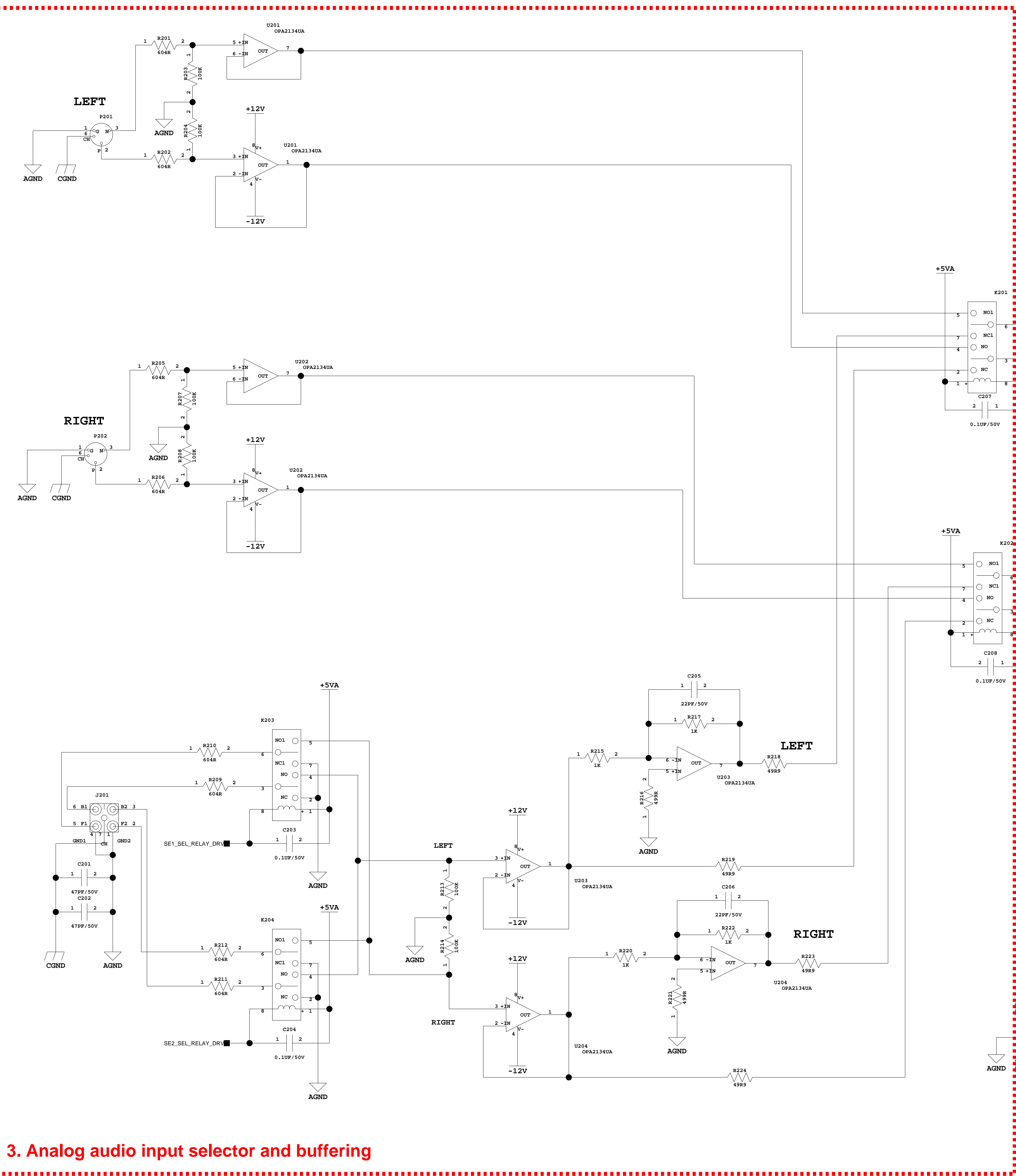
A

D

C

B

A



TITLE		
R6704-02-00S		
SIZE	DWG NO	REV
D		0
DRAWN BY: R.K.		SHEET 2 of 5
SCALE		10-24-2008 12:09

D

C

B

A

D

C

B

A

## 5. 7.1 Analog Bypass input buffersin SE and BAL modes

## 6. Analog input bypass and 7.1 bypass input selector for Left/Right channels

- To Motherboard (R6703) P601

TITLE

R6704-02-00S

SIZE

DWG NO

D

REV

0

DRAWN BY: R.K.

SCALE

SHEET 3 of 5

10-24-2008 12:09





8

7

6

5

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3

2

1

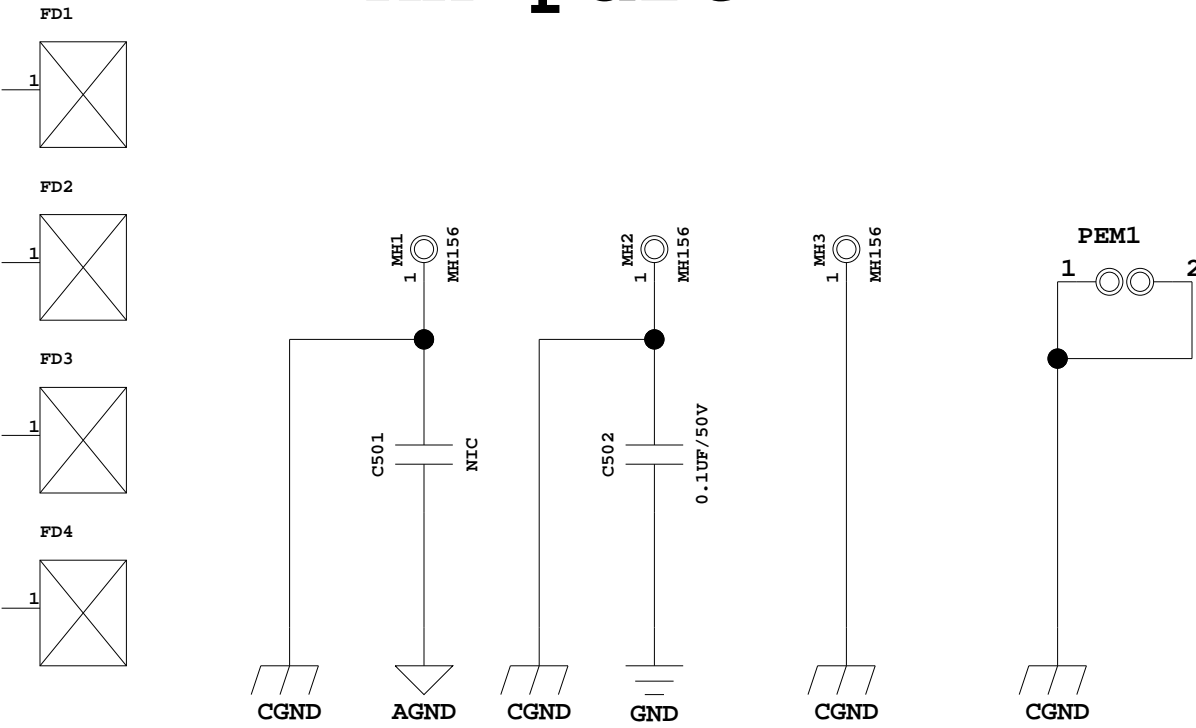
ECN:

ECN357 (01/10/2008): Change U102, U103, U104, U105 to G203-1461-XR00  
Change U401, U402, U407 to P/N 51150978-00  
Change C437, C438 to P/N G201-1112-XR00  
Change R409, R410, R419, R420 to P/N G211-1790-XR00  
Change C401, C402, C403, C404 to Do not populate (N.I.C)

ECN363 (01/21/2008): Change R201, R202, R205, R206, R209, R210, R211, R212, R301, R310, R317, R319, R323, R332, R341, R350 to G211-1916-XR00

ECNxxx (09/24/2008):   Add D101, D102

MH part



TITLE			R6704-02-00S		
SIZE	DWG NO				REV
D					0
SCALE		SHEET 5 of 5		10-24-2008 12:09	

DRAWN BY: R.K.