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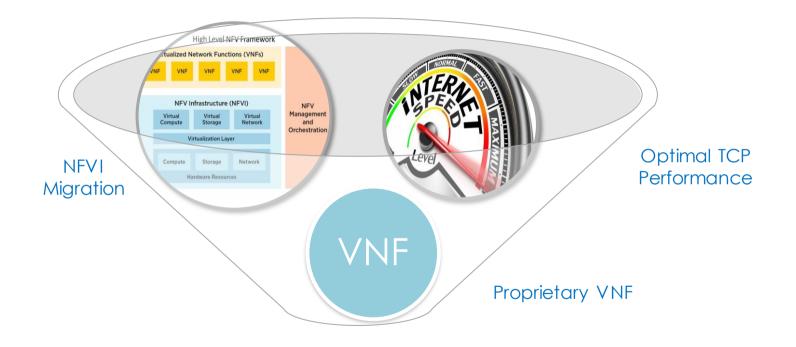
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Problem Domain



Deployment Scenario (Simplified)

Customer-Defined Test Cases

Speed Test Client

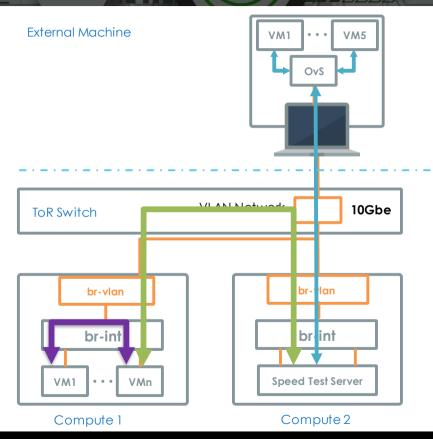
VM
External Network

Speed Test Server
Compute Node

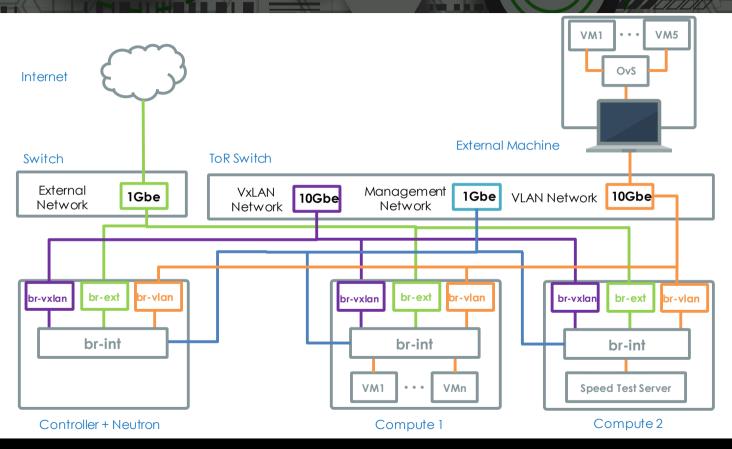
VM
Compute Node 1

VM
Compute Node 1

VM
Compute Node 1



VNF Deployment Scenario (Full)



Anatomy of a VNF Compute Node

Compute Node

VNF – Virtualized Broadband Speed Test Server

iPerf3

Virtio-net

CentOS 7 - 4.5.4

Guest Software Stack

OpenStack Kilo 2015.1.1

QEMU 2.5.0

KVM 2.3.0.5fc21

Open v Switch 2.5.90

DPDK 16.04

Fedora 21 - 4.1.13-100.fc21.x86_64

Host Software Stack

Intel® Xeon E5-2680 v2@ 2.8GHz

Intel® Ethernet Controller 1350 BT2

Intel® 82599ES 10 Gigabit Ethernet Controller

Hardware

Optimizations: Baseline

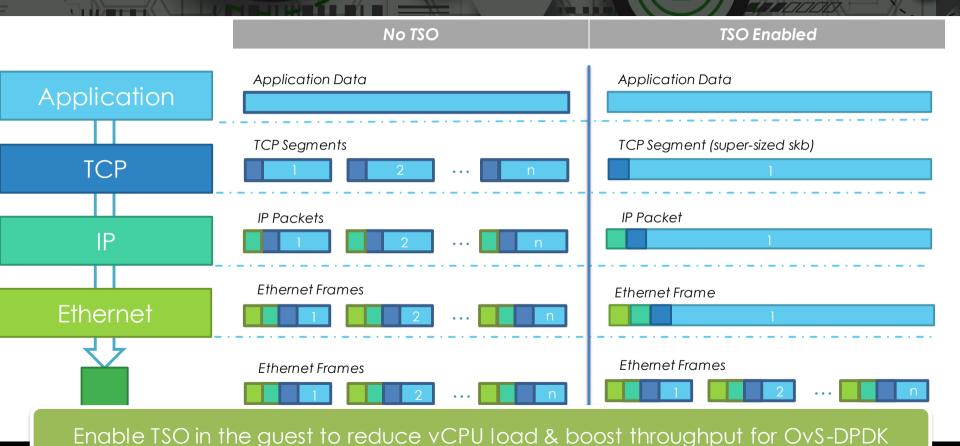
- Enable Hugepages
 - Reduce the impact of Translation Lookaside Buffer (TLB) misses
- ✓ Affinitize DPDK PMDs, and QEMU's virtual CPU threads
 - Maximize CPU occupancy
 - Minimize cache thrashing
- Enable NUM A support for OvS-DPDK
 - Eliminate QPI traversal performance penalties

Additional details available here

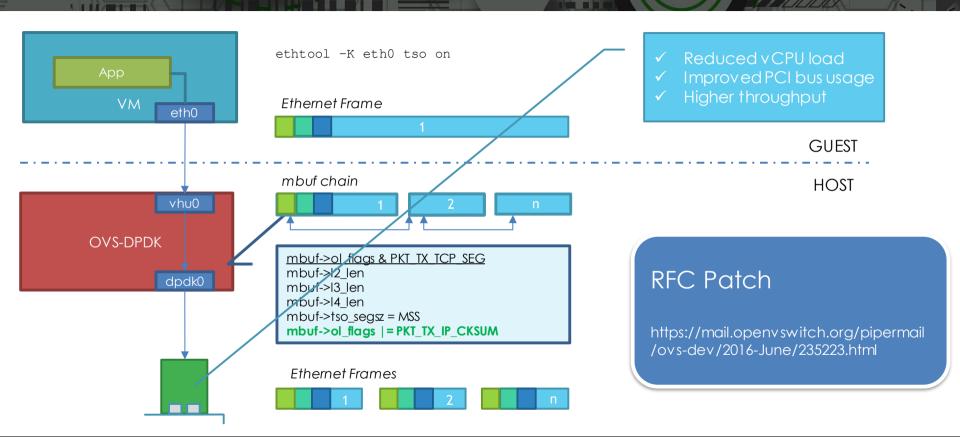


https://github.com/openvswitch/ovs/blob/master/INSTALL.DPDK-ADVANCED.md

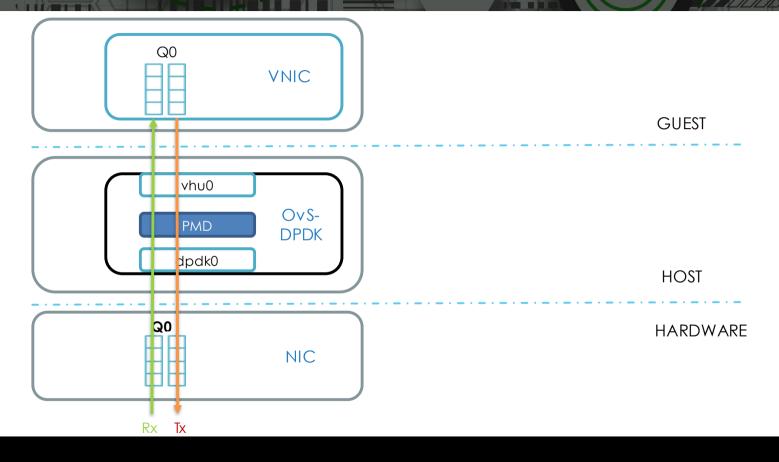
Optimizations: TCP Segmentation Offload (TSO) Overview



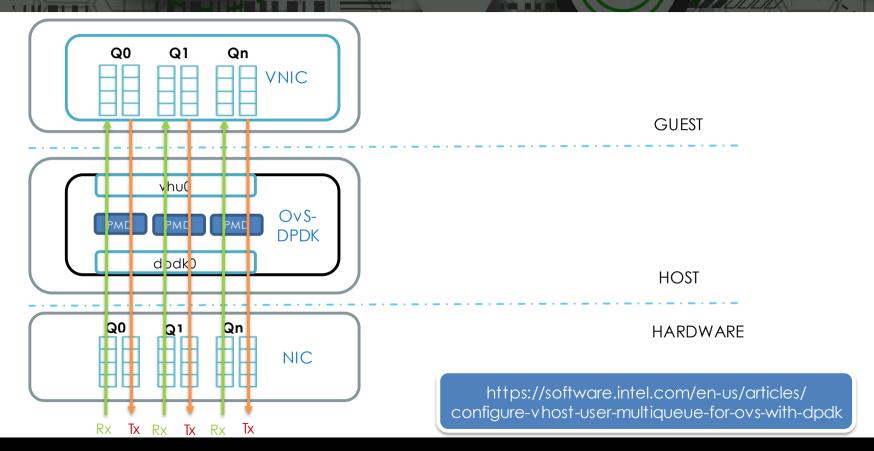
Optimizations: TCP Segmentation Offload



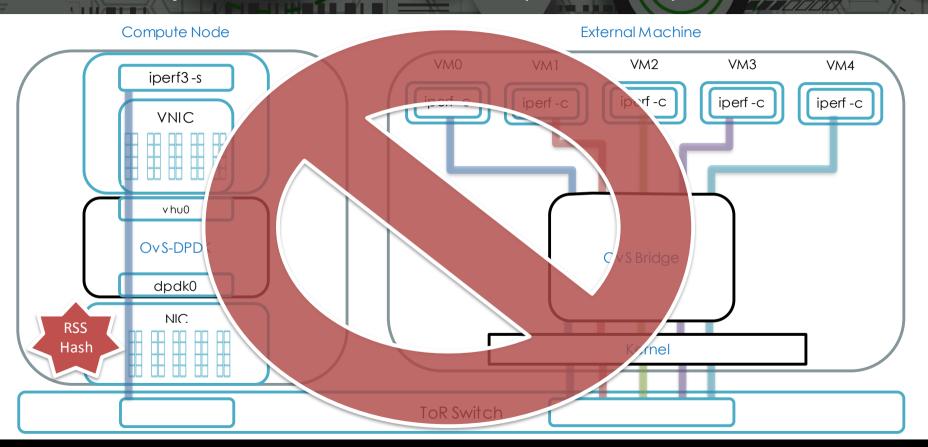
TCP Optimizations: Multi Q (Overview)



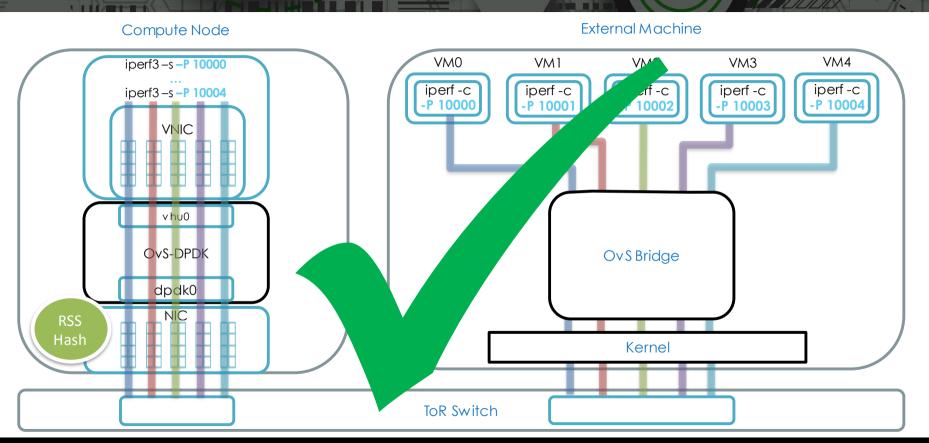
TCP Optimizations: Multi Q (Overview)



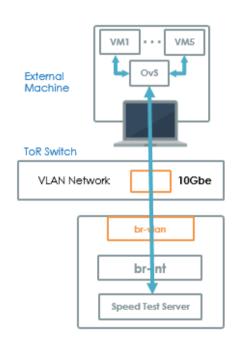
TCP Optimizations: Multi Q (Problem)



TCP Optimizations: Multi Q (Solution)

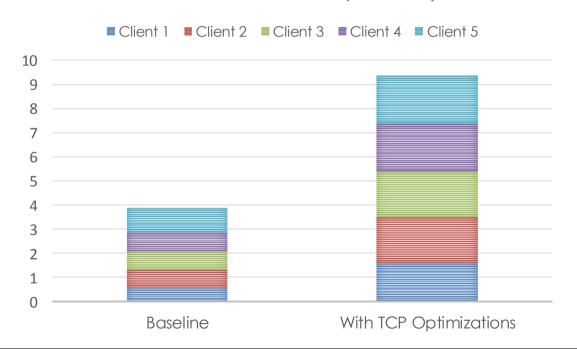


Performance Results – Test Case #1



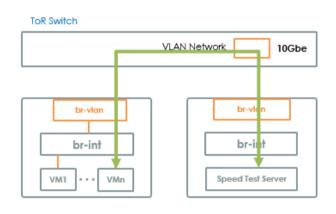
5 x EXTERNAL VM -> SINGLE SPEED TEST SERVER

AVERAGE SPEED TEST SERVER BANDWIDTH (GBPS)



Performance Results – Test Case #2

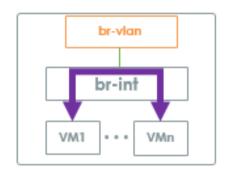
AVERAGE SPEED TEST SERVER BANDWIDTH (GBPS)



SPEED TEST SERVER -> VM
- SEPARATE COMPUTE NODES -



Performance Results – Test Case #3



VM -> VM SAME COMPUTE NODE

AVERAGE SPEED TEST SERVER BANDWIDTH (GBPS)



Optimization Summary

Baseline Optimizations

- Enable hugepages
- Per-port/RxQ PMD
- Affinitize workloads
- Incorporate NUMA support

Avail of Offloads

- TSO = reduced v CPU load
- TSO = efficient PCI bandwidth consumption

Utilize Multi Q for Guests

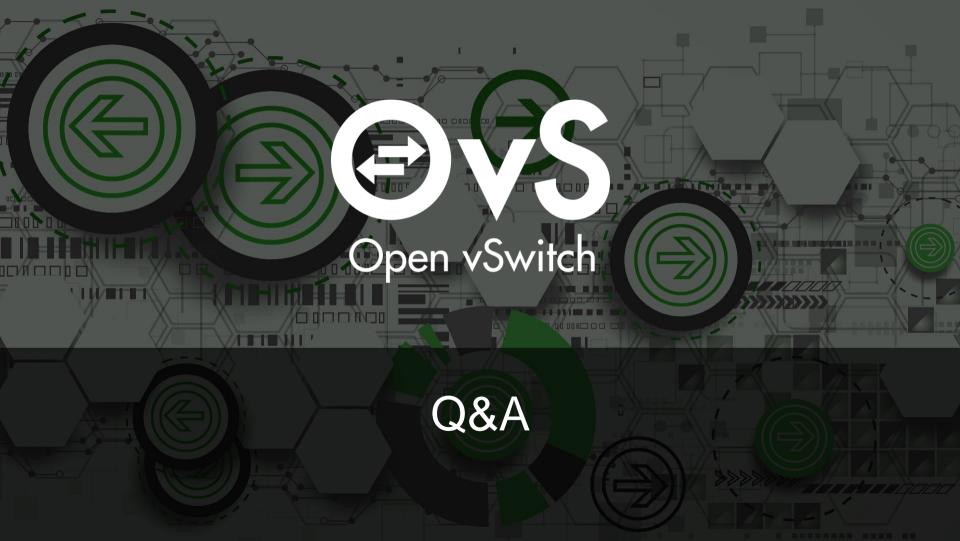
- Saturateline
- Push bottleneck back to the network

Next Steps

- Release non-RFC TSO Support Patch
- Add support for TSO + Tunnels

References

- https://www.measurementlab.net/publications/understanding-broadbandspeed-measurements.pdf
- https://software.intel.com/en-us/articles/configure-vhost-user-multiqueue-forovs-with-dpdk
- http://openvswitch.org/pipermail/dev/2016-June/072871.html





System Configuration: Hardware

Hardware Platform Specification

Server	Processor	Hard Drive	Memory	NIC
Compute1	Intel® Xeon® E5-2680 v2 at 2.80 GHz, 40 logical cores	1 TB	DDR3 1600 MHz	 Intel Ethernet Controller I350 BT2 (management and public networks) Intel® 82599 ES-10 Gigabit Ethernet Controller (VxLAN and VLAN networks
Compute2	Intel® Xeon® E5-2680 v2 at 2.80 GHz, 40 logical cores	1 TB	DDR3 1600 MHz	 Intel Ethernet Controller 1350 BT2 (management and public networks) Intel® 82599 ES-10 Gigabit Ethernet Controller (VxLAN and VLAN networks

System Configuration: Software

Software Ingredients

#	Software BOM Item	Component
1	Operating System	Fedora * 21, Kernel 4.1.13-100.fc21.x86_64
2	Hypervisor	Compute nodes : QEMU-KVM, QEMU 2.5.0
3	Virtual Switch	Compute nodes : Open vSwitch 2.5.9 + TSO RFC patch
4	Packet Processing Acceleration	DPDK v16.04
5	Virtua lized Infrastructure Manager	OpenStack* Kilo 2015.1.0

System Configuration: BIOS Settings 1/2

Aptio Setup Utility - Copyright (C) 2010 - 2013 Ame Processor Configuration

Intel(R) QPI Link Frequency	8.0 GT/s
Intel(R) QPI Frequency Select	[Auto Max]
Intel(R) Turbo Boost Technology	[Enabled]
Enhanced Intel SpeedStep(R) Tech	[Enabled]
Processor C3	[Disabled]
Processor C6	[Enabled]
Intel(R) Hyper-Threading Tech	[Enabled]
Active Processor Cores	[A11]
Execute Disable Bit	[Enabled]
Intel(R) Virtualization Technology	[Enabled]
Intel(R) VT for Directed I/O	[Enabled]
Interrupt Remapping	[Enabled]
Coherency Support	Disabled
ATS Support	[Enabled]
Pass-through DMA Support	[Enabled]
Intel(R) TXT	[Disabled]
Enhanced Error Containment Mode	[Disabled]
MLC Streamer	[Enabled]
MLC Spatial Prefetcher	[Enabled]
DCU Data Prefetcher	[Enabled]
DCU Instruction Prefetcher	[Enabled]
Direct Cache Access (DCA)	[Enabled]
Extended ATR	[0x03]
PFloor Tuning	12
SMM Wait Timeout	20

Aptio Setup Utility - Copyright (C) 2010 - 2013 Ameri Power & Performance

Power & Performance

CPU Power and Performance Policu [Balance

[Balanced Performance]

IPerformance Optimization is strongly toward performance, even at the expense of energy efficiency.

IBalanced Performance Weights optimization toward performance, while conserving energy.

IBalanced Powerl Weights optimization toward energy conservation, with good performance.

IPowerl Optimization is strongly toward energy efficiency, even at the expense of performance.

Memory Configuration

Memory Configuration

Total Memoru 64 GR 65536 MR Effective Memory Current Configuration Independent DDR3-1600 Current Memory Speed Memory Operating Speed Selection Phase Shedding [Enabled] Memory SPD Override [Disabled] Patrol Scrub [Enabled] Demand Scrub [Enabled] Correctable Error Threshold [10]

▶ Memory RAS and Performance Configuration

System Configuration: BIOS Settings 2/2

Aptio Setup Utility - Copyright (C) 2010 - 2013 Americal Memory RAS and Performance Configuration Memory RAS and Performance Configuration Capabilities Memory Mirroring Possible YES Memory Rank Sparing Possible NO Memory Lockstep Possible YES Select Memory RAS Configuration IMaximum Performancel NUMA Optimized Enabled

PCI Configuration	copyright (c) 2010 - 2
PCI Configuration	
Maximize Memory below 4GB	[Disabled]
Memory Mapped I/O above 4GB	[Enabled]
Memory Mapped I/O Size	[Auto]
Onboard Video	[Enabled]
Legacy VGA Socket	[CPU Socket 1]
Dual Monitor Video	[Disabled]

Aptio Setup Utility - Copyright (C) 2010 - 2013 Socket 1 PCIe Ports Link Speed Socket 1, DMI [Gen 2 (5 GT/s)] Socket 1, PCTe Port 1a [Gen 3 (8 GT/s)] Socket 1, PCTe Port 1h [Gen 3 (8 GT/s)] Socket 1. TO Module [Gen 3 (8 GT/s)] Socket 1, SAS Module [Gen 3 (8 GT/s)] Socket 1, PCIe Port 3a [Gen 3 (8 GT/s)] Socket 1, PCIe Port 3c [Gen 3 (8 GT/s)]

