



Friday
August 2014 1

8 am.

ESDM IT I

9 am.

1) Why is EMI a vital problem? What are the ways to prevent EMI? (6)

→ Electromagnetic interference is an electromagnetic emission that causes a disturbance in

another piece of electrical equipment. EMI

can be attributed to a wide span of the electromagnetic spectrum including radio, DC &

even microwave frequencies, because anything that carries rapidly changing electrical current

gives off electromagnetic emissions.

- EMI compromises the performance of electrical

equipment by obstructing & degrading data sometimes even losing data completely.

4 pm.

5 pm. • Ways to prevent EMI

→ Keeping electronic devices away from heavy

machinery, motors & generators.

→ Using shielded cables.

7 pm. → Using EMI filters.

⇒

NOTES



2 Saturday
August 2014



8 am.

2) Compare Systems Approach &

9 am. Crisis approach.

→ Crisis Approach

10 am. • Design first, fix later

• Expensive add ons

11 am. • Band aid approach.

12 pm. Systems Approach

• EMC considered throughout the process

1 pm. • EMC problems addressed at prototype stage with testing.

2 pm. • Cost effective & desirable

• Mitigation techniques are simple at single

3 pm. stage or subsystem.

4 pm.

5 pm.

6 pm.

7 pm.



NOTES



Sunday

August 2014

3

8 am.

9 am. 3) Derive expression for Noise Voltage induced
into the centre conductor due to the outermost
10 am. current in the ?

11 am.

12 pm.

1 pm.

2 pm.

3 pm.

4 pm.

5 pm.

6 pm.

7 pm.

NOTES



4

Monday

August 2014



8 am.

4) In the fig below, the stray capacitance between conductors 1 & 2 is $50 \mu F$. Each conductor has capacitance to ground of $150 \mu F$. conductor

10 am. I has $10V$ ac signal at the frequency of $100KHz$ on it. Find the noise voltage

11 am. picked up by conductor 2 if its termination is at

12 pm. (a) ∞ resistance

(b) 1000Ω resistance

1 pm. (c) 50Ω resistance

2 pm. \Rightarrow (a) at ∞ resistance

$$R \geq \frac{1}{j\omega(C_{12} + C_{2G})}$$

3 pm. $V_N = \left(\frac{C_{12}}{C_{12} + C_{2G}} \right) V_I$

4 pm. $V_N = \left(\frac{50}{50 + 150} \right) \times 10$

5 pm. $V_N = 2.5V$

6 pm.

(b) at 1000Ω resistance

7 pm. when $R \leq \frac{1}{j\omega(C_{12} + C_{2G})}$

$$V_N = j\omega R C_{12} V_I$$

NOTES





Tuesday

August 2014

5

8 am.

$$V_N = j(2\pi 100k) \times 1000 \times 50 \times 10^{-12} + 10$$
$$V_N = 314 \text{ mV}$$

9 am.

10 am. (c) at 50Ω resistance

$$V_N = R \leq \frac{1}{j\omega(C_{12} + G_T)}$$

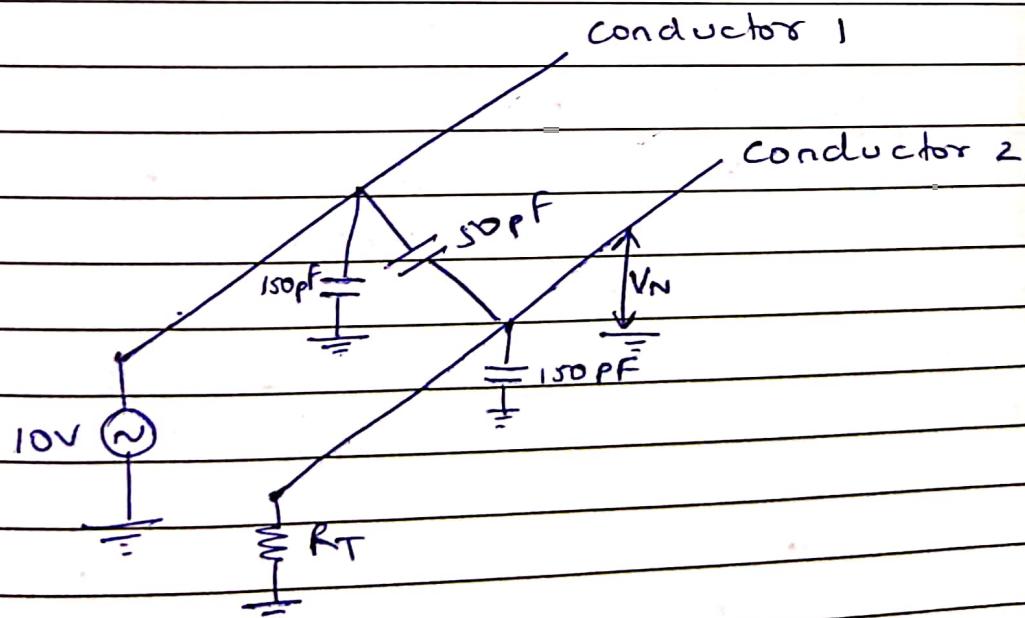
11 am.

$$V_N = j\omega R C_{12} V_1$$

$$V_N = j(2\pi 100k) \times 50 \times 50 \times 10^{-12} + 10$$
$$V_N = 15.7 \text{ mV}$$

1 pm.

2 pm.



3 pm.

4 pm.

5 pm.

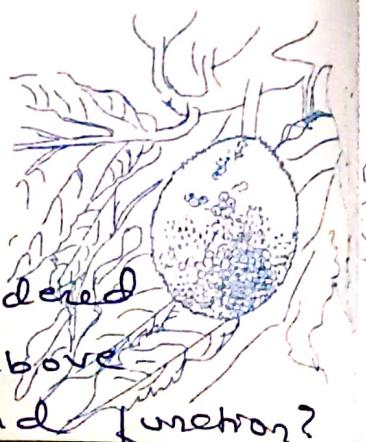
6 pm.

7 pm.

Notes



6 Wednesday
August 2014



- 5) Why is multipoint ground system considered to be a good choice at freq above 100 kHz? How does a hybrid ground function?
- ⇒ Multipoint ground systems are considered to be a good choice at freq above 100 kHz, bcz they minimize the ground noise.
- 11am. Voltage V_g in eqn $V_g = I_g Z_g$ by minimizing the ground impedance Z_g .
- 12pm. - In multipoint system shown circuits are connected to the nearest available low-impedance ground plane.
- The connection between each ckt & ground shd be as short as possible to minimize their impedance.
- Hybrid grounds: Increasing thickness of ground plane has no effect on its high frequency impedance bcz
- It is the inductance not the resistance of the grd that determines its impedance &
 - High frequency current only flow on the surface of the plane because of the skin effect.

- 6pm. Hybrid ground: when signal freq. covers wide range above & below 100 kHz then Hybrid grd may be a soln.
- w.r.t. 7pm. System grounding config. behaves differently at diff. freq.
- ① at low freq. C is high impedance & the cable shield is single-point grounded at load end only.
- at high freq. C is low impedance & cable shield is effectively grounded at both ends.
- Diagram:
-
- cable shielding config
- A cartoon illustration of a person playing soccer is visible on the left side of the page.



Thursday
August 2014

7

8 am. 6) Why is EMC important? Explain briefly the 2 main aspects of EMC.

9 am. What are the advantages of taking the systems approach in EMC design.

10 am. \Rightarrow EMC \rightarrow electromagnetic compatibility

11 am.

12 pm. EMC \rightarrow 2 aspects $\xleftarrow{\text{Emission}}$ $\xrightarrow{\text{Susceptibility}}$

Susceptibility: 1) Capability of device or circuit to respond to unwanted EM energy

2) Opposite of susceptibility is immunity

2 pm. immunity level

- upto which the device or circuit can work

3 pm. without degradation or with defined margin of safety.

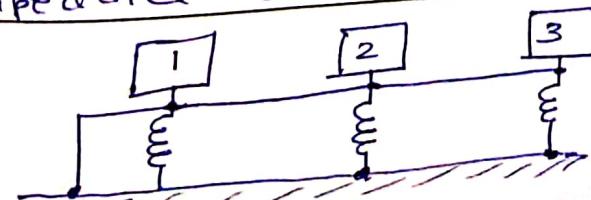
4 pm.

+ 5) The ground conductors provide a low impedance safety ground at 50/60 Hz, & ground isolation at high freq.

If the ground conductor is removed the

6 pm. product passes EMC, but that is a safety violation.

7 pm. An inductor or choke added in series with the ground wire will provide a low impedance at 50/60 Hz, while providing a high impedance at the much higher noise frequencies



8 Friday
August 2014



8 am.

AG) Susceptibility can be self regulating

9 am. - If a device is being affected by radiation,
one may stop buying the product.

10 am.

Emission

11 am. • it is the interference causing potential of the product.

12 pm. • Controlling emission from one product may eliminate an interference problem for many other products.

• emission is not self regulating.

2 pm. - its own emission → may not affect itself.

3 pm.

advantages of systems approach

4 pm. 1) EMC is considered throughout the design process

2) EMC problems are addressed at prototype

5 pm. stage with testing.

3) cost effective & desirable.

6 pm. 4) Mitigation techniques are simple at single stage or subsystem

7 pm.

NOTES





Saturday
August 2014 9

8 am.

9 am. 7) Discuss the effect of shield on capacitive coupling. Explain why an unshielded twisted pair cable will provide protection against capacitive pick up, when its terminations are balanced.

10 am. 11 am. \Rightarrow

12 pm.

1 pm.

2 pm.

3 pm.

4 pm.

5 pm.

6 pm.

7 pm.

NOTES





Monday

August 2014

11

8 am.

5) Define signal grounding with neat diagrams

9 am. Explain single pt ground systems with neat diagrams. why do single pt & ground systems fail at high freq.

10 am. 11 am. A ground is often defined as an equipotential point or plane that serves as a reference potential for a circuit or system → voltage definition of ground

12 pm. 1pm. Low impedance path for current to return to source → current definition of a ground.

2 pm.

• Search for diagram of signal grounding

3 pm.

Single pt ground systems:

4 pm. • There are 2 subclasses → with series connections with parallel "

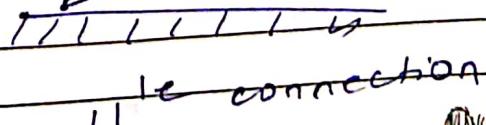
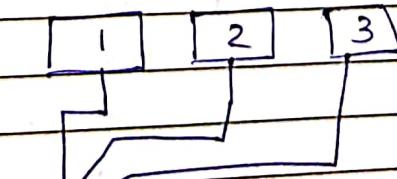
• The series connection is called common or daisy chain & 1st connection is called separate or star grounded system

5 pm.

6 pm.

7 pm.

NOTES



series connection



12

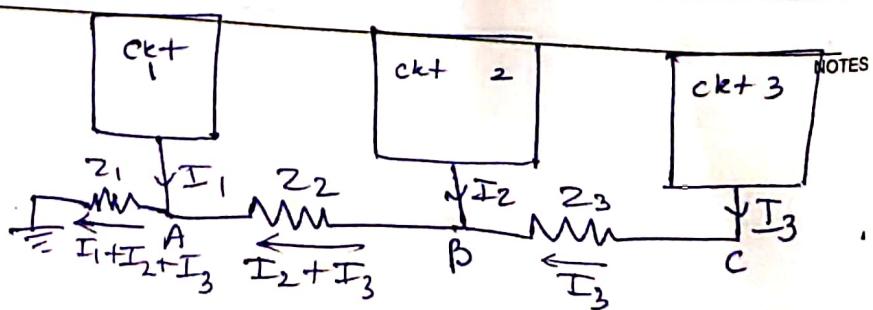
Tuesday
August 2014



- 8 am. • Single pt grounds are used at low frequencies from dc up to 20 kHz.
- 9 am. • They should usually not be used above 100 kHz, although sometimes this limit can be pushed as high as 1 MHz.
- 10 am. • With single-pt grounding, we control the ground topology to direct the ground current to flow where we want it to flow.
- 11 am. which decreased I_g in sensitive portions of ground.
- 12 pm. • from $V_g = I_g Z_g$ we see that as $I_g \downarrow$ voltage drop in that portion of ground \downarrow .
- 1pm. • 2pm.

3pm. Series Connection of all the individual ckt grounds

- 4pm. • The impedance Z shown represents those of the ground conductors & $I_1, I_2 \& I_3$ are the ground currents of circuits 1, 2 & 3 respectively.
- 5pm. • Potential at pt A $V_A = (I_1 + I_2 + I_3) Z_1$, & $V_c = (I_1 + I_2 + I_3) Z_1 + (I_2 + I_3) Z_2 + I_3 Z_3$
- 6pm. • This ckt is least desirable but is used
- 7pm. • bcz of simplicity.
- pt A is at lower potential than B & C





Wednesday

August 2014

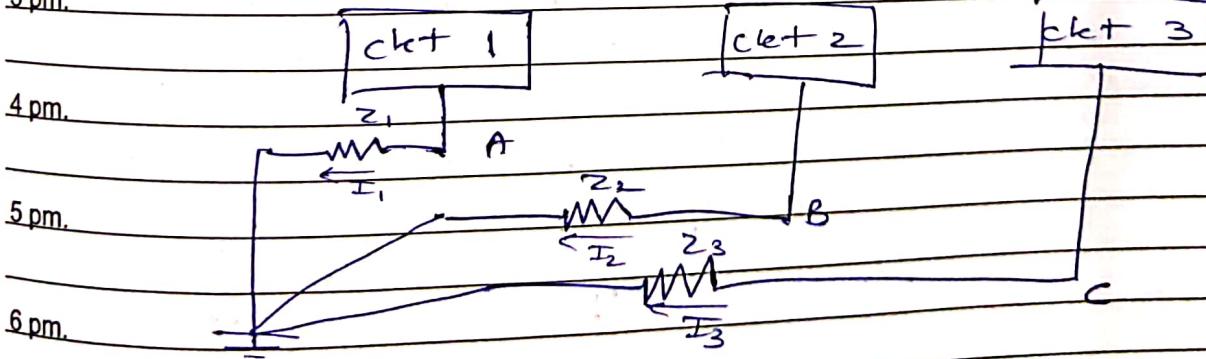
13

8 am.

Parallel ground system

- 9 am. • Most desirable. bcz no cross coupling occurs between ground current from different ckt. The potential at A & C are
- 10 am. $V_A = I_1 Z_1$,
 $V_C = I_3 Z_3$
- 11 am. • The ground potential of a ckt is now a function of the ground current & impedance of that ckt only.
- 12 pm. • This sys system can be mechanically cumbersome however because in a large system an unreasonable no of ground conductors may be necessary.

3 pm.



4 pm.

NOTES



14

Thursday
August 2014



8 am. at high frequencies

9 am. :) at high freq. there is no such thing.
called single pt ground.

10 am. 2) figure shown below shows what happens
when a single pt ground configuration
is attempted at high frequencies.

11 am. 3) Bcz of their inductance, the ground
conductors represent high impedances.

12 pm. 1) However at high freq. the impedance
of the stray capacitance between the

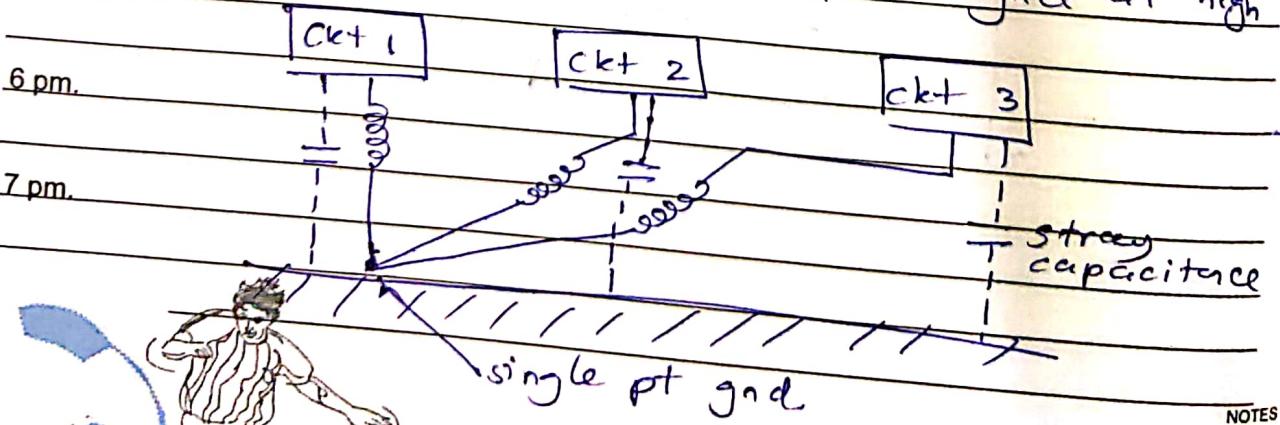
circuits & ground is low.

2 pm. 5) The ground current flows through the
low impedance of the stray capacitance

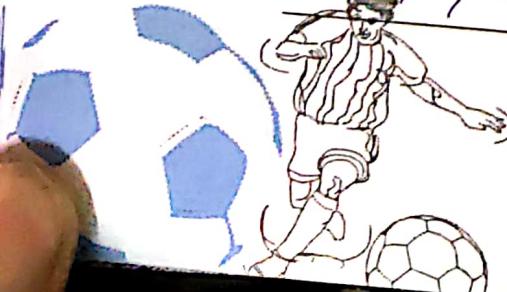
3 pm. & not the high impedance that results
from the inductance of the long ground

4 pm. conductors.

6) The result is a multipoint gnd at high F.



NOTES





Friday

August 2014

15

8 am.

9 am. ① Explain how shielding helps in preventing magnetic radiation from a conductor.

10 am. → The best way to protect against magnetic fields at the receptor is to

11 am. decrease the area of the receptor loop.

→ The area of interest is the total area

12 pm. enclosed by current flow in the receptor circuit.

→ If a non-magnetic shield placed around

1 pm. a conductor causes the current to return over a path that encloses a smaller area

2 pm. then some protection against magnetic fields will have been provided by the

3 pm. shield

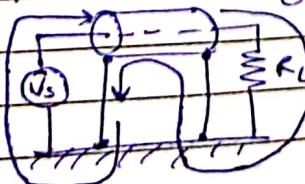
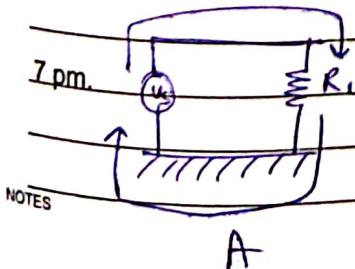
→ This protection is caused by reduced loop

4 pm. area and not by shielding properties of the shield

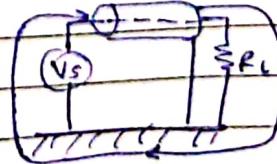
5 pm.

→ Figure illustrates the effect of a shield

6 pm. on the loop area of a circuit



B



C



16

Saturday

August 2014



8 am. • The source V_s is connected to the load R_L by a single conductor, using

9 am. a ground return path

- The area enclosed by the current is

10 am. the rectangle between the conductor & the ground plane.

11 am.

- in fig B, a shield is placed

12 pm. around the conductor & grounded at both ends. If the current returns through the shield rather than the ground plane Then the area of the loop is decreased.

1 pm. & a degree of magnetic protection is provided.

2 pm. 3 pm. • The current will return through the shield if the freq. is greater than 5 times the

4 pm. shield cutoff freq. as previously shows

5 pm. • A shield placed around the conductor & grounded at one end only, as shown

6 pm. in C, does not change the loop area & provides no magnetic protection.

7 pm. • The arrangement in B does not protect against magnetic fields at freq. below the shield cutoff freq. because then most of the current returns through the ground plane & not through the shield.

