ABHILASHA JHA

CONTACT

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QUALIFICATIONS

- Currently pursuing Bachelor of Technology (B Tech.) degree in Computer Science and Engineering from VIT University, Vellore.
- Have maintained a CGPA of 9.36 over past three semesters and had been awarded with the Certificate Of Merit in Winter Semester 2016 for exceptional academic performance (awarded to ten top ranking students of a given branch every year).
- Was awarded an S grade(awarded to top 1% for excellent performance in given subject) in nine out of twenty courses I have pursued so far, some of which include,Operating Systems,Database Management Systems,Engineering Physics,Object oriented programming and Calculus.

EDUCATION

Matriculation — April 2011 — March 2012

Kendriya Vidyalaya,Airforce Station,Jaisalmer

- Secured overall 10 CGPA in the CBSE AISSE Examination and A1 grade in all the subjects.

Intermediate/+2 — April 2013 — March 2014

Kendriya Vidyalaya,Andrews Ganj, New Delhi

- Secured 91.2 % in the CBSE AISSCE Examination.

SKILLS

- Have knowledge of Python,C,C++,Bash,MATLAB,Scikit-learn(the machine learning libraries in python),MySql,Mongodb,Linux(have been using it for past year as only operating system),HTML,CSS and Javascript

PROJECTS

- Created a Turing Machine simulator using C in Fall Semester 2016 for the course "Theory of computation and Compiler design" .New data structures were devised to simulate the pointer, the infinite tape and for storage of states.
- Created a Python tool which detected malicious and benign connections using the Decision Tree algorithm in the Winter Semester 2017 for the course "Networks and Communication" .It was trained with the DARPA datasets using the SciKit-learn and NumPy libraries in Python.
- Created an automated and self learning irrigation system to analyze the moisture in the soil and take informed decisions like number of equipments to keep switched on at a moment using Principal Component analysis and inform the farmer using Arduino,Matlab and NumPy libraries in Python in Winter Semester 2017 for the course "Internet Of Things" .
- Worked on "Performance analysis and comparison of multi threaded against single threaded programs" in Fall Semester 2016 for the course "Operating Systems". Our work majorly focused on whether all sequential programs can be parallelized and if they can be is there any difference in their performances in terms of

execution time. OpenMp library in C was used to create the parallel versions of the chosen sequential programs.

- Worked on "Energy Efficient Scheduling Of Virtual Machines for Cloud Data Centers" in Winter Semester 2017 for the course "Green and Energy Aware computing". Our work focused on visualizing the allocation of virtual machines to the servers as a three dimensional bin packing problem. We used Javascript and C++ to simulate the same.
- Created a "Library Database Management command line tool" for the course "Database Management Systems" in Fall Semester 2016 using Python and MySql.

WORK EXPERIENCE

COMPUTER SOCIETY OF INDIA CHAPTER,VIT UNIVERSITY

APRIL 2017 — PRESENT

TECHNICAL MENTOR

I was recently appointed as member of the board at the Computer Society of India chapter at VIT University. My responsibilities as the technical mentor include supervising the core committee members with their technical work,ensuring that the projects that are undertaken are completed in time, are properly documented and follow good programming practices.

INTERESTS

- Algorithm analysis,cryptographic and hashing algorithms, machine learning and image processing
- Competitive programming,participating in hackathons

REFERENCES

References available upon request.