

# COP 3402 Systems Software

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## OS Intro

**Thanks to Euripides Montagne**

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**1.- Absolute Loader**

**2.- Bootstrap Loader**

**3.- Operating System: Loading application programs**

**3.1- The Process concept.**

**3.2- Creating a process.**

**4.- Relocating Loaders**

**4.1- Relocation bits**

**4.2- Relocation maps (modification records)**

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## Assembly program

<u>Label</u>	<u>opcode</u>	<u>address</u>
01	; This is	
02	; a comment	
03 start	.begin	x200
04 here	LOAD	sum
05	ADD	a
06	STORE	sum
07	LOAD	b
08	SUB	one
09	STORE	b
0A	SKIPZ	
0B	JMP	here
0C	LOAD	sum
0D	HALT	
0E sum	.data	x000
0F a	.data	x005
10 b	.data	x003
11 one	.data	x001
12	.end	start

## object code file

Program name: start  
Starting address text: x200  
Length of text in bytes: x14  
Starting address data: x20A  
Length of data in bytes: 8

0001000000001001  
0010000000001001  
0011000000000111  
0001000000001000  
0100000000001000  
0011000000000110  
1001000000000000  
1000111111111000  
0001000000000001  
0111000000000000

0000000000000000  
0000000000000101  
0000000000000011  
0000000000000001

H  
e  
a  
d  
e  
r

Text

Data

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## Assembler object code file

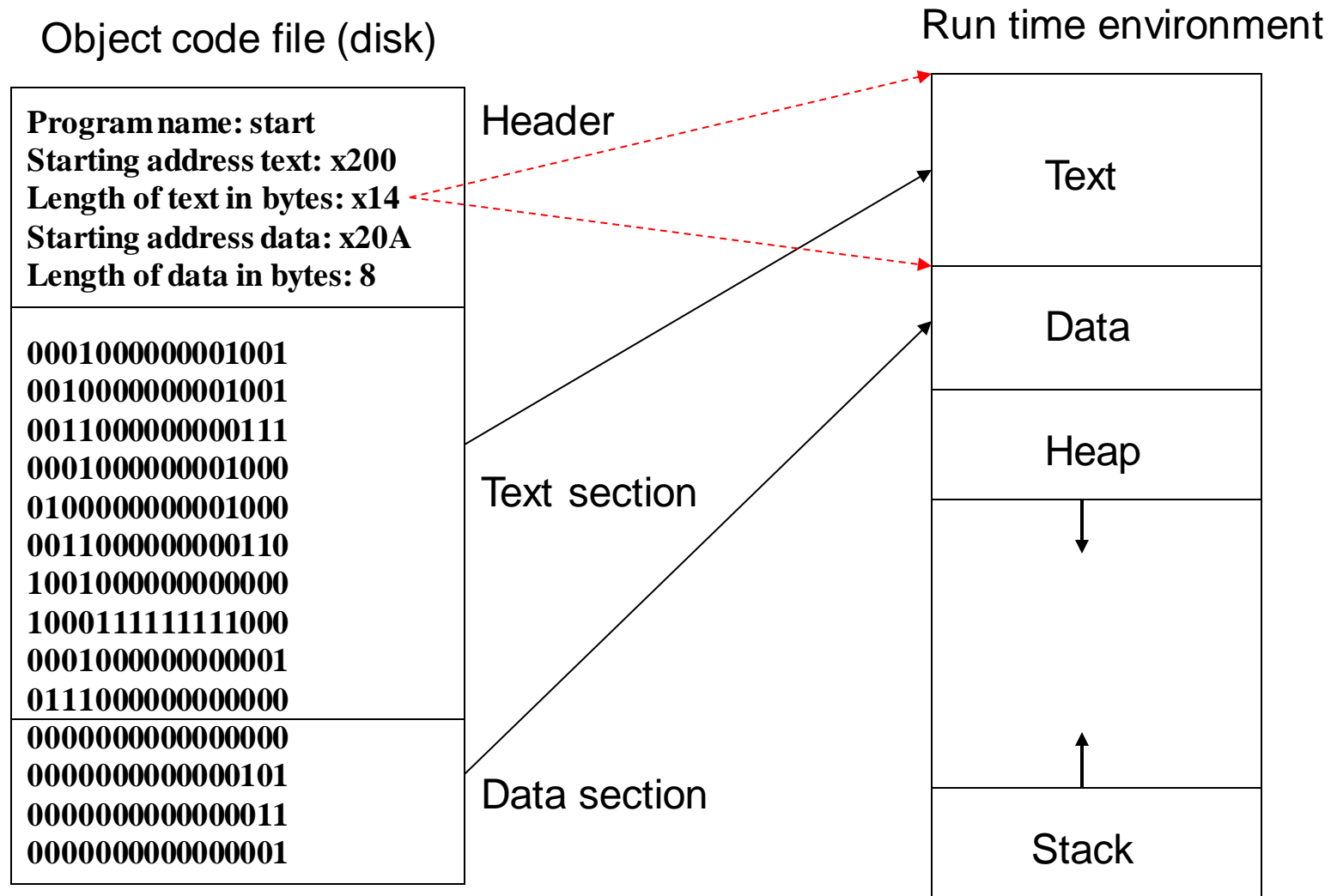
<b>Program name: start</b> <b>Starting address text: x200</b> <b>Length of text in bytes: x14</b> <b>Starting address data: x20A</b> <b>Length of data in bytes: 8</b>	Header
0001000000001001 0010000000001001 0011000000000111 0001000000001000 0100000000001000 0011000000000110 1001000000000000 1000111111111000 0001000000000001 0111000000000000	Text section
0000000000000000 0000000000000101 0000000000000011 0000000000000001	Data section

### Absolute loader:

**The absolute loader will load the program at memory location x200:**

- 1.- The header record is checked to verify that the correct program has been presented for loading.**
- 2.- Each text record is read and moved to the indicate address in memory**
- 3.- When the “end” record (EOF) is encountered, the loader jumps to the specified address to begin execution.**

# Loading object code into memory



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## Bootstrapping:

Computers execute programs stored in main memory, and initially the operating system is on the hard disk.

When the computer is turned on it does not have an operating system loaded in memory and the hardware alone cannot do the operations of an OS. To solve this paradox a special program called bootstrap loader is created.

## Bootstrapping continued...

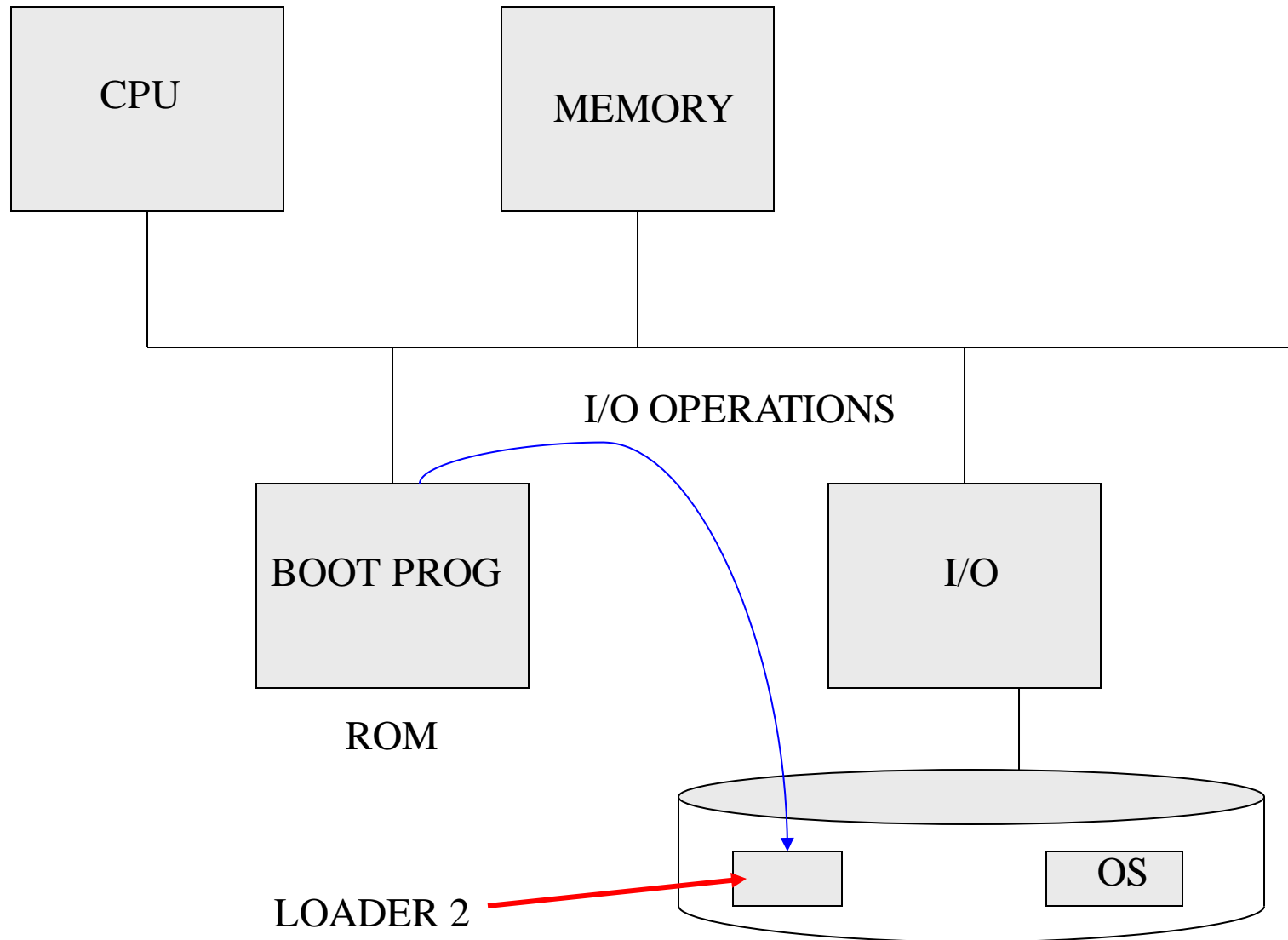
- This program does not have the full functionality of an operating system, but it is capable of loading into memory a more elaborated software (i.e. loader2) which in its turn will load the operating system.
- Once the OS has been loaded the loader transfers the control of the computer system to the operating system.

## Bootstrapping continued...

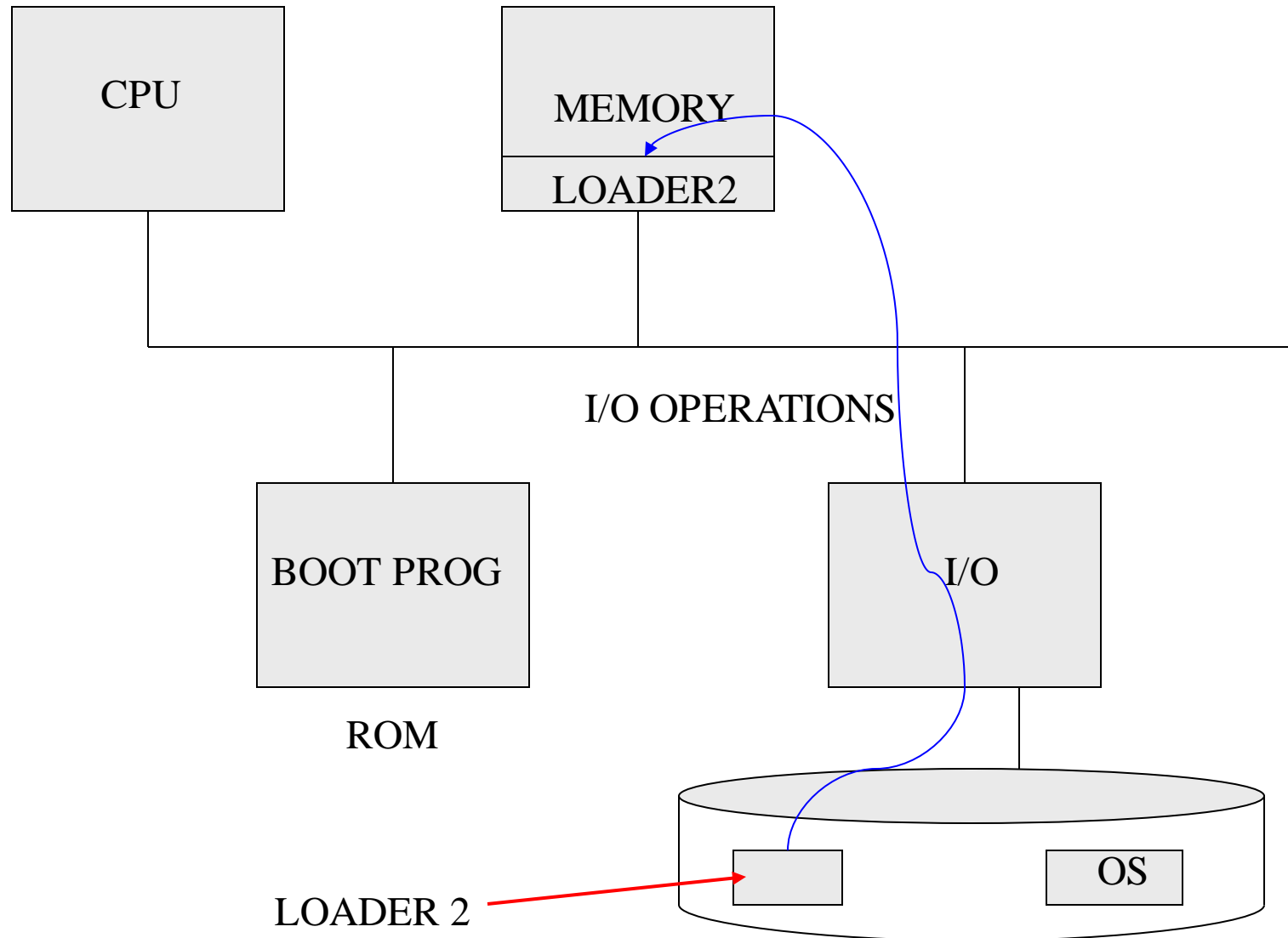
- Early programmable computers had toggle switches on the front panel to allow the operator to place the bootloader into the program store before starting the CPU.
- In modern computers the bootstrapping process begins with the CPU executing software contained in ROM at a predefined address whose elementary functionality is to search for devices eligible to participate in booting, and load a small program from a special section of a device.



# Bootstrapping continued...

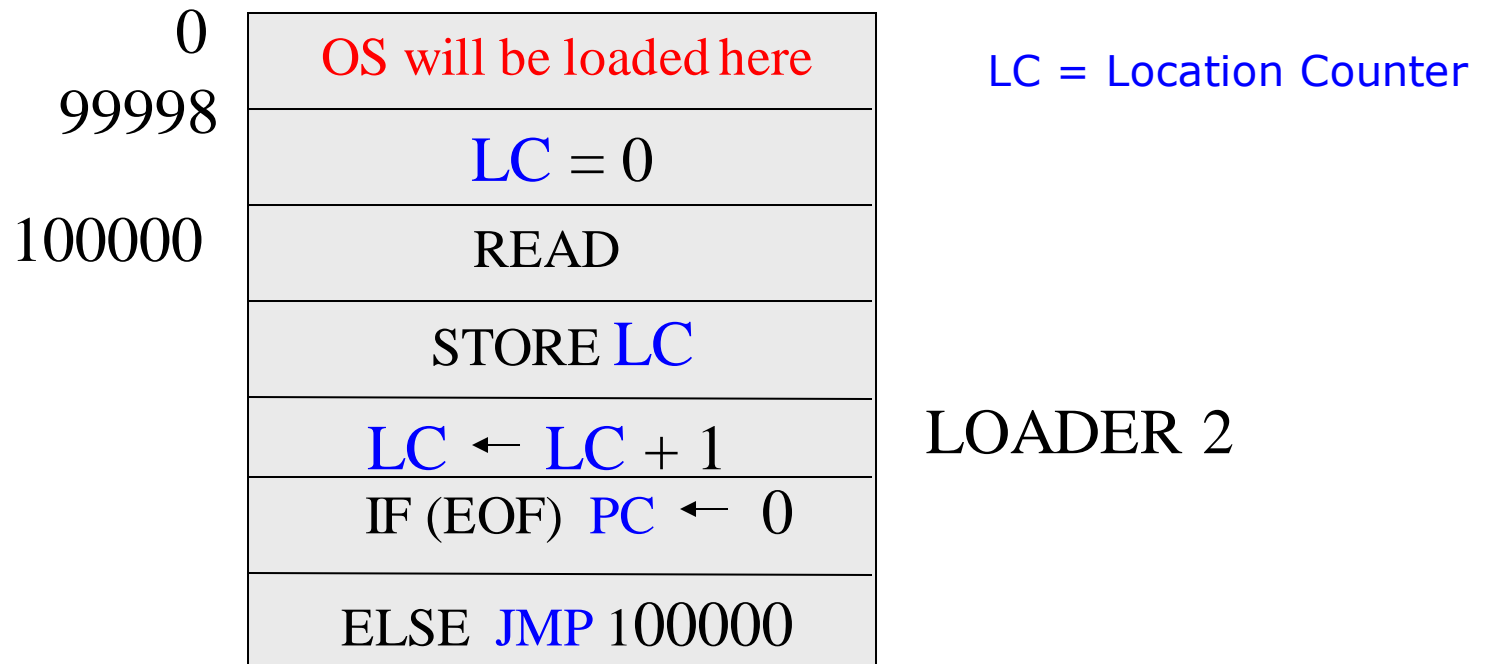


# Bootstrapping continued...

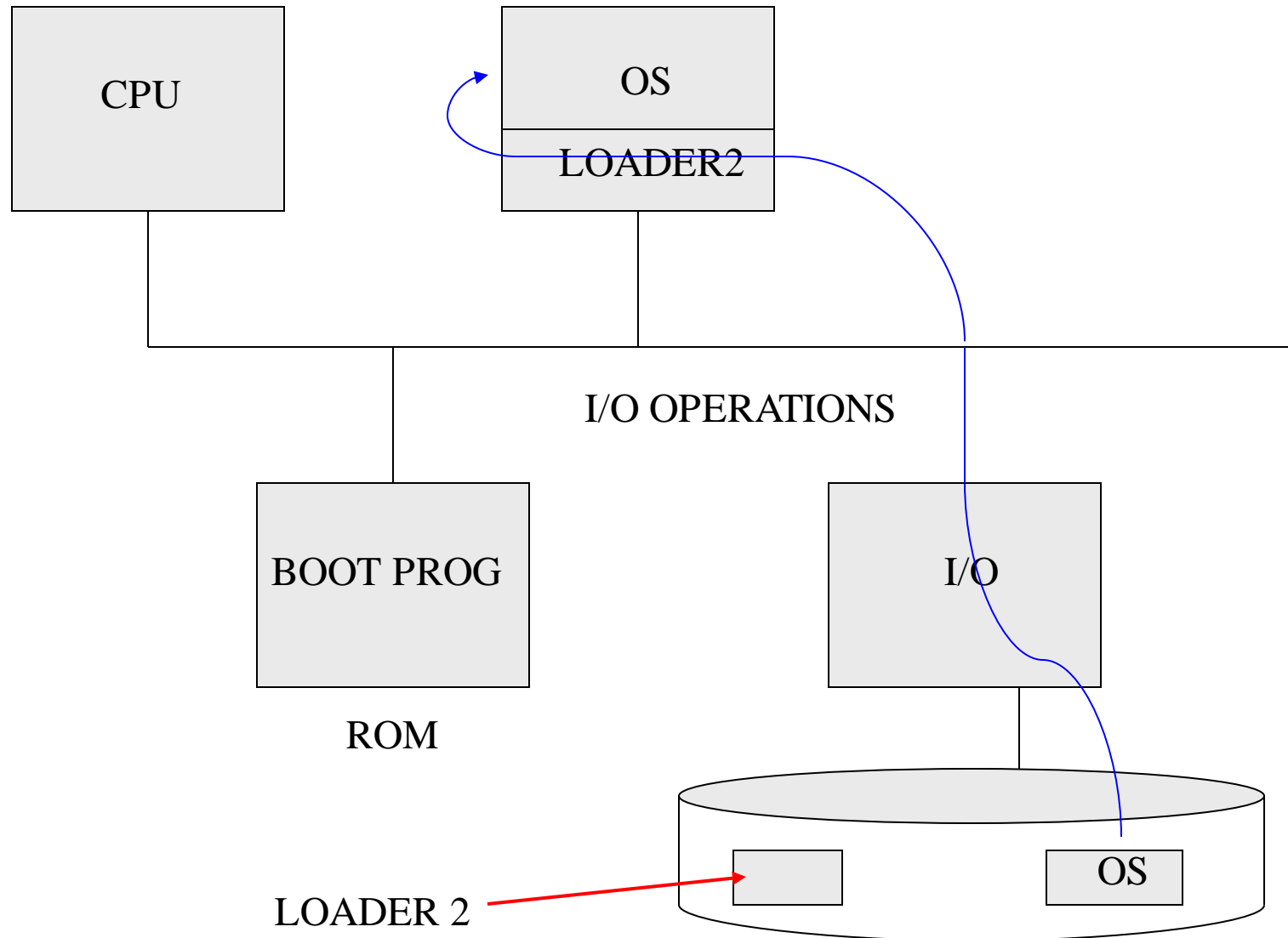


# Bootstrapping continued...

- In earlier computers data had to be hand loaded as specified before, but nowadays a small piece of software called loader helps us to avoid the manual loading.



# Bootstrapping continued...



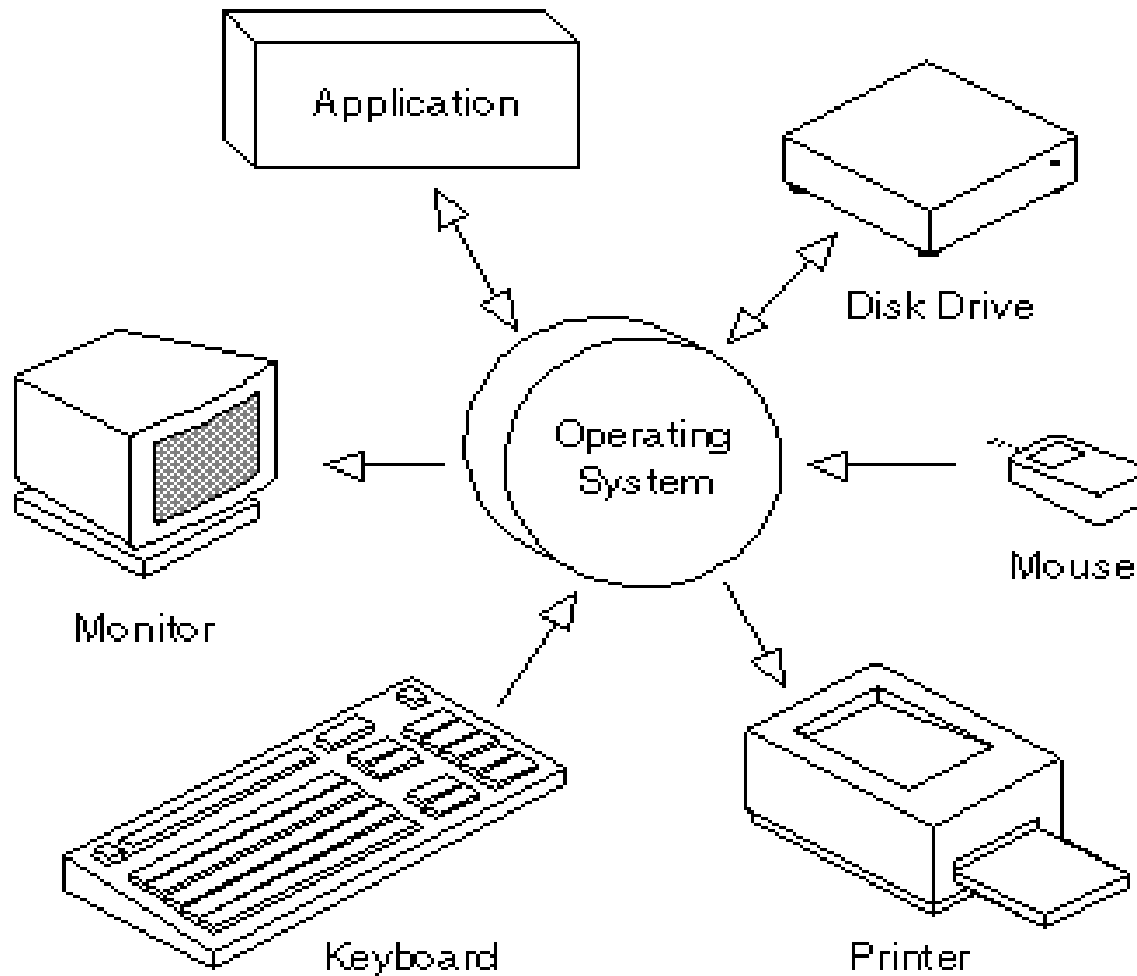
## Bootstrapping continued...

- The above diagram can be explained in the following steps.
  1. Check hardware
  2. Initiate I/O to load the loader 2 program into memory
  3. Loader 2 loads the OS and passes control to it

# Conclusion

- We have seen that once the OS has control over the system , it can create an environment for programs to run.
- The operating system will load device drivers and other programs that are needed for the normal operation of the computer system.

# Operating system



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## The Process Concept



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## Programs and processes

**Once the operating system takes control of the computer system, an applications program (object module or ELF) can be loaded into memory to be executed.**

**When the program is loaded into memory a process is created.**

**What is a process?**

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## Process

### ■ Definition:

- A program in execution
- An asynchronous activity
- The “locus of control” of a procedure in execution
- It is manifested by the existence of a process control block (PCB) in the operating system.

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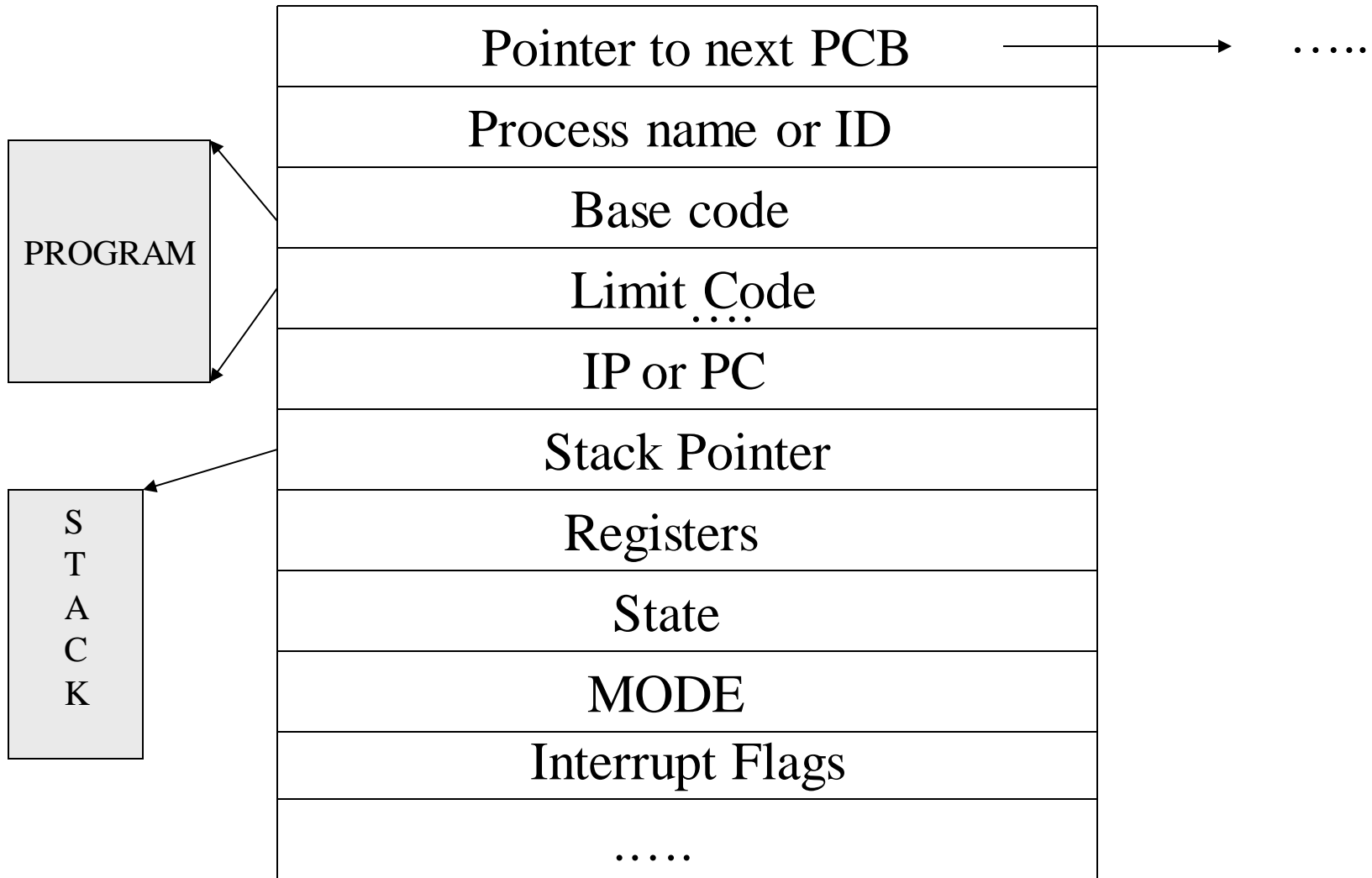
## Process Continued...

- The activity of a process is controlled by a data structure called **P**rocess **C**ontrol **B**lock(**PCB**).
- A PCB is created every time a program is loaded to be executed.
- So, a process is defined by a PCB-Program couple.

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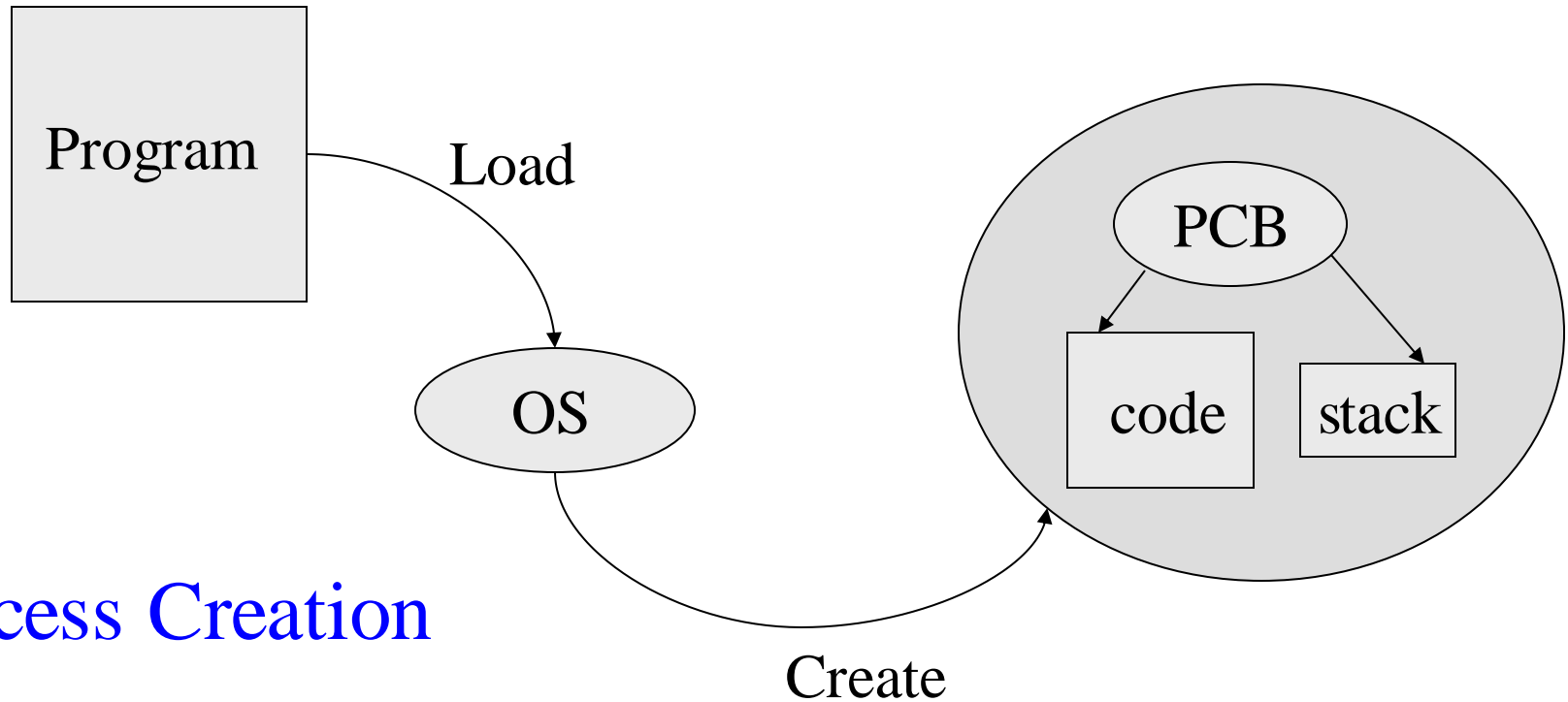
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## Structure of the PCB



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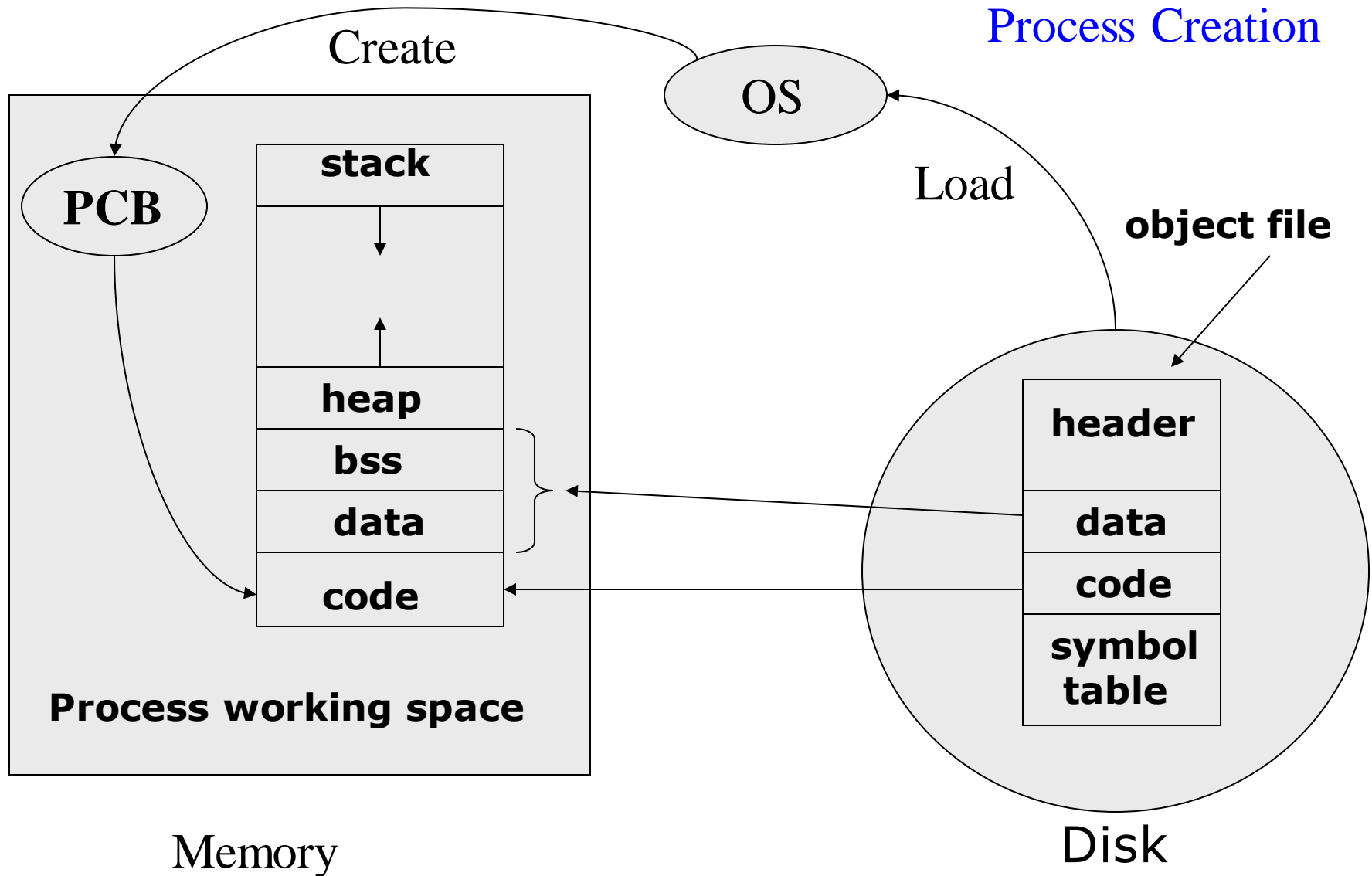
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Process Creation

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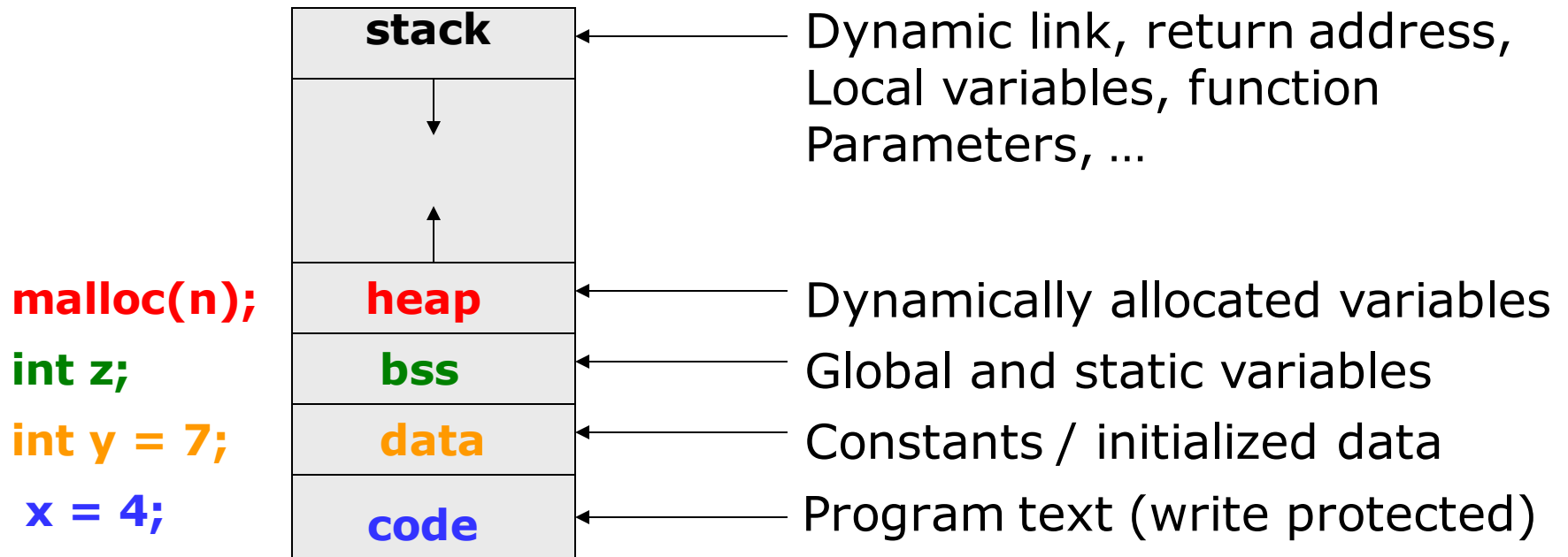
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## Process working space (run-time environment)



**Process working space  
or  
Run-time environment**

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## Process working space

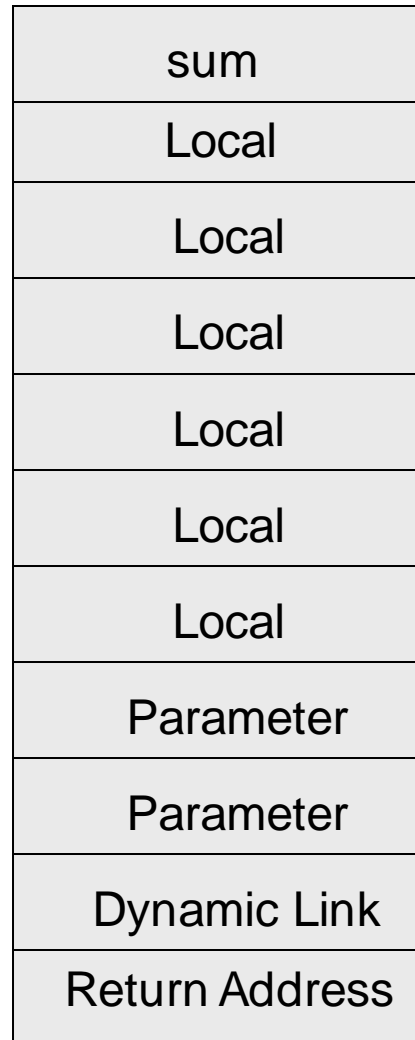
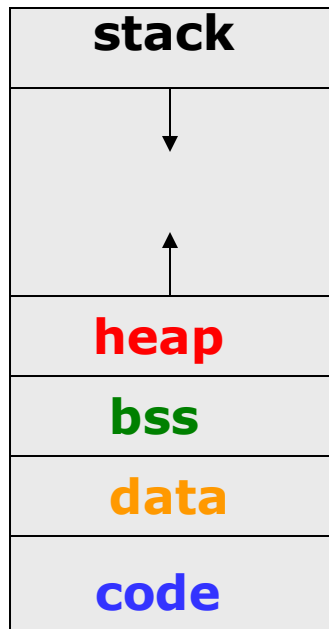
**bss:** means “block started by symbol” and has that name for historical reasons.

**malloc(n);**

**int z;**

**int y = 7;**

**x = 4;**



```
void sub(float total, int part ) {  
    int List[5];  
    float sum;  
    ...  
}
```



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## Relocating Loaders

**Absolute loaders loads a program on a specific memory location but it is often desirable to have two or more programs residing in memory sharing the different resources of a computer system.**

**It would be impractical to assign starting addresses to each program to plan program execution.**

**A loader able to load a program into memory wherever there is room for it is called a relocating loader.**

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## Relocation bits

Assemblers generate code that starts at address zero but it can also emit with each line of text (code) relocation bits indicating what fields in the object code must be modified when the program is loaded in an address different from zero.

For example, if the program will be loaded at address 40, a relocation bits equal to “1” indicates what part of the instruction must be modified:

<u>Loc#</u>	<u>Len</u>	<u>reloc</u>	<u>text</u>		<u>Loc#</u>	<u>text</u>
00	3	0 <b>11</b>	13 <b>33 35</b>	→	40	13 <b>73 75</b>

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source program				before relocation				after relocation	
<u>Label</u>	<u>opcode</u>	<u>address</u>	<u>address</u>	<u>Loc#</u>	<u>Len</u>	<u>reloc</u>	<u>text</u>	<u>Loc#</u>	<u>text</u>
00	copy	zero	older	00	3	011	13 33 35	40	13 73 75
03	copy	one	old	03	3	011	13 34 36	43	13 74 76
06	read	limit		06	2	01	12 38	46	12 78
08	write	old		08	2	01	08 36	48	08 76
10 comp	load	older		10	2	01	03 35	50	03 75
12	add	old		12	2	01	02 36	52	02 76
14	store	new		14	2	01	07 37	54	07 77
16	sub	limit		16	2	01	06 38	56	06 78
18	brpos	finalL		18	2	01	01 30	58	01 70
20	write	new		20	2	01	08 37	60	08 77
22	copy	old	older	22	3	011	13 36 35	62	13 76 75
25	copy	new	old	25	3	011	13 37 36	65	13 77 76
28	br	comp		28	2	01	00 10	68	00 50
30 final	write	limit		30	2	01	08 38	70	08 78
32	stop			32	1	0	11	72	11
33 zero	CONST	0		33	1	0	00	73	00
34 one	CONST	1		34	1	0	01	74	01
35 older	SPACE			35				75	
36 old	SPACE			36				76	
37 new	SPACE			37				77	
38 limit	SPACE			38				78	

Relocation constant to be added is 40

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## **2.- Relocation maps (modification records)**

Interleaving relocation bits with the program text makes cumbersome the process of loading the text directly into memory.

This problem can be resolved by collecting all relocation bits into a single contiguous relocation map that we will call the relocation section of the object code file (ELF).

The relocation section will be appended to the text and data sections.

The header will contain the entry point and length of the relocation section in the object module.

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