

Software Release and Errata Notice

Le880SLVVP VeriVoice Professional Software P1.8.0

Document ID# 133742 Version P1.8.0

Jul 21, 2014

This document describes new features and corrected errata for the Le880SLVVP VeriVoiceTM Professional Software. Refer to the Line Test API User's Guide (Document ID #081470), Rev 22 for more details.

1.0 REVISION SUMMARY

This is a bug fix and feature addition to revision P1.7.2 of the Le880SLVVP software. *This release replaces all previous release.*

This release provides a complete set of the available VeriVoice Professional tests as described in Table 2-2 of the LT-API User's Guide.

PLEASE READ THE FOLLOWING CAREFULLY

This release is compatible with the following VP-API-II release packages:

- LE71SK0002 P2.17.0 to P2.23.0
- LE71SDKAPIL P2.17.0 to P2.23.0

2.0 SOURCE FILES

The release package has the following file/folder structure:

- ReadMe.txt High level installation instructions.
- \api lib\vp880 api VE880 VP-API-II extension source files (primitives).
- \lt api\common LT-API interface implementation files.
- \lt api\documents LT-API User's Guide and this document.
- \lt_api\includes LT-API interface definition source files.
- \lt api\vp880 lt LT-API interface definition source files.

Details pertaining to the source files can be found in the LT-API User's Guide section 1.3.

All source files were updated to replace the Zarlink Copyright notice with the Microsemi Copyright notice.

Release Notice

3.0 CORRECTED ERRATA FROM P1.7.2

- Prevent VP-API ring cadencer from interfering with the Ringing Self Test (LT_TID_RINGING_ST). If an application configured a line to use a ringing cadence, the VP-API would enable the cadence during the Ringing Self Test and cause measurements to be taken during the silent period of the cadence rather than when ringing was applied to the line. The issue would manifest itself as corrupted ringing self test results.
- Prevent large longitudinal capacitive impedances from causing the Line Voltage (LT_TID_LINE_V) test to report false Tip to Ground (vDcTip) or Ring to Ground (vDcRing) voltages.
- Prevent Receiver Off-Hook (LT_TID_ROH) test from incorrectly reporting off-hook phones with a low impedance as a resistive loop (LT_ROHM_RES_LOOP).
- Corrected a Ringer Equivalence Number (REN) test issue in polarity reversal. Starting either electronic ringers test (LT_RINGER_ELECTRONIC_PHNE_TEST_Or

 LT_RINGER_ELECTRONIC_PHNE_TEST_3_ELE) in any VP-API-II polarity reversal state (VP_LINE_STANDBY_POLREV, VP_LINE_ACTIVE_POLREV, VP_LINE_OHT_POLREV Or VP_LINE_TALK_POLREV) caused the test to report a differential value of 0 REN regardless of the actual impedance on the line.

4.0 ADDITIONS / FEATURES

- C5 Improved accuracy /range of Electronic Ringers Test

 (LT_RINGER_ELECTRONIC_PHNE_TEST_ and

 LT_RINGER_ELECTRONIC_PHNE_TEST_3_ELE) measurements in the presences of large longitudinal capacitive impedances.
- **C6** Updated source files to compile without warnings using modern compilers.

5.0 OPERATIONAL NOTES

N1 Operational Note: Customers using VP-API termination type VP_TERM_FXS_GENERIC will observe a reduction in the measurable range of the LT_TID_RD_LOOP_COND imt and ilg results when running from VP-API line states VP_LINE_STANDBY or VP_LINE_STANDBY_POLREV. The ilg and imt return values will be representative of the feed characteristics in these line states causing ilg to report LT_MAX_CURRENT starting at approximately 14mA and imt to report some limited value less than the programed ILA.



Information relating to products and services furnished herein by Microsemi Corporation or its subsidiaries (collectively "Microsemi") is believed to be reliable. However, Microsemi assumes no liability for errors that may appear in this publication, or for liability otherwise arising from the application or use of any such information, product or service or for any infringement of patents or other intellectual property rights owned by third parties which may result from such application or use. Neither the supply of such information or purchase of product or service conveys any license, either express or implied, under patents or other intellectual property rights owned by Microsemi or licensed from third parties by Microsemi, whatsoever. Purchasers of products are also hereby notified that the use of product in certain ways or in combination with Microsemi, or non-Microsemi furnished goods or services may infringe patents or other intellectual property rights owned by Microsemi.

This publication is issued to provide information only and (unless agreed by Microsemi in writing) may not be used, applied or reproduced for any purpose nor form part of any order or contract nor to be regarded as a representation relating to the products or services concerned. The products, their specifications, services and other information appearing in this publication are subject to change by Microsemi without notice. No warranty or guarantee express or implied is made regarding the capability, performance or suitability of any product or service. Information concerning possible methods of use is provided as a guide only and does not constitute any guarantee that such methods of use will be satisfactory in a specific piece of equipment. It is the user's responsibility to fully determine the performance and suitability of any equipment using such information and to ensure that any publication or data used is up to date and has not been superseded. Manufacturing does not necessarily include testing of all functions or parameters. These products are not suitable for use in any medical and other products whose failure to perform may result in significant injury or death to the user. All products and materials are sold and services provided subject to Microsemi's conditions of sale which are available on request.

For more information about all Microsemi products visit our website at www.microsemi.com

TECHNICAL DOCUMENTATION - NOT FOR RESALE



Microsemi Corporate Headquarters One Enterprise, Aliso Viejo CA 92656 USA Within the USA: +1 (949) 380-6100 Sales: +1 (949) 380-6136 Fax: +1 (949) 215-4996 Microsemi Corporation (NASDAQ: MSCC) offers a comprehensive portfolio of semiconductor solutions for: aerospace, defense and security; enterprise and communications; and industrial and alternative energy markets. Products include high-performance, high-reliability analog and RF devices, mixed signal and RF integrated circuits, customizable SoCs, FPGAs, and complete subsystems. Microsemi is headquartered in Aliso Viejo, Calif. Learn more at www.microsemi.com.

© 2014 Microsemi Corporation. All rights reserved. Microsemi and the Microsemi logo are trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners.